

XRT Timeline to be uploaded on 2011/07/26

Period: 2011/07/26 10:06:00 - 2011/07/30 10:24:00

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Normal mode

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XOB #1880: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 22sec-cad													
Term	Pointing (x, y)		Comment										
07/26 10:19:00 - 07/26 10:51:30	Track (401.6, 282.0) ^{@ 07/26 10:16:00}		# OP start + 10min, obs AR11259										
PROG= 01 Inf.-time(s)													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 41 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 13 55-time(s) 22.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT													
Term	Pointing (x, y)		Comment										
07/26 12:25:00 - 07/26 14:09:54	Fixed (0.0, 945.0)		Co-alignment @ N limb										
PROG= 14 1-time(s)													
└─ Subr= 1 1-time(s) 360.0sec													
└─ Seqn= 21 24-time(s) 300.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT													
Term	Pointing (x, y)		Comment										
07/26 14:25:00 - 07/26 16:49:54	Fixed (-945.0, 0.0)		Co-alignment @ E limb										
PROG= 06 1-time(s)													
└─ Subr= 1 1-time(s) 360.0sec													
└─ Seqn= 22 15-time(s) 480.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17DC: Synoptic Q95 2x2 - Al/mesh(33/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1024) + G-band(12)													
Term	Pointing (x, y)		Comment										
07/26 16:53:00 - 07/26 16:59:54	Fixed (0.0, 0.0)		synoptic										
07/28 03:33:00 - 07/28 03:39:54	Fixed (0.0, 0.0)		synoptic, shifted 4.5 min										
PROG= 18 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 20 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 31 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 77 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 63 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #189B: XBP Al/mesh - Ti/Poly - FOV384 - Q90 - AEC4 - 10s cadence													
Term	Pointing (x, y)		Comment										
07/26 17:03:00 - 07/26 20:43:30	Track (6.5, -30.7) ^{@ 07/26 17:00:00}		HOP196										
PROG= 12 Inf.-time(s)													
└─ Subr= 1 1-time(s) 180.0sec													
└─ Seqn= 84 60-time(s) 10.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	4	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	4	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #189C: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 15sec-cad												
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Term	Pointing (x, y)	Comment
07/26 21:26:30 - 07/27 01:00:00	Track (478.7, 285.6) ^{Ⓜ 07/26 21:00:00}	obs AR11259
07/28 01:48:00 - 07/28 03:29:54	Track (658.5, 298.9) ^{Ⓜ 07/28 01:45:00}	HOP173, AR11259
07/28 03:43:00 - 07/28 05:24:00	Track (-448.7, 186.6) ^{Ⓜ 07/28 03:40:00}	HOP173, AR11260
07/28 06:03:00 - 07/28 10:25:30	Track (681.2, 301.2) ^{Ⓜ 07/28 06:00:00}	obs AR11259

PROG= 02 Inf.-time(s)

Subr= 2	1-time(s)	2.0sec
Seqn= 19	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 41	4-time(s)	2.0sec
Open/Al-mesh	Open/Ti-poly close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Subr= 1	1-time(s)	2.0sec
Seqn= 13	55-time(s)	15.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 1 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 2 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 3 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
07/28 01:08:00 - 07/28 01:14:54	Fixed (-528.4, -528.4)	XRT quadrant pointing 1/4

PROG= 16 1-time(s)

Subr= 1	1-time(s)	12.0sec
Seqn= 38	1-time(s)	12.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
Subr= 2	1-time(s)	2.0sec
Seqn= 93	2-time(s)	2.0sec
Open/Al-mesh	Open/Ti-poly close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly -long

Term	Pointing (x, y)	Comment
07/28 01:18:00 - 07/28 01:24:54	Fixed (528.4, -528.4)	XRT quadrant pointing 2/4

PROG= 15 1-time(s)

Subr= 1	1-time(s)	12.0sec
Seqn= 36	1-time(s)	12.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
Subr= 2	1-time(s)	2.0sec
Seqn= 93	2-time(s)	2.0sec
Open/Al-mesh	Open/Ti-poly close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
07/28 01:28:00 - 07/28 01:34:54	Fixed (528.4, 528.4)	XRT quadrant pointing 3/4

PROG= 09 1-time(s)

Subr= 1	1-time(s)	12.0sec
Seqn= 39	1-time(s)	12.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
Subr= 2	1-time(s)	2.0sec
Seqn= 93	2-time(s)	2.0sec
Open/Al-mesh	Open/Ti-poly close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
07/28 01:38:00 - 07/28 01:44:54	Fixed (-528.4, 528.4)	XRT quadrant pointing 4/4

PROG= 10 1-time(s)

Subr= 1	1-time(s)	12.0sec
Seqn= 40	1-time(s)	12.0sec

Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 93 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term	Pointing (x, y)		Comment									
07/26 10:19:00 - 07/26 10:51:30	Track (401.6,	282.0)	# OP start + 10min, obs AR11259									
07/26 17:03:00 - 07/26 20:43:30	Track (6.5,	-30.7)	HOP196									
07/26 21:26:30 - 07/27 01:00:00	Track (478.7,	285.6)	obs AR11259									
07/28 01:48:00 - 07/28 03:29:54	Track (658.5,	298.9)	HOP173, AR11259									
07/28 03:43:00 - 07/28 05:24:00	Track (-448.7,	186.6)	HOP173, AR11260									
07/28 06:03:00 - 07/28 10:25:30	Track (681.2,	301.2)	obs AR11259									
PROG= 08 1-time(s)												
Subr= 1 30-time(s) 20.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec												
Seqn= 89 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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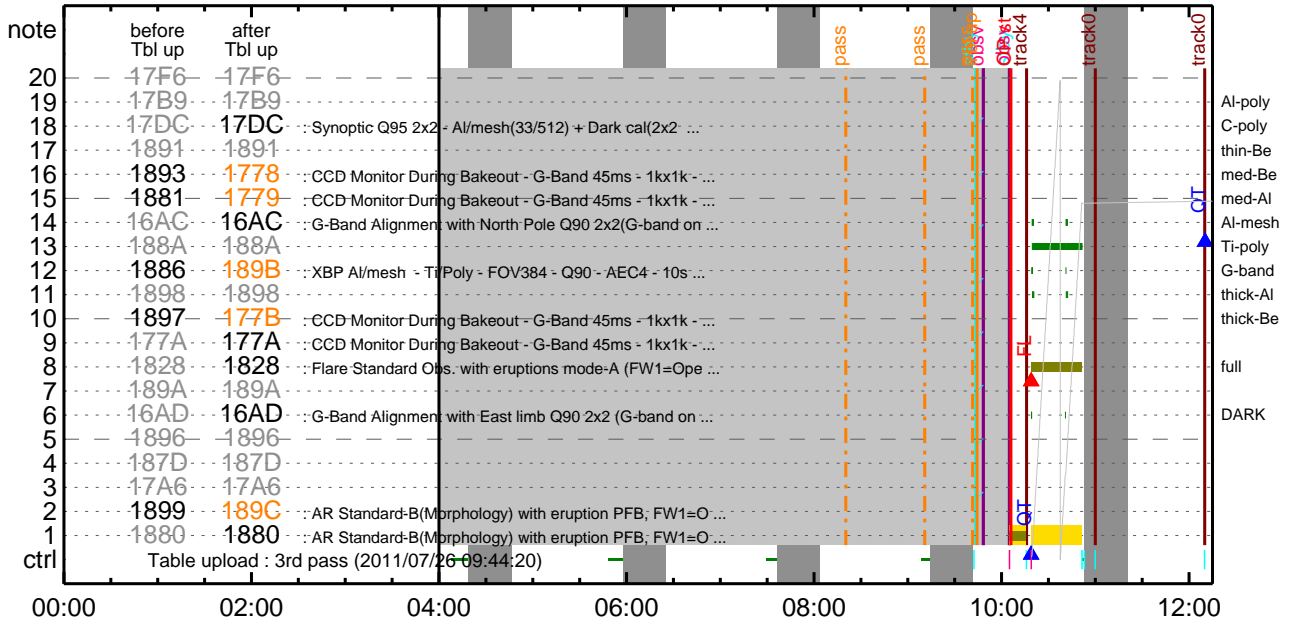
Flare Detection

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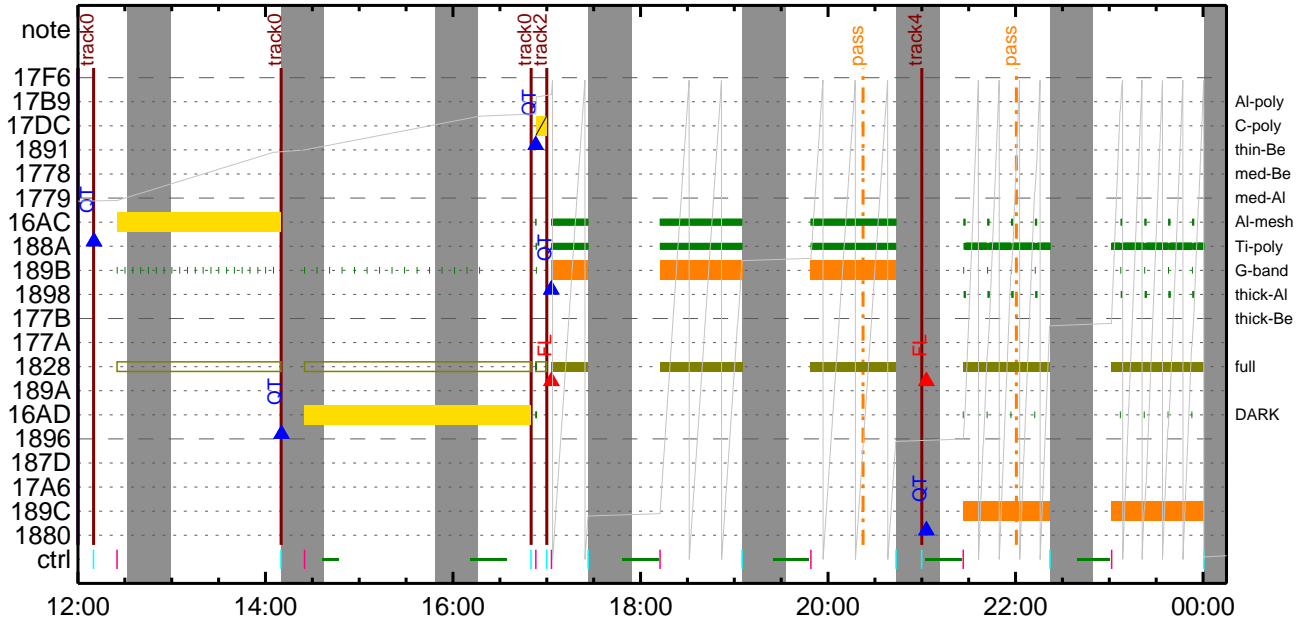
FLD Patrol

Term	Pointing (x, y)	Comment
07/26 17:02:46 - 07/28 01:07:54	Track (6.5, -30.7) @ 07/26 17:00:00	HOP196
07/28 01:47:46 - 07/28 03:30:16	Track (658.5, 298.9) @ 07/28 01:45:00	HOP173, AR11259
07/28 03:42:46 - 07/30 10:24:00	Track (-448.7, 186.6) @ 07/28 03:40:00	HOP173, AR11260
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

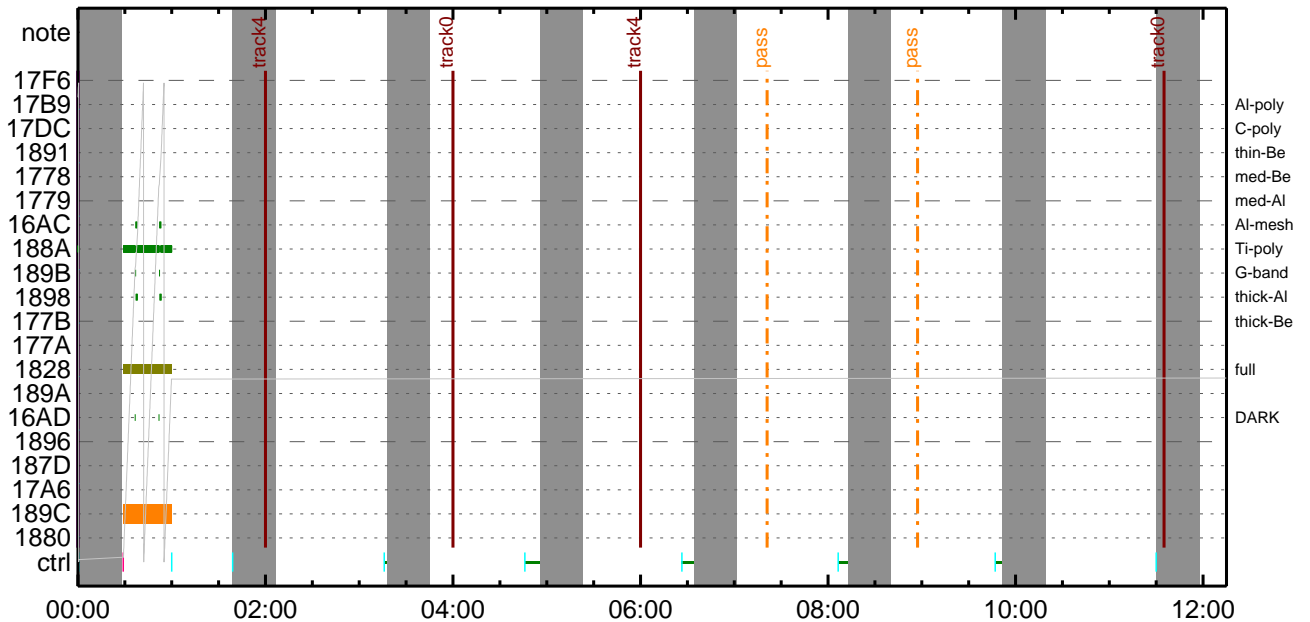
CMDI #0057 2011/07/26



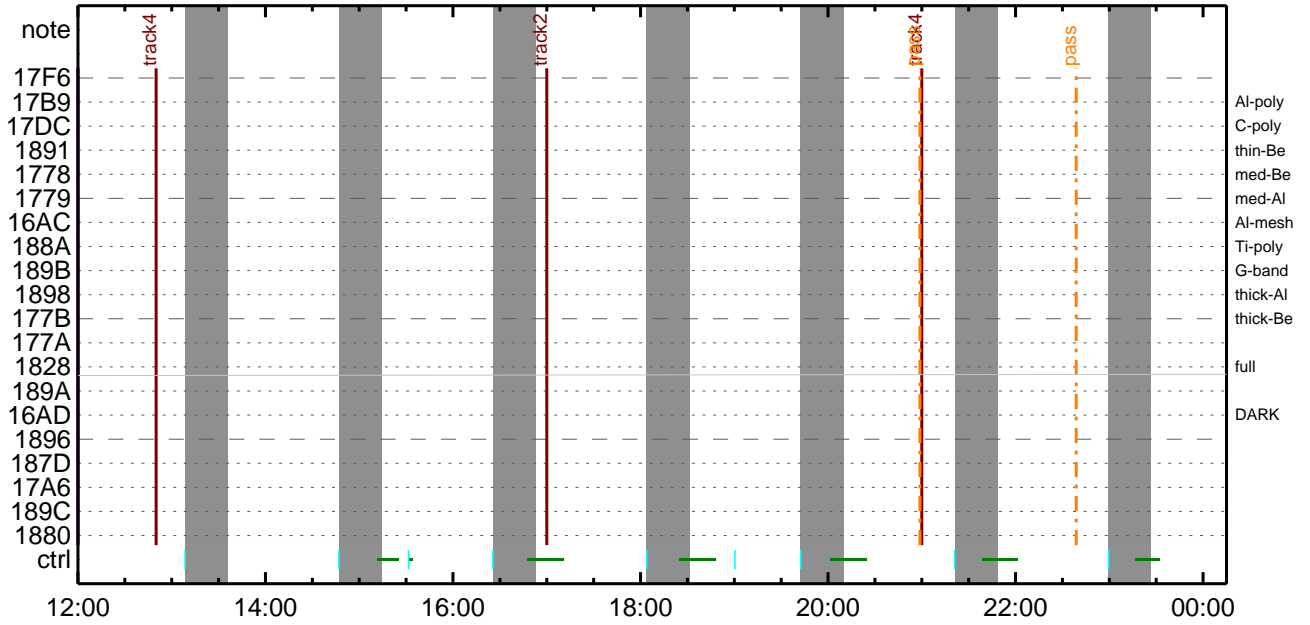
CMDI #0057 2011/07/26



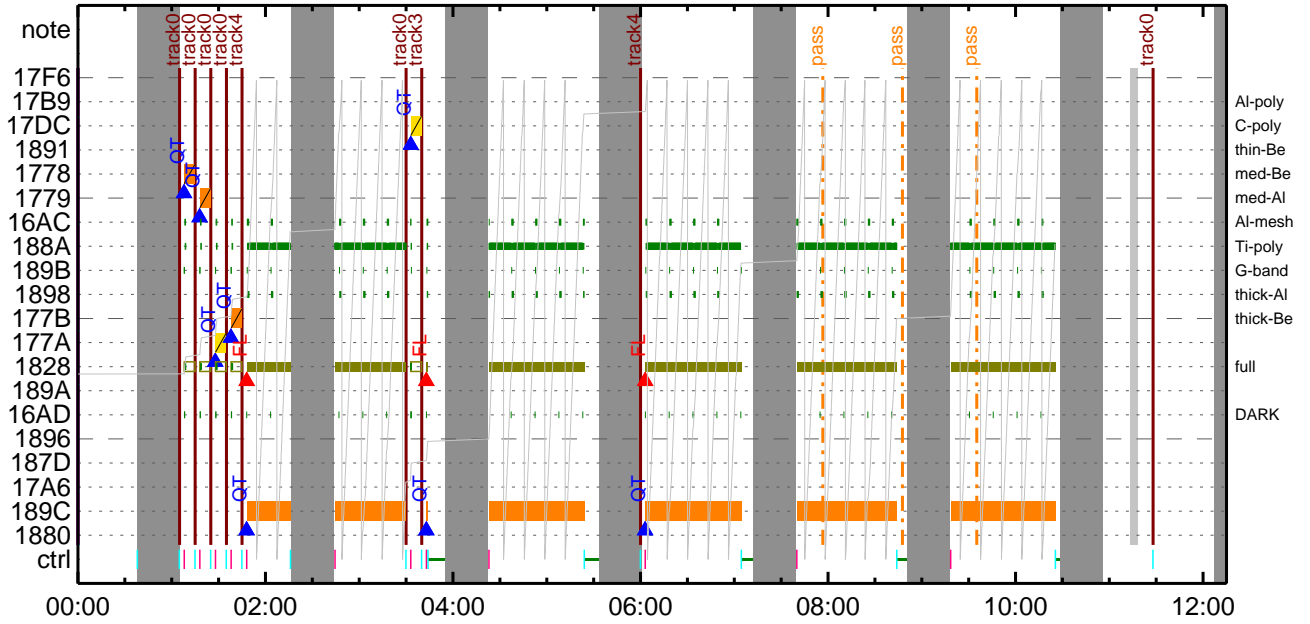
CMDI #0057 2011/07/27



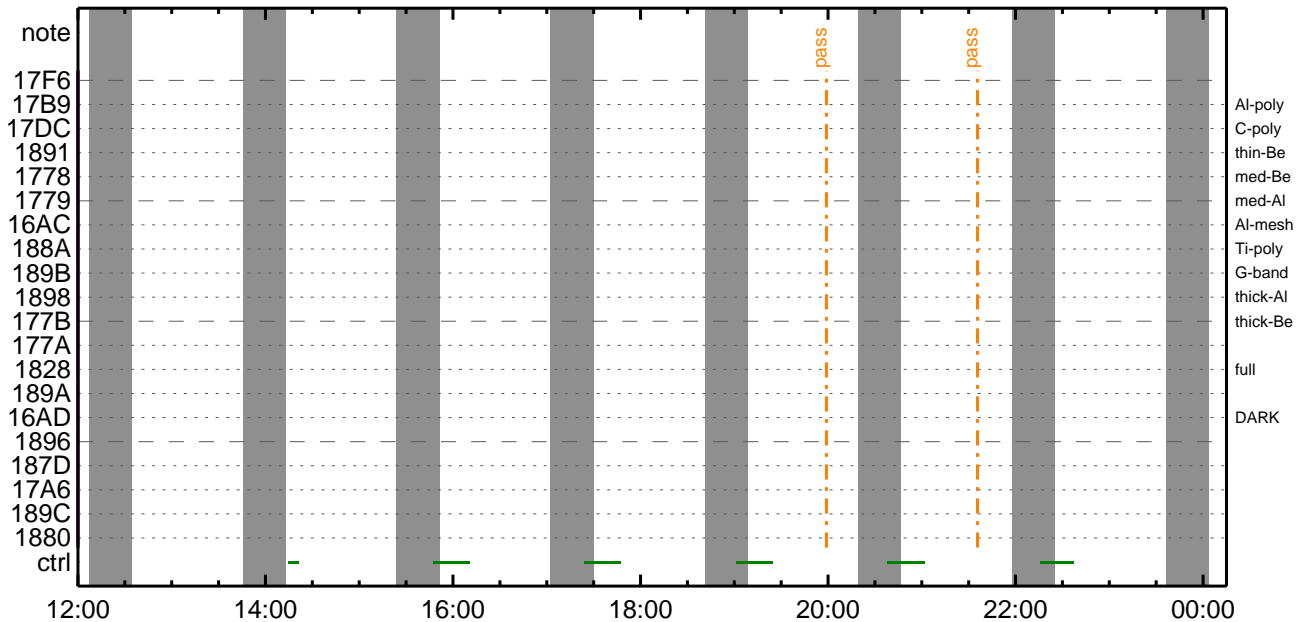
CMDI #0057 2011/07/27



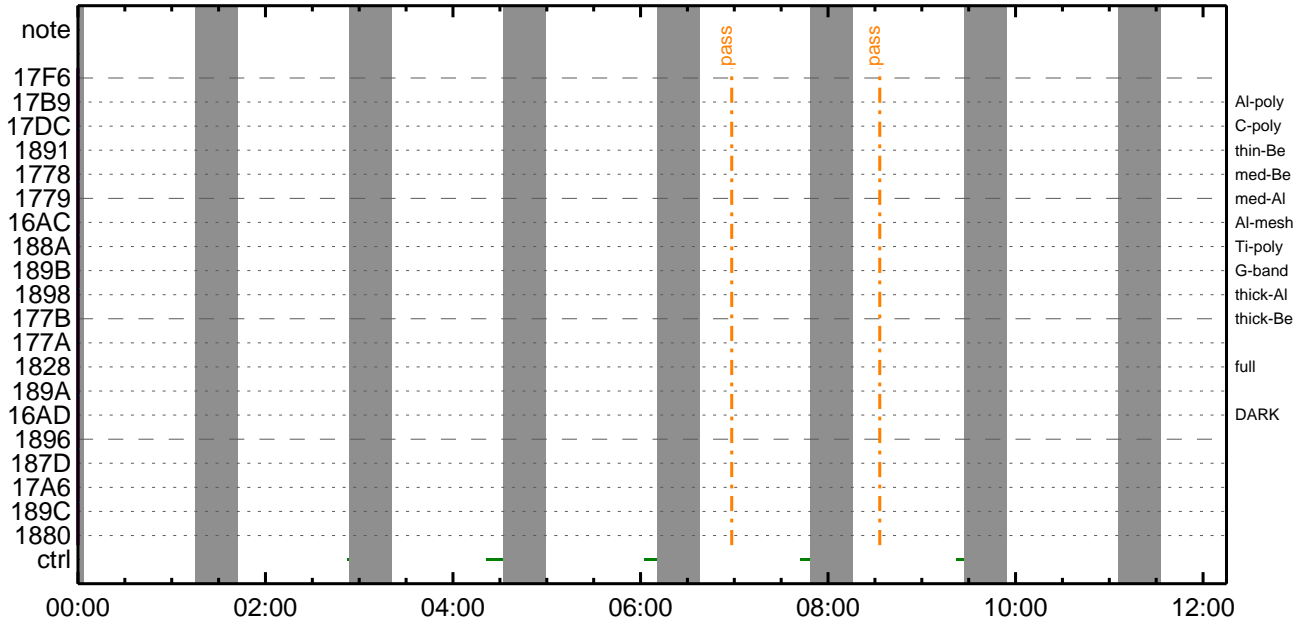
CMDI #0057 2011/07/28



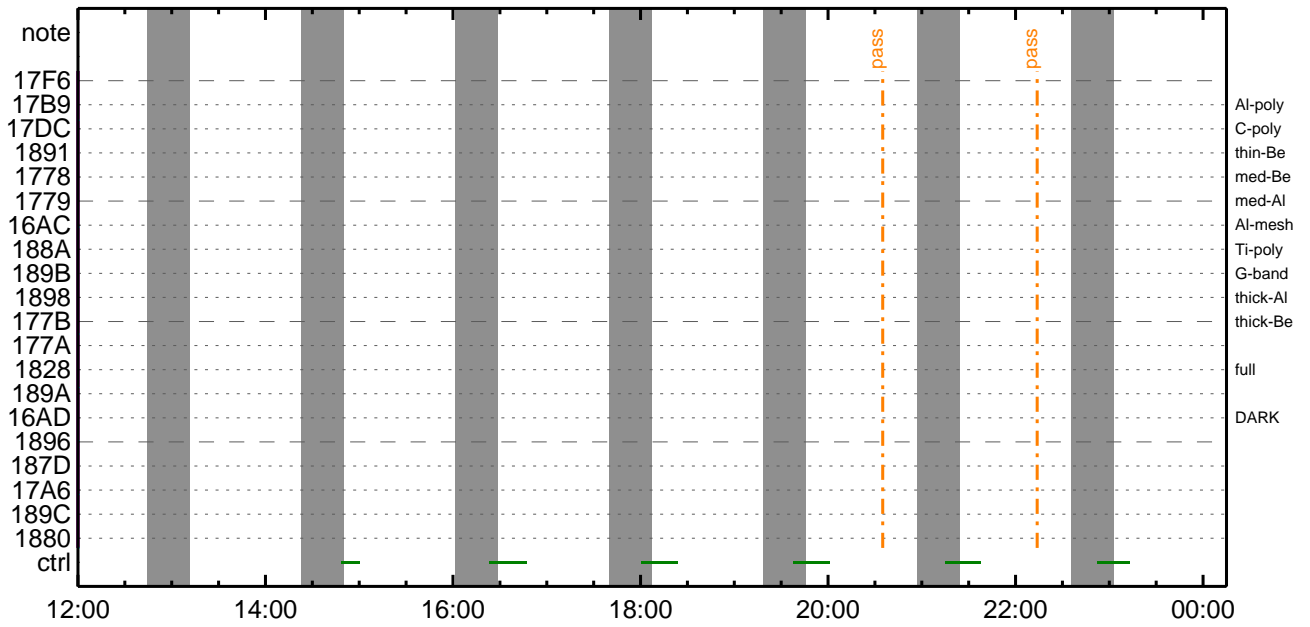
CMDI #0057 2011/07/28



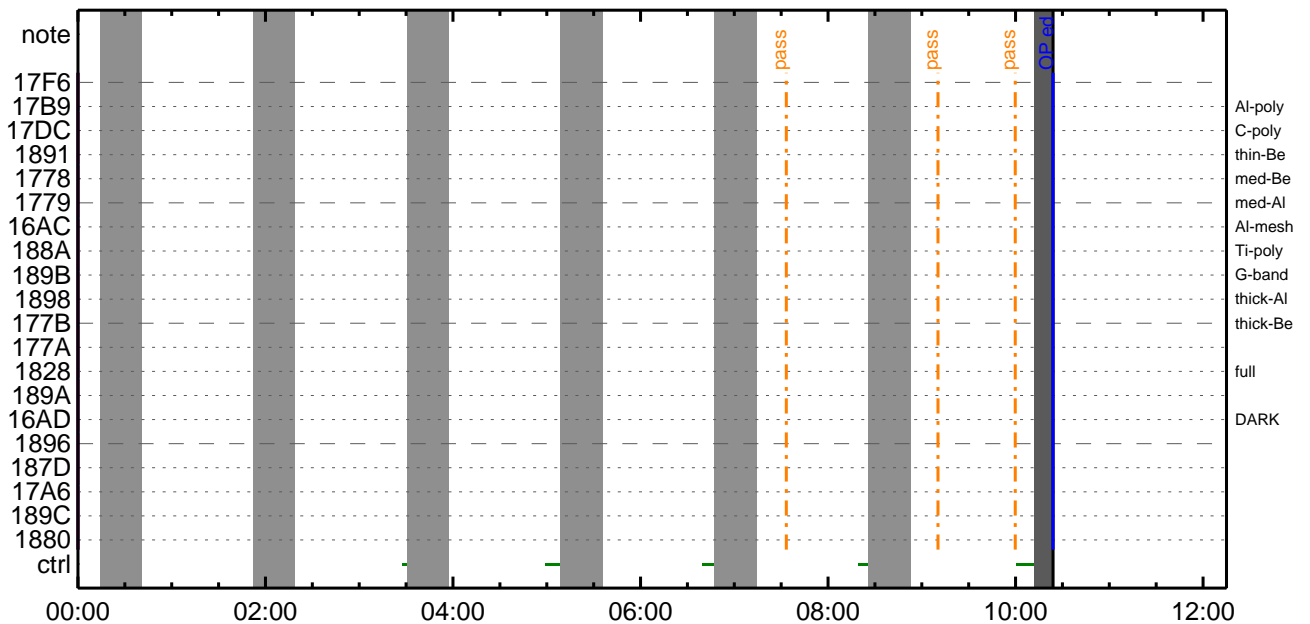
CMDI #0057 2011/07/29



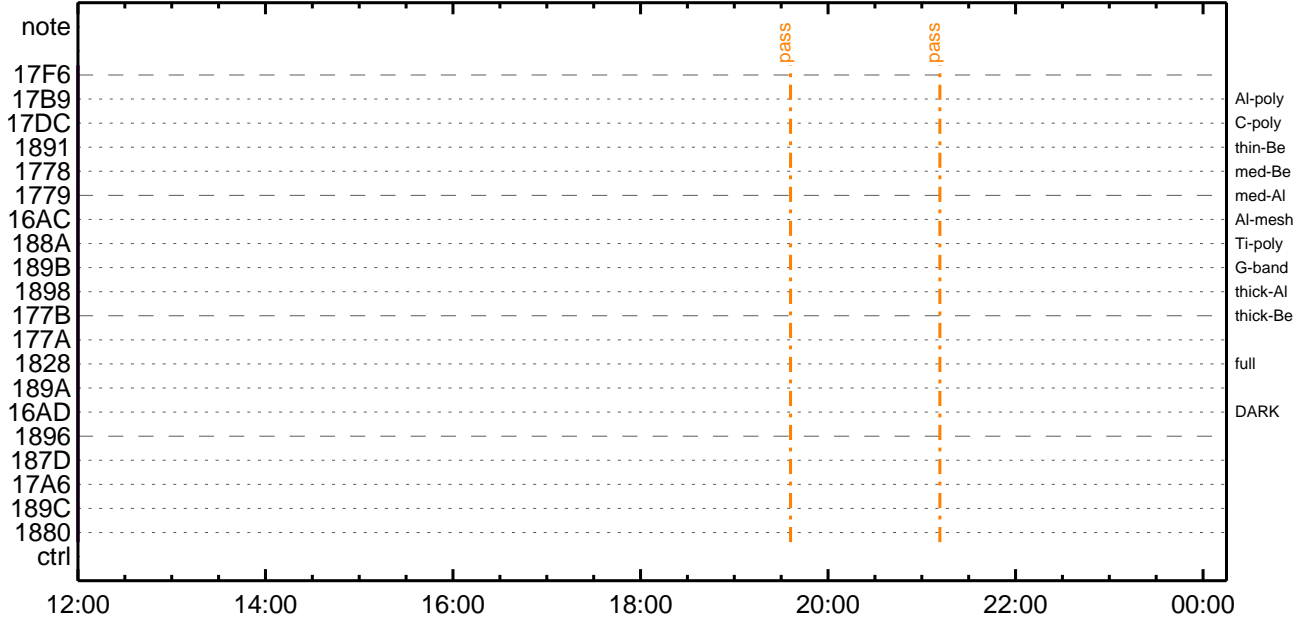
CMDI #0057 2011/07/29



CMDI #0057 2011/07/30



CMDI #0057 2011/07/30




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-219:OP
0104 ( )
0105 S. OG og-219:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPãî½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** òE²¼òî½Ã´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÃÓÃæç¼ª°"òE¼i¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADã-Á÷¿@NGUî½i¹ç;ç°E²¼òîTI-CMDÁ÷¿@ãî½Á¹Ôã•òEòòò³òE;f
0180 C. òEò¿;çSETòEDUMPAîE±°iYNY¹ç¹Ôã|ò³òE;f
0181 C.
0182 C. TIY³YpYóYEòðÃî¿¿(UT)
0183 +. TI 2011-07-26 10:01:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-07-26 10:01:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-07-26 10:01:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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```

0194 C.
0195 +. TI 2011-07-26 10:05:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      ÷÷[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C.      °È²¼āİÄë%îîñāîŷÄŷ§ŷÄŷ-¹âiÛ
0200 C.      ÷÷[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0201 C.      ÷÷[HK1_TI_CMD_NUM]                EQ      4
0202 C.      ÷÷[HK1_NEXT_EXEC_PIM]            EQ      DHU
0203 C.      ÷÷[HK1_NEXT_EXEC_DC]             EQ      0xB3
0204 C.
0205 C.      *****
0206 C.      TIîî°èŷÄŷÖŷ×
0207 C.      *****
0208 C.
0209 C.      TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      ÷÷[HK1_DMP_TOP_ADRS_1]          EQ      07
0213 C.      ÷÷[HK1_DMP_TOP_ADRS_0]          EQ      2B
0214 C.      ÷÷[HK1_DMP_BLOCK_NUM]           EQ      3
0215 C.      ÷÷[HK1_DMP_REPEAT_NUM]         EQ      0
0216 C.      ÷÷[HK1_DMA_DMP_PIM]            EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      ÷÷[HK1_PKT_FORM_NO]             EQ      7
0220 C.      ÷÷[HK1_PKT_GEN_TIME]            EQ      0.25 s
0221 C.      ÷÷[HK1_S_TLM_BIT_RATE]         EQ      32k
0222 C.      ÷÷[HK1_X_TLM_BIT_RATE]         EQ      4M
0223 C.      ÷÷[HK1_DMP_CHK_FLG]            EQ      EXEC
0224 C.
0225 C.      ŷÄŷÖŷ×½ªî»à³îÇ§
0226 C.      ÷÷[HK1_DMP_CHK_FLG]            EQ      NON
0227 C.
0228 C.      RAM ID=TI_TBLñî¼È¹ç•è²îOKà³îÇ§
0229 C.
0230 C.      DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èàðîäñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      ÷÷[HK1_PKT_FORM_NO]             EQ      2
0234 C.      ÷÷[HK1_PKT_GEN_TIME]            EQ      0.5S
0235 C.      ÷÷[HK1_S_TLM_BIT_RATE]         EQ      32K
0236 C.      ÷÷[HK1_X_TLM_BIT_RATE]         EQ      4M
0237 C.
0238 C.      Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C.      ***** Start EIS operation (TI set) *****
0242 C.      Execute, after the success of OP upload.
0243 C.      Set EIS TI-commands
0244 +. TI 2011-07-26 10:05:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2011-07-26 10:05:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.      [ ] [HK1_TI_CMD_NUM]            EQ      2 COUNTUP
0251 C.      ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.      *****
0255 C.      SOT TI command set
0256 C.      *****
0257 C.      Execute, after the success of OP upload.
0258 +. TI 2011-07-26 10:05:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C.      -----
0262 C.      HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0263 C.      -----
0264 C.      ***** SOT END *****
0265 C.
0266 C.      ***** XRT START *****
0267 C.      Execute, after the success of OP upload.
0268 +. TI 2011-07-26 10:05:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM]            EQ      1COUNTUP
0272 C.
0273 C.      ***** XRT END *****
0274 C.
0275 C.      ***** MDP ´úÃîāİ»ö¼ŷñēÄðñ¹ñēDCBC•x²è *****
0276 C.      (¼ª°îŷÖŷÄŷÈŷpŷŷÄŷçŷèè¼āñ»Ûñ¹è)
0277 C.      S. DC-BC dcbc-402:DCBC
0278 C.      (MDP_known_event)
0279 C.
0280 C.
0281 C.      ***** ŷÐŷ¹•İ Daily±çîññē'Øñ¹ñēDCBC•x²è *****
0282 C.      S. DC-BC dcbc-153:DCBC
0283 C.      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C.      ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C.      ***** LOS *****
0289 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-221 2011-07-26 11:40:08 157 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿øÁø•µ°È»ÍxÁÇøíYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøí;çÀ®, ùø¹øèøøøøÇÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 08 08)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 60 20 18)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 a0 80 18 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b c0 c0 10 10)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 40 c0 10 10)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0d 40 40 10 10)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0e c0 40 10 10)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 0f 80 80 06 06)
0062 + DC 07-F0 MDP_XRT_ROI_SET
0063 BC (cd 10 80 80 08 08)
0064 + DC 07-F0 MDP_XRT_FLD_ENA
0065 BC (d8)
0066 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0067 BC (c8)
0068 + DC 07-F0 MDP_XRT_ARS_DIS
0069 BC (d5)
0070 . C. ----- Success Verify ? OK / NG ____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. TI 2011-07-26 10:05:02.0
0078 DC 07-F0 MDP_XRT_MODE_OBSV
0079 BC (c2)
0080 +. TI 2011-07-26 10:05:04.0
0081 DC 07-F0 MDP_XRT_CTRL_AUTO
0082 BC (c0)
0083 . C. ----- Success Verify ? OK / NG ____
0084 C.
0085 C. ***** XRT END *****
0086 . C. *****
0087 C. SOT table upload
0088 C. *****
0089 . C. < Stop FG table >
0090 +. DC 07-F0 MDP_FG_CTRL_MANU
0091 BC (51)
0092 . C. -----
0093 C. MDP_FG_CTRL_MODE = MANU [ ]
0094 C. -----
0095 C.
```


*** OP Sequence for XRT ***

2011/07/26	10:15:54.0	XRT_CTRL_MANU_439_OG [0x1b7]		
		MDP_XRT_CTRL_MANU	1 07-F0	c1
2011/07/26	10:16:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5 02-76	04 00 00 00 00
2011/07/26	10:18:26.0	XRT_FOCUS_POSITION_409_OG [0x199]		
		XRT_FOCUS_POSITION	4 07-F8	22 fe 97 00
2011/07/26	10:18:46.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1 07-F0	d8
2011/07/26	10:18:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		
		MDP_XRT_FLRCTRL_ENA	1 07-F0	c8
2011/07/26	10:18:50.0	XRT_AEC_RESET_443_OG [0x1bb]		
		MDP_XRT_AEC_RESET	1 07-F0	d0
2011/07/26	10:18:52.0	XRT_ARS_DIS_431_OG [0x1af]		
		MDP_XRT_ARS_DIS	1 07-F0	d5
2011/07/26	10:18:54.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1 07-F0	da
2011/07/26	10:18:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]		
		MDP_XRT_QT_PROG_SET	2 07-F0	c4 01
2011/07/26	10:18:58.0	XRT_FL_PROG_SET_414_OG [0x19e]		
		MDP_XRT_FL_PROG_SET	2 07-F0	c5 08
2011/07/26	10:19:00.0	XRT_CTRL_AUTO_406_OG [0x196]		
		MDP_XRT_CTRL_AUTO	1 07-F0	c0
2011/07/26	10:51:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1 07-F0	c1
2011/07/26	10:51:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1 07-F0	da
2011/07/26	10:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1 07-F0	e8
2011/07/26	10:53:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1 07-F0	c1
2011/07/26	10:53:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1 07-F0	da
2011/07/26	10:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1 07-F0	e8
2011/07/26	10:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1 07-F0	e9
2011/07/26	10:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1 07-F0	e9
2011/07/26	10:59:54.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1 07-F0	c1
2011/07/26	11:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5 02-76	00 00 00 00 00
2011/07/26	12:09:54.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1 07-F0	c1
2011/07/26	12:09:56.0	XRT_FOCUS_POSITION_445_OG [0x1bd]		
		XRT_FOCUS_POSITION	4 07-F8	22 fe 97 00
2011/07/26	12:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]		
		AOCU_NM	5 02-76	00 ac 00 00 00
2011/07/26	12:10:16.0	XRT_QT_PROG_SET_429_OG [0x1ad]		
		MDP_XRT_QT_PROG_SET	2 07-F0	c4 0e
2011/07/26	12:10:18.0	XRT_FLD_DIS_423_OG [0x1a7]		
		MDP_XRT_FLD_DIS	1 07-F0	d9
2011/07/26	12:10:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]		
		MDP_XRT_FLRCTRL_DIS	1 07-F0	c9
2011/07/26	12:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]		
		MDP_XRT_ARS_DIS	1 07-F0	d5
2011/07/26	12:25:00.0	XRT_CTRL_AUTO_406_OG [0x196]		
		MDP_XRT_CTRL_AUTO	1 07-F0	c0
2011/07/26	14:09:54.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1 07-F0	c1
2011/07/26	14:09:56.0	XRT_FOCUS_POSITION_445_OG [0x1bd]		
		XRT_FOCUS_POSITION	4 07-F8	22 fe 97 00
2011/07/26	14:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]		
		AOCU_NM	5 02-76	00 00 00 54 00
2011/07/26	14:10:16.0	XRT_QT_PROG_SET_432_OG [0x1b0]		
		MDP_XRT_QT_PROG_SET	2 07-F0	c4 06
2011/07/26	14:10:18.0	XRT_FLD_DIS_423_OG [0x1a7]		
		MDP_XRT_FLD_DIS	1 07-F0	d9
2011/07/26	14:10:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]		
		MDP_XRT_FLRCTRL_DIS	1 07-F0	c9
2011/07/26	14:10:22.0	XRT_ARS_DIS_437_OG [0x1b5]		
		MDP_XRT_ARS_DIS	1 07-F0	d5
2011/07/26	14:25:00.0	XRT_CTRL_AUTO_406_OG [0x196]		
		MDP_XRT_CTRL_AUTO	1 07-F0	c0
2011/07/26	16:49:54.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1 07-F0	c1
2011/07/26	16:49:56.0	XRT_FOCUS_POSITION_401_OG [0x191]		
		XRT_FOCUS_POSITION	4 07-F8	22 ff aa 00
2011/07/26	16:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5 02-76	00 00 00 00 00
2011/07/26	16:50:16.0	XRT_FLD_DIS_402_OG [0x192]		
		MDP_XRT_FLD_DIS	1 07-F0	d9
2011/07/26	16:50:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]		
		MDP_XRT_FLRCTRL_DIS	1 07-F0	c9
2011/07/26	16:50:20.0	XRT_ARS_DIS_438_OG [0x1b6]		
		MDP_XRT_ARS_DIS	1 07-F0	d5
2011/07/26	16:52:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]		
		MDP_XRT_QT_PROG_SET	2 07-F0	c4 12
2011/07/26	16:53:00.0	XRT_CTRL_AUTO_406_OG [0x196]		
		MDP_XRT_CTRL_AUTO	1 07-F0	c0
2011/07/26	16:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]		

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2011/07/26	17:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	02 00 00 00 00
2011/07/26	17:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/26	17:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/26	17:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/26	17:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/26	17:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/26	17:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/26	17:02:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2011/07/26	17:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/26	17:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/26	17:26:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/26	17:26:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/26	17:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/26	17:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/26	18:11:30.0	XRT_Custom_418_OG [0x1a2]				
2011/07/26	18:12:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/26	19:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/26	19:05:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/26	19:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/26	19:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/26	19:48:00.0	XRT_Custom_418_OG [0x1a2]				
2011/07/26	19:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/26	20:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/26	20:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/26	20:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/26	20:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/26	20:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/26	21:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00
2011/07/26	21:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/26	21:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/26	21:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/26	21:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/26	21:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/26	21:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/26	21:02:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2011/07/26	21:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/26	21:25:30.0	XRT_Custom_418_OG [0x1a2]				
2011/07/26	21:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/26	22:22:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/26	22:22:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/26	22:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/26	22:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/26	23:00:30.0	XRT_Custom_418_OG [0x1a2]				
2011/07/26	23:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/27	00:00:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/27	00:00:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/27	00:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/27	00:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2011/07/27	00:28:00.0	XRT_Custom_418_OG [0x1a2]							
2011/07/27	00:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/27	01:00:00.0	XRT_CTRL_MANU_449_OG [0x1c1]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/27	01:00:30.0	XRT_TCIB_XRT_S_HTR_A_ENA_417_OG [0x1a1]							
		TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2011/07/27	01:39:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/27	01:39:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/27	01:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/27	01:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/27	02:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/07/27	03:00:30.0	XRT_Custom_421_OG [0x1a5]							
2011/07/27	03:16:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/27	03:16:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/27	03:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/27	03:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/27	04:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 ed 59 b0 00				
2011/07/27	04:46:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/27	04:46:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/27	04:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/27	04:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/27	05:00:30.0	XRT_Custom_421_OG [0x1a5]							
2011/07/27	06:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/07/27	06:26:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/27	06:26:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/27	06:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/27	06:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/27	07:00:30.0	XRT_Custom_421_OG [0x1a5]							
2011/07/27	08:06:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/27	08:06:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/27	08:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/27	08:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/27	09:00:30.0	XRT_Custom_410_OG [0x19a]							
2011/07/27	09:00:40.0	XRT_Custom_421_OG [0x1a5]							
2011/07/27	09:47:00.5	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/27	09:47:02.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/27	09:47:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/27	09:50:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/27	11:00:40.0	XRT_Custom_421_OG [0x1a5]							
2011/07/27	11:30:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/27	11:30:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/27	11:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/27	11:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/27	11:35:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/07/27	12:50:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/07/27	13:00:40.0	XRT_Custom_421_OG [0x1a5]							
2011/07/27	13:08:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/27	13:08:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/27	13:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/27	13:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/27	14:47:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/27	14:47:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/27	14:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							

2011/07/27	14:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/27	15:00:40.0	XRT_Custom_421_OG [0x1a5]							
2011/07/27	15:31:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/27	15:31:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/27	15:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/27	15:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/27	16:25:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/27	16:25:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/27	16:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/27	16:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/27	17:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
			AOCU_NM	5	02-76	02 00 00 00 00			
2011/07/27	17:00:40.0	XRT_Custom_405_OG [0x195]							
2011/07/27	18:04:00.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/27	18:04:02.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/27	18:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/27	18:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/27	19:00:18.0	XRT_Custom_410_OG [0x19a]							
2011/07/27	19:00:28.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/27	19:00:30.0	XRT_TCIB_XRT_S_HTR_A_DIS_407_OG [0x197]							
			TCIB_XRT_S_HTR_A_DIS	0	04-C0				
2011/07/27	19:42:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/27	19:42:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/27	19:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/27	19:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/27	21:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
			AOCU_NM	5	02-76	04 00 00 00 00			
2011/07/27	21:21:00.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/27	21:21:02.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/27	21:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/27	21:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/27	22:59:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/27	22:59:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/27	22:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/27	23:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/28	00:38:00.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/28	00:38:02.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/07/28	00:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/07/28	00:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/07/28	01:04:54.0	XRT_CTRL_MANU_426_OG [0x1aa]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/28	01:05:00.0	AOCS_Ore-point_Start_7_OG [0x09d]							
			AOCU_NM	5	02-76	00 2e f9 2e f9			
2011/07/28	01:07:32.0	XRT_FOCUS_POSITION_435_OG [0x1b3]							
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2011/07/28	01:07:52.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 10			
2011/07/28	01:07:54.0	XRT_FLD_DIS_402_OG [0x192]							
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/07/28	01:07:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/07/28	01:07:58.0	XRT_ARS_DIS_431_OG [0x1af]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/07/28	01:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/07/28	01:14:54.0	XRT_CTRL_MANU_426_OG [0x1aa]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/07/28	01:15:00.0	AOCS_Ore-point_Start_8_OG [0x09e]							
			AOCU_NM	5	02-76	00 2e f9 d1 07			
2011/07/28	01:17:32.0	XRT_FOCUS_POSITION_435_OG [0x1b3]							
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2011/07/28	01:17:52.0	XRT_QT_PROG_SET_416_OG [0x1a0]							

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2011/07/28	01:17:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2011/07/28	01:17:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/07/28	01:17:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/07/28	01:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/07/28	01:24:54.0	XRT_CTRL_MANU_426_OG [0x1aa]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/28	01:25:00.0	AOCS_Ore-point_Start_9_OG [0x09f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/28	01:27:32.0	XRT_FOCUS_POSITION_435_OG [0x1b3]	AOCU_NM	5	02-76	00 d1 07 d1 07	
2011/07/28	01:27:52.0	XRT_QT_PROG_SET_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/07/28	01:27:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2011/07/28	01:27:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/07/28	01:27:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/07/28	01:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/07/28	01:34:54.0	XRT_CTRL_MANU_426_OG [0x1aa]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/28	01:35:00.0	AOCS_Ore-point_Start_10_OG [0x0a0]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/28	01:37:32.0	XRT_FOCUS_POSITION_435_OG [0x1b3]	AOCU_NM	5	02-76	00 d1 07 2e f9	
2011/07/28	01:37:52.0	XRT_QT_PROG_SET_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/07/28	01:37:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2011/07/28	01:37:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/07/28	01:37:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/07/28	01:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/07/28	01:44:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/28	01:45:00.0	AOCS_Ore-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/28	01:47:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	04 00 00 00 00	
2011/07/28	01:47:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/07/28	01:47:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/07/28	01:47:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/07/28	01:47:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/07/28	01:47:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/07/28	01:47:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/28	01:47:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2011/07/28	01:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/07/28	02:16:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/28	02:16:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/28	02:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/07/28	02:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/07/28	02:43:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/07/28	02:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]				
2011/07/28	03:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/28	03:29:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/07/28	03:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/07/28	03:30:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00	
2011/07/28	03:30:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/07/28	03:30:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/07/28	03:32:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/07/28	03:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2011/07/28	03:39:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/07/28	03:40:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2011/07/28	03:42:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	03	00	00	00	00
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2011/07/28	03:42:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/28	03:42:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/28	03:42:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/28	03:42:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/28	03:42:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/28	03:42:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2011/07/28	03:42:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/07/28	03:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/28	03:44:00.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/28	03:44:02.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/28	03:44:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/28	03:47:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/28	04:22:00.0	XRT_Custom_418_OG [0x1a2]								
2011/07/28	04:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/28	05:24:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/28	05:24:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/28	05:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/28	05:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/28	05:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/28	06:00:00.0	AOCs_OrE-point_Start_1_OG [0x097]								
2011/07/28	06:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	04	00	00	00	00
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2011/07/28	06:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/07/28	06:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/07/28	06:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/07/28	06:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/07/28	06:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/28	06:02:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2011/07/28	06:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2011/07/28	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/28	07:04:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/28	07:04:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/28	07:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/28	07:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/28	07:39:00.0	XRT_Custom_418_OG [0x1a2]								
2011/07/28	07:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/28	08:44:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/28	08:44:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/28	08:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/28	08:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/28	09:17:30.0	XRT_Custom_418_OG [0x1a2]								
2011/07/28	09:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/07/28	10:25:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/28	10:25:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/07/28	10:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/07/28	10:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/07/28	11:27:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/07/28	11:28:00.0	AOCs_OrE-point_Start_2_OG [0x098]								

AOCU_NM

5 02-76 00 00 00 00 00