

# XRT Timeline to be uploaded on 2011/07/30

Period: 2011/07/30 10:24:00 - 2011/08/04 10:13:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

<b>XOB #1880: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 22sec-cad</b>													
Term	Pointing (x, y)		Comment										
07/30 10:37:00 - 07/30 17:59:54	Track ( 37.7, 190.2) <sup>Ⓜ 07/30 10:34:00</sup>		# OP start + 10min, Observe flaring AR 11260.										
07/30 18:13:00 - 07/31 03:44:54	Track ( 103.3, 190.3) <sup>Ⓜ 07/30 18:10:00</sup>		# AR 11260.										
07/31 03:58:00 - 07/31 10:39:30	Track ( 186.6, 191.2) <sup>Ⓜ 07/31 03:55:00</sup>		# AR 11260.										
<b>PROG= 01 Inf.-time(s)</b>													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 41 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 13 55-time(s) 22.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #17DC: Synoptic Q95 2x2 - Al/mesh(33/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1024) + G-band(12)</b>													
Term	Pointing (x, y)		Comment										
07/30 18:03:00 - 07/30 18:09:54	Fixed ( 0.0, 0.0)		synoptic										
07/31 03:48:00 - 07/31 03:54:54	Fixed ( 0.0, 0.0)		synoptic, shifted ~ 2 hours.										
07/31 18:03:30 - 07/31 18:10:24	Fixed ( 0.0, 0.0)		synoptic, shifted 0.5 min										
08/01 04:03:00 - 08/01 04:09:54	Fixed ( 0.0, 0.0)		synoptic, shifted ~ 2 hours.										
08/01 17:48:00 - 08/01 17:53:00	Fixed ( 0.0, 0.0)		synoptic, shifted -19.0 min										
08/02 04:13:00 - 08/02 04:19:54	Fixed ( 0.0, 0.0)		synoptic										
<b>PROG= 18 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 20 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 31 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 77 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 63 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384</b>													
Term	Pointing (x, y)		Comment										
07/31 11:11:37 - 07/31 16:49:54	Fixed ( -6.0, 866.0)		# HOP 81 polar monitoring (North)										
08/01 10:10:06 - 08/01 15:59:54	Fixed ( -6.0, -934.0)		# HOP 81 polar monitoring (South).										
<b>PROG= 05 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 51 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 2 30-time(s) 2.0sec													
└─ Seqn= 58 2-time(s) 30.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1898: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad</b>												
Term	Pointing (x, y)		Comment									
07/31 16:53:00 - 07/31 17:16:00	Track ( 294.4, 193.5) <sup>Ⓜ 07/31 16:50:00</sup>		# AR 11260.									
07/31 18:13:30 - 08/01 03:59:54	Track ( 305.4, 193.8) <sup>Ⓜ 07/31 18:10:30</sup>		# AR 11260. HOP 173 from 2UT.									
08/01 04:13:00 - 08/01 06:15:00	Track ( -94.5, 144.9) <sup>Ⓜ 08/01 04:10:00</sup>		# HOP 173 observing AR 11261.									
08/01 06:51:00 - 08/01 09:35:00	Track ( 399.6, 197.2) <sup>Ⓜ 08/01 06:00:00</sup>		# AR 11260.									
08/01 16:03:00 - 08/01 17:40:54	Track ( 475.3, 201.0) <sup>Ⓜ 08/01 16:00:00</sup>		# AR 11260.									
08/01 18:40:06 - 08/02 03:36:00	Track ( 488.8, 201.7) <sup>Ⓜ 08/01 17:51:00</sup>		# AR 11260. HOP 173 from 2UT.									
08/02 04:23:00 - 08/02 06:53:00	Track ( 118.2, 144.0) <sup>Ⓜ 08/02 04:20:00</sup>		# HOP 173 observing AR 11261.									
08/02 07:28:00 - 08/02 10:14:00	Track ( 573.8, 207.5) <sup>Ⓜ 08/02 06:00:00</sup>		# AR 11260.									
<b>PROG= 11 Inf.-time(s)</b>												

<b>Subr= 2 1-time(s) 2.0sec</b>													
<b>Seqn= 19 1-time(s) 2.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 41 4-time(s) 2.0sec</b>													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 1 1-time(s) 2.0sec</b>													
<b>Seqn= 13 30-time(s) 80.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

**Flare mode**

\* \* \* \* \*

<b>XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)</b>												
Term	Pointing (x, y)							Comment				
07/30 10:37:00 - 07/30 17:59:54	Track ( 37.7, 190.2)	⑧ 07/30 10:34:00						# OP start + 10min, Observe flaring AR 11260.				
07/30 18:13:00 - 07/31 03:44:54	Track ( 103.3, 190.3)	⑧ 07/30 18:10:00						# AR 11260.				
07/31 03:58:00 - 07/31 10:39:30	Track ( 186.6, 191.2)	⑧ 07/31 03:55:00						# AR 11260.				
07/31 11:11:37 - 07/31 16:49:54	Fixed ( -6.0, 866.0)							# HOP 81 polar monitoring (North)				
07/31 16:53:00 - 07/31 17:16:00	Track ( 294.4, 193.5)	⑧ 07/31 16:50:00						# AR 11260.				
07/31 18:13:30 - 08/01 03:59:54	Track ( 305.4, 193.8)	⑧ 07/31 18:10:30						# AR 11260. HOP 173 from 2UT.				
08/01 04:13:00 - 08/01 06:15:00	Track ( -94.5, 144.9)	⑧ 08/01 04:10:00						# HOP 173 observing AR 11261.				
08/01 06:51:00 - 08/01 09:35:00	Track ( 399.6, 197.2)	⑧ 08/01 06:00:00						# AR 11260.				
08/01 10:10:06 - 08/01 15:59:54	Fixed ( -6.0, -934.0)							# HOP 81 polar monitoring (South).				
08/01 16:03:00 - 08/01 17:40:54	Track ( 475.3, 201.0)	⑧ 08/01 16:00:00						# AR 11260.				
08/01 18:40:06 - 08/02 03:36:00	Track ( 488.8, 201.7)	⑧ 08/01 17:51:00						# AR 11260. HOP 173 from 2UT.				
08/02 04:23:00 - 08/02 06:53:00	Track ( 118.2, 144.0)	⑧ 08/02 04:20:00						# HOP 173 observing AR 11261.				
08/02 07:28:00 - 08/02 10:14:00	Track ( 573.8, 207.5)	⑧ 08/02 06:00:00						# AR 11260.				

**PROG= 08 1-time(s)**

<b>Subr= 1 30-time(s) 20.0sec</b>													
<b>Seqn= 87 1-time(s) 2.0sec</b>													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 60 1-time(s) 2.0sec</b>													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>													
<b>Seqn= 90 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>													
<b>Seqn= 87 1-time(s) 2.0sec</b>													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 88 1-time(s) 2.0sec</b>													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>													
<b>Seqn= 90 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>													
<b>Seqn= 87 1-time(s) 2.0sec</b>													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 88 1-time(s) 2.0sec</b>													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 4 24-time(s) 600.0sec</b>													
<b>Seqn= 89 1-time(s) 2.0sec</b>													

Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Active Region Search

\* \* \* \* \*

NOT USED

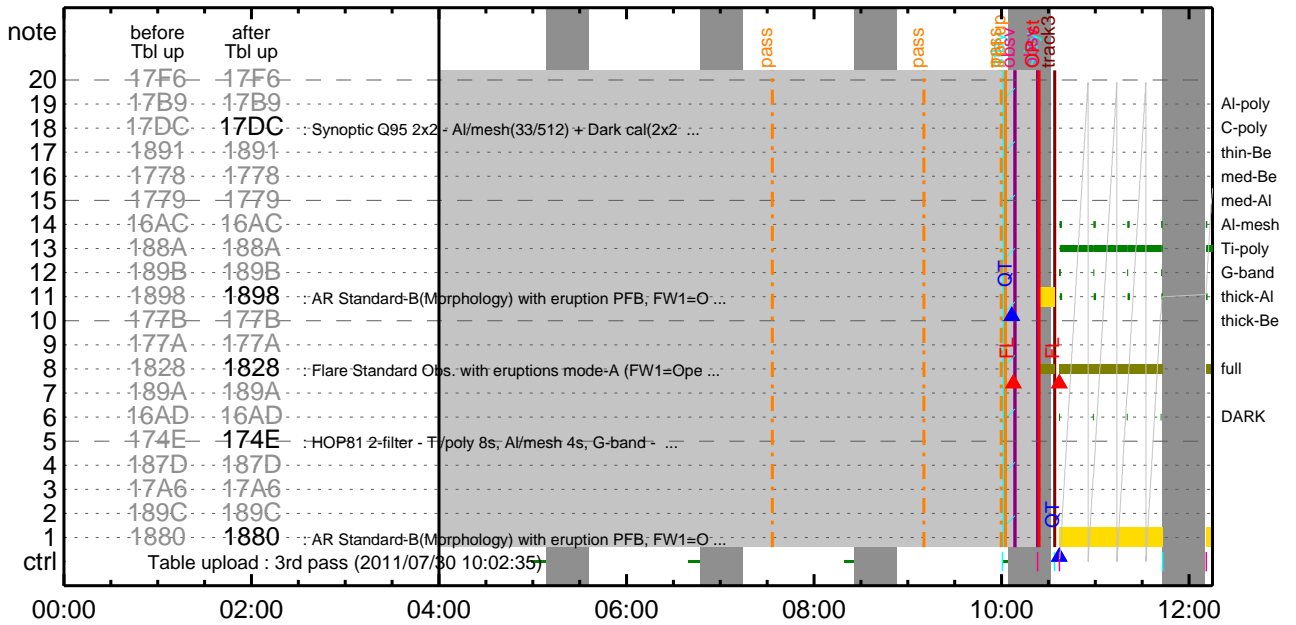
\* \* \* \* \*

### Flare Detection

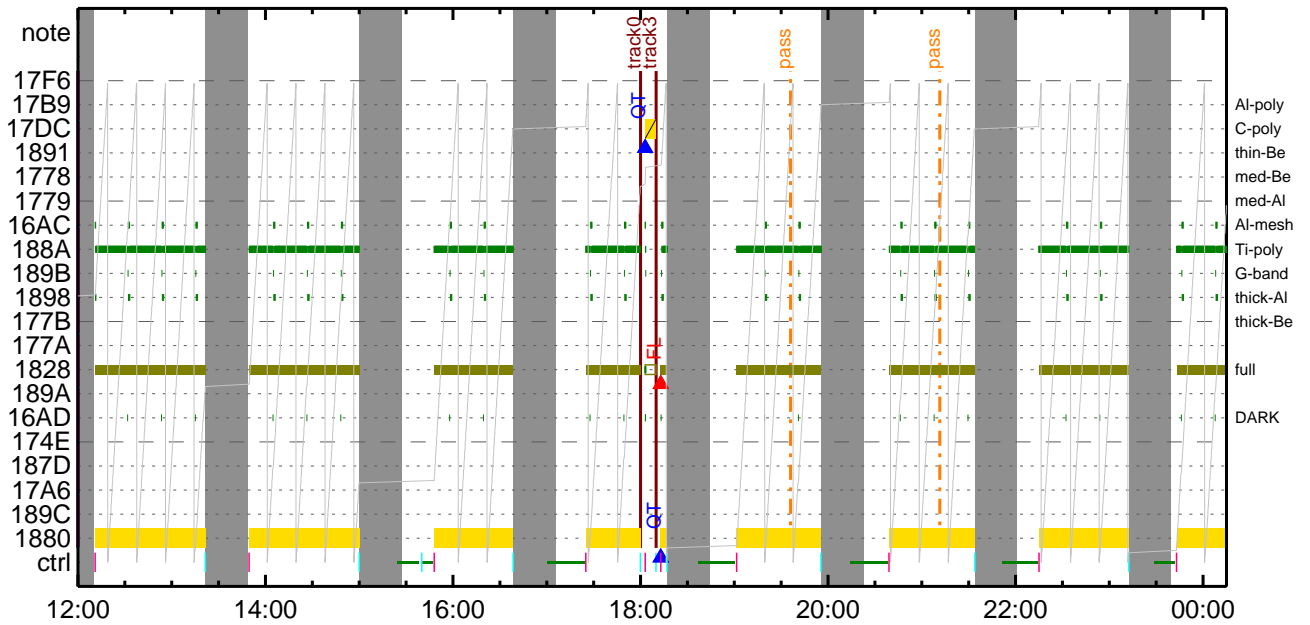
\* \* \* \* \*

FLD Patrol												
Term			Pointing (x, y)					Comment				
07/30 18:12:46 - 07/31 03:45:16	Track ( 103.3,	190.3)	<sup>Ⓢ</sup> 07/30 18:10:00	# AR 11260.								
07/31 03:57:46 - 07/31 18:00:46	Track ( 186.6,	191.2)	<sup>Ⓢ</sup> 07/31 03:55:00	# AR 11260.								
07/31 18:13:16 - 08/01 04:00:16	Track ( 305.4,	193.8)	<sup>Ⓢ</sup> 07/31 18:10:30	# AR 11260. HOP 173 from 2UT.								
08/01 04:12:46 - 08/01 17:45:16	Track ( -94.5,	144.9)	<sup>Ⓢ</sup> 08/01 04:10:00	# HOP 173 observing AR 11261.								
08/01 18:39:52 - 08/02 04:10:16	Track ( 488.8,	201.7)	<sup>Ⓢ</sup> 08/01 17:51:00	# AR 11260. HOP 173 from 2UT.								
08/02 04:22:46 - 08/04 10:13:00	Track ( 118.2,	144.0)	<sup>Ⓢ</sup> 08/02 04:20:00	# HOP 173 observing AR 11261.								
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

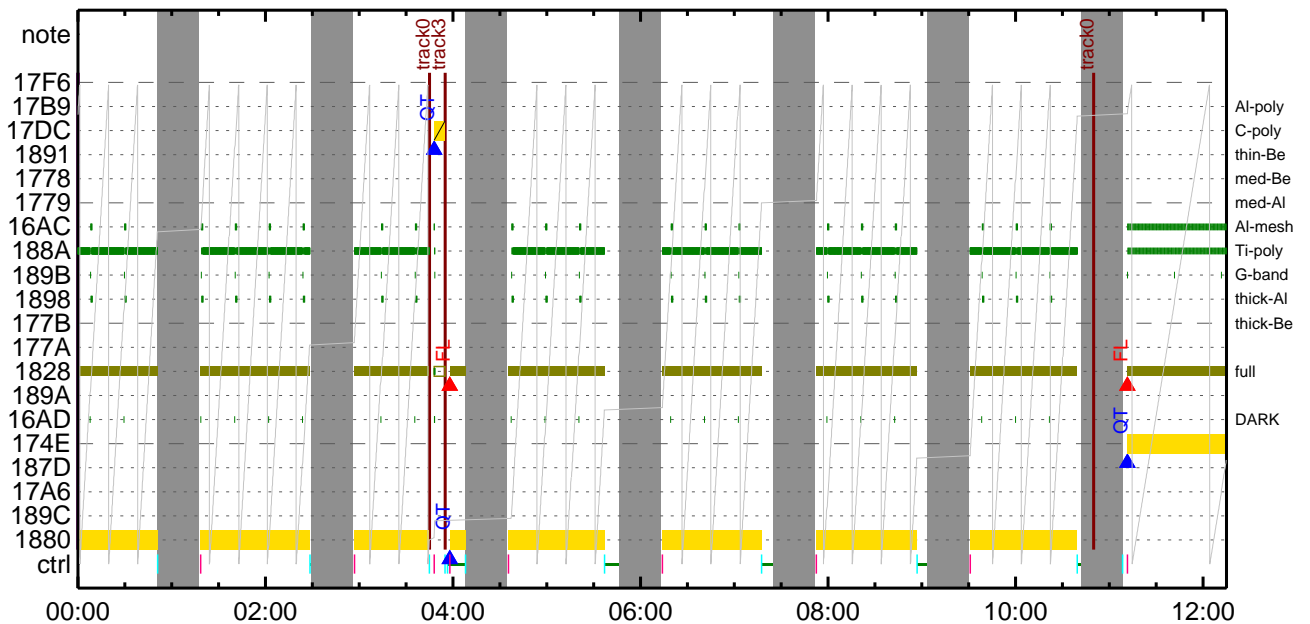
### CMDI #0065 2011/07/30



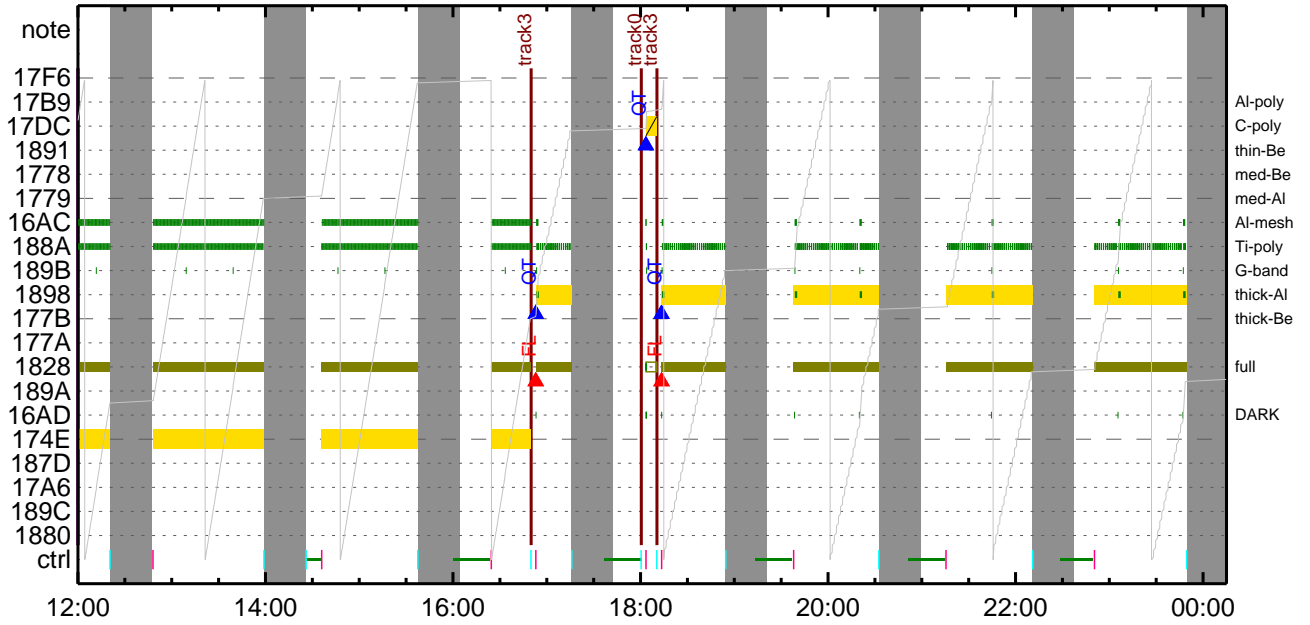
### CMDI #0065 2011/07/30



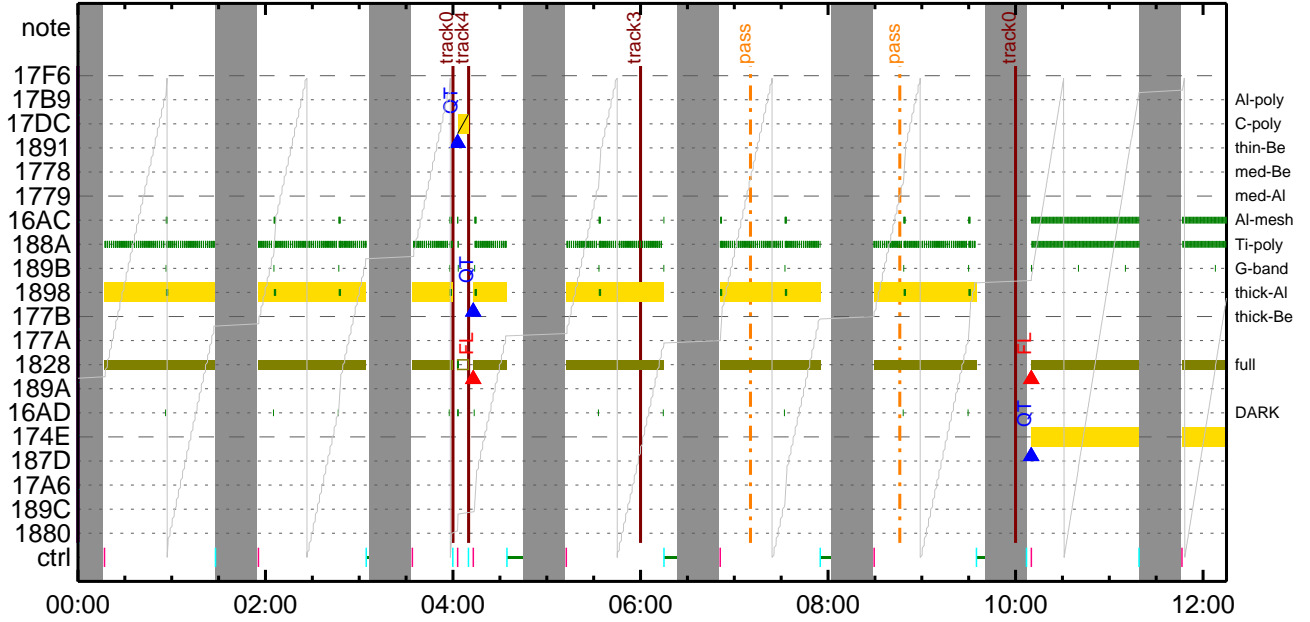
### CMDI #0065 2011/07/31



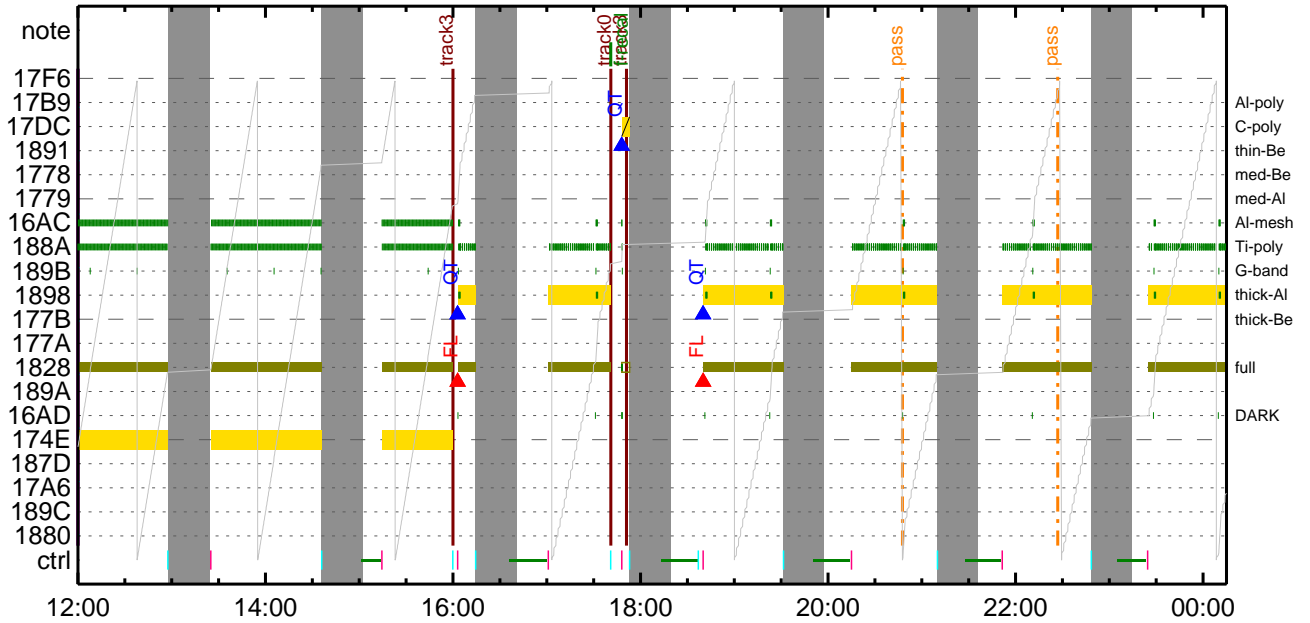
CMDI #0065 2011/07/31



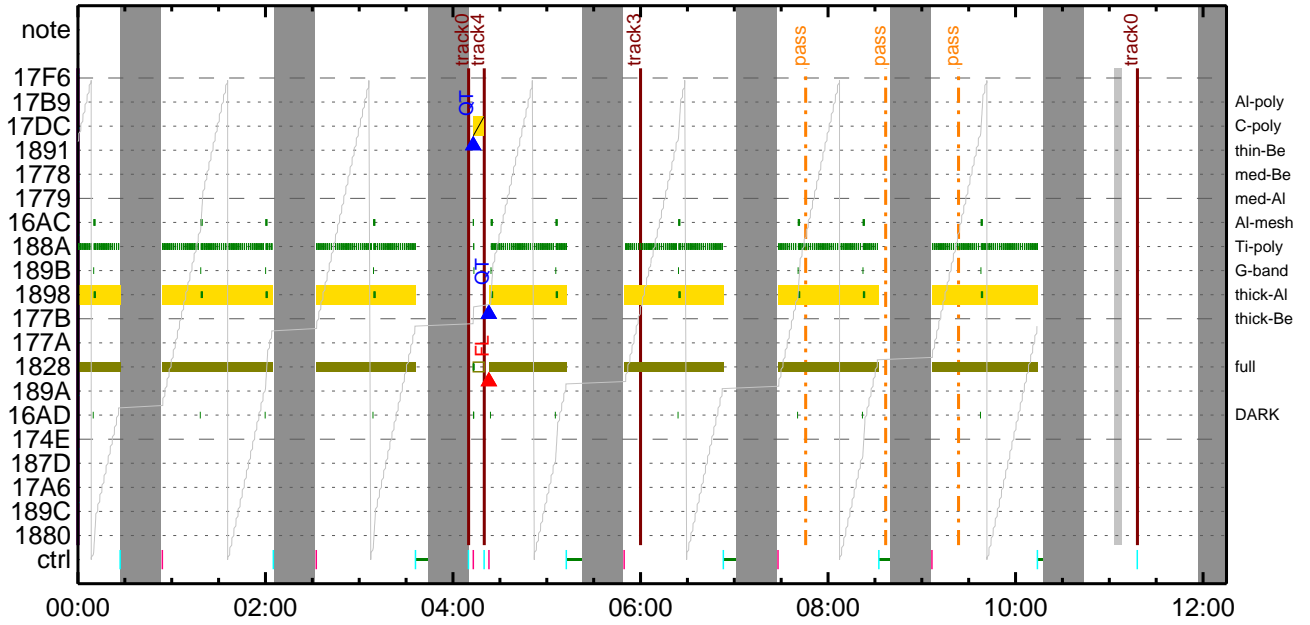
CMDI #0065 2011/08/01



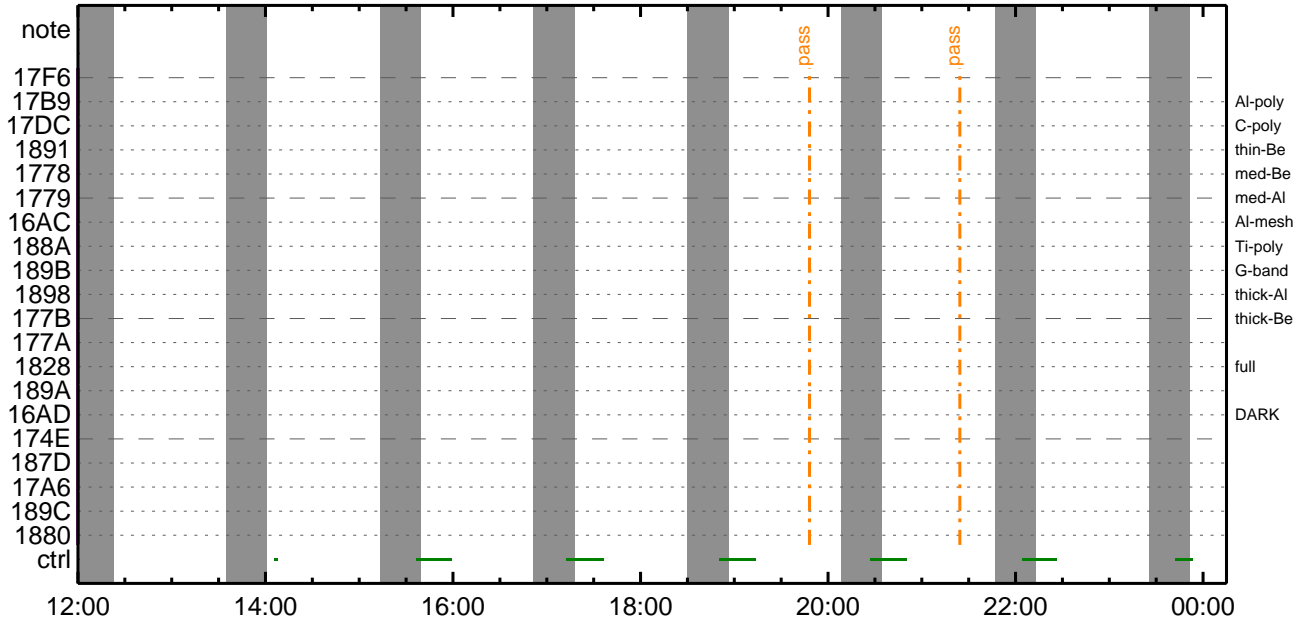
CMDI #0065 2011/08/01



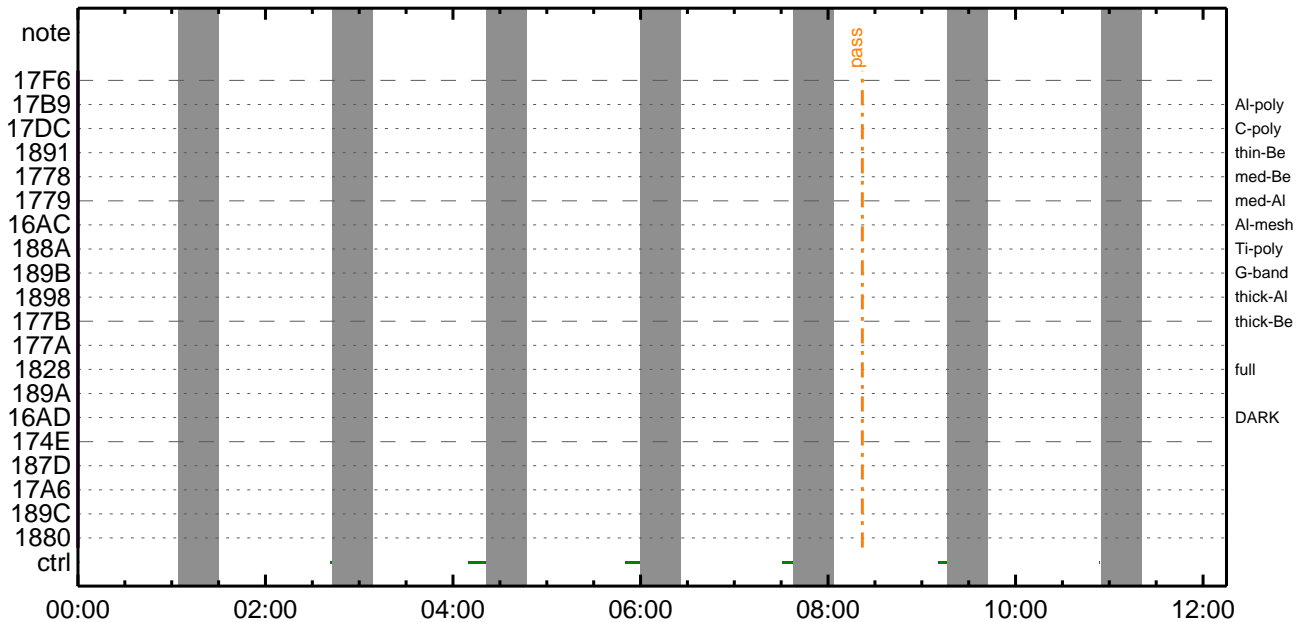
CMDI #0065 2011/08/02



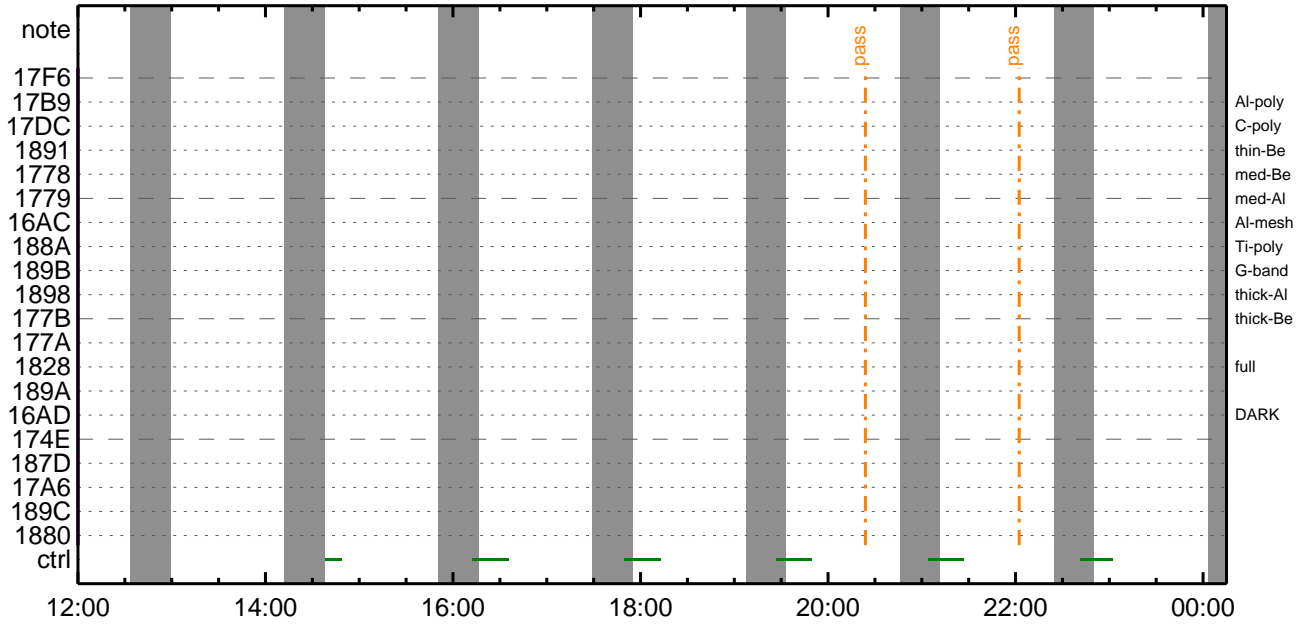
CMDI #0065 2011/08/02



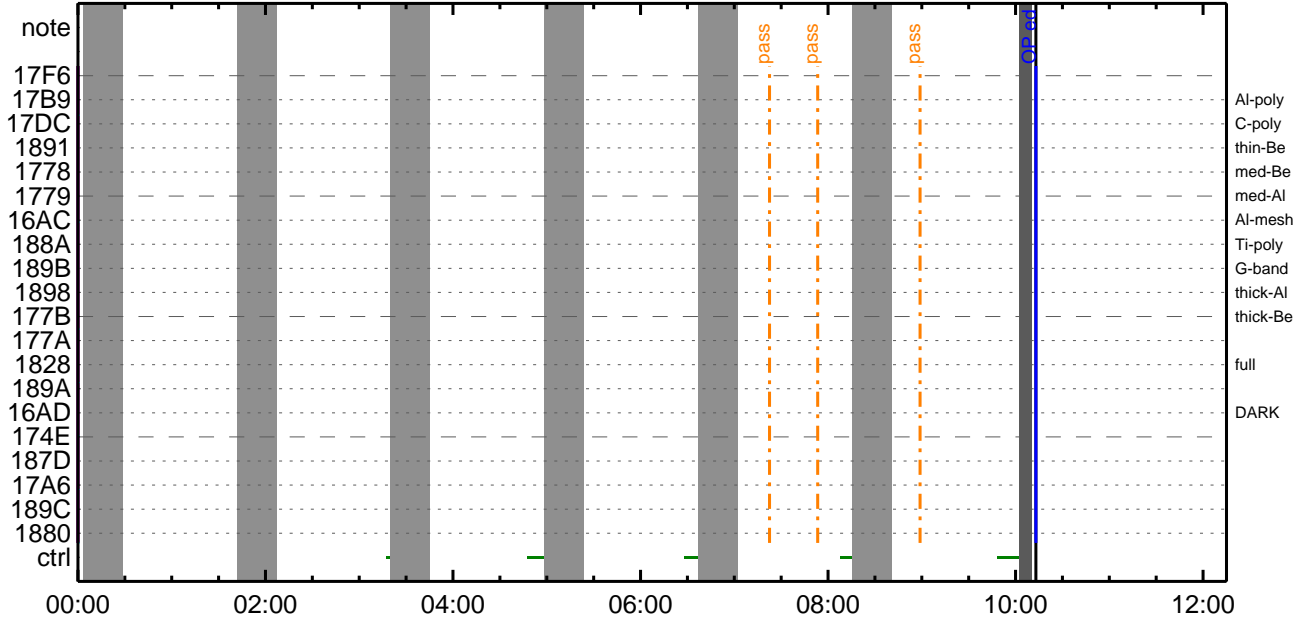
CMDI #0065 2011/08/03



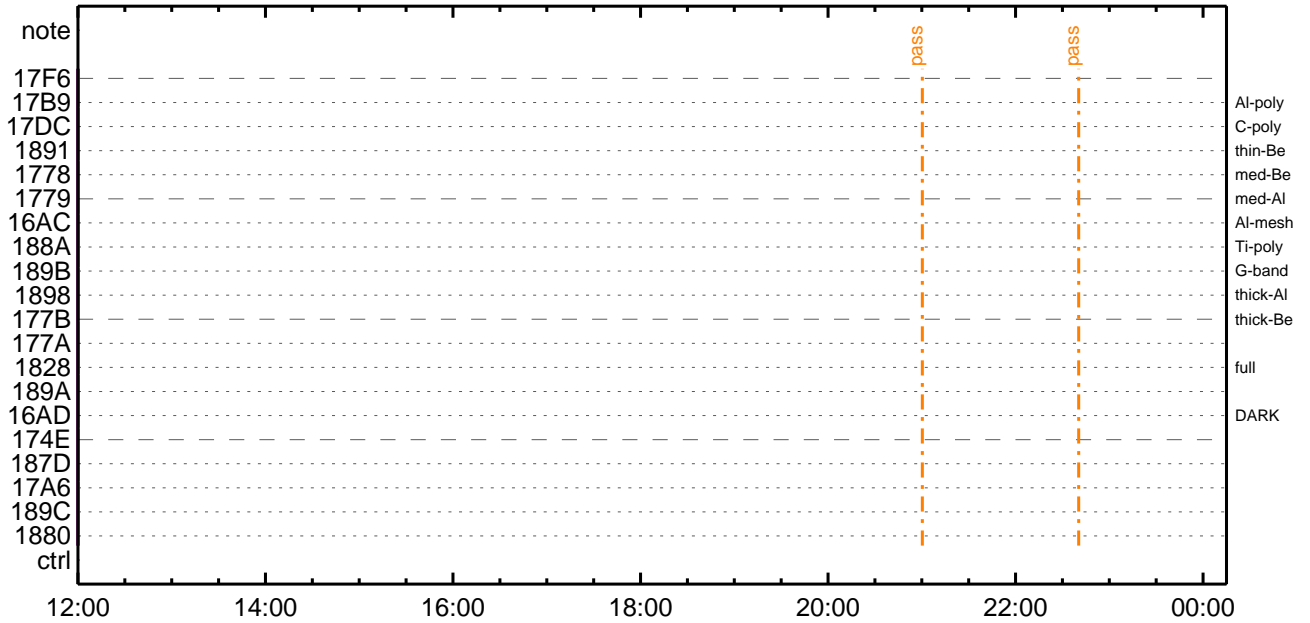
CMDI #0065 2011/08/03



CMDI #0065 2011/08/04



CMDI #0065 2011/08/04







0096 C.  
0097 C.  
0098 C. \*\*\*\*\*  
0099 C. OP/OGY1;4YE;|AY6Yx  
0100 C. \*\*\*\*\*  
0101 C.  
0102 . C. ;ãOP/OGY1;4YE;ã  
0103 . S. OP op-228:OP  
0104 ( )  
0105 . S. OG og-228:OG  
0106 ( )  
0107 C.  
0108 . C. ;ãNMOG&OPfî°èYAY6Yx;ã  
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)  
0110 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0111 BC (20 00 7f 01 02)  
0112 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 40  
0113 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0114 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0115 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0116 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0117 +. DC 01-22 DHU\_MODE\_CHNG  
0118 BC (07 0b f8)  
0119 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0120 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0121 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0122 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0123 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0124 . C. YAY6Yx½ªî»ò³îÇ§  
0125 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0126 . C. RAM ID=NMOGãî¼Ê¹ç•è²îOKò³îÇ§  
0127 C.  
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)  
0129 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0130 BC (20 80 7f 01 02)  
0131 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 41  
0132 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0133 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0134 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0135 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0136 +. DC 01-22 DHU\_MODE\_CHNG  
0137 BC (07 0b f8)  
0138 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0139 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0140 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0141 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0142 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0143 . C. YAY6Yx½ªî»ò³îÇ§  
0144 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0145 . C. RAM ID=NMOGãî¼Ê¹ç•è²îOKò³îÇ§  
0146 C.  
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)  
0148 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0149 BC (21 00 41 01 02)  
0150 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 42  
0151 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0152 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 65  
0153 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0154 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0155 +. DC 01-22 DHU\_MODE\_CHNG  
0156 BC (07 0b f8)  
0157 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0158 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0159 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0160 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0161 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0162 . C. YAY6Yx½ªî»ò³îÇ§  
0163 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0164 . C. RAM ID=NMOG, RAM ID=OPãî¼Ê¹ç•è²îOKò³îÇ§  
0165 C.  
0166 . C. \*\*\*\*\* òÊ²¼òî¼Ã´¶Á°òÊÊ¬ò°Á÷¿@ (¼âµ-YAY6Yx½ê¼çòðÁÔÃæòÇ¼ª°¬òè¼î¹çòçòâ) \*\*\*\*\*  
0167 C. DHUYâ;4YE;Ê¼Y½;Yî;4YE;Êòðîã¹  
0168 +. DC 01-22 DHU\_MODE\_CHNG  
0169 BC (02 0a f8)  
0170 C. çç[HK1\_PKT\_FORM\_NO] EQ 2  
0171 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.5S  
0172 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32K  
0173 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0174 C.  
0175 . C. \*\*\*\*\*  
0176 C. TI-CMD SET (OPOG STOP/COPY/START)  
0177 C. \*\*\*\*\*  
0178 C.  
0179 . C. NOTICE |§ OPOG UPLOAD¬-Á÷¿@NGUî¼î¹ç;ç°Ê²¼òî¼TI-CMDÁ÷¿@î¼Á¹Ôª•òÊòòò³òÊ;f  
0180 C. òÊò¿;çSETòEDUMPAîÆ±òîYÑY¹ç¹Ôª|ò³òÊ;f  
0181 C.  
0182 . C. TIY³YpY6YÊòðÁDî¿¿(UT)  
0183 +. TI 2011-07-30 10:19:00.0  
0184 DC 01-B3 DHU\_OP\_STOP  
0185 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0186 C.  
0187 +. TI 2011-07-30 10:19:01.0  
0188 DC 01-B4 DHU\_OP\_COPY  
0189 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0190 C.  
0191 +. TI 2011-07-30 10:19:01.0  
0192 DC 01-B5 DHU\_OPOG\_COPY  
0193 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP

```

0194 C.
0195 +. TI 2011-07-30 10:23:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ꠕꠕ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼ºïÄë%îíñºîŷĂŷ§ŷĂŷ-¹àîÛ
0200 C.          ꠕꠕ[HK1_TI_CMD_ENA/DIS]          EQ          ENA
0201 C.          ꠕꠕ[HK1_TI_CMD_NUM]          EQ          4
0202 C.          ꠕꠕ[HK1_NEXT_EXEC_PIM]          EQ          DHU
0203 C.          ꠕꠕ[HK1_NEXT_EXEC_DC]          EQ          0xB3
0204 C.
0205 . C. *****
0206 C. TIîŷ°èŷĂŷŌŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          ꠕꠕ[HK1_DMP_TOP_ADRS_1]          EQ          07
0213 C.          ꠕꠕ[HK1_DMP_TOP_ADRS_0]          EQ          2B
0214 C.          ꠕꠕ[HK1_DMP_BLOCK_NUM]          EQ          3
0215 C.          ꠕꠕ[HK1_DMP_REPEAT_NUM]          EQ          0
0216 C.          ꠕꠕ[HK1_DMA_DMP_PIM]          EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          ꠕꠕ[HK1_PKT_FORM_NO]          EQ          7
0220 C.          ꠕꠕ[HK1_PKT_GEN_TIME]          EQ          0.25 s
0221 C.          ꠕꠕ[HK1_S_TLM_BIT_RATE]          EQ          32k
0222 C.          ꠕꠕ[HK1_X_TLM_BIT_RATE]          EQ          4M
0223 C.          ꠕꠕ[HK1_DMP_CHK_FLG]          EQ          EXEC
0224 C.
0225 . C. ŷĂŷŌŷ×½ªî»º³îÇ§
0226 C.          ꠕꠕ[HK1_DMP_CHK_FLG]          EQ          NON
0227 C.
0228 . C. RAM ID=TI_TBLºîŷÊ¹çªë²îOKºº³îÇ§
0229 C.
0230 . C. DHUŷª;¼ŷÊ;Êŷ½ŷ. ŷî;¼ŷÊ;Êººîªº¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          ꠕꠕ[HK1_PKT_FORM_NO]          EQ          2
0234 C.          ꠕꠕ[HK1_PKT_GEN_TIME]          EQ          0.5S
0235 C.          ꠕꠕ[HK1_S_TLM_BIT_RATE]          EQ          32K
0236 C.          ꠕꠕ[HK1_X_TLM_BIT_RATE]          EQ          4M
0237 C.
0238 . C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2011-07-30 10:23:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC          (21 02)
0247 +. TI 2011-07-30 10:23:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC          (22)
0250 . C.          [ ] [HK1_TI_CMD_NUM]          EQ          2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2011-07-30 10:23:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC          (c3)
0260 . C.          [ ] [HK1_TI_CMD_NUM]          EQ          1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 . C. ***** MDP `ûÃîîºó¼ŷºÊªº¹ºèDCBC•×²è *****
0265 C. (¼ªºîŷŌŷĂŷÊŷŰŷÊŷŷªŷçŷèº¼ºª¼ª»Ûº¹ºè)
0266 . S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 . C. ***** ŷÐŷ¹.İ Daily±;îñºè`º¹ºèDCBC•×²è *****
0271 . S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 . C. ;ãLOSŷĂŷŷŷŷŷ-¼ª»Û;ã
0276 C.
0277 . C. ***** LOS *****
0278 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```

main-229 2011-07-30 13:24:26 166 33 SOLAR-B MAIN //
0001  C.
0002  . C. ***** AOS *****
0003  C.
0004  . C. ;ãAOSYÁYŞYÁY-¼Å»Û;ä
0005  C.
0006  C. YÁYŞ;¼Y³YŞYÓYÉÁ+¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008  C.
0009  . C. ***** AOCs : Reload orbital element (send every contact) *****
0010  C. Áí;É½¿ðÅ•µ°Ê»Í×ÁÇóÍYÇYÁY×YÍ;¼YÉ;ÉÈÈ½µ•íÉ;È•É½°ÇÓ•½¿¾í¹¿ç¿Í;ÇÀ®, ù½¹ðÈ¾¿ÇÁ+¿®•½É½½½½½¿;É;É
0011  +. DC 02-8E AOCU_ORB_UPD
0012  C.
0013  C.
0014  . C. *****
0015  C. XÁ+¿µ;ON
0016  C. *****
0017  C. Ç° °ÅÀ, Í×ÈY½¿åLOS¾¿ç¿Í»P´Ö½¿¹ÍÍ, ½.; ÇÉÔÍ×¿ÉXÁÓON¿Í¹Ô½É½¿½½½½½¿;É;É
0018  C.
0019  +. DC 03-B4 TCIA_XPA_ON/HI
0020  M. WAIT_SEC 1
0021  + DC 03-84 TCIA_XMOD_ON
0022  M. WAIT_SEC 1
0023  + DC 03-95 TCIA_XMOD_QPSK
0024  C.                ÇÇ[HK1_XPA_ON/OFF]                EQ          ON
0025  C.                ÇÇ[HK1_XPA_PWR_HI/LO]            EQ          HI
0026  C.                ÇÇ[HK1_XMOD_ON/OFF]               EQ          ON
0027  C.                ÇÇ[HK1_XMOD_QPSK/PM]              EQ          QPSK
0028  C.
0029  . C. XYDÝÓYÉYÍYÁY-¾ÔÅÖ½¿°ÅÀÈ•½¿½¿; Ç°È²½¿¹Ô½É½¿; ¼É½¿ç½¿½¿¹Ô½É½¿;É;É
0030  C.
0031  . C. *****
0032  C. DR PT1 ÁÍ¼í°ÅÀ,
0033  C. *****
0034  C. Ç° RESTART;ÉPT1;È½•½¿ç¿¾í¹¿ç¿Í; Ç°È²½¿¹Ô½É½¿°; ÇDCBC-150¿½¿É½¿;É;É
0035  C.
0036  . C. ;ãPT1°ÅÀ, ¾«»Í;ä
0037  +. DC 01-29 DHU_S/X_VC4_OFF
0038  + DC 06-C8 DR_PT1_REP_SEL
0039  BC          (01 00)
0040  + DC 06-B3 DR_REP_START
0041  + DC 01-32 DHU_X_VC4_ON
0042  C.                ÇÇ[HK1_REP_PT_1/2]                EQ          PT1      (¼Å¹Ô, ;¾Ú)
0043  C.                ÇÇ[HK1_REP_STA/STP]              EQ          START   (¼Å¹Ô, ;¾Ú)
0044  C.                ÇÇ[HK1_X_VC4_ON/OFF]              EQ          ON       (¼Å¹Ô, ;¾Ú)
0045  C.
0046  . C. ;ãYÇYÓYÉYÉÁÚÁÖ;ÉÁ•Å°²óÈð;È, á½¹°ÅÀ, °È³«;ä
0047  +. DC 06-B3 DR_REP_START
0048  + DC 01-32 DHU_X_VC4_ON
0049  C.                ÇÇ[HK1_REP_PT_1/2]                EQ          PT1      (¼Å¹Ô, ;¾Ú)
0050  C.                ÇÇ[HK1_REP_STA/STP]              EQ          START   (¼Å¹Ô, ;¾Ú)
0051  C.                ÇÇ[HK1_X_VC4_ON/OFF]              EQ          ON       (¼Å¹Ô, ;¾Ú)
0052  C.
0053  C.
0054  . C. PT1°ÅÀ, ¾~¼«È°ÅÀ»½¿½¿;á; Ç°È²½¿¹Ô½É½¿;É;É
0055  C. YÇYÓYÉYÉÁÚÁÖ;ÉÁ•Å°²óÈð~¾¿á¿¾í¹¿ç¿Í´°Í»½¿¹¿É½¾¿ÇÁÓ½¿;É;É
0056  C.
0057  . C. *****
0058  C. DR PT2 ÁÍ¼í°ÅÀ,
0059  C. *****
0060  C. Ç° RESTART;ÉPT2;È½•½¿ç¿¾í¹¿ç¿Í; Ç°È²½¿¹Ô½É½¿°; ÇDCBC-151¿½¿É½¿;É;É
0061  C.
0062  . C. ;ãPT2°ÅÀ, ¾«»Í;ä
0063  +. DC 01-29 DHU_S/X_VC4_OFF
0064  + DC 06-C8 DR_PT2_REP_SEL
0065  BC          (02 00)
0066  + DC 06-B3 DR_REP_START
0067  + DC 01-32 DHU_X_VC4_ON
0068  C.                ÇÇ[HK1_REP_PT_1/2]                EQ          PT2      (¼Å¹Ô, ;¾Ú)
0069  C.                ÇÇ[HK1_REP_STA/STP]              EQ          START   (¼Å¹Ô, ;¾Ú)
0070  C.                ÇÇ[HK1_X_VC4_ON/OFF]              EQ          ON       (¼Å¹Ô, ;¾Ú)
0071  C.
0072  . C. ;ãYÇYÓYÉYÉÁÚÁÖ;ÉÁ•Å°²óÈð;È, á½¹°ÅÀ, °È³«;ä
0073  +. DC 06-B3 DR_REP_START
0074  + DC 01-32 DHU_X_VC4_ON
0075  C.                ÇÇ[HK1_REP_PT_1/2]                EQ          PT2      (¼Å¹Ô, ;¾Ú)
0076  C.                ÇÇ[HK1_REP_STA/STP]              EQ          START   (¼Å¹Ô, ;¾Ú)
0077  C.                ÇÇ[HK1_X_VC4_ON/OFF]              EQ          ON       (¼Å¹Ô, ;¾Ú)
0078  C.
0079  . C. *****
0080  C. DR°ÅÀ, ÁÀ»½¿;ÇXÁ+¿µ;OFF
0081  C. *****
0082  C.
0083  . C. ;ãDR°ÅÀ, ÁÀ»½¿;ä
0084  +. DC 06-B4 DR_REP_STOP
0085  + DC 01-29 DHU_S/X_VC4_OFF
0086  C.                ÇÇ[HK1_REP_STA/STP]              EQ          STOP
0087  C.                ÇÇ[HK1_S_VC4_ON/OFF]              EQ          OFF
0088  C.                ÇÇ[HK1_X_VC4_ON/OFF]              EQ          OFF
0089  C.
0090  C. ;ãXÁ+¿µ;OFF;ä
0091  +. DC 03-85 TCIA_XMOD_OFF
0092  M. WAIT_SEC 1
0093  + DC 03-B5 TCIA_XPA_OFF
0094  C.                ÇÇ[HK1_XMOD_ON/OFF]                EQ          OFF
0095  C.                ÇÇ[HK1_XPA_ON/OFF]                EQ          OFF

```

```
0096 . C.
0097 . C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 . C. Upload the Orbit Element and the Target Attitude
0100 . C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 . ( )
0103 . C.
0104 . C.
0105 . C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 . C.
0109 . C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 . C.
0111 . C.
0112 . C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 . C.
0116 . C. Wait for AOCS DUMP to end
0117 . C.
0118 . C. Check the dump memory
0119 . C.
0120 . C. Result = OK [ ]
0121 . C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 . C.
0125 . C. <A_***>[TLM STS] FMT = 2 [ ]
0126 . C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 28s
0134 . C. *****
0135 . C. EIS START OBSTBL LOAD
0136 . C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 . ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 . C.
0142 . C. Execute, after the success of OBSTBL upload.
0143 . C. Set EIS TI-commands
0144 +. TI 2011-07-30 10:23:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 . C. *****
0149 . C. EIS END OBSTBL LOAD
0150 . C. *****
0151 . C.
0152 . C. ***** MDP `ûÃîî»ö¼ÝðĒÂÐ¹æDCBC•x²è *****
0153 . C. (¼â°îÝÖÝÁÝĒÝÞÝĒÝ¥¼ÝĒæ¹¼¼â»Û¹æ)
0154 . S. DC-BC dcbc-402:DCBC
0155 . (MDP_known_event)
0156 . C.
0157 . C.
0158 . C. ***** ¥Đ¥¹•İ Daily±ġİŃæ'Ø¹æDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 . (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 . C.
0162 . C.
0163 . C. ;ãLOS¥Á¥Ş¥Ā¥-¼Ā»Û;ã
0164 . C.
0165 . C. ***** LOS *****
0166 . C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-230 2011-07-30 13:24:26 91 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁò•µ°È»Í×ÁÇòÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÍÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòÍ;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 08 08)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 0f 80 80 06 06)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 10 80 80 08 08)
0050 + DC 07-F0 MDP_XRT_FLD_ENA
0051 BC (d8)
0052 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0053 BC (c8)
0054 + DC 07-F0 MDP_XRT_ARS_DIS
0055 BC (d5)
0056 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0057 BC (c4 0b)
0058 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0059 BC (c5 08)
0060 . C. ----- Success Verify ? OK / NG ____
0061 C.
0062 C.
0063 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0064 C.
0065 +. DC 07-F0 MDP_XRT_MODE_OBSV
0066 BC (c2)
0067 +. TI 2011-07-30 10:23:02.0
0068 DC 07-F0 MDP_XRT_MODE_OBSV
0069 BC (c2)
0070 +. TI 2011-07-30 10:23:04.0
0071 DC 07-F0 MDP_XRT_CTRL_AUTO
0072 BC (c0)
0073 . C. ----- Success Verify ? OK / NG ____
0074 C.
0075 C. ***** XRT END *****
0076 C.
0077 . C. ***** MDP `ûÁÍòÍ»ò¼YòÈÁò¹òèDCBC•x²è *****
0078 C. (¼á°íYÓYÁYÈYbYÈYáYçYèòÈ¼¼ò¼Á»Ûò¹òè)
0079 . S. DC-BC dcbc-402:DCBC
0080 (MDP_known_event)
0081 C.
0082 C.
0083 . C. ***** YD¥¹•İ Daily±¿ÍÑòÈ`Øò¹òèDCBC•x²è *****
0084 . S. DC-BC dcbc-153:DCBC
0085 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086 C.
0087 C.
0088 . C. ;ãLOS¥ÁY$YÁY-¼Á»Û;ã
0089 C.
0090 . C. ***** LOS *****
0091 C.
```

Jul 30, 11 13:24

XRT\_OGLIST\_0065.chk

Page 1/10

\*\*\* OP Sequence for XRT \*\*\*

2011/07/30	10:33:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/30	10:34:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/07/30	10:36:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/30	10:36:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/30	10:36:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/30	10:36:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/30	10:36:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/30	10:36:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/30	10:36:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2011/07/30	10:36:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/30	10:37:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/30	11:43:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/30	11:43:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/30	11:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/30	11:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/30	12:10:00.0	XRT_Custom_418_OG [0x1a2]			
2011/07/30	12:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/30	13:21:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/30	13:21:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/30	13:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/30	13:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/30	13:48:30.0	XRT_Custom_418_OG [0x1a2]			
2011/07/30	13:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/30	15:00:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/30	15:00:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/30	15:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/30	15:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/30	15:40:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/30	15:40:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/30	15:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/30	15:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/30	15:47:00.0	XRT_Custom_418_OG [0x1a2]			
2011/07/30	15:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/30	16:38:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/30	16:38:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/30	16:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/30	16:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/30	17:24:00.0	XRT_Custom_418_OG [0x1a2]			
2011/07/30	17:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/30	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/30	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/07/30	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/07/30	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/07/30	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/07/30	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/30	18:02:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2011/07/30	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/30	18:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			

Saturday July 30, 2011

1/10

Jul 30, 11 13:24

## XRT\_OGLIST\_0065.chk

Page 2/10

2011/07/30	18:10:00.0	AOCS_Or-e-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			AOCU_NM	5	02-76	03 00 00 00 00
2011/07/30	18:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/30	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/30	18:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/30	18:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/30	18:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/30	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/30	18:12:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2011/07/30	18:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/30	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/30	18:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/30	18:17:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/30	18:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/30	18:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/30	19:00:30.0	XRT_Custom_418_OG [0x1a2]				
2011/07/30	19:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/30	19:55:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/30	19:55:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/30	19:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/30	19:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/30	20:38:00.0	XRT_Custom_418_OG [0x1a2]				
2011/07/30	20:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/30	21:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/30	21:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/30	21:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/30	21:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/30	22:14:00.0	XRT_Custom_418_OG [0x1a2]				
2011/07/30	22:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/30	23:12:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/30	23:12:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/30	23:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/30	23:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/30	23:42:00.0	XRT_Custom_418_OG [0x1a2]				
2011/07/30	23:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	00:51:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	00:51:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/31	00:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/31	00:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/31	01:17:30.0	XRT_Custom_418_OG [0x1a2]				
2011/07/31	01:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	02:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	02:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/31	02:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/31	02:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/31	02:56:00.0	XRT_Custom_418_OG [0x1a2]				
2011/07/31	02:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	03:44:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	03:44:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/07/31	03:45:00.0	AOCS_Or-e-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00

Jul 30, 11 13:24

## XRT\_OGLIST\_0065.chk

Page 3/10

2011/07/31	03:45:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/07/31	03:45:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/07/31	03:45:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/31	03:47:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2011/07/31	03:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	03:54:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	03:55:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/07/31	03:56:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	03:56:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/31	03:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/31	03:57:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/31	03:57:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/31	03:57:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/31	03:57:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/31	03:57:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/31	03:57:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/31	03:57:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2011/07/31	03:57:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/31	03:58:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	03:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/31	04:08:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	04:08:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/31	04:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/31	04:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/31	04:34:30.0	XRT_Custom_418_OG [0x1a2]			
2011/07/31	04:35:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	05:37:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	05:37:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/31	05:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/31	05:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/31	06:13:00.5	XRT_Custom_418_OG [0x1a2]			
2011/07/31	06:14:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	07:17:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	07:17:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/31	07:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/31	07:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/31	07:51:30.0	XRT_Custom_418_OG [0x1a2]			
2011/07/31	07:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	08:57:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	08:57:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/31	08:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/31	09:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/31	09:30:00.0	XRT_Custom_418_OG [0x1a2]			
2011/07/31	09:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	10:39:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	10:39:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/31	10:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/31	10:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/31	10:50:00.0	AOCS_OrE-point_Start_3_OG [0x099]			



Jul 30, 11 13:24

## XRT\_OGLIST\_0065.chk

Page 4/10

2011/07/31	11:08:30.0	XRT_CTRL_MANU_439_OG [0x1b7]	AOCU_NM	5	02-76	00	b3	03	00	8b
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/07/31	11:11:02.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/07/31	11:11:22.0	XRT_FLD_ENA_447_OG [0x1bf]	MDP_XRT_FLD_ENA	1	07-F0		d8			
2011/07/31	11:11:24.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2011/07/31	11:11:26.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0		d0			
2011/07/31	11:11:28.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0		d5			
2011/07/31	11:11:30.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da			
2011/07/31	11:11:32.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	05		
2011/07/31	11:11:34.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0		c5	08		
2011/07/31	11:11:37.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/07/31	12:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/07/31	12:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da			
2011/07/31	12:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/07/31	12:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/07/31	12:47:00.0	XRT_Custom_418_OG [0x1a2]								
2011/07/31	12:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/07/31	13:59:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/07/31	13:59:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da			
2011/07/31	13:59:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/07/31	14:02:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/07/31	14:26:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/07/31	14:26:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da			
2011/07/31	14:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/07/31	14:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/07/31	14:35:00.0	XRT_Custom_418_OG [0x1a2]								
2011/07/31	14:36:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/07/31	15:37:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/07/31	15:37:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da			
2011/07/31	15:37:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/07/31	15:40:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/07/31	16:23:30.0	XRT_Custom_418_OG [0x1a2]								
2011/07/31	16:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/07/31	16:49:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/07/31	16:50:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03	00	00	00	00
2011/07/31	16:52:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/07/31	16:52:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0		d8			
2011/07/31	16:52:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2011/07/31	16:52:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0		d0			
2011/07/31	16:52:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0		d5			
2011/07/31	16:52:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da			
2011/07/31	16:52:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	0b		
2011/07/31	16:52:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0		c5	08		
2011/07/31	16:53:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/07/31	17:16:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/07/31	17:16:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da			
2011/07/31	17:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/07/31	17:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/07/31	18:00:24.0	XRT_CTRL_MANU_400_OG [0x190]								

Jul 30, 11 13:24

## XRT\_OGLIST\_0065.chk

Page 5/10

2011/07/31	18:00:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	18:00:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/07/31	18:00:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00
2011/07/31	18:00:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/07/31	18:00:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/07/31	18:03:28.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/31	18:03:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2011/07/31	18:10:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	18:10:30.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	18:12:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	03 00 00 00
2011/07/31	18:13:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/07/31	18:13:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/07/31	18:13:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/07/31	18:13:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/07/31	18:13:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/07/31	18:13:26.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/31	18:13:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2011/07/31	18:13:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/07/31	18:54:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	18:54:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	18:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/31	18:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/31	19:37:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/31	19:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/07/31	20:32:30.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/07/31	20:32:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	20:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	20:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/31	21:14:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/31	21:15:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/31	22:11:00.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/07/31	22:11:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	22:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/07/31	22:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/07/31	22:49:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/07/31	22:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/07/31	23:49:30.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_Custom_418_OG [0x1a2]			
2011/07/31	23:49:32.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/07/31	23:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/07/31	23:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/01	00:16:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/01	00:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/01	01:28:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/01	01:28:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/01	01:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/08/01	01:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/01	01:54:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/01	01:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/01	03:04:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
			XRT_Custom_418_OG [0x1a2]			
			XRT_CTRL_AUTO_419_OG [0x1a3]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
			XRT_Custom_418_OG [0x1a2]			
			XRT_CTRL_AUTO_419_OG [0x1a3]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0

2011/08/01	03:04:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/01	03:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/01	03:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/01	03:33:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/01	03:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/01	03:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/01	03:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2011/08/01	04:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2011/08/01	04:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/08/01	04:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/08/01	04:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/01	04:02:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2011/08/01	04:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/01	04:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/01	04:10:00.0	AOCS_Orе-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04 00 00 00 00			
2011/08/01	04:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/08/01	04:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/08/01	04:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/08/01	04:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/08/01	04:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/01	04:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/01	04:12:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2011/08/01	04:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08			
2011/08/01	04:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/01	04:34:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/01	04:34:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/01	04:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/01	04:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/01	05:11:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/01	05:12:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/01	06:00:00.5	AOCS_Orе-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00			
2011/08/01	06:15:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/01	06:15:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/01	06:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/01	06:18:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/01	06:50:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/01	06:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/01	07:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/01	07:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/01	07:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/01	07:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/01	08:28:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/01	08:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/01	09:35:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/01	09:35:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/01	09:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/01	09:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/01	10:00:00.0	AOCS_Orе-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 53 02 00 8b			

Jul 30, 11 13:24

## XRT\_OGLIST\_0065.chk

Page 7/10

2011/08/01	10:07:00.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/01	10:09:32.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/08/01	10:09:52.0	XRT_FLD_ENA_447_OG [0x1bf]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/08/01	10:09:54.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/08/01	10:09:56.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/08/01	10:09:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/08/01	10:10:00.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/01	10:10:02.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2011/08/01	10:10:04.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/08/01	10:10:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/01	11:19:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/01	11:19:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/01	11:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/01	11:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/01	11:45:30.0	XRT_Custom_418_OG [0x1a2]					
2011/08/01	11:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/01	12:57:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/01	12:57:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/01	12:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/01	13:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/01	13:24:00.0	XRT_Custom_418_OG [0x1a2]					
2011/08/01	13:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/01	14:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/01	14:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/01	14:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/01	14:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/01	15:13:30.0	XRT_Custom_418_OG [0x1a2]					
2011/08/01	15:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/01	15:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/01	16:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00	
2011/08/01	16:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/08/01	16:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/08/01	16:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/08/01	16:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/08/01	16:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/08/01	16:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/01	16:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2011/08/01	16:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/08/01	16:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/01	16:14:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/01	16:14:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/01	16:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/01	16:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/01	17:00:00.0	XRT_Custom_418_OG [0x1a2]					
2011/08/01	17:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/01	17:40:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/01	17:40:56.0	XRT_FOCUS_RECALIBRATE_430_OG [0x1ae]	XRT_FOCUS_RECAL	2	07-F8	78 00	
2011/08/01	17:41:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2011/08/01	17:44:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					

Jul 30, 11 13:24

## XRT\_OGLIST\_0065.chk

Page 8/10

2011/08/01	17:45:16.0	XRT_FLD_DIS_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		MDP_XRT_FLD_DIS		1	07-F0		d9		
2011/08/01	17:45:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9		
2011/08/01	17:45:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2011/08/01	17:47:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	12	
2011/08/01	17:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2011/08/01	17:51:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03	00	00	00
2011/08/01	17:53:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2011/08/01	17:53:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da		
2011/08/01	17:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2011/08/01	17:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2011/08/01	18:37:00.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2011/08/01	18:39:32.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/08/01	18:39:52.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0		d8		
2011/08/01	18:39:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2011/08/01	18:39:56.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0		d0		
2011/08/01	18:39:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2011/08/01	18:40:00.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da		
2011/08/01	18:40:02.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	0b	
2011/08/01	18:40:04.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0		c5	08	
2011/08/01	18:40:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2011/08/01	19:31:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2011/08/01	19:31:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da		
2011/08/01	19:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2011/08/01	19:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2011/08/01	20:14:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/01	20:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2011/08/01	21:10:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2011/08/01	21:10:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da		
2011/08/01	21:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2011/08/01	21:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2011/08/01	21:50:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/01	21:51:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2011/08/01	22:48:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2011/08/01	22:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da		
2011/08/01	22:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2011/08/01	22:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2011/08/01	23:23:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/01	23:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2011/08/02	00:27:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2011/08/02	00:27:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da		
2011/08/02	00:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2011/08/02	00:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2011/08/02	00:53:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/02	00:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2011/08/02	02:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2011/08/02	02:05:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da		
2011/08/02	02:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2011/08/02	02:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		

Jul 30, 11 13:24

## XRT\_OGLIST\_0065.chk

Page 9/10

2011/08/02	02:31:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/02	02:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/02	03:36:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/02	03:36:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/02	03:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/02	03:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/02	04:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/02	04:09:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/08/02	04:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/08/02	04:10:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/08/02	04:10:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/08/02	04:10:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/02	04:12:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/08/02	04:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/02	04:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/02	04:20:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/08/02	04:22:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/08/02	04:22:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/02	04:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/02	04:22:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/08/02	04:22:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/02	04:22:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/02	04:22:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2011/08/02	04:22:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/08/02	04:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/02	05:12:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/02	05:12:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/02	05:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/02	05:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/02	05:48:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/02	05:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/02	06:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/08/02	06:53:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/02	06:53:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/02	06:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/02	06:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/02	07:27:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/02	07:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/02	08:32:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/02	08:32:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/02	08:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/02	08:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/02	09:05:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/02	09:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/02	10:14:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/02	10:14:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/02	10:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/02	10:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/02	11:17:54.0	XRT_CTRL_MANU_400_OG [0x190]							

2011/08/02	11:18:00.5	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00