

XRT Timeline to be uploaded on 2011/08/04

Period: 2011/08/04 10:13:00 - 2011/08/09 09:28:00

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Normal mode

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XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT												
Term	Pointing (x, y)							Comment				
08/04 10:38:00 - 08/04 12:22:54	Fixed (0.0, 945.0)	# OP start + 10min, co-alignment, N.										
PROG= 14 1-time(s)												
└─ Subr= 1 1-time(s) 360.0sec												
└─ Seqn= 21 24-time(s) 300.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs 2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT												
Term	Pointing (x, y)							Comment				
08/04 12:38:00 - 08/04 14:22:54	Fixed (-945.0, 0.0)	* Co-alignment, E.										
PROG= 06 1-time(s)												
└─ Subr= 1 1-time(s) 360.0sec												
└─ Seqn= 22 15-time(s) 480.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs 2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #189C: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 15sec-cad												
Term	Pointing (x, y)							Comment				
08/04 14:26:00 - 08/04 17:54:54	Track (189.5, 159.5) ^{Ⓢ 08/04 14:23:00}	* AR 11263.										
08/04 18:52:30 - 08/05 01:59:54	Track (221.1, 160.1) ^{Ⓢ 08/04 18:05:00}	# Cont.										
PROG= 02 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
└─ Seqn= 41 4-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 13 55-time(s) 15.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs 2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs 2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs 1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #17DC: Synoptic Q95 2x2 - Al/mesh(33/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1024) + G-band(12)												
Term	Pointing (x, y)							Comment				
08/04 18:02:00 - 08/04 18:06:00	Fixed (0.0, 0.0)	synoptic, shifted -5.0 min										
08/05 04:26:00 - 08/05 04:32:54	Fixed (0.0, 0.0)	synoptic, shifted manually.										
08/05 18:08:00 - 08/05 18:14:54	Fixed (0.0, 0.0)	synoptic, shifted 5.0 min										
08/06 04:03:00 - 08/06 04:09:54	Fixed (0.0, 0.0)	synoptic, shifted manually.										
PROG= 18 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 20 1-time(s) 4.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Seqn= 31 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Seqn= 77 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Seqn= 63 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1880: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 22sec-cad												
Term	Pointing (x, y)							Comment				
08/05 02:03:00 - 08/05 03:45:00	Track (287.7, 161.8) ^{Ⓢ 08/05 02:00:00}	* HOP 173, EPO with high schools. AR 11263.										
08/05 16:15:30 - 08/05 18:04:54	Fixed (-862.0, 300.0)	* ARs at E limb.										
08/05 18:18:00 - 08/05 22:00:30	Fixed (-862.0, 300.0)	# Cont.										
PROG= 01 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	

Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 41 4-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1 1-time(s) 2.0sec												
Seqn= 13 55-time(s) 22.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1898: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad												
Term	Pointing (x, y)							Comment				
08/05 04:36:00 - 08/05 10:08:54	Track (308.8, 162.5) @ 08/05 04:33:00							# Cont.				
08/06 02:03:00 - 08/06 03:59:54	Track (477.9, 170.1) @ 08/06 02:00:00							* HOP 173, AR 11263.				
08/06 04:13:00 - 08/06 05:59:54	Track (494.0, 171.1) @ 08/06 04:10:00							# Cont., HOP 173.				

PROG= 11 Inf.-time(s)												
Subr= 2 1-time(s) 2.0sec												
Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 41 4-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1 1-time(s) 2.0sec												
Seqn= 13 30-time(s) 80.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly												
Term	Pointing (x, y)							Comment				
08/05 22:42:00 - 08/06 01:59:54	Fixed (0.0, 0.0)							* EIS sensitivity monitoring.				

PROG= 17 Inf.-time(s)												
Subr= 1 1-time(s) 120.0sec												
Seqn= 33 1-time(s) 4.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #189D: HOP195 2-filter - Ti/poly, Al/mesh, - 384x384, 60sec cad												
Term	Pointing (x, y)							Comment				
08/06 06:03:00 - 08/06 09:23:30	Track (509.3, 159.0) @ 08/06 06:00:00							* HOP 195, QS.				

PROG= 03 Inf.-time(s)												
Subr= 2 1-time(s) 2.0sec												
Seqn= 58 1-time(s) 60.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term	Pointing (x, y)							Comment				

08/04 14:26:00 - 08/04 17:54:54	Track (189.5, 159.5) @ 08/04 14:23:00							* AR 11263.
08/04 18:52:30 - 08/05 01:59:54	Track (221.1, 160.1) @ 08/04 18:05:00							# Cont.
08/05 02:03:00 - 08/05 03:45:00	Track (287.7, 161.8) @ 08/05 02:00:00							* HOP 173, EPO with high schools. AR 11263.
08/05 04:36:00 - 08/05 10:08:54	Track (308.8, 162.5) @ 08/05 04:33:00							# Cont.
08/05 16:15:30 - 08/05 18:04:54	Fixed (-862.0, 300.0)							* ARs at E limb.
08/05 18:18:00 - 08/05 22:00:30	Fixed (-862.0, 300.0)							# Cont.
08/05 22:42:00 - 08/06 01:59:54	Fixed (0.0, 0.0)							* EIS sensitivity monitoring.
08/06 02:03:00 - 08/06 03:59:54	Track (477.9, 170.1) @ 08/06 02:00:00							* HOP 173, AR 11263.
08/06 04:13:00 - 08/06 05:59:54	Track (494.0, 171.1) @ 08/06 04:10:00							# Cont., HOP 173.
08/06 06:03:00 - 08/06 09:23:30	Track (509.3, 159.0) @ 08/06 06:00:00							* HOP 195, QS.

PROG= 08 1-time(s)												
Subr= 1 30-time(s) 20.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Subr= 2		1-time(s)		2.0sec													
└─		Seqn= 90		1-time(s)		2.0sec											
	└─	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
	└─	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
	└─	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec			
Subr= 3		30-time(s)		60.0sec													
└─		Seqn= 87		1-time(s)		2.0sec											
	└─	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
	└─	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
└─		Seqn= 88		1-time(s)		2.0sec											
	└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
	└─	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Subr= 2		1-time(s)		2.0sec													
└─		Seqn= 90		1-time(s)		2.0sec											
	└─	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
	└─	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
	└─	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec			
Subr= 3		30-time(s)		60.0sec													
└─		Seqn= 87		1-time(s)		2.0sec											
	└─	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
	└─	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
└─		Seqn= 88		1-time(s)		2.0sec											
	└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
	└─	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Subr= 2		1-time(s)		2.0sec													
└─		Seqn= 90		1-time(s)		2.0sec											
	└─	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
	└─	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
	└─	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec			
Subr= 3		30-time(s)		60.0sec													
└─		Seqn= 87		1-time(s)		2.0sec											
	└─	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
	└─	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
└─		Seqn= 88		1-time(s)		2.0sec											
	└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
	└─	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Subr= 4		24-time(s)		600.0sec													
└─		Seqn= 89		1-time(s)		2.0sec											
	└─	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec			
	└─	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec			
	└─	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
	└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

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Active Region Search

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NOT USED

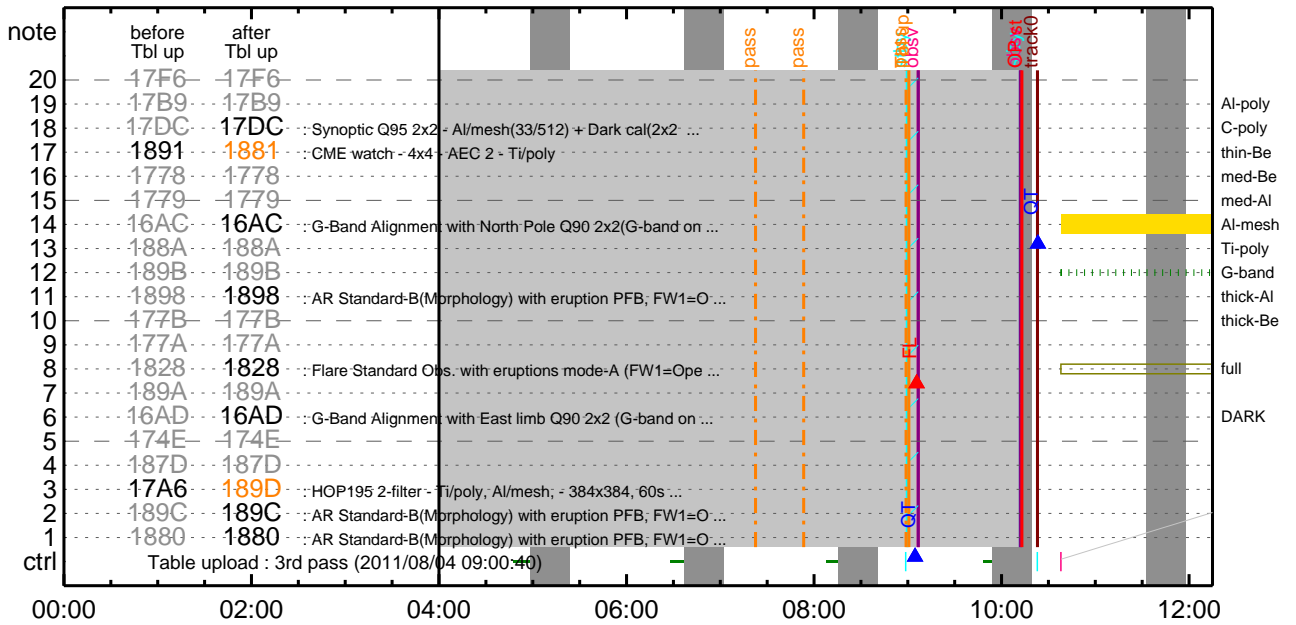
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Flare Detection

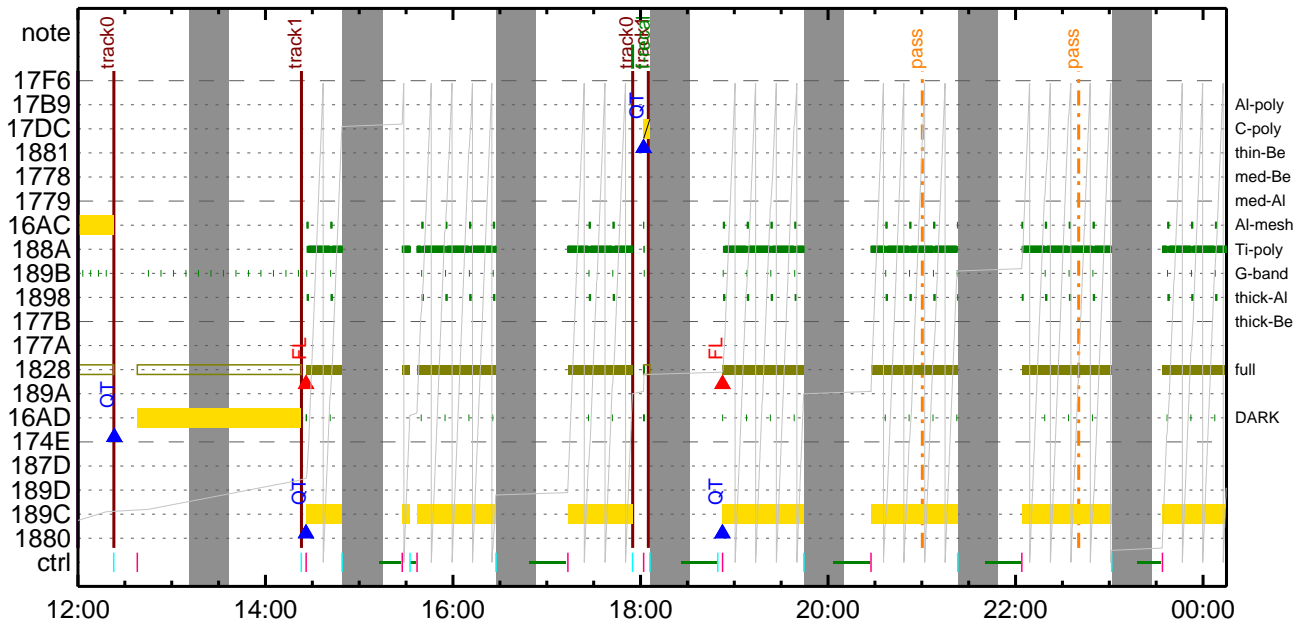
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FLD Patrol													
Term		Pointing (x, y)						Comment					
08/04 14:25:46 - 08/04 17:59:16		Track (189.5, 159.5) @ 08/04 14:23:00						* AR 11263.					
08/04 18:52:16 - 08/05 04:23:16		Track (221.1, 160.1) @ 08/04 18:05:00						# Cont.					
08/05 04:35:46 - 08/05 18:05:16		Track (308.8, 162.5) @ 08/05 04:33:00						# Cont.					
08/05 18:17:46 - 08/06 04:00:16		Fixed (-862.0, 300.0)						# Cont.					
08/06 04:12:46 - 08/09 09:28:00		Track (494.0, 171.1) @ 08/06 04:10:00						# Cont., HOP 173.					
		Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

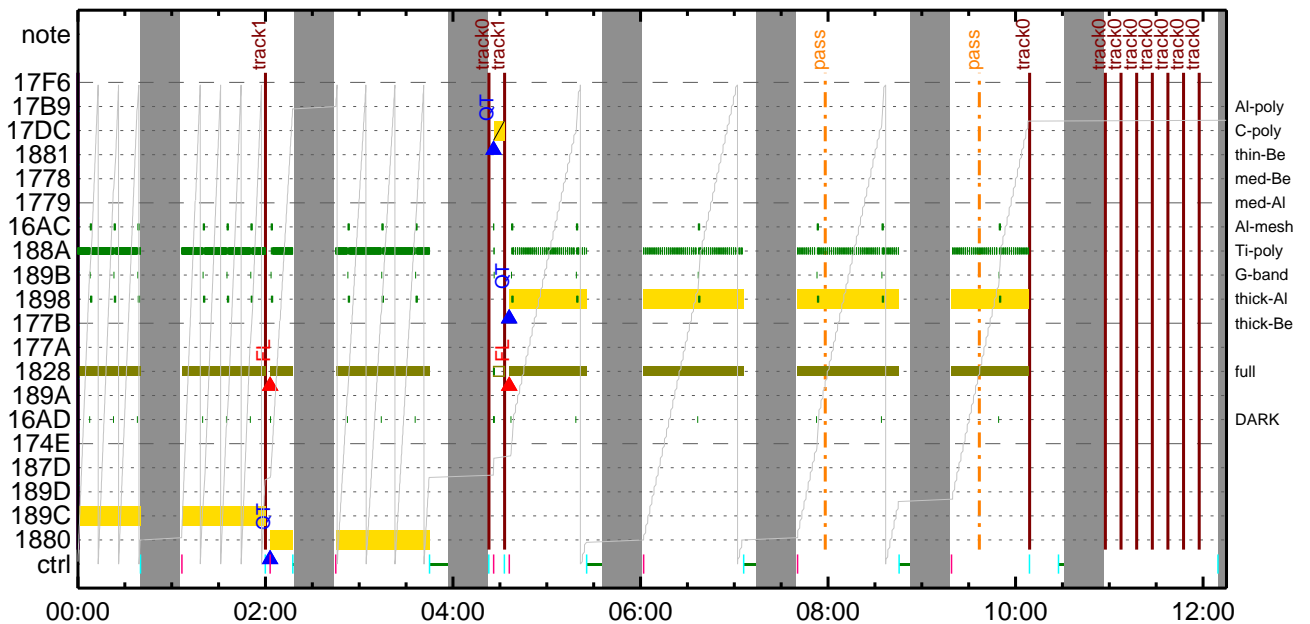
CMDI #0072 2011/08/04



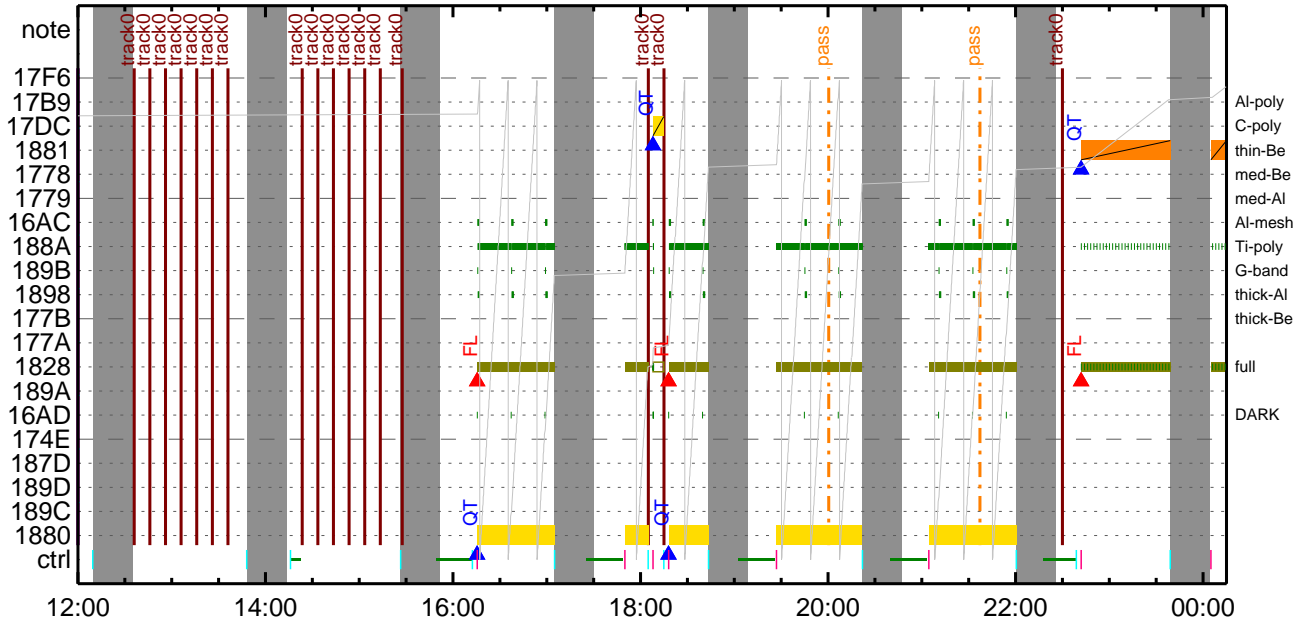
CMDI #0072 2011/08/04



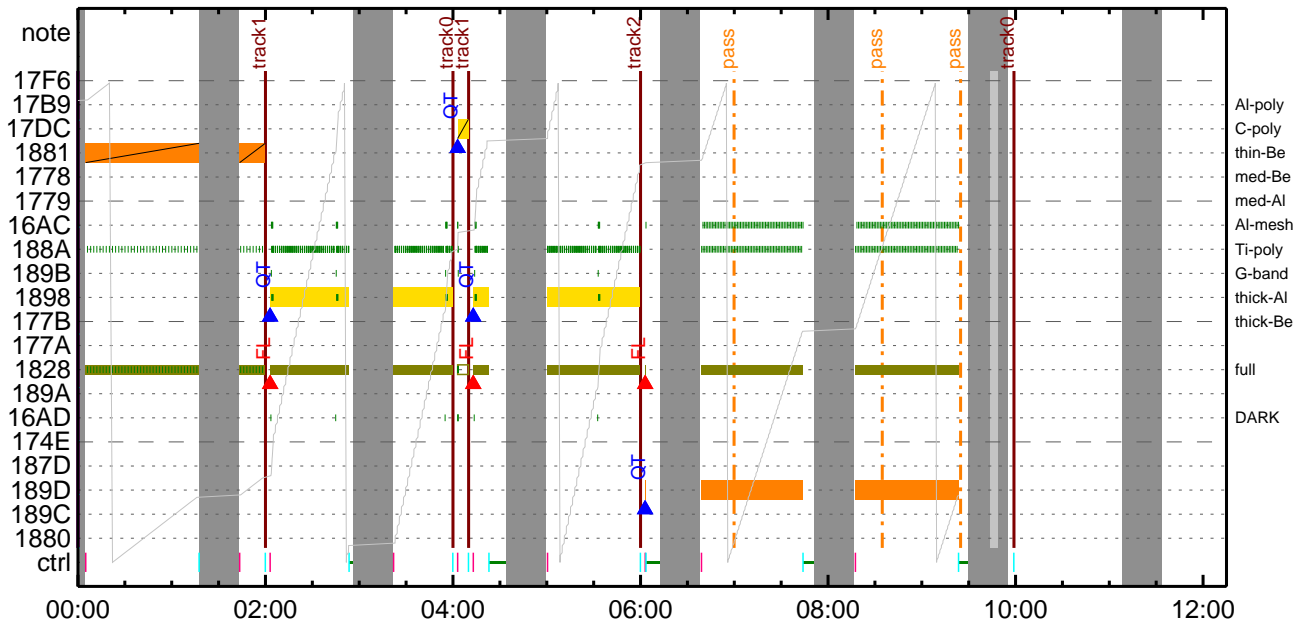
CMDI #0072 2011/08/05



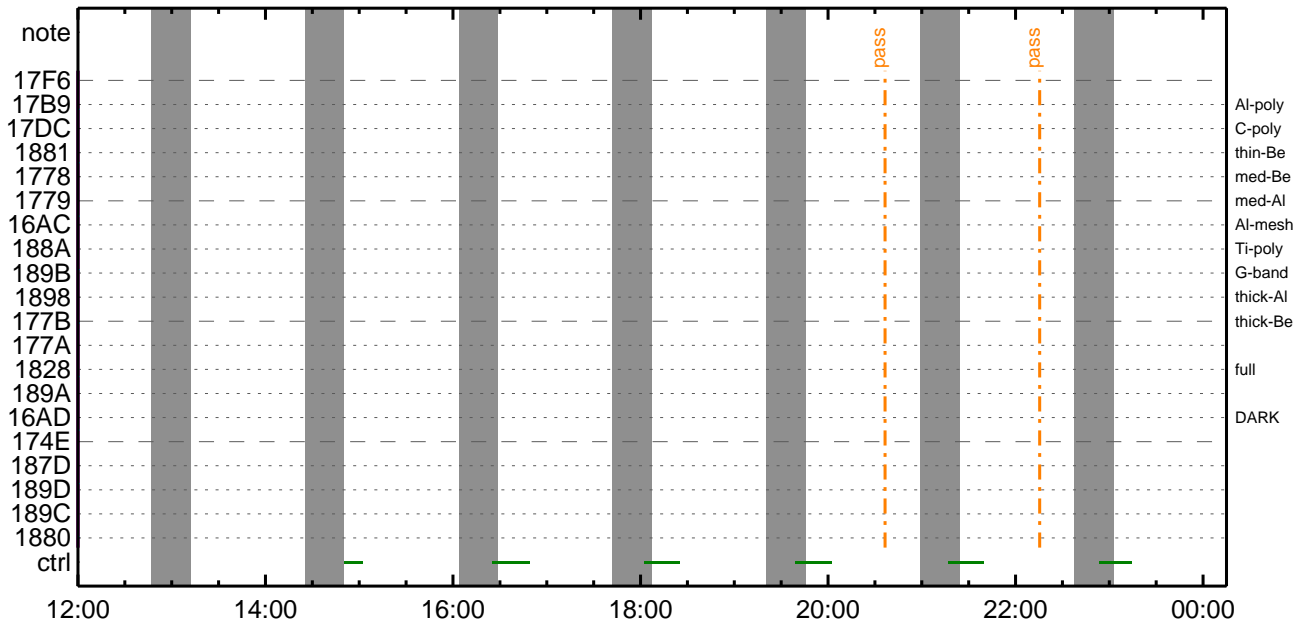
CMDI #0072 2011/08/05



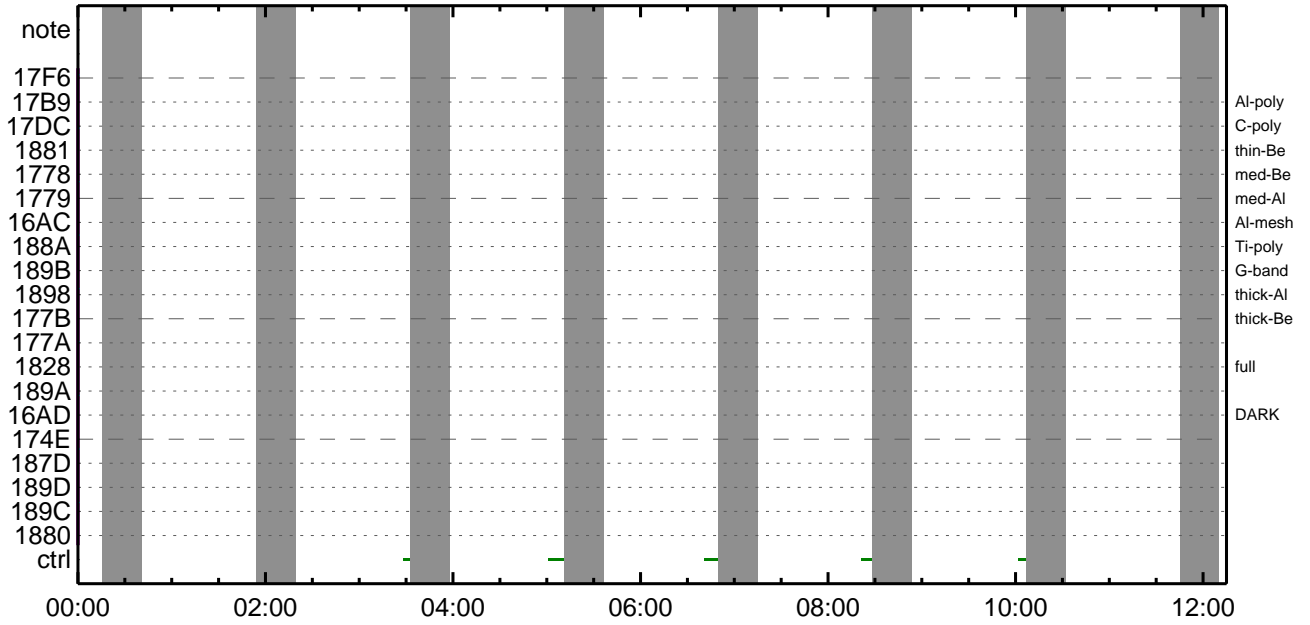
CMDI #0072 2011/08/06



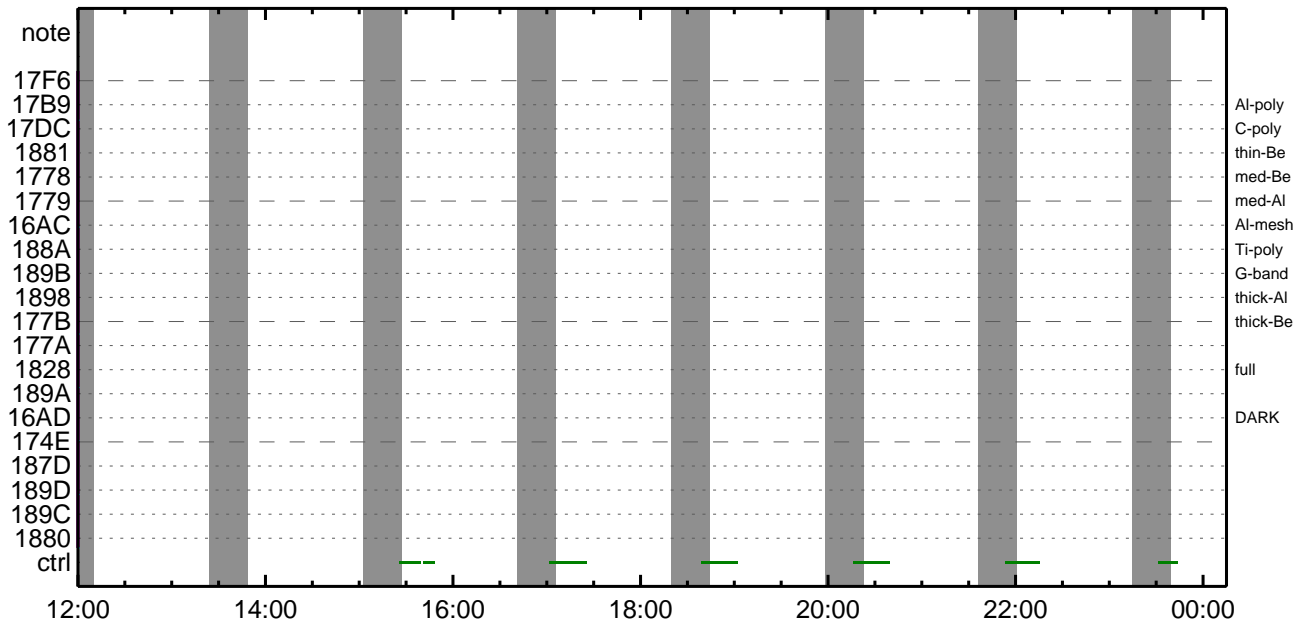
CMDI #0072 2011/08/06



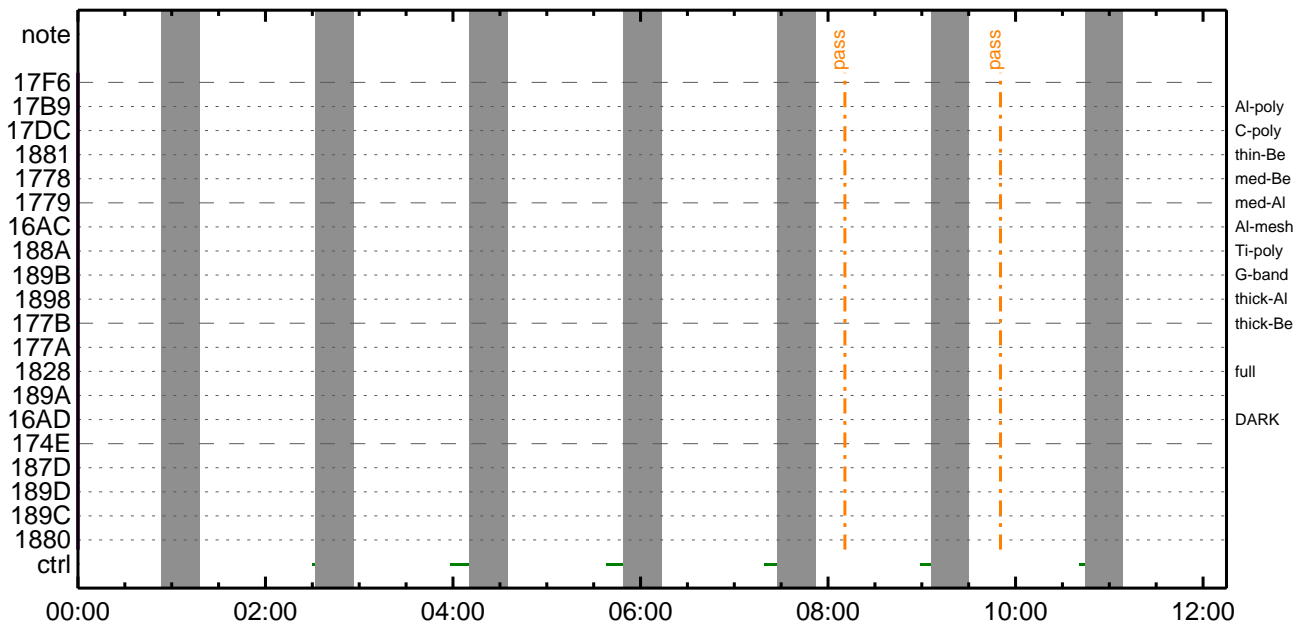
CMDI #0072 2011/08/07



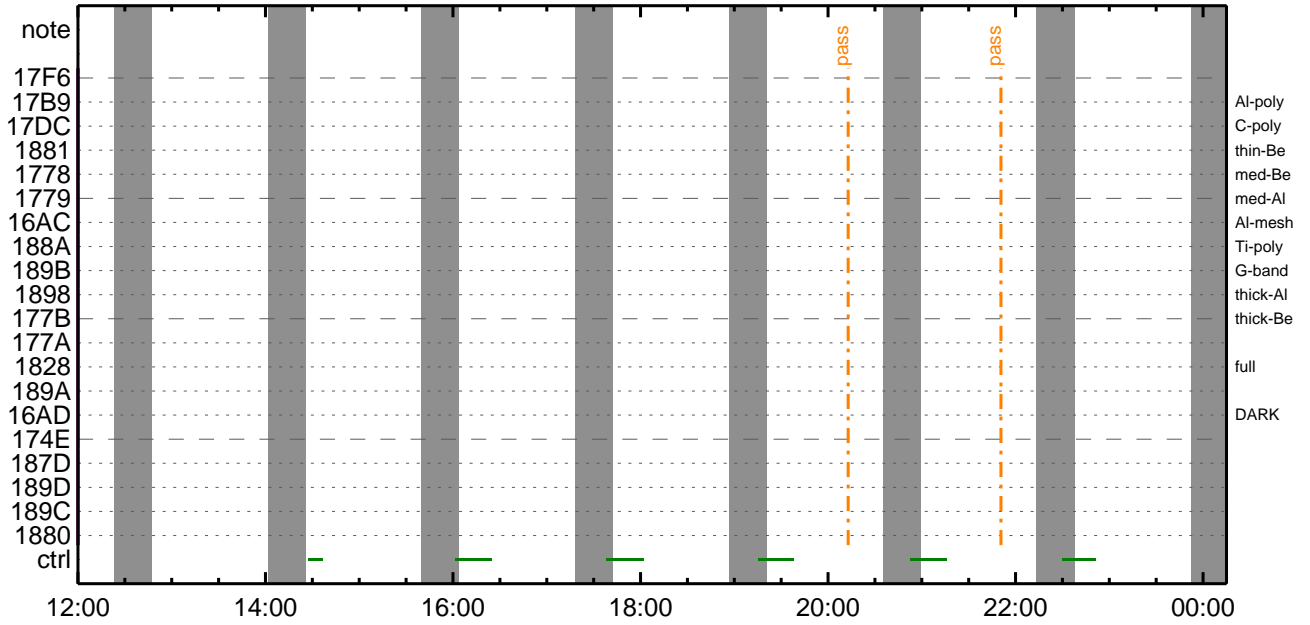
CMDI #0072 2011/08/07



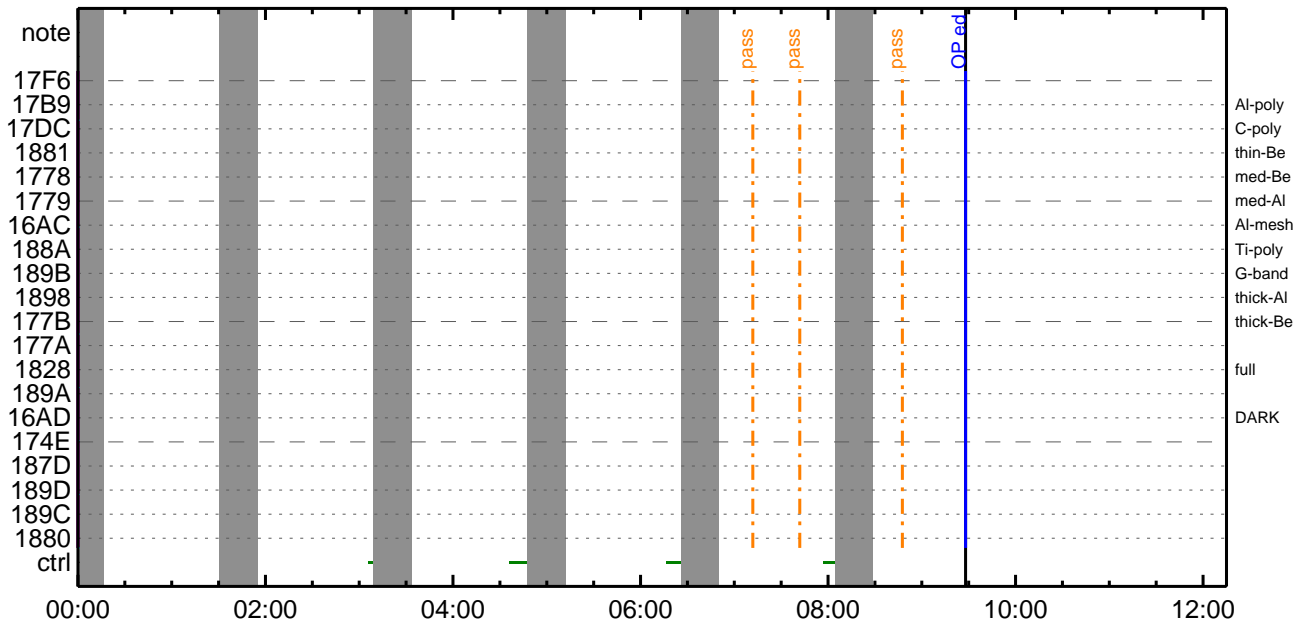
CMDI #0072 2011/08/08



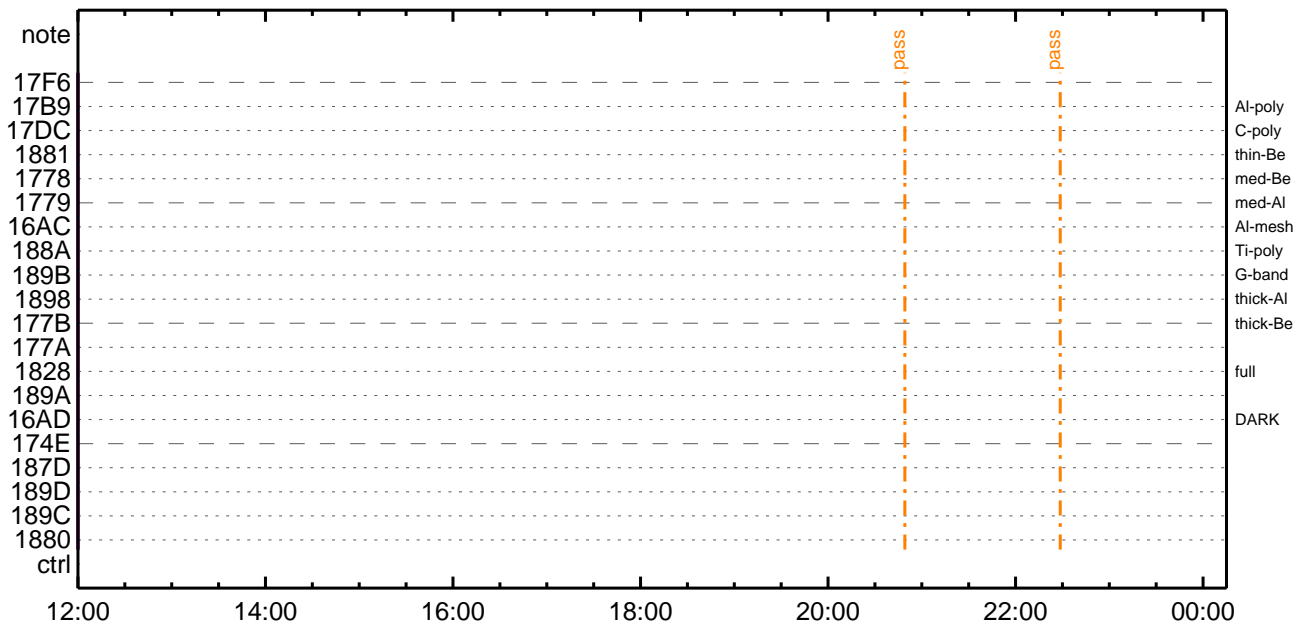
CMDI #0072 2011/08/08



CMDI #0072 2011/08/09



CMDI #0072 2011/08/09



(a) Spacecraft Operation Procedure (real-commands)

```

main-237 2011-08-04 12:04:25 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Ü;ã
0005 C.
0006 C. YÁYŞ;¼Y³YÞYÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èµ¿µÄµ•µ°È»Í×ÁÇµÍYÇYÁY×YÍ;¼YÉ;ÈÈÈµ•íÉ;ÈÈÈ¼°ÇÖµ•µ¿¼í¹Çµí;çÀ®, ùµ¹µÈµßµÇÁ+¿®µ•µÈµµµ³µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. ç“ °ÆÀ, Í×ÈYµäLOSµßµÇµÍ»´Öµµ¹íí, µ•; çÉÖÍ×µÈXÁÖONµí¹ÖµÈµíµÈµµµ³µÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY~¾ÖÄÖµ~ºÄÈµ•µ¿µÉ; ç°È²¼µí°ÆÀ, ¼É¾Çµµµ¹Öµ¹µÈ;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. ç“ RESTART;ÈPT1;Èµ•µ¿µµ¾í¹Çµí; ç°È²¼µí°ÆÀ¹Öµµ°; çDCBC-150µØ¿Èµà;f
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¾Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¾Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¾Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYÉÁÜÁÏ;ÈÁ•ÁºóÈð;È, áµí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¾Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¾Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¾Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, µ~¼«Æ°Áä»ßµ•µ¿, á; ç°È²¼µµ¹Öµ¹µÈ;f
0055 C. YÇYÓYÉYÉÁÜÁÏµÁ•ÁºóÈðµ~¶áµµ¾í¹Çµí°í»µ¹µÈµßµÇÁÖµÁ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. ç“ RESTART;ÈPT2;Èµ•µ¿µµ¾í¹Çµí; ç°È²¼µí°ÆÀ¹Öµµ°; çDCBC-151µØ¿Èµà;f
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¾Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¾Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¾Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYÉÁÜÁÏ;ÈÁ•ÁºóÈð;È, áµí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¾Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¾Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¾Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áä»ß;çXÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áä»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 C. ;ãXÁ+¿µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF

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0096 C.
0097 C.
0098 C.
0099 C. *****
0099 C. OP/OGY1;|YÉ;|YAYÓYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;|YÉ;ã
0103 S. OP op-237:OP
0104 ( )
0105 S. OG og-237:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYÓYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYÓYx½ª î»ò³ îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGªî½ª¹ç.ê² îOKªð³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYÓYx½ª î»ò³ îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGªî½ª¹ç.ê² îOKªð³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYÓYx½ª î»ò³ îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPªî½ª¹ç.ê² îOKªð³ îÇ§
0165 C.
0166 C. ***** °Ê²¼ª î½ª¹ç.ê² îOKªð³ îÇ§ *****
0167 C. DHUYâ;|YÉ;|YAYÓYx½ª î»ò³ îÇ§
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADª-Á÷ç;ç°Ê²¼ª î½ª¹ç.ê² îOKªð³ îÇ§ª±º³ ç³ªÊ;ç
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 C. TIY³YbYóYÉªððÁîç(UT)
0183 +. TI 2011-08-04 10:08:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-08-04 10:08:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-08-04 10:08:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
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0194 C.
0195 +. TI 2011-08-04 10:12:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ïãîÄë%îíññîîŷÄŷ§ŷÄŷ-¹âiü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLñî¾È¹ç•è²îOKò³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;È¼ŷ¼.ŷî;¼ŷÈ;Èòðîãñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2011-08-04 10:12:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2011-08-04 10:12:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2011-08-04 10:12:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C. -----
0262 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-08-04 10:12:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîãî»ö¼ŷñëÄðñ¹ñèDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÈŷŷŷŷÄŷçŷèè%¼ññ¼ª»Ûñ¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîññè'Øñ¹ñèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼ª»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F8 XRT_OPERATE
0102 BC (03 02)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG ____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ( )
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 08 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 80 60 20 18)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 a0 80 18 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 80 80 20 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_ARS_DIS
0145 BC (d5)
0146 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0147 BC (c4 01)
0148 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0149 BC (c5 08)
0150 . C. ----- Success Verify ? OK / NG ____
0151 C.
0152 C.
0153 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0154 C.
0155 +. DC 07-F0 MDP_XRT_MODE_OBSV
0156 BC (c2)
0157 +. TI 2011-08-04 10:12:02.0
0158 DC 07-F0 MDP_XRT_MODE_OBSV
0159 BC (c2)
0160 . C. ----- Success Verify ? OK / NG ____
0161 C.
0162 C. ***** XRT END *****
0163 . C. *****
0164 C. SOT table upload
0165 C. *****
0166 . C. < Stop FG table >
0167 +. DC 07-F0 MDP_FG_CTRL_MANU
0168 BC (51)
0169 . C. -----
0170 C. MDP_FG_CTRL_MODE = MANU [ ]
0171 C. -----
0172 C.
0173 . C. <Upload FG Observation Table>
0174 . S. RAM ram-262:MDP_OBS_F
0175 ( )
0176 C.
0177 . C. < Dump RAMID=MDP_OBS_F >
0178 +. DC 07-F0 MDP_DUMP_FGTBL
0179 BC (82 07 00 00 00 38 b8)
0180 C. -----
0181 C. MDP_OBS_F verify = OK/NG [ ]
0182 C. -----
0183 C.
0184 C. *****
0185 C. SOT TI command set
0186 C. *****
0187 C. Execute, after the success of TBL upload.
0188 +. TI 2011-08-04 10:12:18.0
0189 DC 07-F0 MDP_SOT_MODE_OBSV
0190 BC (40)
0191 . C. -----
0192 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0193 C. -----

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0194 C.
0195 C.
0196 . C. ***** MDP 'úÃîî»ô¼ÿñÊâðñ¹ñêDCBC•x²è *****
0197 C. (¼ã°îÿÓÿÃÿËÿÏÿËÿáÿçÿèñ¼ññ¼Ã»Ûñ¹ñê)
0198 . S. DC-BC dcbc-402:DCBC
0199 (MDP_known_event)
0200 C.
0201 C.
0202 . C. ***** ÿÐÿ¹•ï Daily±¿îññË´øñ¹ñêDCBC•x²è *****
0203 . S. DC-BC dcbc-153:DCBC
0204 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0205 C.
0206 C.
0207 . C. ;ãLÖSÿÁÿ§ÿÛÿÿ¼Ã»Û;ã
0208 C.
0209 . C. ***** LOS *****
0210 C.
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Aug 04, 11 12:06

XRT_OGLIST_0072.chk

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*** OP Sequence for XRT ***

2011/08/04	10:22:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/04	10:22:56.0	XRT_FOCUS_POSITION_445_OG [0x1bd]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/08/04	10:23:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 ac 00 00 00		
2011/08/04	10:23:16.0	XRT_QT_PROG_SET_429_OG [0x1ad]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2011/08/04	10:23:18.0	XRT_FLD_DIS_423_OG [0x1a7]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/08/04	10:23:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/08/04	10:23:22.0	XRT_ARS_DIS_437_OG [0x1b5]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/08/04	10:38:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/04	12:22:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/04	12:22:56.0	XRT_FOCUS_POSITION_445_OG [0x1bd]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/08/04	12:23:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 54 00		
2011/08/04	12:23:16.0	XRT_QT_PROG_SET_432_OG [0x1b0]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2011/08/04	12:23:18.0	XRT_FLD_DIS_423_OG [0x1a7]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/08/04	12:23:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/08/04	12:23:22.0	XRT_ARS_DIS_437_OG [0x1b5]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/08/04	12:38:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/04	14:22:54.0	XRT_CTRL_MANU_439_OG [0x1b7]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/04	14:23:00.0	AOCS_ORe-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2011/08/04	14:25:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/08/04	14:25:46.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/08/04	14:25:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/08/04	14:25:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/08/04	14:25:52.0	XRT_ARS_DIS_431_OG [0x1af]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/08/04	14:25:54.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/04	14:25:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02		
2011/08/04	14:25:58.0	XRT_FL_PROG_SET_414_OG [0x19e]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2011/08/04	14:26:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/04	14:49:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/04	14:49:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/04	14:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/04	14:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/04	15:26:30.0	XRT_Custom_418_OG [0x1a2]					
2011/08/04	15:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/04	15:32:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/04	15:32:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/04	15:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/04	15:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/04	15:36:00.0	XRT_Custom_418_OG [0x1a2]					
2011/08/04	15:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/04	16:27:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/04	16:27:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/04	16:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/04	16:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/04	17:12:30.0	XRT_Custom_418_OG [0x1a2]					
2011/08/04	17:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/04	17:54:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/04	17:54:56.0	XRT_FOCUS_RECALIBRATE_430_OG [0x1ae]					
		XRT_FOCUS_RECAL	2	07-F8	78 00		

2011/08/04	17:55:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	00	00	00	00
2011/08/04	17:58:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	00
2011/08/04	17:59:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0		d9			
2011/08/04	17:59:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2011/08/04	17:59:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0		d5			
2011/08/04	18:01:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	12		
2011/08/04	18:02:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/08/04	18:05:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00	00	00	00
2011/08/04	18:06:00.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/08/04	18:06:02.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da			
2011/08/04	18:06:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/08/04	18:09:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/08/04	18:49:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/08/04	18:51:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	00
2011/08/04	18:52:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0		d8			
2011/08/04	18:52:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2011/08/04	18:52:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0		d0			
2011/08/04	18:52:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0		d5			
2011/08/04	18:52:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da			
2011/08/04	18:52:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	02		
2011/08/04	18:52:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0		c5	08		
2011/08/04	18:52:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/08/04	19:44:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/08/04	19:44:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da			
2011/08/04	19:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/08/04	19:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/08/04	20:26:30.0	XRT_Custom_418_OG [0x1a2]								
2011/08/04	20:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/08/04	21:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/08/04	21:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da			
2011/08/04	21:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/08/04	21:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/08/04	22:03:00.0	XRT_Custom_418_OG [0x1a2]								
2011/08/04	22:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/08/04	23:01:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/08/04	23:01:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da			
2011/08/04	23:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/08/04	23:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/08/04	23:33:00.0	XRT_Custom_418_OG [0x1a2]								
2011/08/04	23:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/08/05	00:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/08/05	00:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da			
2011/08/05	00:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/08/05	00:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/08/05	01:05:30.0	XRT_Custom_418_OG [0x1a2]								
2011/08/05	01:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/08/05	01:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/08/05	02:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00	00	00	00
2011/08/05	02:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]								

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2011/08/05	02:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		MDP_XRT_FLD_ENA		1	07-F0	d8			
2011/08/05	02:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/08/05	02:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/08/05	02:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/05	02:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/05	02:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2011/08/05	02:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/08/05	02:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/05	02:17:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/05	02:17:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/05	02:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/05	02:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/05	02:44:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/05	02:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/05	03:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/05	03:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/05	03:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/05	03:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/05	04:22:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/05	04:22:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2011/08/05	04:23:00.0	AOCs_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	00	00	00
2011/08/05	04:23:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/08/05	04:23:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/08/05	04:23:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/05	04:25:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12		
2011/08/05	04:26:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/05	04:32:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/05	04:33:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00	00	00
2011/08/05	04:35:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/08/05	04:35:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/08/05	04:35:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/08/05	04:35:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/08/05	04:35:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/05	04:35:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/05	04:35:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b		
2011/08/05	04:35:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/08/05	04:36:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/05	05:25:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/05	05:25:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/05	05:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/05	05:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/05	06:01:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/05	06:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/05	07:06:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/05	07:06:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/05	07:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/05	07:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			

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2011/08/05	07:39:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/05	07:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/05	08:45:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/05	08:45:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/05	08:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/05	08:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/05	09:18:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/05	09:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/05	10:08:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/05	10:09:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 02 7e 4e b8				
2011/08/05	10:27:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/05	10:27:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/05	10:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/05	10:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/05	10:57:30.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 02 7e 3d 06				
2011/08/05	11:07:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 02 7e 2b 53				
2011/08/05	11:17:30.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 02 7e 19 a1				
2011/08/05	11:27:30.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 02 7e 07 f7				
2011/08/05	11:37:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 02 7e f6 46				
2011/08/05	11:47:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 02 7e e4 8c				
2011/08/05	11:57:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00 02 7e d2 da				
2011/08/05	12:09:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/05	12:09:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/05	12:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/05	12:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/05	12:36:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 02 7e bd a2				
2011/08/05	12:46:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00 02 7e af 7d				
2011/08/05	12:56:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 15 a1 51 26				
2011/08/05	13:06:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00 1d 58 40 fd				
2011/08/05	13:16:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00 1f 02 30 8b				
2011/08/05	13:26:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00 20 00 1f df				
2011/08/05	13:36:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00 20 8b 0e e9				
2011/08/05	13:48:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/05	13:48:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/05	13:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/05	13:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/05	14:16:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/05	14:16:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/05	14:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/05	14:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/05	14:23:30.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00 20 ac fd 92				
2011/08/05	14:33:30.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00 20 8b f2 57				
2011/08/05	14:43:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00 20 00 e1 59				
2011/08/05	14:53:30.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 1f 02 d0 b5				
2011/08/05	15:03:30.0	AOCS_ORe-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	00 1d 58 c0 42				
2011/08/05	15:13:30.0	AOCS_ORe-point_Start_25_OG [0x0af]							
		AOCU_NM	5	02-76	00 15 a1 ae da				
2011/08/05	15:26:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/05	15:26:32.0	XRT_FLD_RESET_412_OG [0x19c]							

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2011/08/05	15:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/05	15:27:30.0	AOCS_ORe-point_Start_26_OG [0x0b0]		5	02-76	00 e5 59 4c 9b
			AOCU_NM			
2011/08/05	15:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/05	16:12:24.0	XRT_CTRL_MANU_439_OG [0x1b7]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/05	16:14:56.0	XRT_FOCUS_POSITION_409_OG [0x199]				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/08/05	16:15:16.0	XRT_FLD_ENA_411_OG [0x19b]				
			MDP_XRT_FLD_ENA	1	07-F0	d8
2011/08/05	16:15:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/08/05	16:15:20.0	XRT_AEC_RESET_443_OG [0x1bb]				
			MDP_XRT_AEC_RESET	1	07-F0	d0
2011/08/05	16:15:22.0	XRT_ARS_DIS_431_OG [0x1af]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2011/08/05	16:15:24.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/05	16:15:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2011/08/05	16:15:28.0	XRT_FL_PROG_SET_414_OG [0x19e]				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/08/05	16:15:30.0	XRT_CTRL_AUTO_406_OG [0x196]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/05	17:05:00.0	XRT_CTRL_MANU_408_OG [0x198]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/05	17:05:02.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/05	17:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/05	17:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/05	17:49:00.0	XRT_Custom_418_OG [0x1a2]				
2011/08/05	17:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/05	18:04:54.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/05	18:04:56.0	XRT_FOCUS_POSITION_401_OG [0x191]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/08/05	18:05:00.0	AOCS_ORe-point_Start_4_OG [0x09a]				
			AOCU_NM	5	02-76	00 00 00 00 00
2011/08/05	18:05:16.0	XRT_FLD_DIS_402_OG [0x192]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2011/08/05	18:05:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/08/05	18:05:20.0	XRT_ARS_DIS_438_OG [0x1b6]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2011/08/05	18:07:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2011/08/05	18:08:00.0	XRT_CTRL_AUTO_406_OG [0x196]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/05	18:14:54.0	XRT_CTRL_MANU_439_OG [0x1b7]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/05	18:15:00.0	AOCS_ORe-point_Start_26_OG [0x0b0]				
			AOCU_NM	5	02-76	00 e5 59 4c 9b
2011/08/05	18:17:26.0	XRT_FOCUS_POSITION_409_OG [0x199]				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/08/05	18:17:46.0	XRT_FLD_ENA_411_OG [0x19b]				
			MDP_XRT_FLD_ENA	1	07-F0	d8
2011/08/05	18:17:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/08/05	18:17:50.0	XRT_AEC_RESET_443_OG [0x1bb]				
			MDP_XRT_AEC_RESET	1	07-F0	d0
2011/08/05	18:17:52.0	XRT_ARS_DIS_431_OG [0x1af]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2011/08/05	18:17:54.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/05	18:17:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2011/08/05	18:17:58.0	XRT_FL_PROG_SET_414_OG [0x19e]				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/08/05	18:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/05	18:43:30.0	XRT_CTRL_MANU_408_OG [0x198]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/05	18:43:32.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/05	18:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/05	18:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/05	19:26:00.0	XRT_Custom_418_OG [0x1a2]				
2011/08/05	19:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/05	20:22:00.0	XRT_CTRL_MANU_408_OG [0x198]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/05	20:22:02.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/05	20:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2011/08/05	20:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/05	21:03:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/05	21:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/05	22:00:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/05	22:00:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/05	22:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/05	22:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/05	22:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/08/05	22:38:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/05	22:41:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/08/05	22:41:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/05	22:41:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/05	22:41:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/08/05	22:41:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/05	22:41:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/05	22:41:56.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2011/08/05	22:41:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/08/05	22:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/05	23:39:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/05	23:39:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/05	23:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/05	23:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/06	00:04:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/06	00:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/06	01:17:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/06	01:17:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/06	01:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/06	01:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/06	01:42:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/06	01:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/06	01:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/06	02:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/08/06	02:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/08/06	02:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/06	02:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/06	02:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/08/06	02:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/06	02:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/06	02:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2011/08/06	02:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/08/06	02:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/06	02:53:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/06	02:53:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/06	02:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/06	02:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/06	03:21:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/06	03:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/06	03:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/06	03:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							

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2011/08/06	04:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00
2011/08/06	04:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/08/06	04:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/08/06	04:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/06	04:02:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12		
2011/08/06	04:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/06	04:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/06	04:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00	00	00
2011/08/06	04:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/08/06	04:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/08/06	04:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/08/06	04:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/08/06	04:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/06	04:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/06	04:12:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b		
2011/08/06	04:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/08/06	04:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/06	04:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/06	04:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/06	04:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/06	04:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/06	04:59:30.5	XRT_Custom_418_OG [0x1a2]							
2011/08/06	05:00:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/06	05:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/06	06:00:00.0	AOCS_OrE-point_Start_27_OG [0x0b1]	AOCU_NM	5	02-76	02	00	00	00
2011/08/06	06:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/08/06	06:02:46.0	XRT_FLD_ENA_447_OG [0x1bf]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/08/06	06:02:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/08/06	06:02:50.5	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/08/06	06:02:52.5	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/06	06:02:54.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/06	06:02:56.5	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2011/08/06	06:02:58.5	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/08/06	06:03:00.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/06	06:03:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/06	06:03:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/06	06:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/06	06:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/06	06:38:00.5	XRT_Custom_418_OG [0x1a2]							
2011/08/06	06:39:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/06	07:44:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/06	07:44:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/06	07:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/06	07:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/06	08:16:30.5	XRT_Custom_418_OG [0x1a2]							
2011/08/06	08:17:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/06	09:23:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/06	09:23:32.0	XRT_FLD_RESET_412_OG [0x19c]							

2011/08/06	09:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/06	09:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/06	09:58:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/06	09:59:00.0	AOCS_Or-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00