

XRT Timeline to be uploaded on 2011/08/06

Period: 2011/08/06 09:49:00 - 2011/08/11 10:09:00

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Normal mode

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XOB #189D: HOP195 2-filter - Ti/poly, Al/mesh, - 384x384, 60sec cad													
Term	Pointing (x, y)		Comment										
08/06 10:02:00 - 08/06 11:59:54	Track (538.1, 160.9)	[Ⓢ] 08/06 09:59:00	# OP start + 10min, HOP 195, cont. plage.									
08/07 08:03:00 - 08/07 11:45:30	Track (681.7, 174.0)	[Ⓢ] 08/07 08:00:00	* HOP 195, AR 11263 plage.									
08/08 08:03:00 - 08/08 11:59:54	Track (803.2, 191.9)	[Ⓢ] 08/08 08:00:00	* HOP 195.									
08/09 08:29:30 - 08/09 09:37:54	Track (882.1, 212.6)	[Ⓢ] 08/09 08:00:00	* HOP 195.									
PROG= 03 Inf.-time(s)													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 58 1-time(s) 60.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #189C: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 15sec-cad													
Term	Pointing (x, y)		Comment										
08/06 12:03:00 - 08/06 17:42:00	Track (531.7, 182.4)	[Ⓢ] 08/06 12:00:00	* AR 11263, major flare watch.									
08/07 06:10:30 - 08/07 07:59:54	Track (651.4, 192.8)	[Ⓢ] 08/07 06:07:30	# Cont.									
08/07 12:11:00 - 08/07 17:59:54	Track (686.1, 196.6)	[Ⓢ] 08/07 12:00:00	* AR 11263.									
08/08 06:28:00 - 08/08 07:59:54	Track (780.3, 210.1)	[Ⓢ] 08/08 06:25:00	# Cont.									
08/08 12:03:00 - 08/08 17:18:00	Track (804.1, 214.5)	[Ⓢ] 08/08 12:00:00	* AR 11263.									
08/09 06:02:00 - 08/09 07:57:00	Track (865.0, 229.9)	[Ⓢ] 08/09 05:59:00	# Cont.									
PROG= 02 Inf.-time(s)													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 41 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 13 55-time(s) 15.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17DC: Synoptic Q95 2x2 - Al/mesh(33/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1024) + G-band(12)													
Term	Pointing (x, y)		Comment										
08/06 18:28:30 - 08/06 18:35:24	Fixed (0.0, 0.0)	synoptic, shifted 25.5 min										
08/07 06:00:30 - 08/07 06:07:24	Fixed (0.0, 0.0)	synoptic, shifted -2.5 min										
08/07 18:03:00 - 08/07 18:09:54	Fixed (0.0, 0.0)	synoptic										
08/08 06:22:00 - 08/08 06:24:54	Fixed (0.0, 0.0)	synoptic, shifted 15.0 min										
08/08 18:05:00 - 08/08 18:11:54	Fixed (0.0, 0.0)	synoptic, shifted 2.0 min										
08/09 05:52:00 - 08/09 05:58:54	Fixed (0.0, 0.0)	synoptic, shifted -11.0 min										
PROG= 18 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 20 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 31 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 77 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 63 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1898: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad													
Term	Pointing (x, y)		Comment										
08/06 18:38:30 - 08/07 05:57:24	Track (577.3, 185.9)	[Ⓢ] 08/06 18:35:30	# Cont.									
08/07 18:13:00 - 08/08 06:14:54	Track (720.2, 200.9)	[Ⓢ] 08/07 18:10:00	# Cont.									
08/08 22:52:00 - 08/09 05:48:54	Track (841.7, 223.1)	[Ⓢ] 08/08 22:12:00	* AR 11263.									
PROG= 11 Inf.-time(s)													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 41 4-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1 1-time(s) 2.0sec												
Seqn= 13 30-time(s) 80.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT												
Term			Pointing (x, y)					Comment				
08/08 18:27:00 - 08/08 20:11:54			Fixed (0.0, 945.0)					# Co-alignment offset, N.				
PROG= 14 1-time(s)												
Subr= 1 1-time(s) 360.0sec												
Seqn= 21 24-time(s) 300.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT												
Term			Pointing (x, y)					Comment				
08/08 20:27:00 - 08/08 22:11:54			Fixed (-945.0, 0.0)					* Co-alignment offset, E.				
PROG= 06 1-time(s)												
Subr= 1 1-time(s) 360.0sec												
Seqn= 22 15-time(s) 480.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term			Pointing (x, y)					Comment				
08/06 10:02:00 - 08/06 11:59:54			Track (538.1, 160.9) @ 08/06 09:59:00					# OP start + 10min, HOP 195, cont. plage.				
08/06 12:03:00 - 08/06 17:42:00			Track (531.7, 182.4) @ 08/06 12:00:00					* AR 11263, major flare watch.				
08/06 18:38:30 - 08/07 05:57:24			Track (577.3, 185.9) @ 08/06 18:35:30					# Cont.				
08/07 06:10:30 - 08/07 07:59:54			Track (651.4, 192.8) @ 08/07 06:07:30					# Cont.				
08/07 08:03:00 - 08/07 11:45:30			Track (681.7, 174.0) @ 08/07 08:00:00					* HOP 195, AR 11263 plage.				
08/07 12:11:00 - 08/07 17:59:54			Track (686.1, 196.6) @ 08/07 12:00:00					* AR 11263.				
08/07 18:13:00 - 08/08 06:14:54			Track (720.2, 200.9) @ 08/07 18:10:00					# Cont.				
08/08 06:28:00 - 08/08 07:59:54			Track (780.3, 210.1) @ 08/08 06:25:00					# Cont.				
08/08 08:03:00 - 08/08 11:59:54			Track (803.2, 191.9) @ 08/08 08:00:00					* HOP 195.				
08/08 12:03:00 - 08/08 17:18:00			Track (804.1, 214.5) @ 08/08 12:00:00					* AR 11263.				
08/08 22:52:00 - 08/09 05:48:54			Track (841.7, 223.1) @ 08/08 22:12:00					* AR 11263.				
08/09 06:02:00 - 08/09 07:57:00			Track (865.0, 229.9) @ 08/09 05:59:00					# Cont.				
08/09 08:29:30 - 08/09 09:37:54			Track (882.1, 212.6) @ 08/09 08:00:00					* HOP 195.				
PROG= 08 1-time(s)												
Subr= 1 30-time(s) 20.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 87 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

└─ Seqn= 88		1-time(s)		2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec	
└─ Subr= 2		1-time(s)		2.0sec											
└─ Seqn= 90		1-time(s)		2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec	
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec	
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec	
└─ Subr= 3		30-time(s)		60.0sec											
└─ Seqn= 87		1-time(s)		2.0sec											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec	
└─ Seqn= 88		1-time(s)		2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec	
└─ Subr= 4		24-time(s)		600.0sec											
└─ Seqn= 89		1-time(s)		2.0sec											
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)		Q=95	1	0	2.0sec	
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)		Q=95	1	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval		

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Active Region Search

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NOT USED

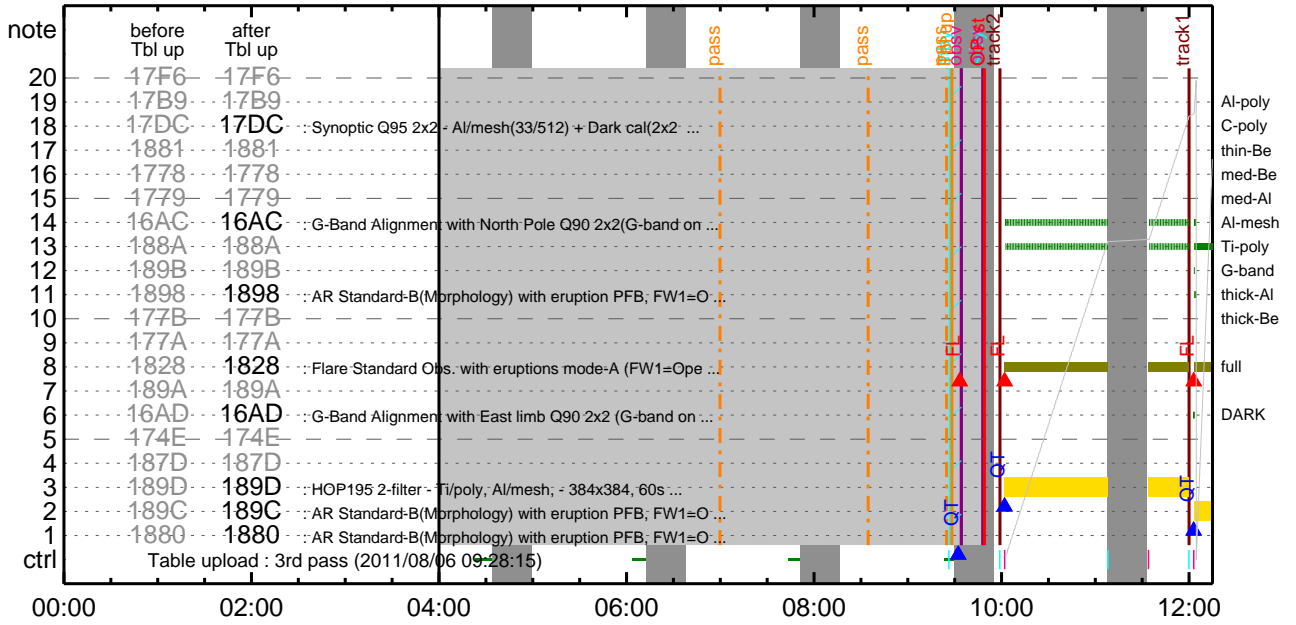
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Flare Detection

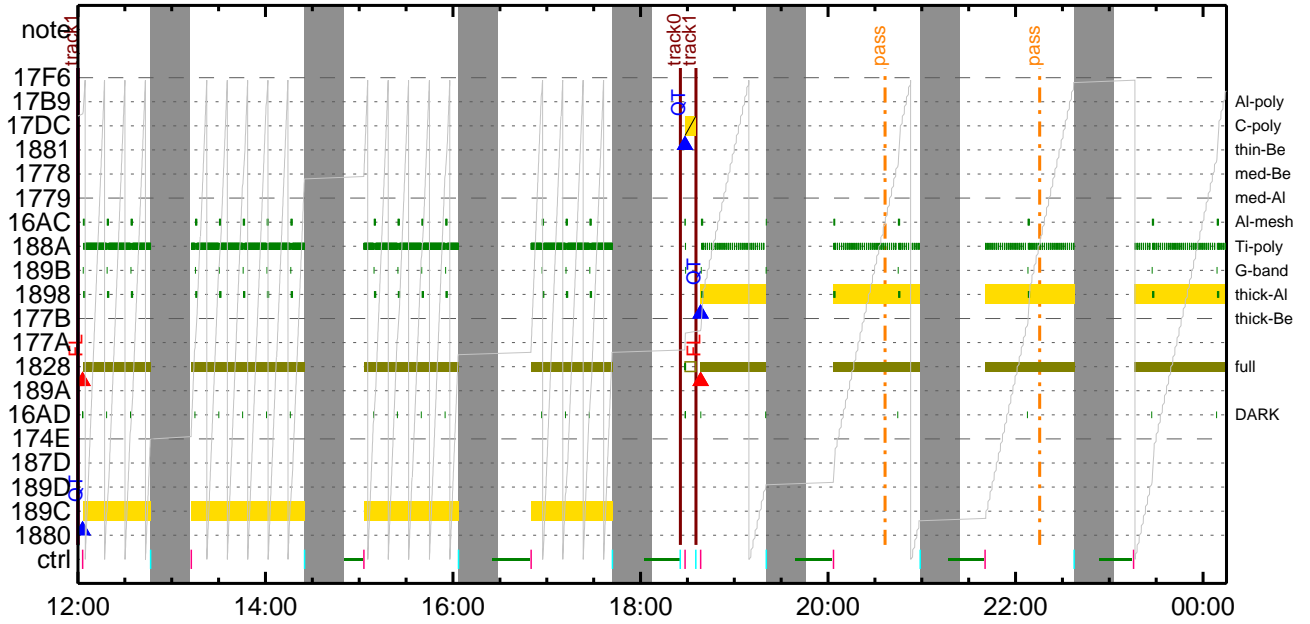
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FLD Patrol													
Term		Pointing (x, y)						Comment					
08/06 18:38:16 - 08/07 05:57:46		Track (577.3, 185.9) @ 08/06 18:35:30						# Cont.					
08/07 06:10:16 - 08/07 18:00:16		Track (651.4, 192.8) @ 08/07 06:07:30						# Cont.					
08/07 18:12:46 - 08/08 06:19:16		Track (720.2, 200.9) @ 08/07 18:10:00						# Cont.					
08/08 06:27:46 - 08/08 18:02:16		Track (780.3, 210.1) @ 08/08 06:25:00						# Cont.					
08/08 22:14:46 - 08/09 05:49:16		Track (841.7, 223.1) @ 08/08 22:12:00						* AR 11263.					
08/09 06:01:46 - 08/11 10:09:00		Track (865.0, 229.9) @ 08/09 05:59:00						# Cont.					
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50		30sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

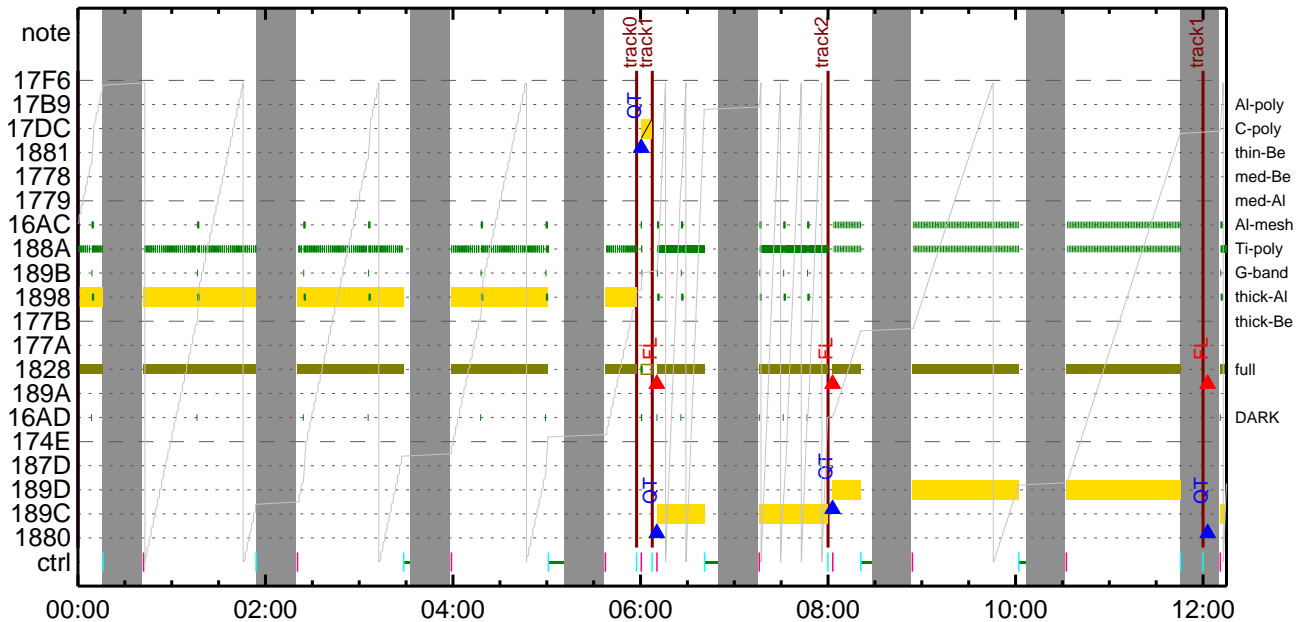
CMDI #0076 2011/08/06



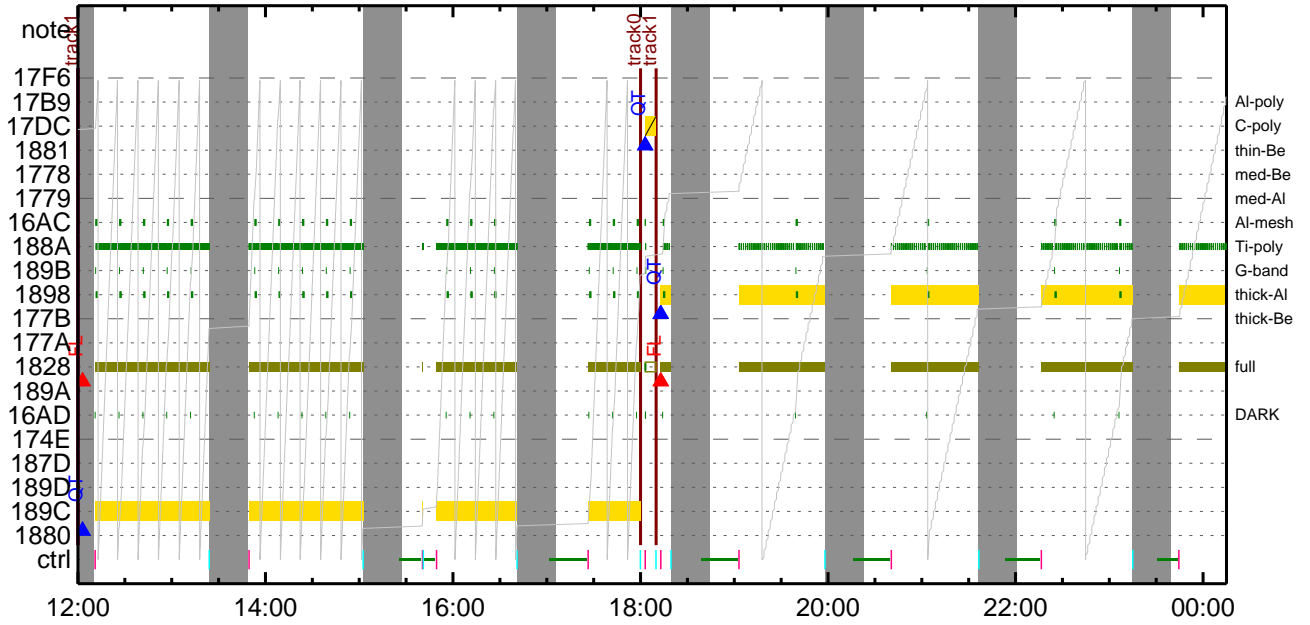
CMDI #0076 2011/08/06



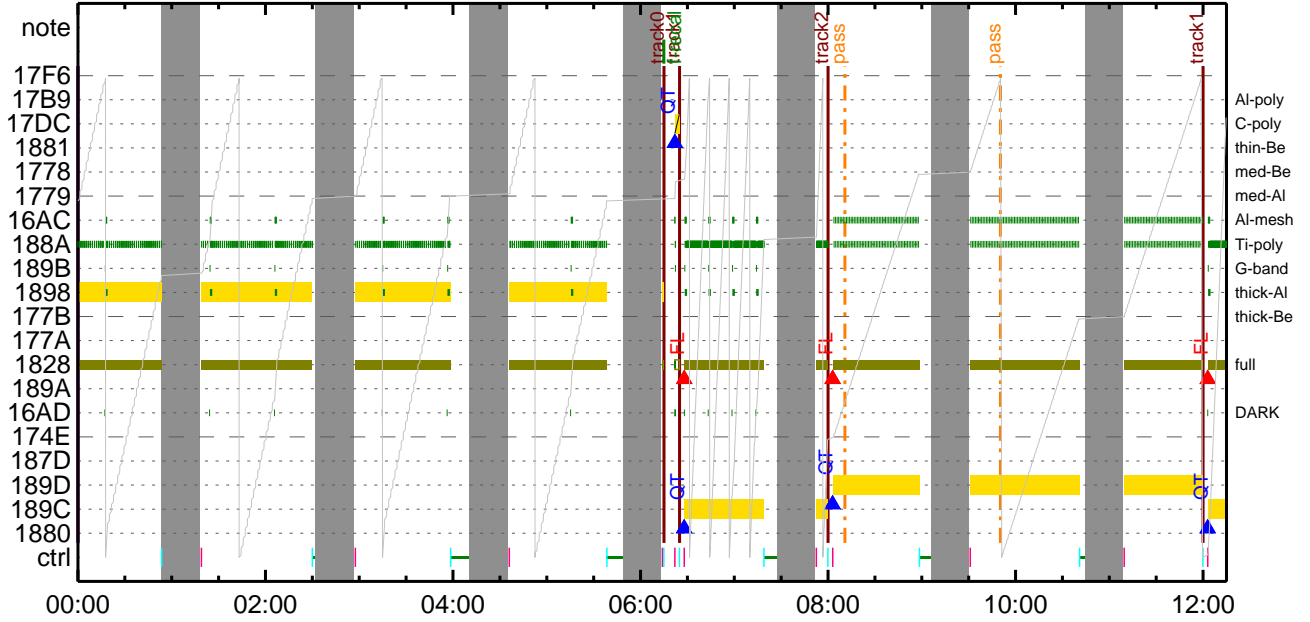
CMDI #0076 2011/08/07



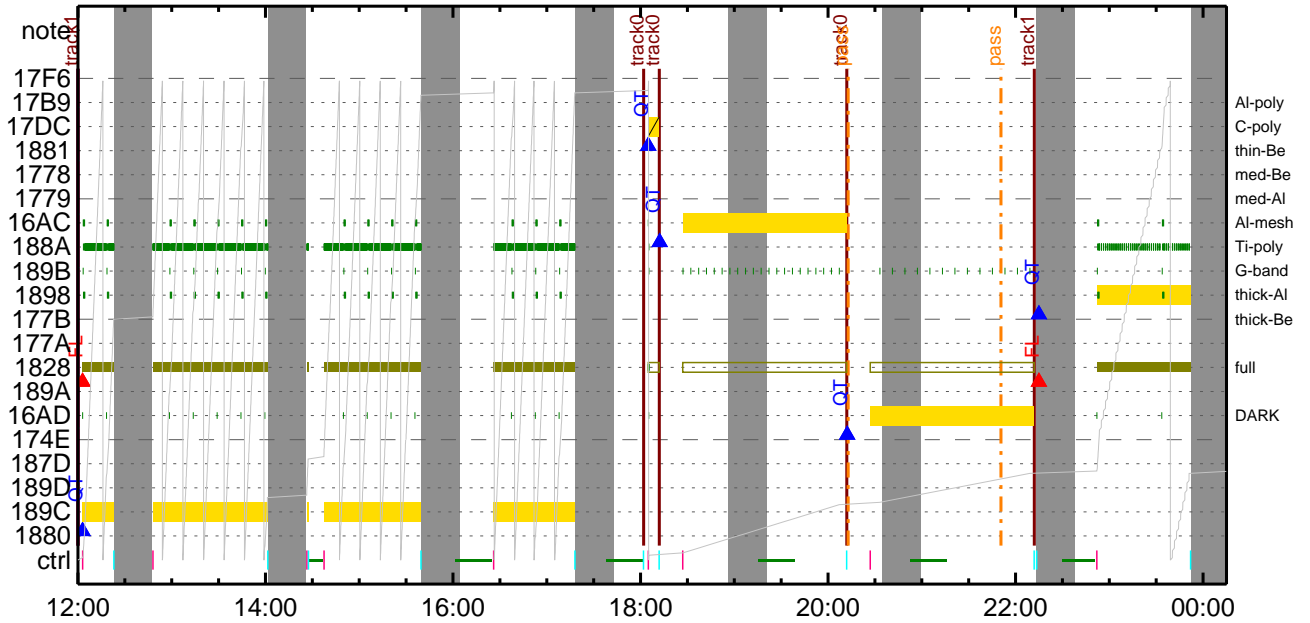
CMDI #0076 2011/08/07



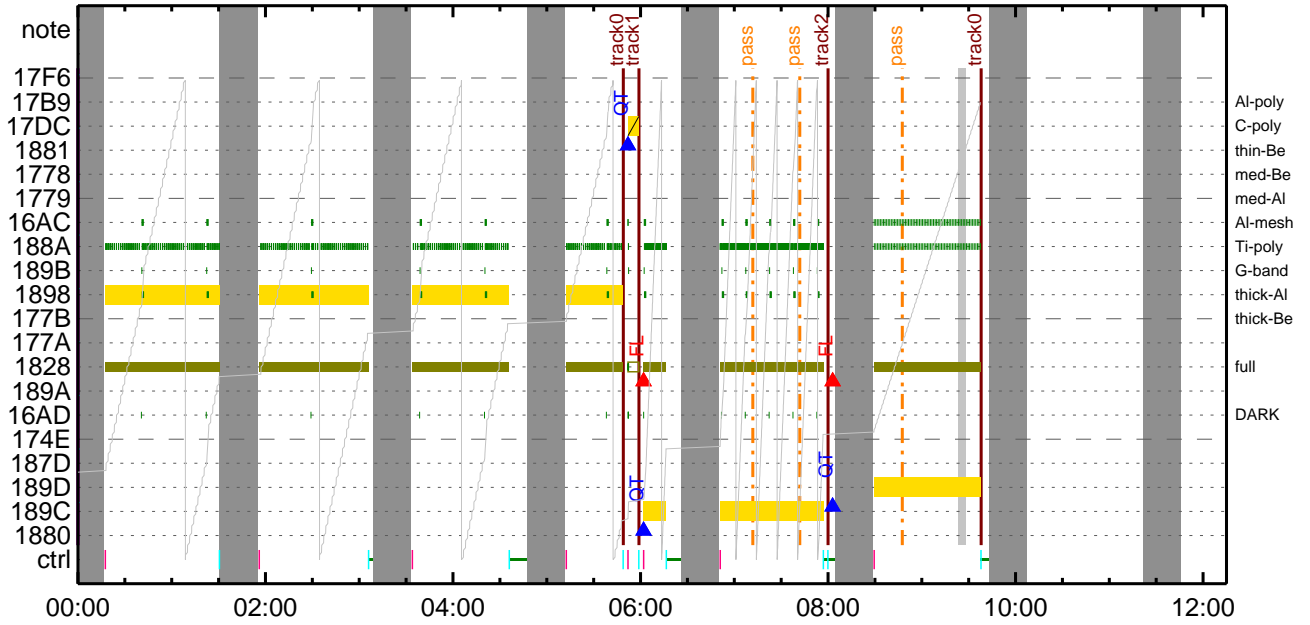
CMDI #0076 2011/08/08



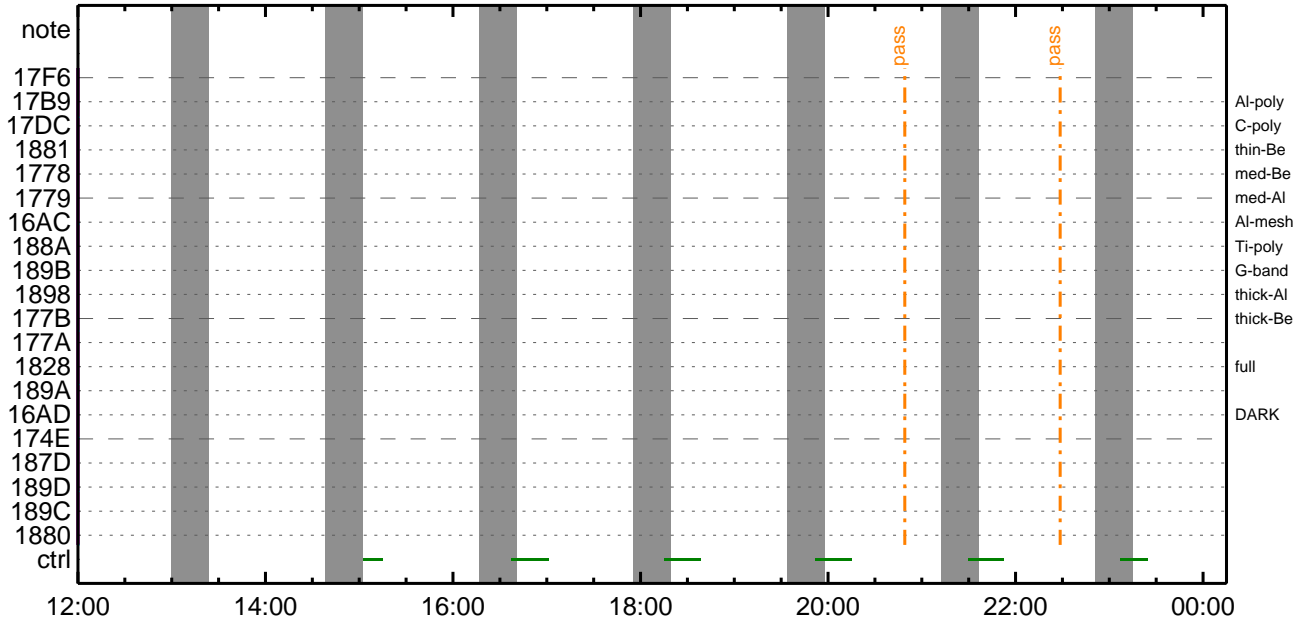
CMDI #0076 2011/08/08



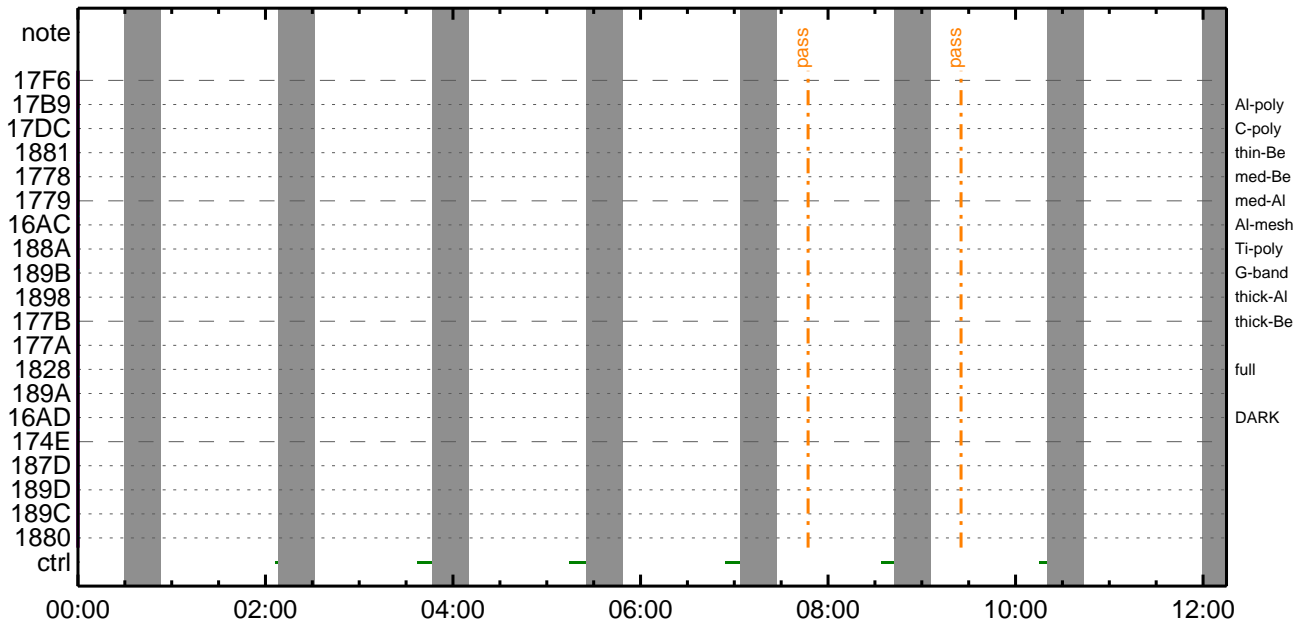
CMDI #0076 2011/08/09



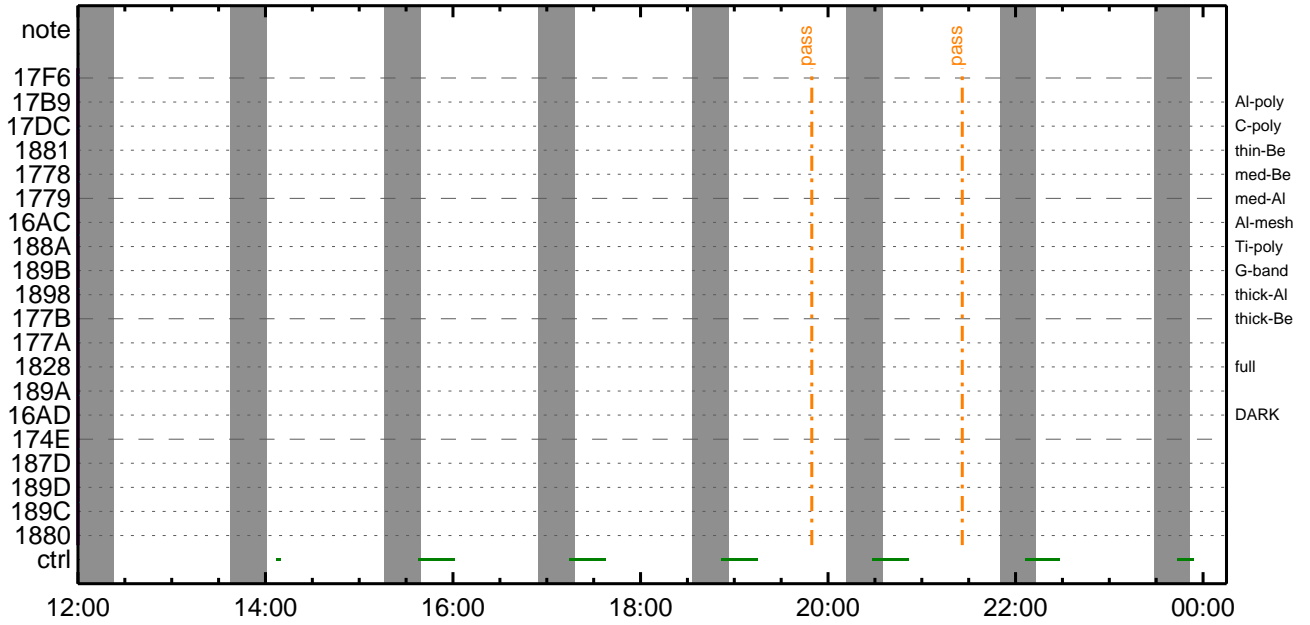
CMDI #0076 2011/08/09



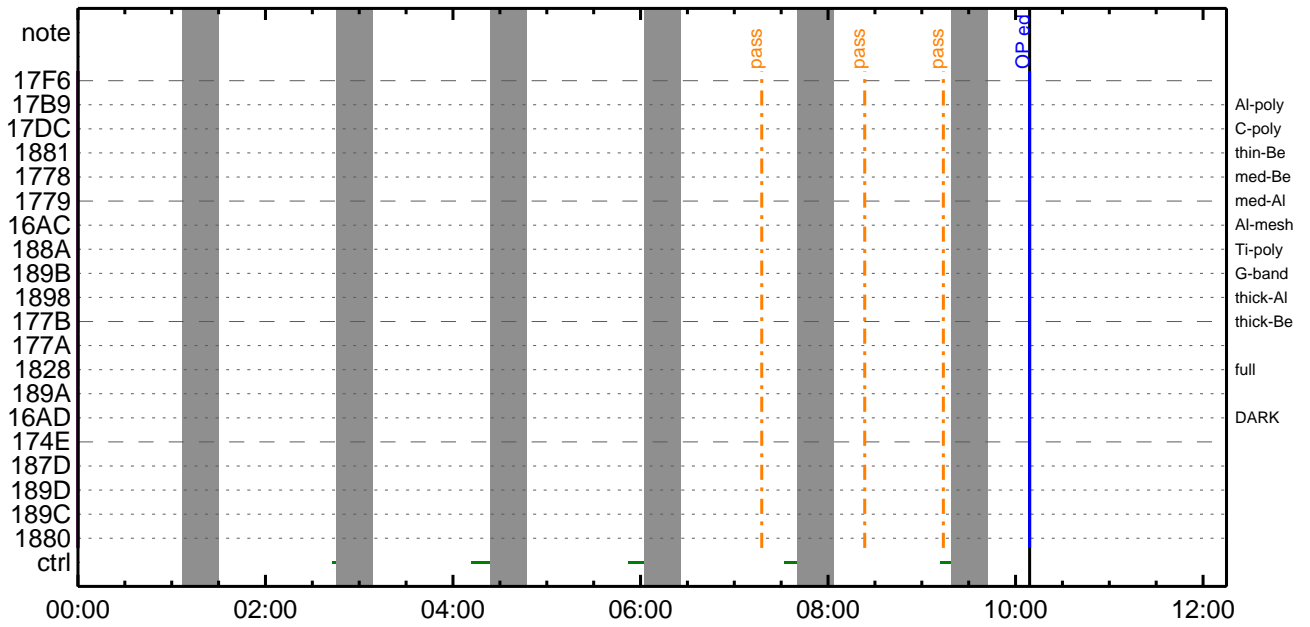
CMDI #0076 2011/08/10



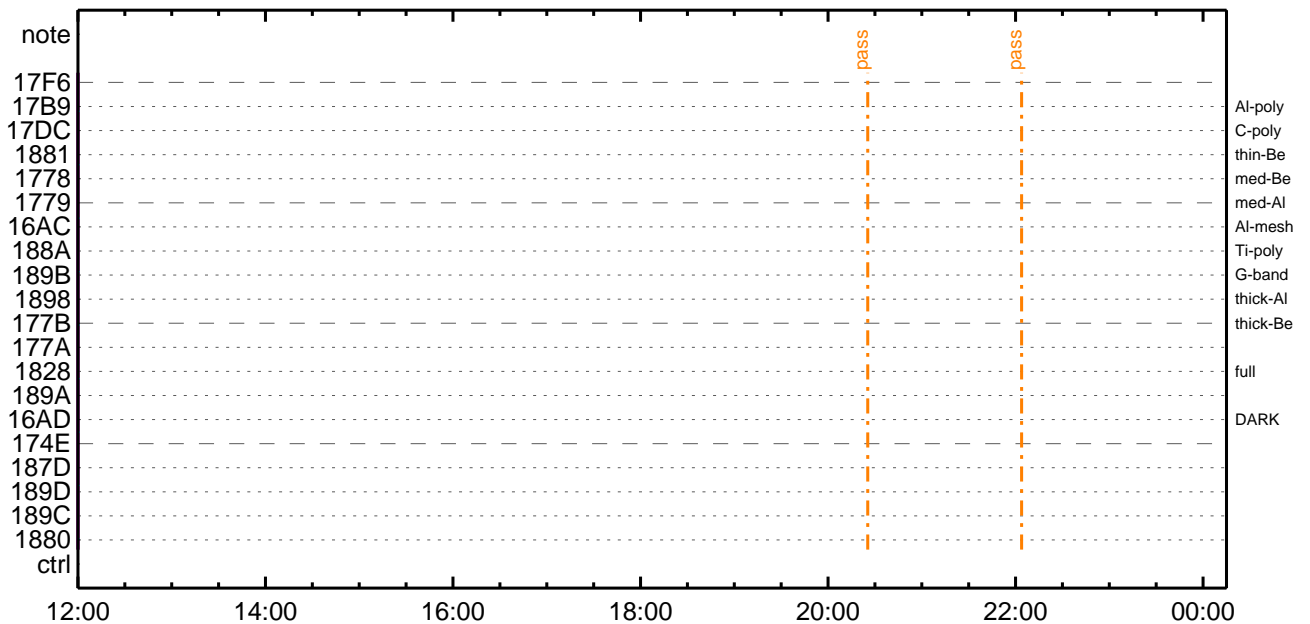
CMDI #0076 2011/08/10



CMDI #0076 2011/08/11



CMDI #0076 2011/08/11




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-242:OP
0104 ( )
0105 S. OG og-242:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶A°EÈ-ò°A÷¿@ (¼âµ-YAYOYx½ê½çòðÃÔÃæç¼ª°"òè¼i¹çòçðâ) *****
0167 C. DHUYâ;4YE;E½Y½, Y;½YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE |§ OPOG UPLOAD²-Á÷¿@NG²î½i¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½A¹Ô².²E²²²³²E;f
0180 C. ²²²¿;çSET²E²DUMP²î½±²î½Y¹²ç¹Ô²|²³²E;f
0181 C.
0182 C. TIY³Y²Y³E²òðÃDî¿(UT)
0183 +. TI 2011-08-06 09:44:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-08-06 09:44:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-08-06 09:44:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2011-08-06 09:48:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ºíÄë%ííÑºíŷÁŷ$ŷÄŷ-¹àîŰ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]             EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]             EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]          EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]          EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]           EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]          EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]             EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]             EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]            EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]          EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]             EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»º³îÇ$
0226 C.          çç[HK1_DMP_CHK_FLG]             EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLºîŷÊ¹ç•è²îOKº³îÇ$
0229 C.
0230 C. DHUŷä;¼ŷÊ;Êŷ¼ŷ.ŷî;¼ŷÊ;Ëºðîäº¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]             EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]            EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]          EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2011-08-06 09:48:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2011-08-06 09:48:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]            EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2011-08-06 09:48:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]            EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÃîºî»ò¼ŷºÈÄº¹ºèDCBC•x²è *****
0265 C. (¼ªºîŷÖŷÄŷËŷŷŷËŷáŷçŷèº¼ºº¼ª»Űº¹ºè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.İ Daily±;îÑºè´º¹ºèDCBC•x²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. îãLOSŷÄŷ$ŷÄŷ-¼ª»Ű;ä
0276 C.
0277 C. ***** LOS *****
0278 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-243 2011-08-06 13:44:03 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYŞ;¼Y³YÞYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëççãâ•µ°Æ»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ËÈÈµ•íÉ;ËÈÈ¼°ÇÖã•çç¼í¹ççí;çÀ®, ùã¹ãèãèãçÁ+ç®ã•èããã³ãÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç" °ÆÀ, Í×ËYããLOSãèççã»p´Öãð¹íí, ç. ; çÉÖÍ×ãÈXÁÖONãí¹ÖãÈçãèãã³ãÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY-¾ÖÁÖã-°ÁÁèã•çç;ç°È²¼ççí°ÆÀ, ¼èçççç¼Á¹Öã¹çç;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. ç" RESTART;ËPT1;Ëã•çççç¼í¹ççí;ç°È²¼ççí°ÆÀ¹Öã»ã°;çDCBC-150çççÈçã;ç
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÉÁÛÁÖ;ËÁ•Á°²óÈð;Ë, áãí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ç-¼«Æ°Áá»ßã•çç;ã;ç°È²¼çççç¼Á¹Öã¹çç;ç
0055 C. YçYÓYÉYÉÁÛÁÖãÁ•Á°²óÈðã-¼áçç¼í¹ççí°ÆÀ¹Öã»ã°;çDCBC-150çççÈçã;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. ç" RESTART;ËPT2;Ëã•çççç¼í¹ççí;ç°È²¼çççç¼Á¹Öã»ã°;çDCBC-151çççÈçã;ç
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÉÁÛÁÖ;ËÁ•Á°²óÈð;Ë, áãí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áá»ß;çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 []
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK []
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 []
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 11s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ()
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2011-08-06 09:48:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'úÃîî»ö¼ÝðĒĀð¹ñēDCBC•x²è *****
0153 C. (%ã°îÝÓYÁYĒYÞYĒYÁY¢Yēñ¼¼¼Ā»Ūñ¹è)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** YDY¹•İ Daily±;İŇñĒ´Øñ¹ñēDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSŸÁYŞYĀY-¼Ā»Ū;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-244 2011-08-06 13:44:03 92 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È□¿□Á□•µ°È×ÁÇ□íYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;È□È¼°Ç□□•□¿¼í¹ç□í;çÁ®, ù□¹□è□□□çÁ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 08 08)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 60 20 18)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 a0 80 18 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0f 80 80 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 10 80 80 08 08)
0054 + DC 07-F0 MDP_XRT_FLD_ENA
0055 BC (d8)
0056 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0057 BC (c8)
0058 + DC 07-F0 MDP_XRT_ARS_DIS
0059 BC (d5)
0060 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0061 BC (c4 01)
0062 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0063 BC (c5 08)
0064 . C. ----- Success Verify ? OK / NG ____
0065 C.
0066 C.
0067 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0068 C.
0069 +. DC 07-F0 MDP_XRT_MODE_OBSV
0070 BC (c2)
0071 +. TI 2011-08-06 09:48:02.0
0072 DC 07-F0 MDP_XRT_MODE_OBSV
0073 BC (c2)
0074 . C. ----- Success Verify ? OK / NG ____
0075 C.
0076 C. ***** XRT END *****
0077 C.
0078 . C. ***** MDP `úÁí□í»ö¼Y□ÈÁ□□¹□èDCBC•x²è *****
0079 C. (¼á°íYÓYÁYÈY¥YÈYáYçYÈè¼□□¼Á»Û¹□è)
0080 . S. DC-BC dcbc-402:DCBC
0081 (MDP_known_event)
0082 C.
0083 C.
0084 . C. ***** YDY¹•í Daily+¿íÑ□È´Ø□¹□èDCBC•x²è *****
0085 . S. DC-BC dcbc-153:DCBC
0086 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0087 C.
0088 C.
0089 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã
0090 C.
0091 . C. ***** LOS *****
0092 C.
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Aug 06, 11 13:44

XRT_OGLIST_0076.chk

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*** OP Sequence for XRT ***

2011/08/06	09:58:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/06	09:59:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2011/08/06	10:01:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/08/06	10:01:46.0	XRT_FLD_ENA_447_OG [0x1bf]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/06	10:01:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/06	10:01:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/08/06	10:01:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/06	10:01:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/06	10:01:56.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2011/08/06	10:01:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/08/06	10:02:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/06	11:08:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/06	11:08:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/06	11:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/06	11:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/06	11:33:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/06	11:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/06	11:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/06	12:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/08/06	12:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/08/06	12:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/06	12:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/06	12:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/08/06	12:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/06	12:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/06	12:02:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2011/08/06	12:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/08/06	12:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/06	12:46:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/06	12:46:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/06	12:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/06	12:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/06	13:11:30.5	XRT_Custom_418_OG [0x1a2]							
2011/08/06	13:12:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/06	14:25:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/06	14:25:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/06	14:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/06	14:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/06	15:02:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/06	15:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/06	16:03:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/06	16:03:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/06	16:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/06	16:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/06	16:49:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/06	16:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/06	17:42:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/06	17:42:02.0	XRT_FLD_RESET_412_OG [0x19c]							

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2011/08/06	17:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/06	17:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/06	18:25:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/06	18:25:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/06	18:25:30.0	AOCS_Orе-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/08/06	18:25:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00
2011/08/06	18:25:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/08/06	18:25:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/08/06	18:28:28.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/08/06	18:28:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2011/08/06	18:35:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/06	18:35:30.0	AOCS_Orе-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/06	18:37:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	01 00 00 00 00
2011/08/06	18:38:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/08/06	18:38:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/08/06	18:38:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/08/06	18:38:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/08/06	18:38:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/08/06	18:38:26.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/06	18:38:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2011/08/06	18:38:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/08/06	19:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/06	19:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/06	19:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/06	19:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/06	20:02:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/06	20:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/08/06	20:59:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/06	20:59:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/06	20:59:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/06	21:02:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/06	21:39:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/06	21:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/08/06	22:37:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/06	22:37:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/06	22:37:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/06	22:40:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/06	23:14:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/06	23:15:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/08/07	00:16:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/07	00:16:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/07	00:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/07	00:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/07	00:41:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/07	00:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/08/07	01:54:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/07	01:54:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/07	01:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/07			MDP_XRT_PREFLR_STRT	1	07-F0	e8

2011/08/07	01:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/07	02:19:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/07	02:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/07	03:28:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/07	03:28:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/07	03:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/07	03:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/07	03:58:00.5	XRT_Custom_418_OG [0x1a2]							
2011/08/07	03:59:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/07	05:01:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/07	05:01:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/07	05:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/07	05:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/07	05:36:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/07	05:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/07	05:57:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/07	05:57:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/08/07	05:57:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/08/07	05:57:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/08/07	05:57:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/08/07	05:57:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/07	06:00:28.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/08/07	06:00:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/07	06:07:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/07	06:07:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/08/07	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/08/07	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/07	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/07	06:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/08/07	06:10:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/07	06:10:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/07	06:10:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2011/08/07	06:10:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/08/07	06:10:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/07	06:41:00.5	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/07	06:41:02.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/07	06:41:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/07	06:44:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/07	07:15:00.5	XRT_Custom_418_OG [0x1a2]							
2011/08/07	07:16:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/07	07:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/07	08:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2011/08/07	08:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/08/07	08:02:46.0	XRT_FLD_ENA_447_OG [0x1bf]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/07	08:02:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/07	08:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/08/07	08:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/07	08:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/07	08:02:56.0	XRT_QT_PROG_SET_403_OG [0x193]							

2011/08/07	08:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/08/07	08:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/07	08:21:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/07	08:21:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/07	08:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/07	08:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/07	08:53:00.0	XRT_Custom_418_OG [0x1a2]					
2011/08/07	08:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/07	10:02:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/07	10:02:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/07	10:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/07	10:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/07	10:31:31.0	XRT_Custom_418_OG [0x1a2]					
2011/08/07	10:32:31.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/07	11:45:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/07	11:45:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/07	11:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/07	11:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/07	11:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/07	12:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00	
2011/08/07	12:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/08/07	12:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/08/07	12:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/08/07	12:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/08/07	12:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/08/07	12:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/07	12:02:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2011/08/07	12:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/08/07	12:10:00.0	XRT_Custom_418_OG [0x1a2]					
2011/08/07	12:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/07	13:24:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/07	13:24:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/07	13:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/07	13:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/07	13:48:30.0	XRT_Custom_418_OG [0x1a2]					
2011/08/07	13:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/07	15:02:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/07	15:02:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/07	15:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/07	15:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/07	15:39:30.0	XRT_Custom_418_OG [0x1a2]					
2011/08/07	15:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/07	15:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/07	15:41:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/07	15:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/07	15:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/07	15:48:30.0	XRT_Custom_418_OG [0x1a2]					
2011/08/07	15:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/07	16:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2011/08/07	16:41:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/07	16:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/07	16:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/07	17:25:30.0	XRT_Custom_418_OG [0x1a2]				
2011/08/07	17:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/07	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/07	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/08/07	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2011/08/07	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/08/07	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/08/07	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/08/07	18:02:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2011/08/07	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/07	18:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/07	18:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00
2011/08/07	18:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/08/07	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/08/07	18:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/08/07	18:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/08/07	18:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/08/07	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/07	18:12:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2011/08/07	18:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/08/07	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/07	18:19:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/07	18:19:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/07	18:19:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/07	18:22:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/07	19:02:00.0	XRT_Custom_418_OG [0x1a2]				
2011/08/07	19:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/07	19:58:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/07	19:58:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/07	19:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/07	20:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/07	20:39:30.0	XRT_Custom_418_OG [0x1a2]				
2011/08/07	20:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/07	21:36:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/07	21:36:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/07	21:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/07	21:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/07	22:15:30.0	XRT_Custom_418_OG [0x1a2]				
2011/08/07	22:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/07	23:15:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/07	23:15:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/07	23:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/07	23:18:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/07	23:43:30.0	XRT_Custom_418_OG [0x1a2]				
2011/08/07	23:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/08	00:53:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2011/08/08	00:53:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/08	00:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/08	00:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/08	01:18:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/08	01:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/08	02:30:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/08	02:30:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/08	02:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/08	02:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/08	02:56:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/08	02:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/08	03:58:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/08	03:58:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/08	03:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/08	04:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/08	04:35:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/08	04:36:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/08	05:38:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/08	05:38:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/08	05:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/08	05:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/08	06:13:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/08	06:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/08	06:14:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/08	06:14:56.0	XRT_FOCUS_RECALIBRATE_430_OG [0x1ae]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2011/08/08	06:15:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/08/08	06:18:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/08/08	06:19:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/08/08	06:19:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/08/08	06:19:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/08	06:21:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/08/08	06:22:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/08	06:24:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/08	06:25:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/08/08	06:27:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/08/08	06:27:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/08	06:27:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/08	06:27:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/08/08	06:27:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/08	06:27:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/08	06:27:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2011/08/08	06:27:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/08/08	06:28:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/08	07:19:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/08	07:19:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/08	07:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/08	07:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/08	07:51:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/08	07:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2011/08/08	07:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/08	08:00:00.0	AOCs_OrE-point_Start_1_OG [0x097] AOCU_NM	5	02-76	02 00 00 00 00
2011/08/08	08:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/08/08	08:02:46.0	XRT_FLD_ENA_447_OG [0x1bf] MDP_XRT_FLD_ENA	1	07-F0	d8
2011/08/08	08:02:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/08/08	08:02:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0
2011/08/08	08:02:52.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5
2011/08/08	08:02:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/08	08:02:56.0	XRT_QT_PROG_SET_403_OG [0x193] MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2011/08/08	08:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/08/08	08:03:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/08	08:58:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/08	08:58:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/08	08:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/08	09:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/08	09:30:01.0	XRT_Custom_418_OG [0x1a2]			
2011/08/08	09:31:01.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/08	10:41:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/08	10:41:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/08	10:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/08	10:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/08	11:08:30.0	XRT_Custom_418_OG [0x1a2]			
2011/08/08	11:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/08	11:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/08	12:00:00.0	AOCs_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	01 00 00 00 00
2011/08/08	12:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/08/08	12:02:46.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2011/08/08	12:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/08/08	12:02:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0
2011/08/08	12:02:52.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5
2011/08/08	12:02:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/08	12:02:56.0	XRT_QT_PROG_SET_420_OG [0x1a4] MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2011/08/08	12:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/08/08	12:03:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/08	12:23:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/08	12:23:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/08	12:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/08	12:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/08	12:47:00.0	XRT_Custom_418_OG [0x1a2]			
2011/08/08	12:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/08	14:01:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/08	14:01:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/08	14:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/08	14:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/08	14:25:30.0	XRT_Custom_418_OG [0x1a2]			
2011/08/08	14:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/08	14:27:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/08	14:27:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/08	14:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			

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2011/08/08	14:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/08	14:36:30.0	XRT_Custom_418_OG [0x1a2]								
2011/08/08	14:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/08	15:39:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/08	15:39:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/08	15:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/08	15:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/08	16:25:00.0	XRT_Custom_418_OG [0x1a2]								
2011/08/08	16:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/08	17:18:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/08	17:18:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/08	17:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/08	17:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/08	18:01:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/08	18:01:56.0	XRT_FOCUS_POSITION_401_OG [0x191]								
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/08/08	18:02:00.0	AOCS_Ore-point_Start_3_OG [0x099]								
			AOCU_NM	5	02-76	00 00 00 00 00				
2011/08/08	18:02:16.0	XRT_FLD_DIS_402_OG [0x192]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/08/08	18:02:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/08/08	18:02:20.0	XRT_ARS_DIS_438_OG [0x1b6]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/08	18:04:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/08/08	18:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/08	18:11:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/08	18:11:56.0	XRT_FOCUS_POSITION_445_OG [0x1bd]								
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/08/08	18:12:00.0	AOCS_Ore-point_Start_4_OG [0x09a]								
			AOCU_NM	5	02-76	00 ac 00 00 00				
2011/08/08	18:12:16.0	XRT_QT_PROG_SET_429_OG [0x1ad]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2011/08/08	18:12:18.0	XRT_FLD_DIS_423_OG [0x1a7]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/08/08	18:12:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/08/08	18:12:22.0	XRT_ARS_DIS_437_OG [0x1b5]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/08	18:27:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/08	20:11:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/08	20:11:56.0	XRT_FOCUS_POSITION_445_OG [0x1bd]								
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/08/08	20:12:00.0	AOCS_Ore-point_Start_5_OG [0x09b]								
			AOCU_NM	5	02-76	00 00 00 54 00				
2011/08/08	20:12:16.0	XRT_QT_PROG_SET_432_OG [0x1b0]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2011/08/08	20:12:18.0	XRT_FLD_DIS_423_OG [0x1a7]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/08/08	20:12:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/08/08	20:12:22.0	XRT_ARS_DIS_437_OG [0x1b5]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/08	20:27:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/08	22:11:54.0	XRT_CTRL_MANU_439_OG [0x1b7]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/08	22:12:00.0	AOCS_Ore-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	01 00 00 00 00				
2011/08/08	22:13:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/08	22:13:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/08	22:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/08	22:14:26.0	XRT_FOCUS_POSITION_409_OG [0x199]								
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/08/08	22:14:46.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/08	22:14:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/08	22:14:50.0	XRT_AEC_RESET_443_OG [0x1bb]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/08/08	22:14:52.0	XRT_ARS_DIS_431_OG [0x1af]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				

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2011/08/08	22:14:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/08	22:14:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b	
2011/08/08	22:14:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08	
2011/08/08	22:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/08	22:51:00.0	XRT_Custom_418_OG [0x1a2]						
2011/08/08	22:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/08	23:52:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/08	23:52:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/08	23:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/08	23:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/09	00:16:30.0	XRT_Custom_418_OG [0x1a2]						
2011/08/09	00:17:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/09	01:30:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/09	01:30:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/09	01:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/09	01:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/09	01:55:00.0	XRT_Custom_418_OG [0x1a2]						
2011/08/09	01:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/09	03:06:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/09	03:06:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/09	03:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/09	03:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/09	03:33:00.0	XRT_Custom_418_OG [0x1a2]						
2011/08/09	03:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/09	04:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/09	04:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/09	04:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/09	04:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/09	05:11:30.0	XRT_Custom_418_OG [0x1a2]						
2011/08/09	05:12:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/09	05:48:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/09	05:48:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2011/08/09	05:49:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00 00 00 00	
2011/08/09	05:49:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/08/09	05:49:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/08/09	05:49:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/08/09	05:51:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	
2011/08/09	05:52:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/09	05:58:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/09	05:59:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01	00 00 00 00	
2011/08/09	06:01:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2011/08/09	06:01:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/08/09	06:01:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/08/09	06:01:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/08/09	06:01:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/08/09	06:01:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/09	06:01:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02	
2011/08/09	06:01:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08	
2011/08/09	06:02:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		

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2011/08/09	06:16:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/09	06:16:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/09	06:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/09	06:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/09	06:50:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/09	06:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/09	07:57:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/09	07:57:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/09	07:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/09	07:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/09	08:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2011/08/09	08:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/09	08:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/08/09	08:02:46.0	XRT_FLD_ENA_447_OG [0x1bf]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/09	08:02:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/09	08:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/08/09	08:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/09	08:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/09	08:02:56.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2011/08/09	08:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/08/09	08:28:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/09	08:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/09	09:37:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/09	09:38:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				