

XRT Timeline to be uploaded on 2011/08/09

Period: 2011/08/09 09:28:00 - 2011/08/13 10:51:00

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Normal mode

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Term	Pointing (x, y)	Comment
08/09 09:41:00 - 08/09 10:06:54	Fixed (0.0, 0.0)	# OP start + 10min: HOP 130
08/09 10:10:00 - 08/09 10:22:54	Fixed (936.0, 0.0)	HOP 130 - 01/15
08/09 10:26:00 - 08/09 10:38:54	Fixed (468.0, 0.0)	02/15
08/09 10:42:00 - 08/09 10:54:54	Fixed (0.0, 0.0)	03/15
08/09 10:58:00 - 08/09 11:21:30	Fixed (-468.0, 0.0)	04/15
08/09 11:48:30 - 08/09 12:01:24	Fixed (-936.0, 0.0)	05/15
08/09 12:04:30 - 08/09 12:11:24	Fixed (815.0, 470.0)	06/15
08/09 12:14:30 - 08/09 12:27:24	Fixed (411.0, 847.0)	07/15
08/09 12:30:30 - 08/09 12:43:24	Fixed (0.0, 941.0)	08/15
08/09 12:46:30 - 08/09 13:00:00	Fixed (-411.0, 847.0)	09/15
08/09 13:27:00 - 08/09 13:33:54	Fixed (-815.0, 470.0)	10/15
08/09 13:37:00 - 08/09 13:43:54	Fixed (815.0, -470.0)	11/15
08/09 13:47:00 - 08/09 13:59:54	Fixed (411.0, -847.0)	12/15
08/09 14:03:00 - 08/09 14:15:54	Fixed (0.0, -941.0)	13/15
08/09 14:19:00 - 08/09 15:20:54	Fixed (-411.0, -847.0)	14/15
08/09 15:24:00 - 08/09 15:29:54	Fixed (-815.0, -471.0)	15/15

PROG= 13 Inf.-time(s)

Subr= 1	1-time(s)	120.0sec
Seqn= 66	1-time(s)	4.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Open/Al-mesh	Open/thick-Al close	Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #189C: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 15sec-cad

Term	Pointing (x, y)	Comment
08/09 15:33:00 - 08/09 17:45:24	Fixed (877.0, 238.5)	AR 11263 with fixed pointing
08/09 18:39:30 - 08/10 05:59:54	Fixed (877.0, 238.5)	# AR 11263 with fixed pointing

PROG= 02 Inf.-time(s)

Subr= 2	1-time(s)	2.0sec
Seqn= 19	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 41	4-time(s)	2.0sec
Open/Al-mesh	Open/Ti-poly close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Subr= 1	1-time(s)	2.0sec
Seqn= 13	55-time(s)	15.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 1 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 2x2 512x512 (1064, 1048) Q=95 2 2 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 3 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #17DC: Synoptic Q95 2x2 - Al/mesh(33/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1024) + G-band(12)

Term	Pointing (x, y)	Comment
08/09 17:48:30 - 08/09 17:55:24	Fixed (0.0, 0.0)	synoptic, shifted -14.5 min
08/10 06:03:00 - 08/10 07:59:54	Fixed (0.0, 0.0)	synoptic + stay disc center for SOT warming up
08/10 16:43:00 - 08/10 16:54:30	Fixed (0.0, 0.0)	Synoptic
08/11 07:23:00 - 08/11 07:29:54	Fixed (0.0, 0.0)	synoptic, shifted 28.0 min

PROG= 18 1-time(s)

Subr= 1	1-time(s)	12.0sec
Seqn= 20	1-time(s)	4.0sec
Open/Al-mesh	Open/Ti-poly close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 31	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 77	1-time(s)	4.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 63	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1898: AR Standard-B(Morphology) with eruption PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad

Term	Pointing (x, y)	Comment
08/10 08:03:00 - 08/10 11:59:00	Track (150.0, 183.9) © 08/10 08:00:00	# HOP195 with La Palma at AR 11268

08/10 12:23:30 - 08/10 16:39:54 Fixed (877.0, 238.5) * AR 11163 at limb, for EIS scan.
 08/11 08:05:00 - 08/11 10:18:54 Track (345.2, 189.2) ^{08/11 07:30:00} HOP 195 with La Palma at AR 11268

PROG= 11 Inf.-time(s)

Subr= 2		1-time(s)		2.0sec											
Seqn= 19		1-time(s)		2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec		
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec		
Seqn= 41		4-time(s)		2.0sec											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
Subr= 1		1-time(s)		2.0sec											
Seqn= 13		30-time(s)		80.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	1	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	512x512 (1064, 1048)	Q=95	2	2	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

XOB #189D: HOP195 2-filter - Ti/poly, Al/mesh, - 384x384, 60sec cad

Term	Pointing (x, y)	Comment
08/10 17:39:00 - 08/10 20:59:54	Track (-513.9, -27.0) ^{08/10 17:00:00}	HOP 196 - coronal hole tracking
08/10 21:03:00 - 08/11 07:19:54	Track (783.1, -382.3) ^{08/10 21:00:00}	# track old AR

PROG= 03 Inf.-time(s)

Subr= 2		1-time(s)		2.0sec											
Seqn= 58		1-time(s)		60.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec		
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
08/09 09:41:00 - 08/09 10:06:54	Fixed (0.0, 0.0)	# OP start + 10min: HOP 130
08/09 10:10:00 - 08/09 10:22:54	Fixed (936.0, 0.0)	HOP 130 - 01/15
08/09 10:26:00 - 08/09 10:38:54	Fixed (468.0, 0.0)	02/15
08/09 10:42:00 - 08/09 10:54:54	Fixed (0.0, 0.0)	03/15
08/09 10:58:00 - 08/09 11:21:30	Fixed (-468.0, 0.0)	04/15
08/09 11:48:30 - 08/09 12:01:24	Fixed (-936.0, 0.0)	05/15
08/09 12:04:30 - 08/09 12:11:24	Fixed (815.0, 470.0)	06/15
08/09 12:14:30 - 08/09 12:27:24	Fixed (411.0, 847.0)	07/15
08/09 12:30:30 - 08/09 12:43:24	Fixed (0.0, 941.0)	08/15
08/09 12:46:30 - 08/09 13:00:00	Fixed (-411.0, 847.0)	09/15
08/09 13:27:00 - 08/09 13:33:54	Fixed (-815.0, 470.0)	10/15
08/09 13:37:00 - 08/09 13:43:54	Fixed (815.0, -470.0)	11/15
08/09 13:47:00 - 08/09 13:59:54	Fixed (411.0, -847.0)	12/15
08/09 14:03:00 - 08/09 14:15:54	Fixed (0.0, -941.0)	13/15
08/09 14:19:00 - 08/09 15:20:54	Fixed (-411.0, -847.0)	14/15
08/09 15:24:00 - 08/09 15:29:54	Fixed (-815.0, -471.0)	15/15
08/09 15:33:00 - 08/09 17:45:24	Fixed (877.0, 238.5)	AR 11263 with fixed pointing
08/09 18:39:30 - 08/10 05:59:54	Fixed (877.0, 238.5)	# AR 11263 with fixed pointing
08/10 08:03:00 - 08/10 11:59:00	Track (150.0, 183.9) ^{08/10 08:00:00}	# HOP195 with La Palma at AR 11268
08/10 12:23:30 - 08/10 16:39:54	Fixed (877.0, 238.5)	* AR 11163 at limb, for EIS scan.
08/10 17:39:00 - 08/10 20:59:54	Track (-513.9, -27.0) ^{08/10 17:00:00}	HOP 196 - coronal hole tracking
08/10 21:03:00 - 08/11 07:19:54	Track (783.1, -382.3) ^{08/10 21:00:00}	# track old AR
08/11 08:05:00 - 08/11 10:18:54	Track (345.2, 189.2) ^{08/11 07:30:00}	HOP 195 with La Palma at AR 11268

PROG= 08 1-time(s)

Subr= 1		30-time(s)		20.0sec											
Seqn= 87		1-time(s)		2.0sec											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 60		1-time(s)		2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
Seqn= 90		1-time(s)		2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
Subr= 3		30-time(s)		60.0sec											
Seqn= 87		1-time(s)		2.0sec											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 88		1-time(s)		2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											

└─ Seqn= 90		1-time(s)		2.0sec											
┌	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
┌	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
┌	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
Subr= 3		30-time(s)		60.0sec											
└─ Seqn= 87		1-time(s)		2.0sec											
┌	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
┌	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
└─ Seqn= 88		1-time(s)		2.0sec											
┌	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
┌	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
└─ Seqn= 90		1-time(s)		2.0sec											
┌	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
┌	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
┌	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
Subr= 3		30-time(s)		60.0sec											
└─ Seqn= 87		1-time(s)		2.0sec											
┌	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
┌	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
└─ Seqn= 88		1-time(s)		2.0sec											
┌	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
┌	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Subr= 4		24-time(s)		600.0sec											
└─ Seqn= 89		1-time(s)		2.0sec											
┌	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec		
┌	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec		
┌	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
┌	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

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Active Region Search

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NOT USED

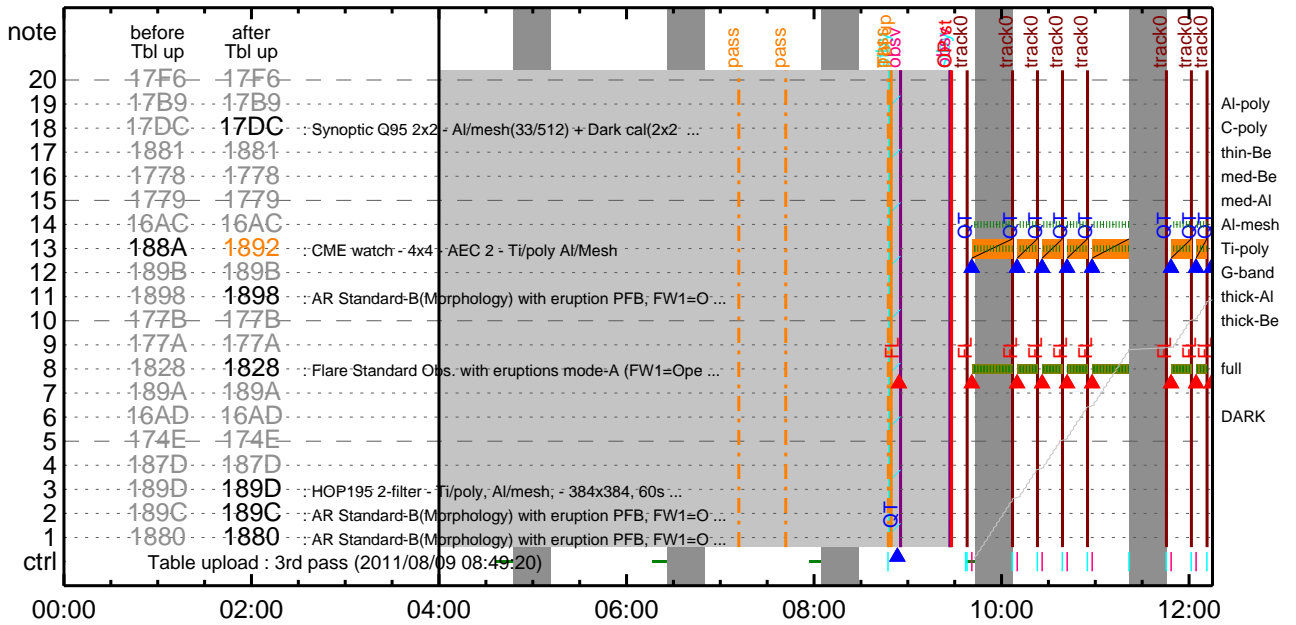
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Flare Detection

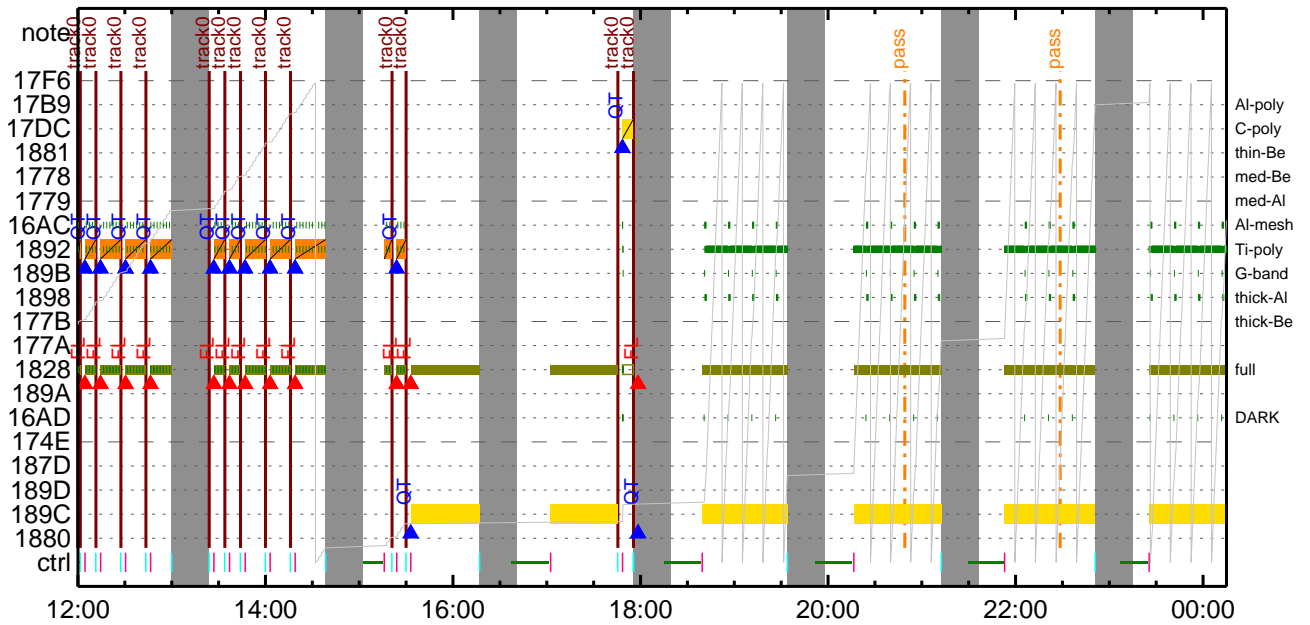
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FLD Patrol												
Term		Pointing (x, y)					Comment					
08/09 17:58:16 - 08/10 06:00:16		Fixed (877.0, 238.5)					# AR 11263 with fixed pointing					
08/10 08:02:46 - 08/10 16:40:16		Track (150.0, 183.9) @ 08/10 08:00:00					# HOP195 with La Palma at AR 11268					
08/10 17:02:46 - 08/11 07:20:16		Track (-513.9, -27.0) @ 08/10 17:00:00					HOP 196 - coronal hole tracking					
08/11 07:32:46 - 08/13 10:51:00		Track (345.2, 189.2) @ 08/11 07:30:00					HOP 195 with La Palma at AR 11268					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50				30sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

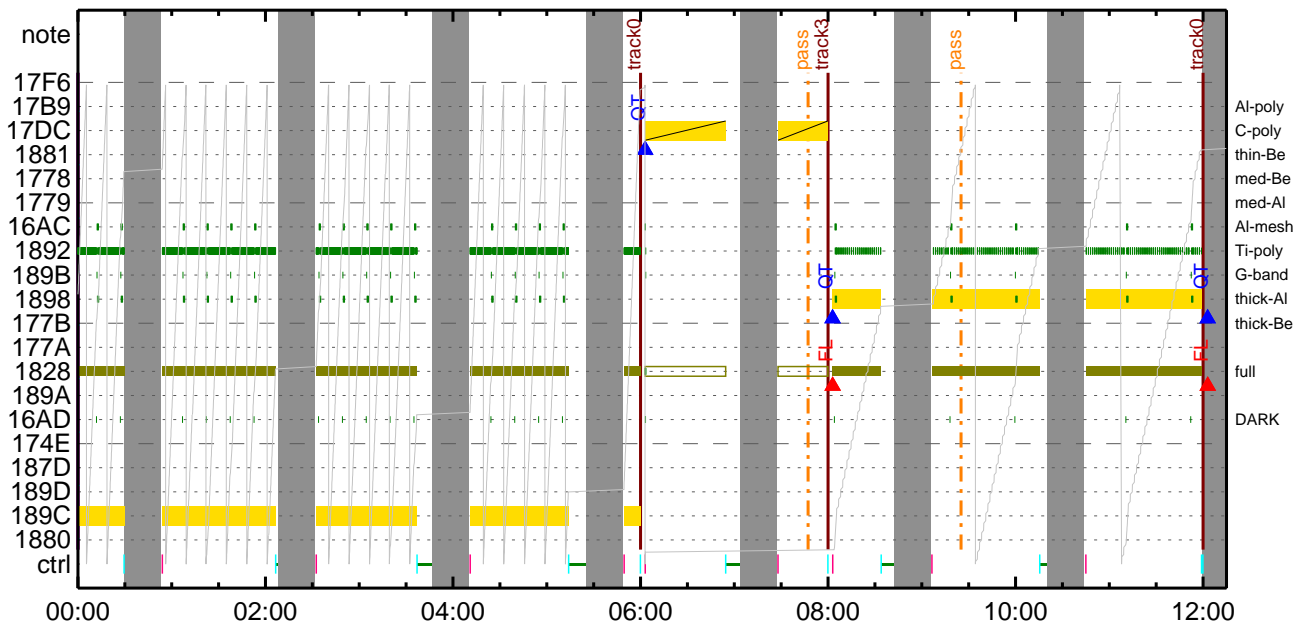
CMDI #0080 2011/08/09



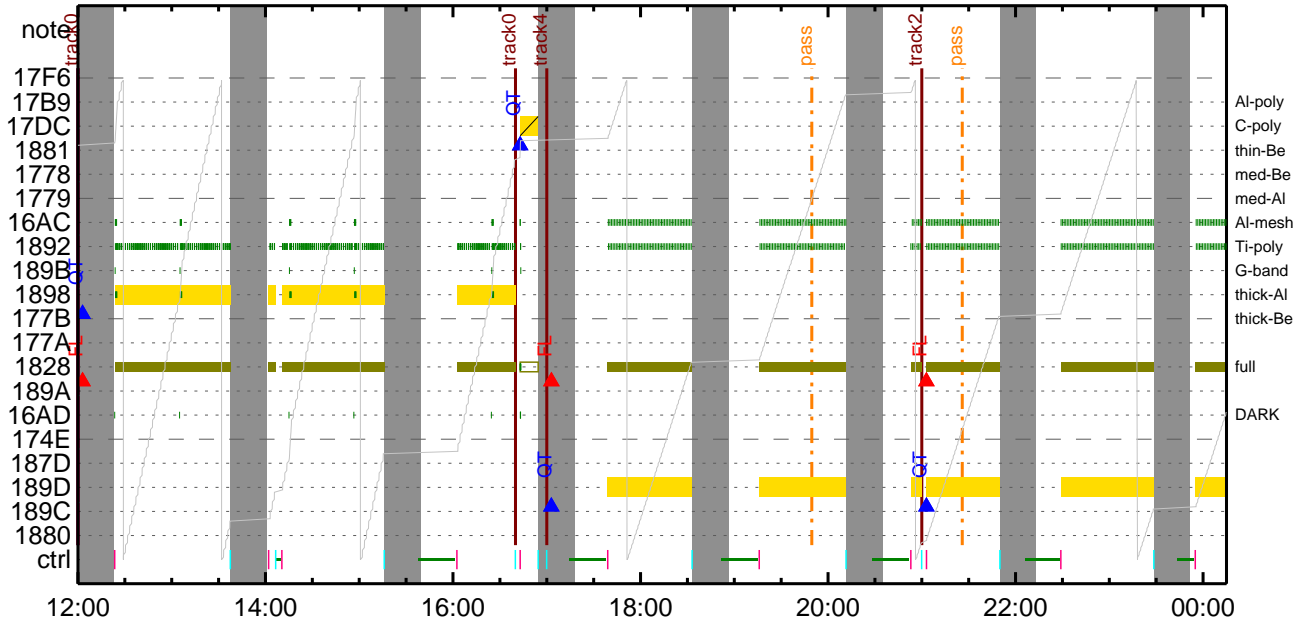
CMDI #0080 2011/08/09



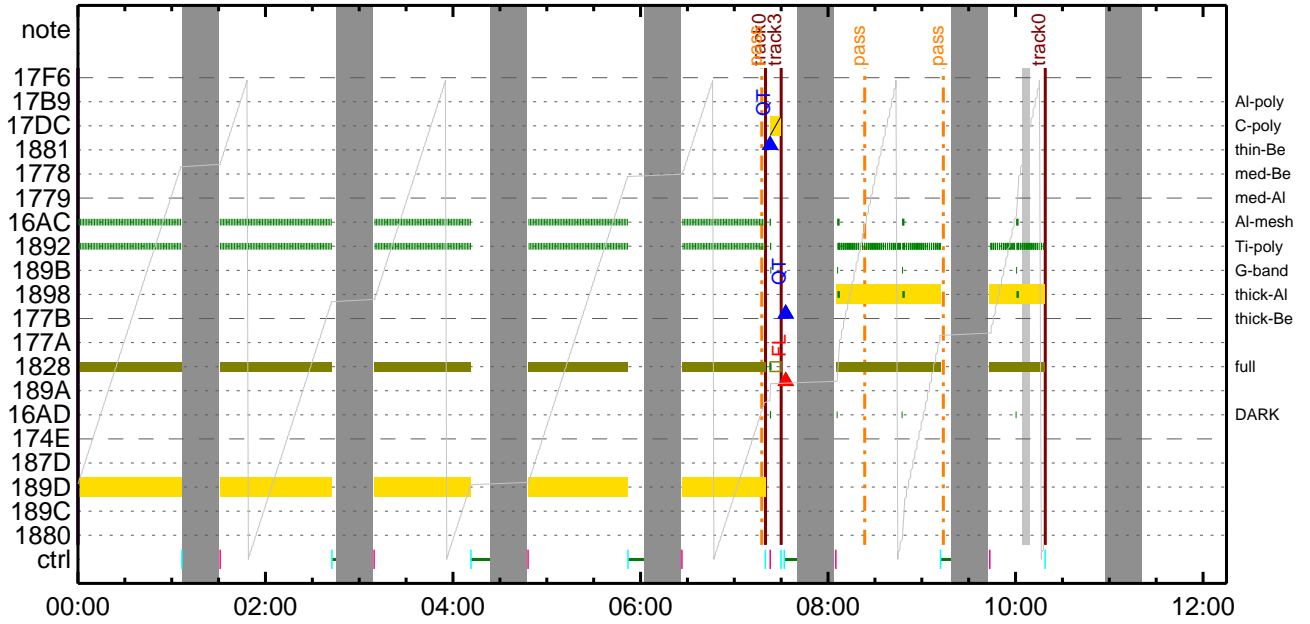
CMDI #0080 2011/08/10



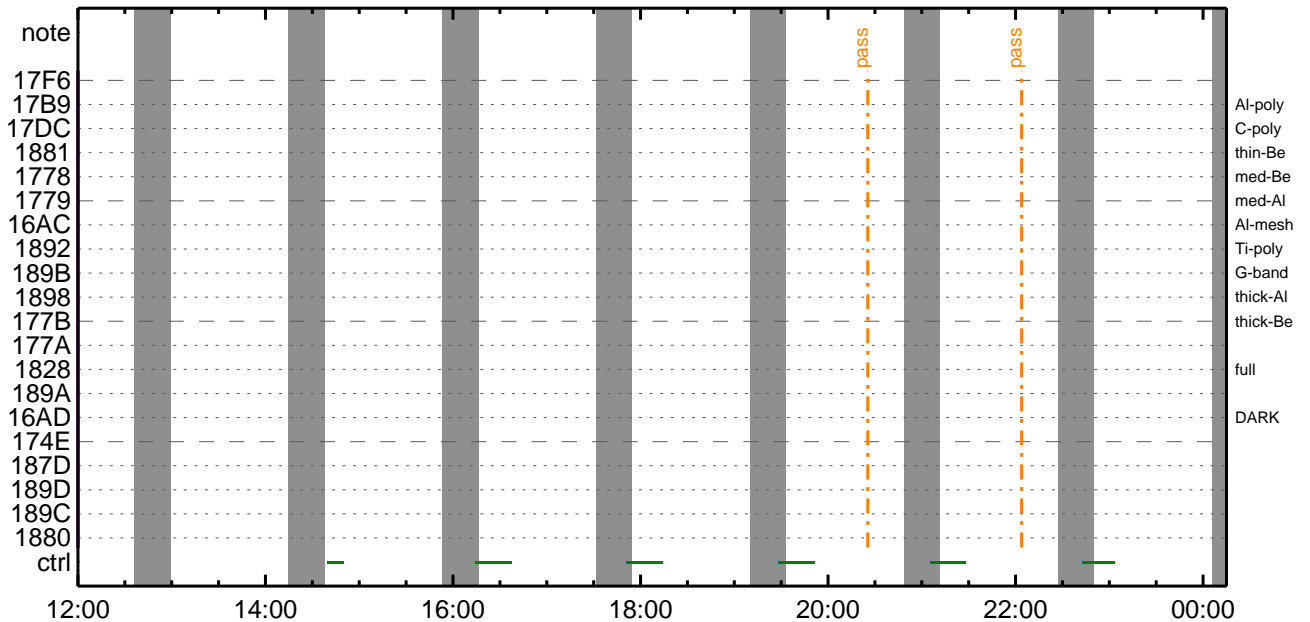
CMDI #0080 2011/08/10



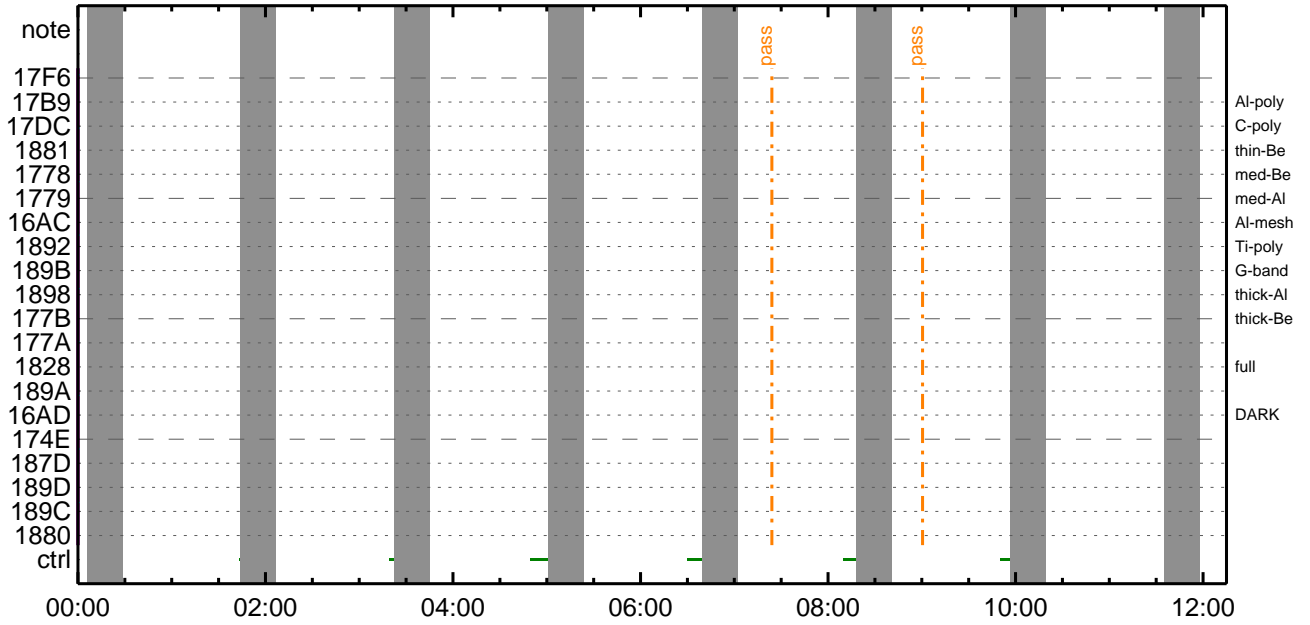
CMDI #0080 2011/08/11



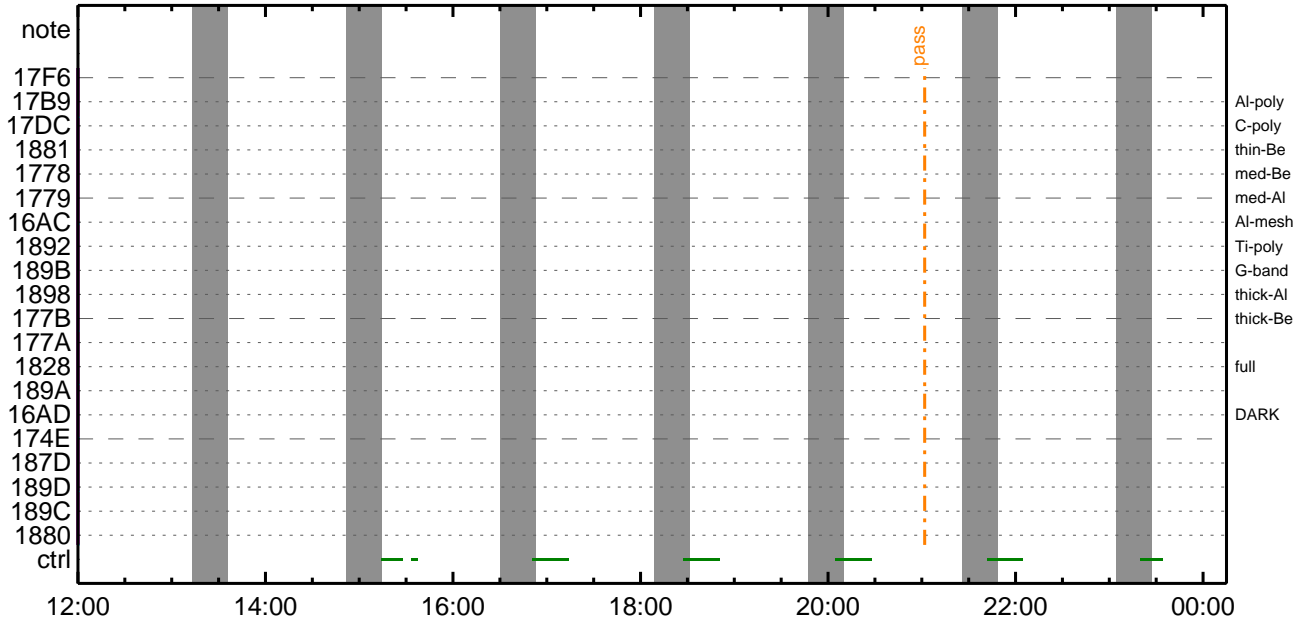
CMDI #0080 2011/08/11



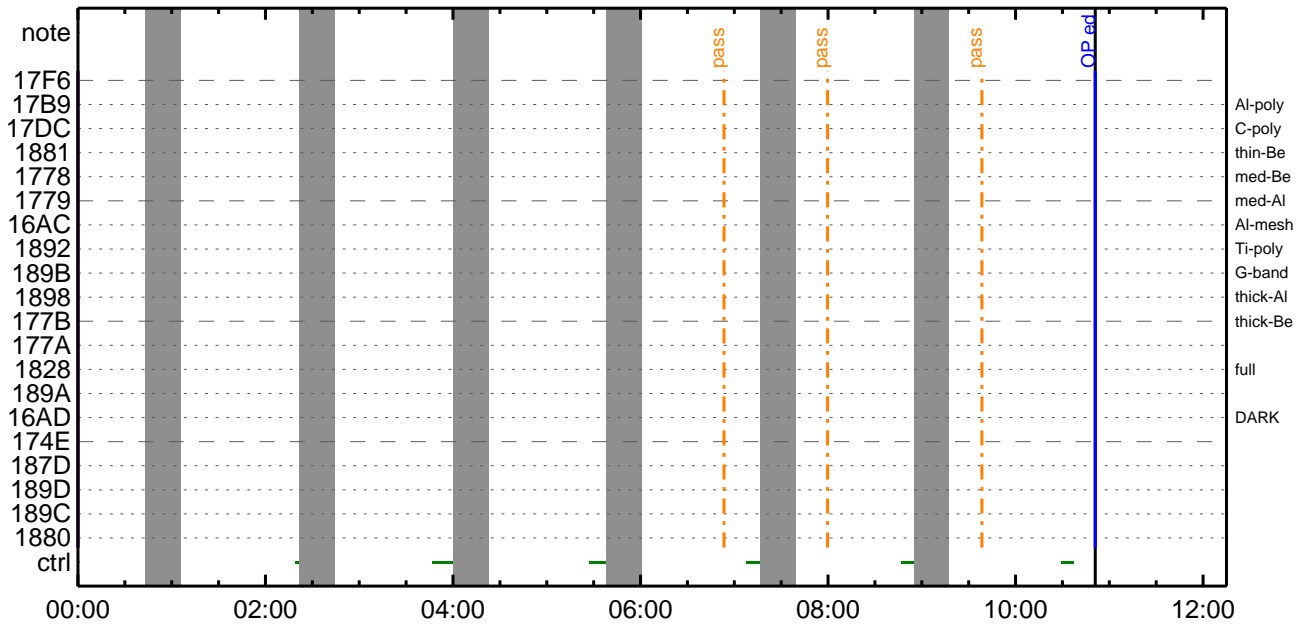
CMDI #0080 2011/08/12



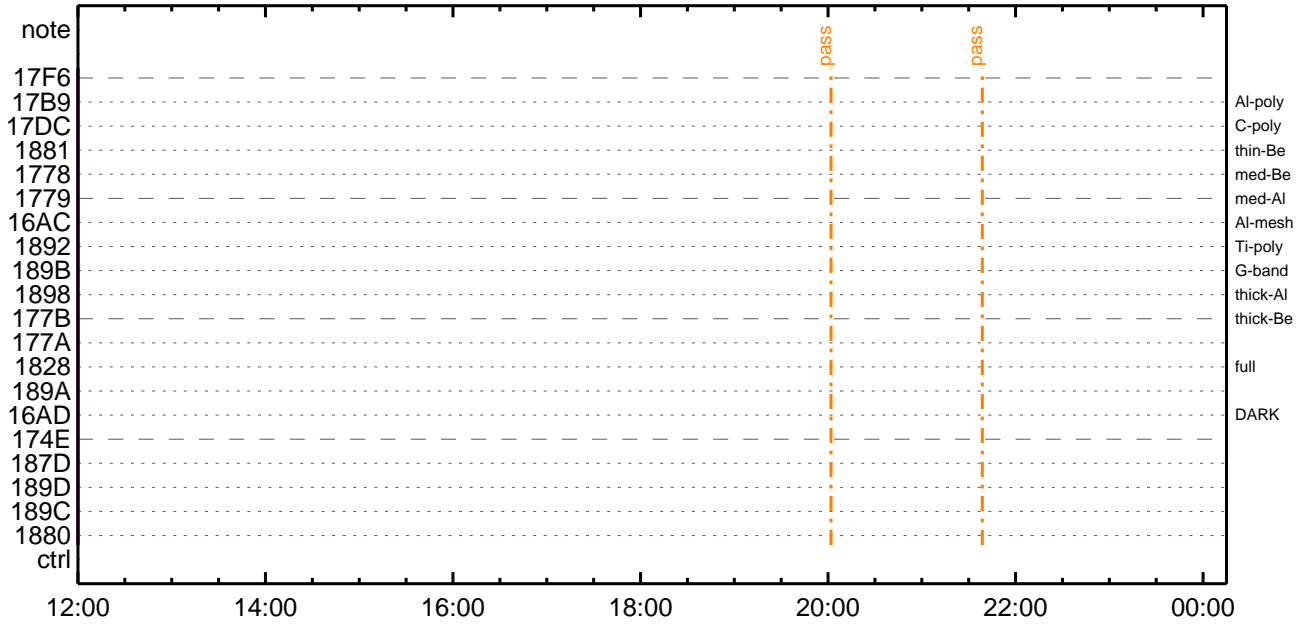
CMDI #0080 2011/08/12



CMDI #0080 2011/08/13



CMDI #0080 2011/08/13




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;YÉ;YAYÓYx
0100 C. *****
0101 C.
0102 . C. ;äOP/OGY1;YÉ;ä
0103 . S. OP op-246:OP
0104 ( )
0105 . S. OG og-246:OG
0106 ( )
0107 C.
0108 . C. ;äNMOG&OPîî°èYAYÓYx;ä
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAYÓYx½ªî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOGðîî½ª¹ç•è²îOKð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAYÓYx½ªªî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOGðîî½ª¹ç•è²îOKð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAYÓYx½ªªî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OPðîî½ª¹ç•è²îOKð³îÇ§
0165 C.
0166 . C. ***** òÊ²¼ðîî½ª¹ç•è²îOKð³îÇ§ *****
0167 C. DHUYâ;YÉ;Ê½Y½;Yî;YÉ;Êðîî½ª¹ç
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOADð-Á÷ç@NGðîî½ª¹ç;ç°Ê²¼ðîî½ª¹ç@ðîî½ª¹çð³îÇ§;f
0180 C. ð³îÇ§;çSETðEDUMPðîî½ª¹çY¹ç¹ç¹çð³îÇ§;f
0181 C.
0182 . C. TIY³YþYÓYÉðððÁDîç(UT)
0183 +. TI 2011-08-09 09:23:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-08-09 09:23:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-08-09 09:23:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
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0194 C.
0195 +. TI 2011-08-09 09:27:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼õİÄê%İİñõİŷÄŷ§ŷÄŷ-¹àİÜ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                      EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                     EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                    EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                       EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªİ»õ³İÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]                       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLõİ¼Ê¹ç•è²İOKõõ³İÇ§
0229 C.
0230 C. DHUŷã;¼ŷÉ;Ê¼ŷ¼. ŷİ;¼ŷÈ;Èõõİãõ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                       EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                    EQ      4M
0237 C.
0238 C. *****
0239 C. OTA CLU OP HTR DIS
0240 C. *****
0241 C.
0242 C. < Check initial temperatures >
0243 C. -----
0244 C. HK1_OTA_CLU_HTR_TMP (BChtr)= [ ]
0245 C. HK1_OTA_M2_HTR_TMP (M2htr) = [ ]
0246 C. HK1_OTA-13_TEMP (MC) = [ ]
0247 C. HK1_OTA-29_TEMP (CLU) = [ ]
0248 C. HK1_OTA-30_TEMP (CTM-TM) = [ ]
0249 C. HK1_FPP_PMU_TEMP = [ ]
0250 C. -----
0251 C.
0252 C. < Disable Heaters >
0253 +. DC 07-56 CTM_OTA_HTR_ALL_DIS
0254 C. -----
0255 C. CTM_CLU_HTR_OP1_E/D = DIS [ ]
0256 C. CTM_CLU_HTR_OP2_E/D = DIS [ ]
0257 C. CTM_M2_HTR_OP1_E/D = DIS [ ]
0258 C. CTM_M2_HTR_OP2_E/D = DIS [ ]
0259 C. -----
0260 C.
0261 C. < Enable M2 OP1 Heater >
0262 +. DC 07-50 CTM_M2_HTR_OP1_ENA
0263 C. -----
0264 C. CTM_M2_HTR_OP1_E/D = ENA [ ]
0265 C. CTM_OTA_HTR_VOLT = NORM [ ]
0266 C. HK1_OTA_M2_HTR_TMP increase [ ]
0267 C. -----
0268 C.
0269 C. -----
0270 C. °Ê²¼õİİDŷÄŷ-ŷçŷÄŷ×İñ; İİãÄêõ-İµõ±õİõD»Èİñõ•õÈõõ
0271 C. < Enable Back-up Heaters >
0272 +. DC 07-52 CTM_CLU_HTR_OP1_ENA
0273 C. -----
0274 C. CTM_CLU_HTR_OP1_E/D = ENA [ ]
0275 C. CTM_OTA_HTR_VOLT = NORM [ ]
0276 C. -----
0277 C.
0278 +. DC 07-53 CTM_CLU_HTR_OP2_ENA
0279 C. -----
0280 C. CTM_CLU_HTR_OP2_E/D = ENA [ ]
0281 C. CTM_OTA_HTR_VOLT = NORM [ ]
0282 C. -----
0283 C.
0284 +. DC 07-51 CTM_M2_HTR_OP2_ENA
0285 C. -----
0286 C. CTM_M2_HTR_OP2_E/D = ENA [ ]
0287 C. CTM_OTA_HTR_VOLT = NORM [ ]
0288 C. -----
0289 C.
0290 C. ***** SOT END *****
0291 C. *****

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```

0292 C. SOT TI command set
0293 C. *****
0294 C. Execute, after the success of OP upload.
0295 +. TI 2011-08-09 09:27:16.0
0296 DC 07-F0 MDP_SOT_MODE_STBY
0297 BC (41)
0298 C. -----
0299 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0300 C. -----
0301 C. ***** SOT END *****
0302 . C. Stop EIS observation and temporarily disable EIS mode changes
0303 C.
0304 C.
0305 C. ***** Start EIS operation (TI set) *****
0306 C. Execute, after the success of OP upload.
0307 C. Set EIS TI-commands
0308 +. TI 2011-08-09 09:27:30.0
0309 DC 07-FC EIS_MODE_MANU
0310 BC (21 02)
0311 +. TI 2011-08-09 09:27:40.0
0312 DC 07-FC EIS_MODE_CHG_DIS
0313 BC (22)
0314 . C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0315 C. ***** End EIS operation (TI set) *****
0316 C.
0317 C.
0318 C.
0319 C. ***** XRT START *****
0320 C. Execute, after the success of OP upload.
0321 +. TI 2011-08-09 09:27:00.0
0322 DC 07-F0 MDP_XRT_MODE_STBY
0323 BC (c3)
0324 . C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0325 C.
0326 C. ***** XRT END *****
0327 C.
0328 . C. ***** MDP `úÃîï»ö%ÝðÊÃð¹ðÊDCBC•x²è *****
0329 C. (%á°îÿÓÿÃÿÈÿPÿËÿáÿçÿèðÊ%¼ð¼Ã»Û¹ðè)
0330 . S. DC-BC dcbc-402:DCBC
0331 (MDP_known_event)
0332 C.
0333 C.
0334 . C. ***** ÿDÿ¹•Ï Daily±¿ÎñðÊ´Ø¹ðèDCBC•x²è *****
0335 . S. DC-BC dcbc-153:DCBC
0336 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0337 C.
0338 C.
0339 . C. ;ãLOSÿÁÿ§ÿÃÿ-¼Ã»Û;ã
0340 C.
0341 . C. ***** LOS *****
0342 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```

main-247 2011-08-09 13:50:59 91 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÅY$Y$YÅY~¼Å»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÿóYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Āí;Èq¿qÅq•µ°È»ÍxÁÇqñÍYçYÅY×Yí;¼YÉ;ÈÈè%µ•íÍÈ;ÈqÈ¼°ÇÔq•q¿¼í¹çqÍ;çÄ®, ùq¹qÈqBqçÄ+¿®q•qÈqçq³qÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCs Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 2557610.9 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_MANU
0056 BC (21 02)
0057 . C. Verify EIS in MANUAL mode
0058 C. Estimated OBSTBL upload time is 50s
0059 C. *****
0060 C. EIS START OBSTBL LOAD
0061 C. *****
0062 . S. RAM ram-820:EIS_OBSTBL
0063 ( )
0064 +. DC 07-FC EIS_DUMP_OBSTBL
0065 BC (07 07 07 00 00 70 00)
0066 C.
0067 C. Execute, after the success of OBSTBL upload.
0068 C. Set EIS TI-commands
0069 +. TI 2011-08-09 09:27:50.0
0070 DC 07-FC EIS_MODE_CHG_ENA
0071 BC (20)
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0073 C. *****
0074 C. EIS END OBSTBL LOAD
0075 C. *****
0076 C.
0077 . C. ***** MDP `ˆÃÏñÎ»ó¼YqÈÄDq¹qèDCBC•x²è *****
0078 C. (¼á°ÍYóYÅYÈYBpYÈYáYçYèqE¼qçq¼Ä»Ûq¹qè)
0079 . S. DC-BC dcbc-402:DCBC
0080 (MDP_known_event)
0081 C.
0082 C.
0083 . C. ***** YDY!•İ Daily±¿ÍÑqÈ´Øq¹qèDCBC•x²è *****
0084 . S. DC-BC dcbc-153:DCBC
0085 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086 C.
0087 C.
0088 . C. ;ãLOSYÅY$Y$YÅY~¼Å»Û;ã
0089 C.
0090 . C. ***** LOS *****
0091 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-248 2011-08-09 13:50:59 243 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ä
0005 C.
0006 C. YÄYß;¼Y³YßYóYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;È¿¿ãã•µ°Æ»Í×ÁÇ¿íYÇYÄY×Yí;¼YÉ;ÈÈÈ%µ•íÉ;ÈãÈ%Ç¿ã•¿¿¼í¹Ç¿í;ÇÄ®, ù¿¹ãÈããÇÄ+¿®ã•ãÈããã¿È;É
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ+¿¿µ;ON
0016 C. *****
0017 C. Ç“ °ÆÄ, í×ÈY¿ããLOSããÇ¿í»¿Ö¿¿íí, ¿. ; ÇÉÔÍ×ãÈXÄÓON¿í¹Ô¿È¿íãÈããã¿È;É
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYóYÉYíYÄY-¾ÔÄÖã-°ÄÄêã•¿¿¿; Ç°È²¼¿¿í°ÆÄ, ¼¿¿¿Ç¿¿¼Ä¹Ô¿¹¿¿;É
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÆÄ,
0033 C. *****
0034 C. Ç“ RESTART;ÈPT1;Èã•¿¿¿¼í¹Ç¿í; Ç°È²¼¿¿í¼Ä¹Ô¿¹¿¿; ÇDCBC-150¿¿¿¿È¿¿;É
0035 C.
0036 . C. ;ãPT1°ÆÄ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ;¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYóYÄYÈÄÛÄÖ;ÈÄ•Ä°¿¿È¿¿;È, áãí°ÆÄ, °Æ³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ;¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, ¿-¼«Æ°Ää»ßã•¿¿, á; Ç°È²¼¿¿¼Ä¹Ô¿¹¿¿;É
0055 C. YÇYóYÄYÈÄÛÄÖ¿¿Ä•Ä°¿¿È¿¿-¼¿¿¿¼í¹Ç¿í°¿í»¿¹¿¿ÈããÇÄÔ¿¿;É
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÆÄ,
0059 C. *****
0060 C. Ç“ RESTART;ÈPT2;Èã•¿¿¿¼í¹Ç¿í; Ç°È²¼¿¿í¼Ä¹Ô¿¹¿¿; ÇDCBC-151¿¿¿¿È¿¿;É
0061 C.
0062 . C. ;ãPT2°ÆÄ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ;¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYóYÄYÈÄÛÄÖ;ÈÄ•Ä°¿¿È¿¿;È, áãí°ÆÄ, °Æ³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ;¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Ää»ß; ÇXÄ+¿¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÄ, Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ+¿¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-263:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Upload DPL table >
0120 C.
0121 C. ¥ç¥Ã¥×¥í;¼¥ÉñîÁññÈSTS_CHKñðOFFñÈñ¹ñë
0122 C.
0123 . S. RAM ram-271:MDP_DPL
0124 ( )
0125 C.
0126 . C. < Dump RAMID=MDP_DPL >
0127 +. DC 07-F0 MDP_DUMP_FGTBL
0128 BC (82 07 00 38 b8 00 40)
0129 C. -----
0130 C. MDP_DPL verify = OK [ ]
0131 C. -----
0132 C.
0133 C. STS_CHKñðONñÈñ¹ñë
0134 C.
0135 . C. < Update MDP DSC PAR1 >
0136 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0137 BC (4c)
0138 C. MDP_CMD_CODE = F04C0700[ ]
0139 C. MDP_CMD_CNT (count-up 1) [ ]
0140 C. -----
0141 C.
0142 . C.
0143 . C. < Resume FG table (auto mode) >
0144 +. DC 07-F0 MDP_FG_CTRL_AUTO
0145 BC (50)
0146 . C. -----
0147 C. MDP_FG_CTRL_MODE = AUTO [ ]
0148 C. -----
0149 C.
0150 C. *****
0151 C. SOT TI command set
0152 C. *****
0153 C. Execute, after the success of TBL upload.
0154 +. TI 2011-08-09 09:27:18.0
0155 DC 07-F0 MDP_SOT_MODE_OBSV
0156 BC (40)
0157 . C. -----
0158 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0159 C. -----
0160 C.
0161 C. Only when FG_CTRL_AUTO is used in RT.
0162 +. TI 2011-08-09 09:27:20.0
0163 DC 07-F0 MDP_FG_CTRL_AUTO
0164 BC (50)
0165 . C. -----
0166 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0167 C. -----
0168 C. ***** SOT END *****
0169 C.
0170 C. ***** XRT START *****
0171 C.
0172 +. DC 07-F0 MDP_XRT_CTRL_MANU
0173 BC (c1)
0174 + DC 07-F0 MDP_XRT_MODE_STBY
0175 BC (c3)
0176 . C. ----- Success Verify ? OK / NG____
0177 C.
0178 C. XRT Obs. Table Upload
0179 . S. RAM ram-291:MDP_OBS_X
0180 ( )
0181 C.
0182 +. DC 07-F0 MDP_DUMP_XRTTBL
0183 BC (84 07 00 00 00 3a d4)
0184 . C. ----- Comparison Check ? OK / ERR ____
0185 C.
0186 C.
0187 +. DC 07-F0 MDP_XRT_ROI_SET
0188 BC (cd 01 b1 b1 04 04)
0189 + DC 07-F0 MDP_XRT_ROI_SET
0190 BC (cd 02 b1 b1 08 08)
0191 + DC 07-F0 MDP_XRT_ROI_SET
0192 BC (cd 03 b1 b1 08 08)
0193 + DC 07-F0 MDP_XRT_ROI_SET

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0194 BC (cd 04 b1 b1 06 06)
0195 + DC 07-F0 MDP_XRT_ROI_SET
0196 BC (cd 05 85 83 08 08)
0197 + DC 07-F0 MDP_XRT_ROI_SET
0198 BC (cd 06 85 83 06 06)
0199 + DC 07-F0 MDP_XRT_ROI_SET
0200 BC (cd 07 80 80 20 20)
0201 + DC 07-F0 MDP_XRT_ROI_SET
0202 BC (cd 0f 80 80 06 06)
0203 + DC 07-F0 MDP_XRT_ROI_SET
0204 BC (cd 10 80 80 08 08)
0205 + DC 07-F0 MDP_XRT_FLD_ENA
0206 BC (d8)
0207 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0208 BC (c8)
0209 + DC 07-F0 MDP_XRT_ARS_DIS
0210 BC (d5)
0211 + DC 07-F0 MDP_XRT_QT_PROG_SET
0212 BC (c4 01)
0213 + DC 07-F0 MDP_XRT_FL_PROG_SET
0214 BC (c5 08)
0215 . C. ----- Success Verify ? OK / NG ____
0216 C.
0217 C.
0218 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0219 C.
0220 + DC 07-F0 MDP_XRT_MODE_OBSV
0221 BC (c2)
0222 + TI 2011-08-09 09:27:02.0
0223 DC 07-F0 MDP_XRT_MODE_OBSV
0224 BC (c2)
0225 . C. ----- Success Verify ? OK / NG ____
0226 C.
0227 C. ***** XRT END *****
0228 C.
0229 . C. ***** MDP 'úÃîáî»ö¼ÝáÈÃðá¹æDCBC•x²è *****
0230 C. (%ã°îÿÓÿÄÿÈÿÞÿËÿÀÿÇÿÈÿ¼á¼Ä»Ûá¹æ)
0231 . S. DC-BC dcbc-402:DCBC
0232 (MDP_known_event)
0233 C.
0234 C.
0235 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑæ´Øá¹æDCBC•x²è *****
0236 . S. DC-BC dcbc-153:DCBC
0237 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0238 C.
0239 C.
0240 . C. ;ãLOSÿÄÿ§ÿÄÿÿ¼Ä»Û;ä
0241 C.
0242 . C. ***** LOS *****
0243 C.

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*** OP Sequence for XRT ***

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2011/08/09 09:37:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/08/09 09:37:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/08/09 09:37:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2011/08/09 09:37:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/08/09 09:38:00.0 AOCS_ORe-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 00 00 00 00
2011/08/09 09:40:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2011/08/09 09:40:26.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2011/08/09 09:40:46.0 XRT_FLD_ENA_447_OG [0x1bf]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2011/08/09 09:40:48.0 XRT_FLRCTRL_ENA_444_OG [0x1bc]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2011/08/09 09:40:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2011/08/09 09:40:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/08/09 09:40:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/08/09 09:40:56.0 XRT_QT_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0d
2011/08/09 09:40:58.0 XRT_FL_PROG_SET_414_OG [0x19e]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 08
2011/08/09 09:41:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/08/09 10:06:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/08/09 10:07:00.0 AOCS_ORe-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 ac cd
2011/08/09 10:09:26.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2011/08/09 10:09:46.0 XRT_FLD_ENA_447_OG [0x1bf]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2011/08/09 10:09:48.0 XRT_FLRCTRL_ENA_444_OG [0x1bc]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2011/08/09 10:09:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2011/08/09 10:09:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/08/09 10:09:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/08/09 10:09:56.0 XRT_QT_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0d
2011/08/09 10:09:58.0 XRT_FL_PROG_SET_414_OG [0x19e]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 08
2011/08/09 10:10:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/08/09 10:22:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/08/09 10:23:00.0 AOCS_ORe-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 d6 67
2011/08/09 10:25:26.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2011/08/09 10:25:46.0 XRT_FLD_ENA_447_OG [0x1bf]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2011/08/09 10:25:48.0 XRT_FLRCTRL_ENA_444_OG [0x1bc]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2011/08/09 10:25:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2011/08/09 10:25:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/08/09 10:25:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/08/09 10:25:56.0 XRT_QT_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0d
2011/08/09 10:25:58.0 XRT_FL_PROG_SET_414_OG [0x19e]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 08
2011/08/09 10:26:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/08/09 10:38:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/08/09 10:39:00.0 AOCS_ORe-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 00 00 00 00
2011/08/09 10:41:26.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2011/08/09 10:41:46.0 XRT_FLD_ENA_447_OG [0x1bf]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2011/08/09 10:41:48.0 XRT_FLRCTRL_ENA_444_OG [0x1bc]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2011/08/09 10:41:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2011/08/09 10:41:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/08/09 10:41:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/08/09 10:41:56.0 XRT_QT_PROG_SET_436_OG [0x1b4]

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2011/08/09	10:41:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2011/08/09	10:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/08/09	10:54:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/09	10:55:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/09	10:57:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 00 00	29 99
2011/08/09	10:57:46.0	XRT_FLD_ENA_447_OG [0x1bf]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2011/08/09	10:57:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/08/09	10:57:50.5	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/08/09	10:57:52.5	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/08/09	10:57:54.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/08/09	10:57:56.5	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/09	10:57:58.5	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2011/08/09	10:58:00.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/08/09	11:21:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/09	11:21:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/09	11:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/09	11:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/09	11:45:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/09	11:45:30.0	AOCS_OrE-point_Start_5_OG [0x09b]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/09	11:47:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 00 00	53 33
2011/08/09	11:48:16.0	XRT_FLD_ENA_447_OG [0x1bf]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2011/08/09	11:48:18.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/08/09	11:48:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/08/09	11:48:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/08/09	11:48:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/08/09	11:48:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/09	11:48:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2011/08/09	11:48:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/08/09	12:01:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/09	12:01:30.0	AOCS_OrE-point_Start_6_OG [0x09c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/09	12:03:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 d6 36 b7	8e
2011/08/09	12:04:16.0	XRT_FLD_ENA_447_OG [0x1bf]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2011/08/09	12:04:18.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/08/09	12:04:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/08/09	12:04:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/08/09	12:04:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/08/09	12:04:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/09	12:04:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2011/08/09	12:04:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/08/09	12:11:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/09	12:11:30.0	AOCS_OrE-point_Start_7_OG [0x09d]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/09	12:13:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 b4 b5 db	75
2011/08/09	12:14:16.0	XRT_FLD_ENA_447_OG [0x1bf]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2011/08/09	12:14:18.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/08/09	12:14:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/08/09	12:14:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/08/09			MDP_XRT_ARS_DIS	1	07-F0	d5	

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2011/08/09	12:14:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/09	12:14:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/08/09	12:14:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/08/09	12:14:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/09	12:27:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/09	12:27:30.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 ac 5b 00 00				
2011/08/09	12:29:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/08/09	12:30:16.0	XRT_FLD_ENA_447_OG [0x1bf]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/09	12:30:18.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/09	12:30:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/08/09	12:30:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/09	12:30:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/09	12:30:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/08/09	12:30:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/08/09	12:30:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/09	12:43:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/09	12:43:30.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 b4 b5 24 8b				
2011/08/09	12:45:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/08/09	12:46:16.0	XRT_FLD_ENA_447_OG [0x1bf]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/09	12:46:18.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/09	12:46:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/08/09	12:46:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/09	12:46:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/09	12:46:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/08/09	12:46:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/08/09	12:46:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/09	13:00:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/09	13:00:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/09	13:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/08/09	13:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/08/09	13:23:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/09	13:24:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 d6 36 48 72				
2011/08/09	13:26:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/08/09	13:26:46.0	XRT_FLD_ENA_447_OG [0x1bf]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/09	13:26:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/09	13:26:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/08/09	13:26:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/08/09	13:26:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/08/09	13:26:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2011/08/09	13:26:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2011/08/09	13:27:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/08/09	13:33:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/08/09	13:34:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 29 ca b7 8e				
2011/08/09	13:36:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/08/09	13:36:46.0	XRT_FLD_ENA_447_OG [0x1bf]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/08/09	13:36:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/08/09	13:36:50.0	XRT_AEC_RESET_443_OG [0x1bb]							

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2011/08/09	13:36:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/09	13:36:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/09	13:36:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2011/08/09	13:36:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/08/09	13:37:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/09	13:43:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/09	13:44:00.0	AOCS_OrE-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	4b	4b	db 75
2011/08/09	13:46:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2011/08/09	13:46:46.0	XRT_FLD_ENA_447_OG [0x1bf]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/08/09	13:46:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/08/09	13:46:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/08/09	13:46:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/09	13:46:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/09	13:46:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2011/08/09	13:46:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/08/09	13:47:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/09	13:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/09	14:00:00.0	AOCS_OrE-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	53	a5	00 00
2011/08/09	14:02:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2011/08/09	14:02:46.0	XRT_FLD_ENA_447_OG [0x1bf]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/08/09	14:02:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/08/09	14:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/08/09	14:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/09	14:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/09	14:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2011/08/09	14:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/08/09	14:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/09	14:15:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/09	14:16:00.0	AOCS_OrE-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	4b	4b	24 8b
2011/08/09	14:18:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2011/08/09	14:18:46.0	XRT_FLD_ENA_447_OG [0x1bf]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/08/09	14:18:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/08/09	14:18:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/08/09	14:18:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/09	14:18:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/09	14:18:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2011/08/09	14:18:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/08/09	14:19:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/09	14:38:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/09	14:38:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/09	14:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/09	14:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/09	15:15:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/09	15:16:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/09	15:20:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/09	15:21:00.0	AOCS_OrE-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00	29	db	48 72
2011/08/09	15:23:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							

2011/08/09	15:23:46.0	XRT_FLD_ENA_447_OG [0x1bf]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		MDP_XRT_FLD_ENA		1	07-F0	d8			
2011/08/09	15:23:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/08/09	15:23:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/08/09	15:23:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/09	15:23:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/09	15:23:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2011/08/09	15:23:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/08/09	15:24:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/09	15:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/09	15:30:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00	ea	d1	b2 0d
2011/08/09	15:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/08/09	15:32:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/08/09	15:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/08/09	15:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/08/09	15:32:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/09	15:32:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/09	15:32:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02		
2011/08/09	15:32:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/08/09	15:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/09	16:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/09	16:17:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/09	16:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/09	16:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/09	17:01:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/09	17:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/09	17:45:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/09	17:45:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2011/08/09	17:45:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00	00	00	00 00
2011/08/09	17:45:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/08/09	17:45:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/08/09	17:45:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/09	17:48:28.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12		
2011/08/09	17:48:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/09	17:55:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/09	17:55:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00	ea	d1	b2 0d
2011/08/09	17:55:30.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/09	17:55:32.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/09	17:55:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/09	17:57:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/08/09	17:58:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/08/09	17:58:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/08/09	17:58:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/08/09	17:58:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/09	17:58:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/09	17:58:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02		
2011/08/09	17:58:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2011/08/09	17:58:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]							

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2011/08/09	18:38:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/09	18:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/08/09	19:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/09	19:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/09	19:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/09	19:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/09	20:15:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/09	20:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/08/09	21:12:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/09	21:12:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/09	21:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/09	21:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/09	21:52:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/09	21:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/08/09	22:51:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/09	22:51:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/09	22:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/09	22:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/09	22:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/09	23:24:30.0	XRT_Custom_418_OG [0x1a2]						
2011/08/09	23:25:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/08/10	00:29:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/10	00:29:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/10	00:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/10	00:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/10	00:53:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/10	00:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/08/10	02:06:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/10	02:06:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/10	02:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/10	02:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/10	02:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/10	02:31:30.0	XRT_Custom_418_OG [0x1a2]						
2011/08/10	02:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/08/10	03:37:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/10	03:37:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/10	03:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/10	03:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/10	03:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/10	04:10:00.0	XRT_Custom_418_OG [0x1a2]						
2011/08/10	04:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/08/10	05:14:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/10	05:14:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/10	05:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/08/10	05:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/08/10	05:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/08/10	05:48:30.0	XRT_Custom_418_OG [0x1a2]						
2011/08/10	05:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/08/10	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/08/10	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/08/10	06:00:00.0	AOCs_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/08/10	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00		
2011/08/10	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/08/10	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/08/10	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		

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2011/08/10	06:02:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2011/08/10	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/10	06:54:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/10	06:54:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/10	06:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/10	06:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/10	07:27:00.0	XRT_Custom_418_OG [0x1a2]					
2011/08/10	07:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/10	07:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/10	08:00:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03	00 00 00 00
2011/08/10	08:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2011/08/10	08:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/08/10	08:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/08/10	08:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/08/10	08:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/08/10	08:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/10	08:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2011/08/10	08:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/08/10	08:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/10	08:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/10	08:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/10	08:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/10	08:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/10	09:05:30.0	XRT_Custom_418_OG [0x1a2]					
2011/08/10	09:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/10	10:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/10	10:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/10	10:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/10	10:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/10	10:44:00.0	XRT_Custom_418_OG [0x1a2]					
2011/08/10	10:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/10	11:59:00.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/10	11:59:03.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/10	11:59:05.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/10	11:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/10	12:00:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00	ea d1 b2 0d
2011/08/10	12:02:15.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/10	12:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2011/08/10	12:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/08/10	12:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/08/10	12:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/08/10	12:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/08/10	12:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/10	12:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2011/08/10	12:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2011/08/10	12:22:30.0	XRT_Custom_418_OG [0x1a2]					
2011/08/10	12:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/10	13:37:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/10	13:37:32.0	XRT_FLD_RESET_412_OG [0x19c]					

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2011/08/10	13:37:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/10	13:40:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/10	14:01:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/10	14:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/10	14:06:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/10	14:06:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/10	14:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/10	14:09:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/10	14:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/10	14:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/10	15:16:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/10	15:16:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/10	15:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/10	15:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/10	16:01:30.0	XRT_Custom_418_OG [0x1a2]							
2011/08/10	16:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/10	16:39:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/10	16:39:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2011/08/10	16:40:00.0	AOCS_Or-e-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00			
2011/08/10	16:40:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/08/10	16:40:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/08/10	16:40:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/10	16:42:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2011/08/10	16:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/10	16:54:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/10	16:54:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/10	16:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/10	16:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/10	16:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/10	17:00:00.0	AOCS_Or-e-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	04 00 00 00 00			
2011/08/10	17:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/08/10	17:02:46.0	XRT_FLD_ENA_447_OG [0x1bf]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/08/10	17:02:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/08/10	17:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/08/10	17:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/08/10	17:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/10	17:02:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2011/08/10	17:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08			
2011/08/10	17:38:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/10	17:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/10	18:33:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/10	18:33:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/10	18:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/08/10	18:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/08/10	19:15:00.0	XRT_Custom_418_OG [0x1a2]							
2011/08/10	19:16:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/08/10	20:11:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/08/10	20:11:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/08/10	20:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							

2011/08/10	20:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/10	20:52:00.0	XRT_Custom_418_OG [0x1a2]					
2011/08/10	20:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/10	20:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/10	21:00:00.0	AOCS_Ore-point_Start_19_OG [0x0a9]					
			AOCU_NM	5	02-76	02 00 00 00 00	
2011/08/10	21:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/08/10	21:02:46.0	XRT_FLD_ENA_447_OG [0x1bf]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/08/10	21:02:48.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/08/10	21:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/08/10	21:02:52.0	XRT_ARS_DIS_431_OG [0x1af]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/08/10	21:02:54.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/10	21:02:56.0	XRT_QT_PROG_SET_403_OG [0x193]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2011/08/10	21:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2011/08/10	21:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/10	21:50:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/10	21:50:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/10	21:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/10	21:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/10	22:28:00.0	XRT_Custom_418_OG [0x1a2]					
2011/08/10	22:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/10	23:28:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/10	23:28:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/10	23:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/10	23:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/10	23:54:00.0	XRT_Custom_418_OG [0x1a2]					
2011/08/10	23:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/11	01:06:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/11	01:06:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/11	01:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/11	01:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/11	01:30:00.0	XRT_Custom_418_OG [0x1a2]					
2011/08/11	01:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/11	02:42:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/11	02:42:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/11	02:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/11	02:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/11	03:08:30.0	XRT_Custom_418_OG [0x1a2]					
2011/08/11	03:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/11	04:11:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/11	04:11:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/11	04:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/11	04:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/11	04:47:00.0	XRT_Custom_418_OG [0x1a2]					
2011/08/11	04:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/08/11	05:52:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/08/11	05:52:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/08/11	05:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/08/11	05:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/08/11	06:25:30.0	XRT_Custom_418_OG [0x1a2]					
2011/08/11	06:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					

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2011/08/11	07:19:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/11	07:19:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/08/11	07:20:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00
2011/08/11	07:20:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/08/11	07:20:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/08/11	07:20:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/08/11	07:22:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2011/08/11	07:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/11	07:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/11	07:30:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03 00 00 00 00
2011/08/11	07:32:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/11	07:32:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/11	07:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/11	07:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/08/11	07:32:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/08/11	07:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/08/11	07:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/08/11	07:32:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/08/11	07:32:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/11	07:32:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2011/08/11	07:32:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2011/08/11	07:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/11	08:04:00.0	XRT_Custom_418_OG [0x1a2]				
2011/08/11	08:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/11	09:12:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/11	09:12:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/08/11	09:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/08/11	09:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/08/11	09:42:30.0	XRT_Custom_418_OG [0x1a2]				
2011/08/11	09:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/08/11	10:18:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/08/11	10:19:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00