

# XRT Timeline to be uploaded on 2011/09/03

Period: 2011/09/03 10:43:00 - 2011/09/07 11:31:00

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## Normal mode

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XOB #189F: AR Standard-A(Filter-Ratio) with PFB, thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048, 60s cad												
Term	Pointing (x, y)				Comment							
09/03 10:56:00 - 09/03 17:59:24	Track (	-465.5,	88.0)	@ 09/03 10:53:00	# OP start + 10min, AR 11283.							
09/03 18:12:30 - 09/04 05:28:54	Track (	-407.6,	84.1)	@ 09/03 18:09:30	# HOP 199, with BBSO.							
09/04 05:42:00 - 09/04 07:29:54	Track (	-312.2,	79.1)	@ 09/04 05:39:00	# AR 11283							
09/04 10:33:00 - 09/04 17:44:54	Track (	-270.6,	77.3)	@ 09/04 10:30:00	* AR 11283							
09/04 18:20:00 - 09/05 03:23:30	Track (	-206.0,	75.1)	@ 09/04 17:55:00	# HOP 199, officially until 20 UT, and also EIS core obs.							
09/05 22:47:00 - 09/06 06:08:24	Track (	51.6,	72.3)	@ 09/05 22:40:00	* AR 11283.							
09/06 06:21:30 - 09/06 07:12:00	Track (	120.2,	73.1)	@ 09/06 06:18:30	# Cont.							
09/06 10:33:00 - 09/06 11:04:54	Track (	157.6,	73.8)	@ 09/06 10:30:00	* AR 11283							
<b>PROG= 18 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 76 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 62 20-time(s) 2.0sec												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #187C: Synoptic Q95 2x2 - Al/mesh(45/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2897) + Thin-Be(36)												
Term	Pointing (x, y)				Comment							
09/03 18:02:30 - 09/03 18:09:24	Fixed (	0.0,	0.0)		synoptic, shifted -0.5 min							
09/04 05:32:00 - 09/04 05:38:54	Fixed (	0.0,	0.0)		synoptic, shifted -31.0 min							
09/04 17:48:00 - 09/04 17:54:54	Fixed (	0.0,	0.0)		synoptic, shifted manually.							
09/05 06:06:58 - 09/05 14:20:24	Fixed (	0.0,	0.0)		synoptic, shifted manually, SOT flat fields, and EIS sensitivity monitoring.							
09/05 14:23:30 - 09/05 22:39:54	Fixed (	0.0,	-45.0)		* With XRT synoptic, shifted manually.							
09/06 06:11:30 - 09/06 06:18:24	Fixed (	0.0,	0.0)		synoptic, shifted 8.5 min							
<b>PROG= 07 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 86 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 37 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 32 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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## Flare mode

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XOB #1869: Flare standard obs. multifilter (thin-Be,med-Al,thick-Be 384x384 - Al-poly 512x512 2x2)												
Term	Pointing (x, y)				Comment							
09/03 10:56:00 - 09/03 17:59:24	Track (	-465.5,	88.0)	@ 09/03 10:53:00	# OP start + 10min, AR 11283.							
09/03 18:12:30 - 09/04 05:28:54	Track (	-407.6,	84.1)	@ 09/03 18:09:30	# HOP 199, with BBSO.							
09/04 05:42:00 - 09/04 07:29:54	Track (	-312.2,	79.1)	@ 09/04 05:39:00	# AR 11283							
09/04 10:33:00 - 09/04 17:44:54	Track (	-270.6,	77.3)	@ 09/04 10:30:00	* AR 11283							
09/04 18:20:00 - 09/05 03:23:30	Track (	-206.0,	75.1)	@ 09/04 17:55:00	# HOP 199, officially until 20 UT, and also EIS core obs.							
09/05 22:47:00 - 09/06 06:08:24	Track (	51.6,	72.3)	@ 09/05 22:40:00	* AR 11283.							
09/06 06:21:30 - 09/06 07:12:00	Track (	120.2,	73.1)	@ 09/06 06:18:30	# Cont.							

PROG= 13 1-time(s)												
Subr= 1 4-time(s) 2.0sec												
Seqn= 55 45-time(s) 20.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 600.0sec												
Seqn= 89 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

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NOT USED

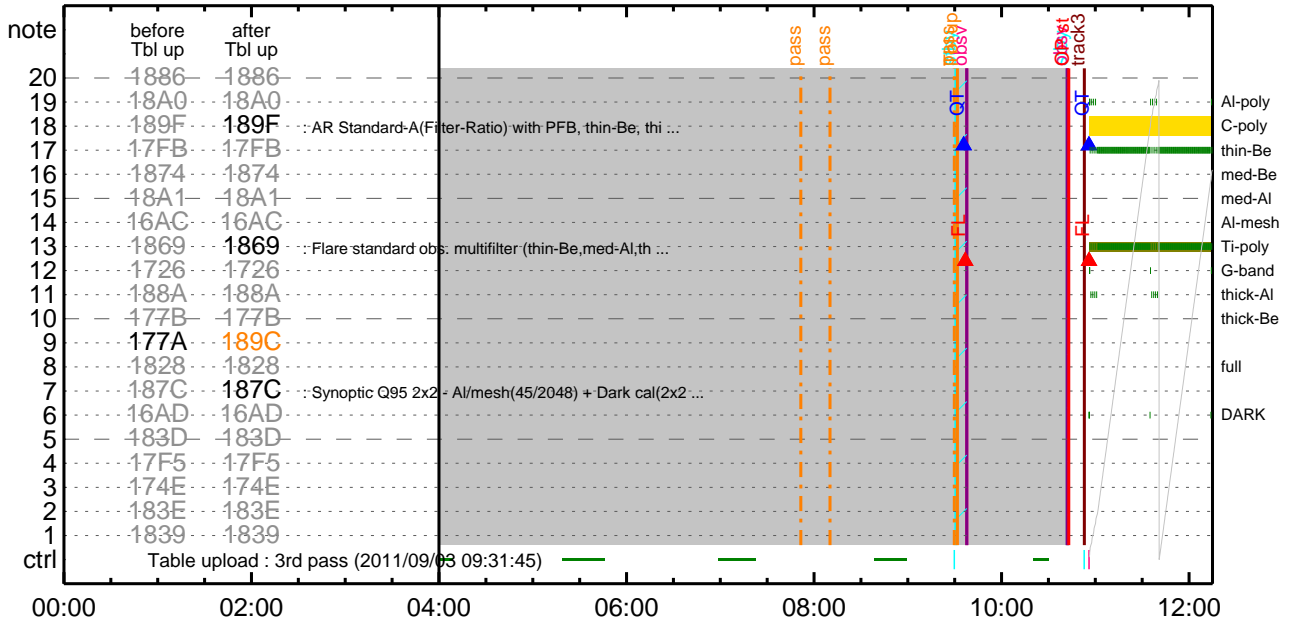
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### Flare Detection

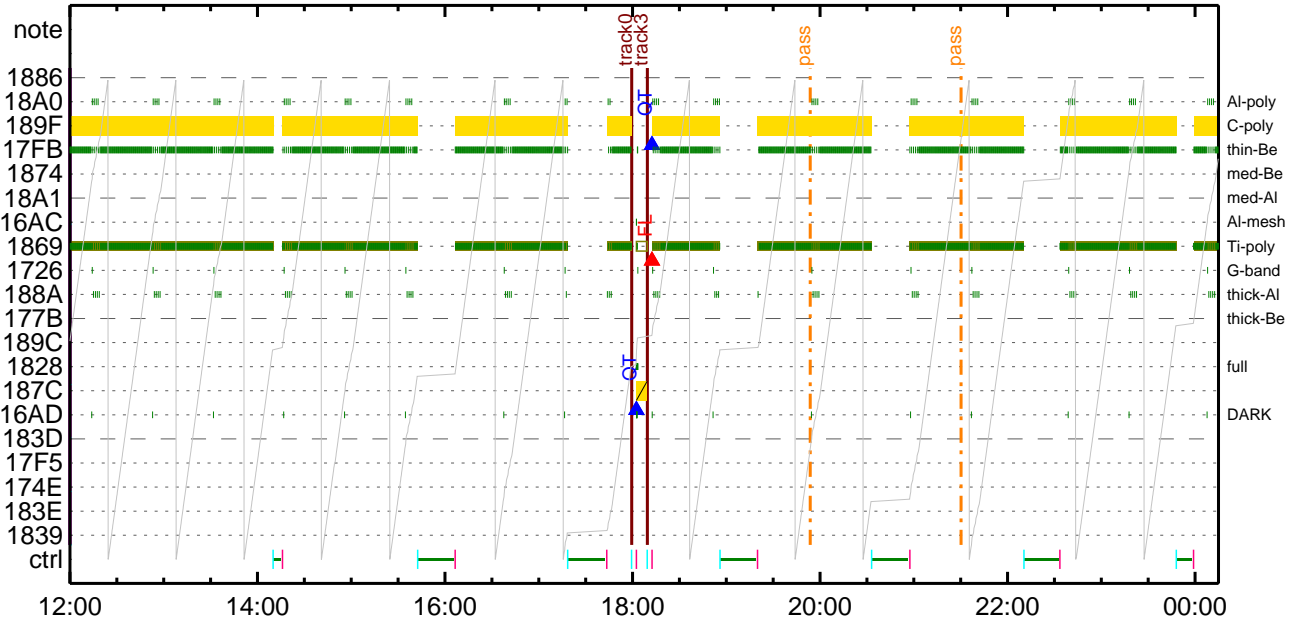
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FLD Patrol												
Term			Pointing (x, y)				Comment					
09/03 18:12:16 - 09/04 05:29:16	Track ( -407.6,	84.1)	@ 09/03 18:09:30	# HOP 199, with BBSO.								
09/04 05:41:46 - 09/04 17:45:16	Track ( -312.2,	79.1)	@ 09/04 05:39:00	# AR 11283								
09/04 17:57:46 - 09/05 06:04:14	Track ( -206.0,	75.1)	@ 09/04 17:55:00	# HOP 199, officially until 20 UT, and also EIS core obs.								
09/05 22:46:46 - 09/06 06:08:46	Track ( 51.6,	72.3)	@ 09/05 22:40:00	* AR 11283.								
09/06 06:21:16 - 09/07 11:31:00	Track ( 120.2,	73.1)	@ 09/06 06:18:30	# Cont.								
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

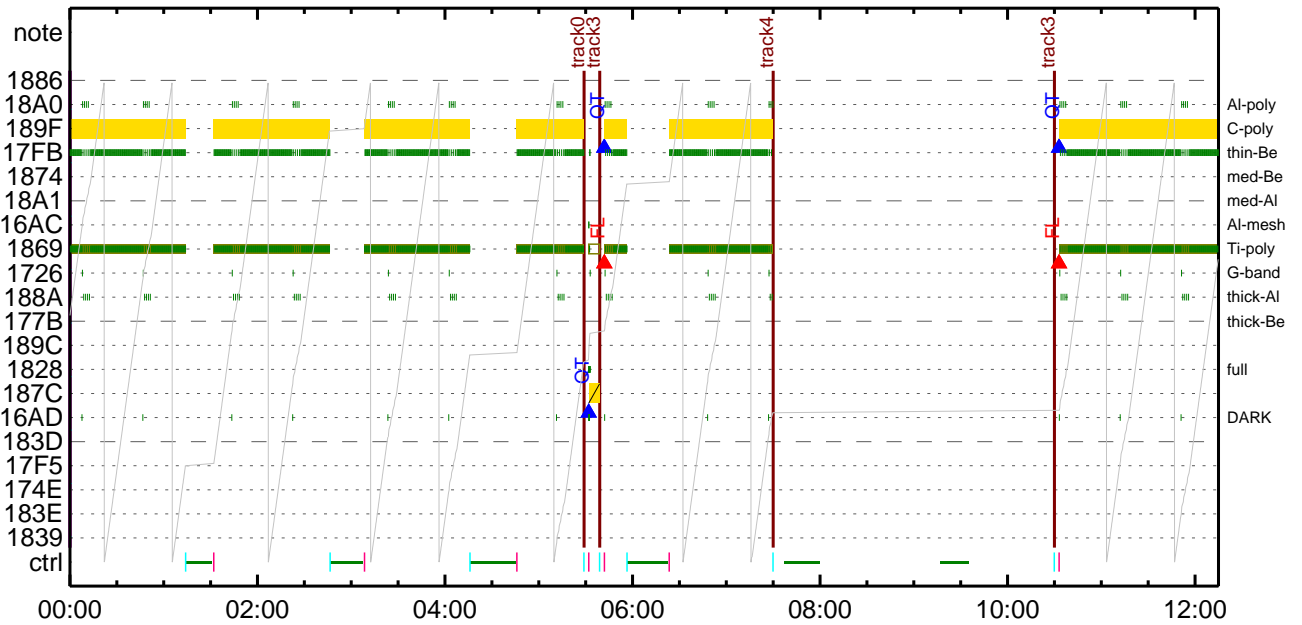
### CMDI #0123 2011/09/03



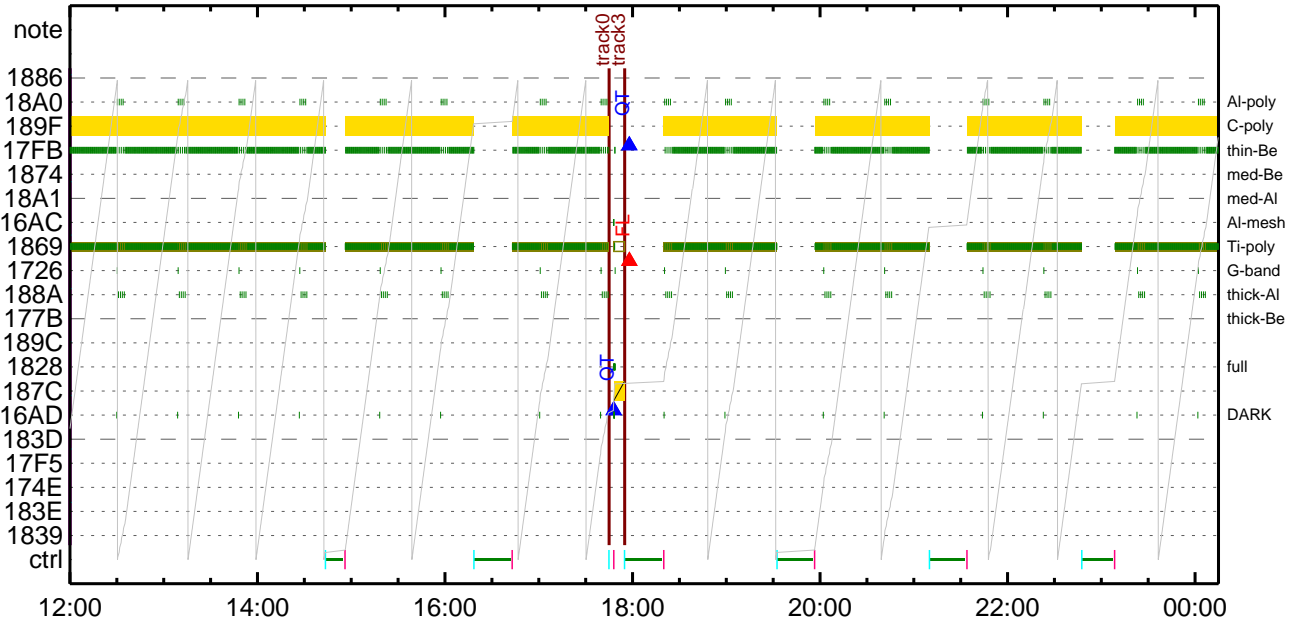
### CMDI #0123 2011/09/03



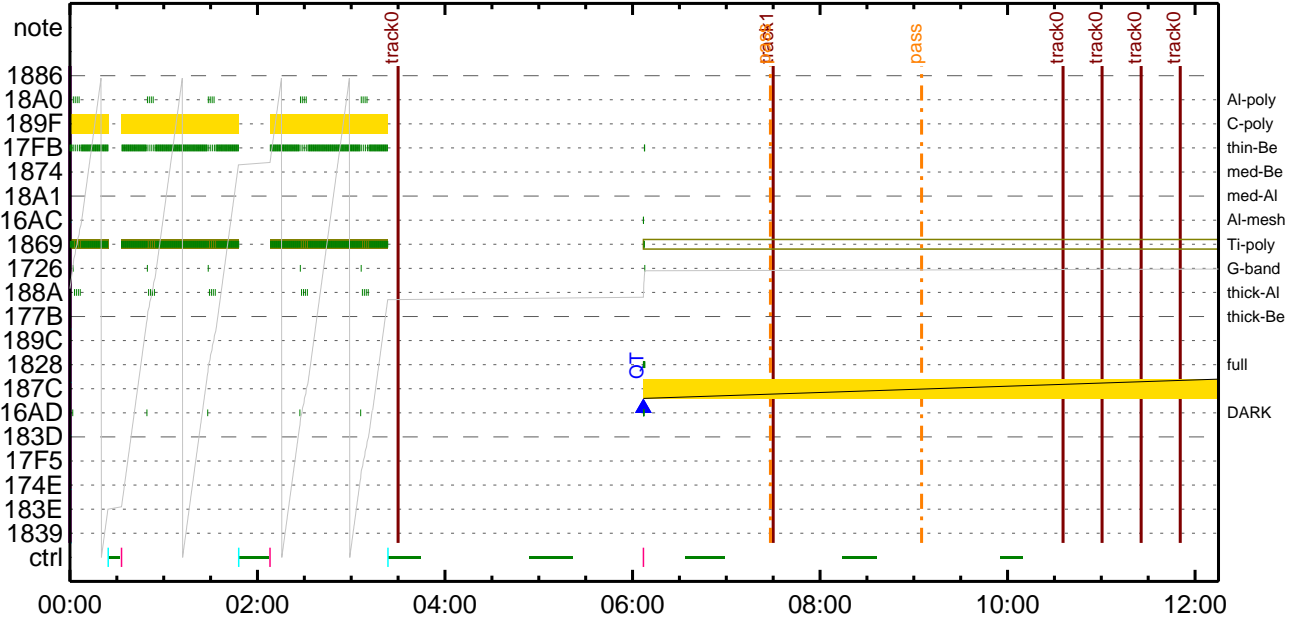
### CMDI #0123 2011/09/04



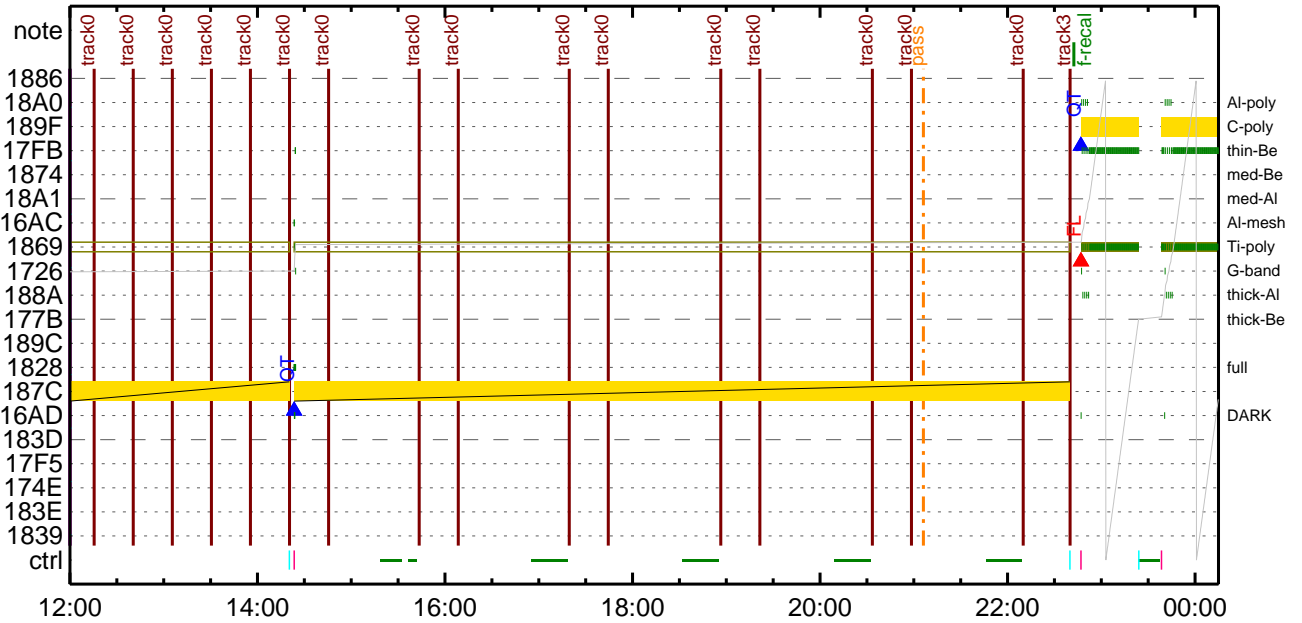
CMDI #0123 2011/09/04



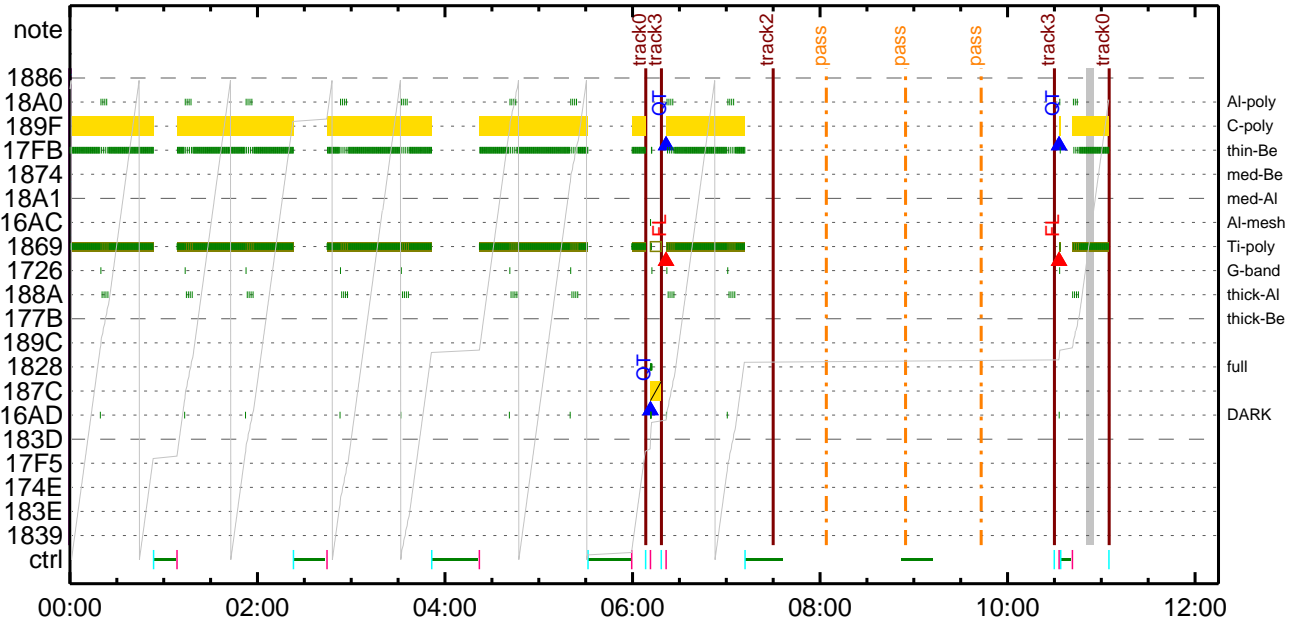
CMDI #0123 2011/09/05



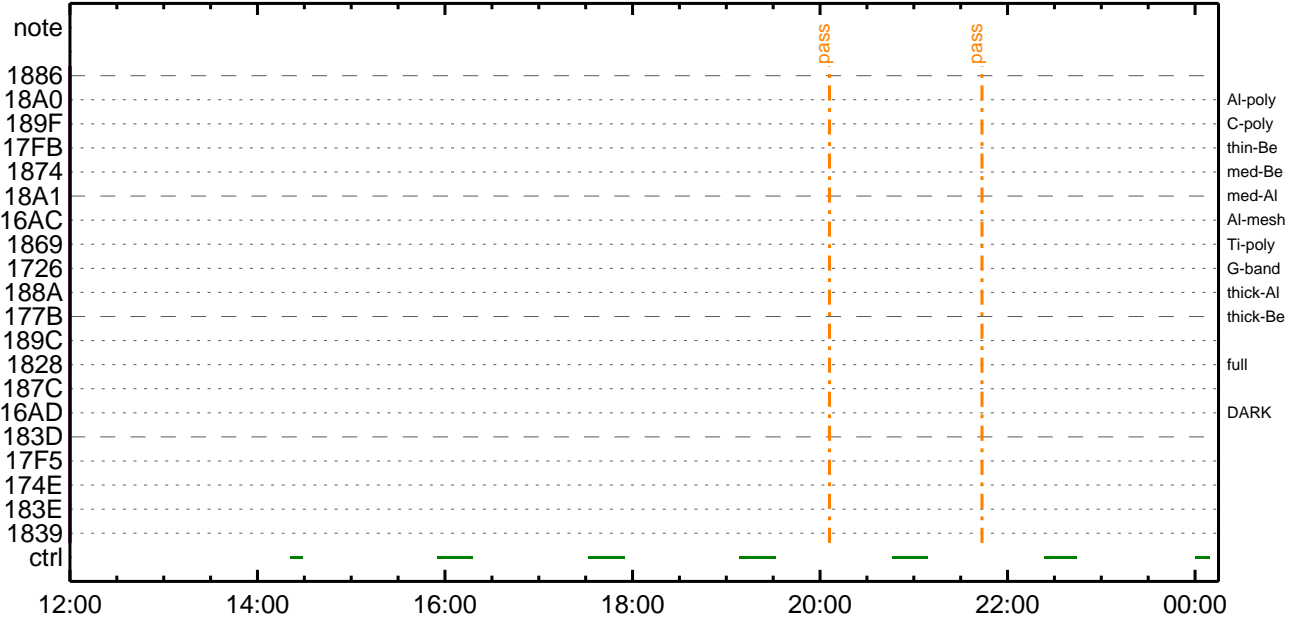
CMDI #0123 2011/09/05



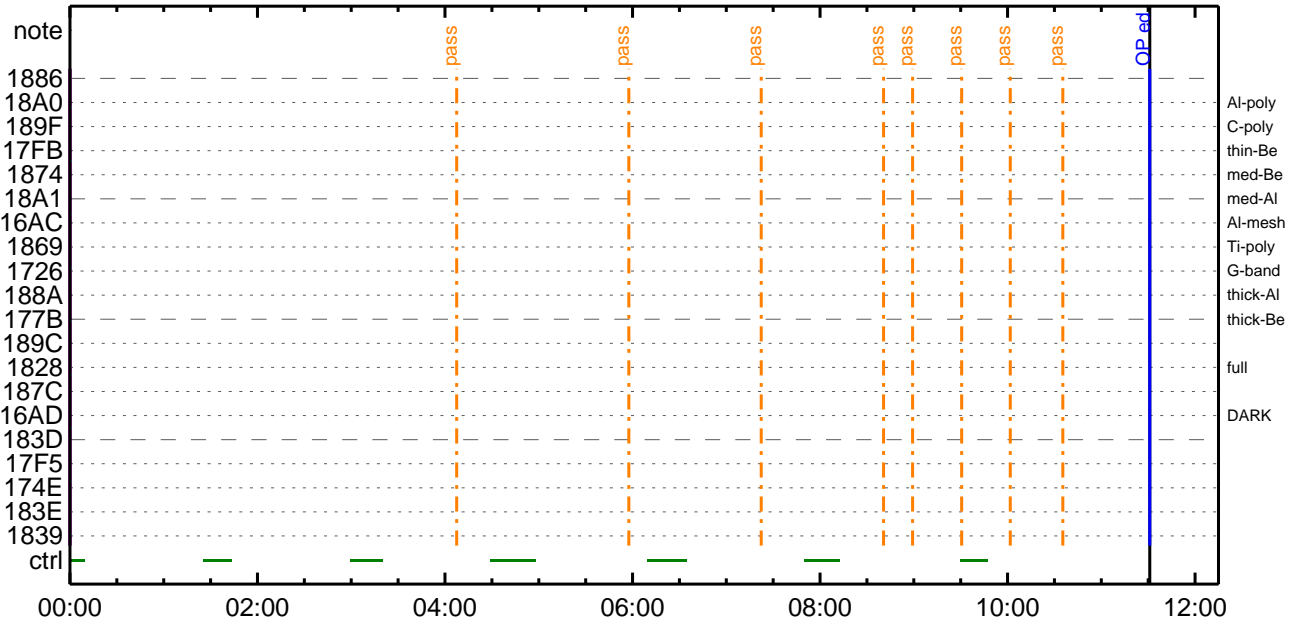
### CMDI #0123 2011/09/06



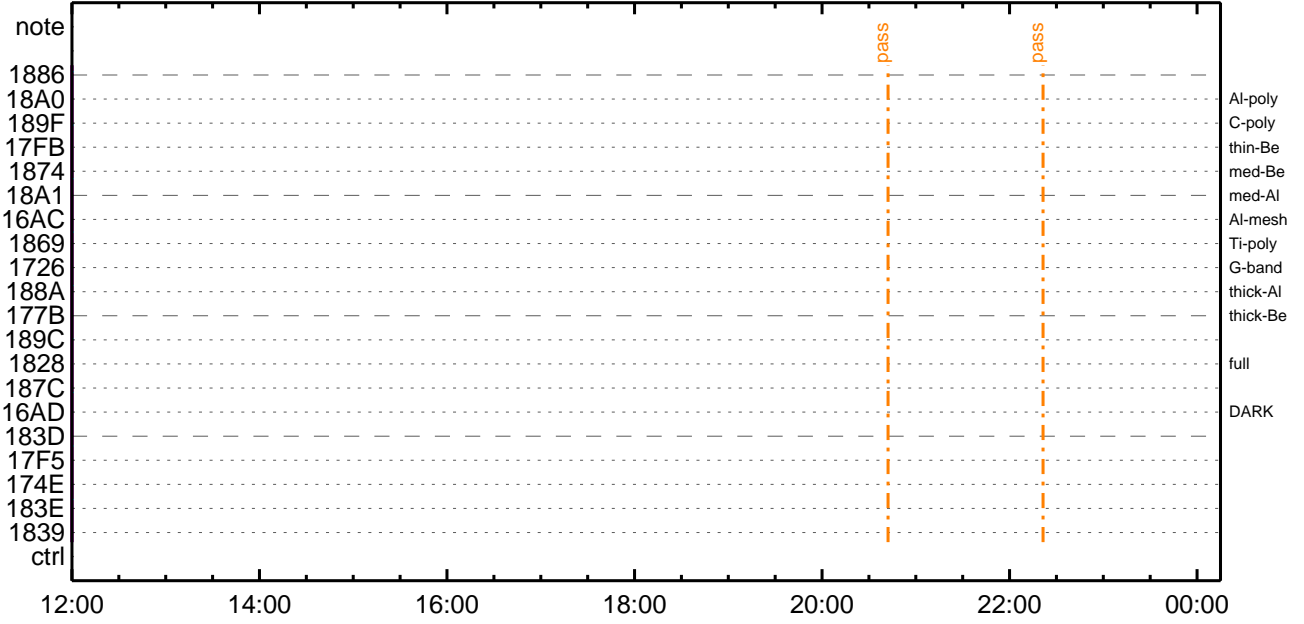
### CMDI #0123 2011/09/06



### CMDI #0123 2011/09/07



CMDI #0123 2011/09/07





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|AYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-300:OP
0104 ( )
0105 S. OG og-300:OG
0106 ( )
0107 C.
0108 C. ;aNMOG&OPf°eAYOX;a
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ;c[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ;c[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ;c[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ;c[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ;c[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ;c[HK1_PKT_FORM_NO] EQ 7
0120 C. ;c[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ;c[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ;c[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ;c[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. AYOXx1/2^i»o³iC§
0125 C. ;c[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGafE¹c•e²iOKo³iC§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ;c[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ;c[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ;c[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ;c[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ;c[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ;c[HK1_PKT_FORM_NO] EQ 7
0139 C. ;c[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ;c[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ;c[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ;c[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. AYOXx1/2^i»o³iC§
0144 C. ;c[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGafE¹c•e²iOKo³iC§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ;c[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ;c[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ;c[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ;c[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ;c[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ;c[HK1_PKT_FORM_NO] EQ 7
0158 C. ;c[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ;c[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ;c[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ;c[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. AYOXx1/2^i»o³iC§
0163 C. ;c[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPafE¹c•e²iOKo³iC§
0165 C.
0166 C. ***** oE²¼oI3A´¶A°EÉ-aoA÷z® (%âµ-AYOXx1/2^e½cãdÃOÃæoc¼^a°oE¼i¹cãcãâ) *****
0167 C. DHUâ;4YE;E½Y½;Yi;4YE;EoðIã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ;c[HK1_PKT_FORM_NO] EQ 2
0171 C. ;c[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ;c[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ;c[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADa-Á÷z@NGuI¼i¹c;ç°E²¼oI¹TI-CMDÁ÷z@I¼A¹Oã•oEããã³oE;f
0180 C. aPãz;çSETaEDUMPaIÆ±°iYÑ¹c¹Oã|ã³oE;f
0181 C.
0182 C. TIY³YpYóYEoðÃDjç(UT)
0183 +. TI 2011-09-03 10:38:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ;c[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-09-03 10:38:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ;c[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-09-03 10:38:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ;c[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2011-09-03 10:42:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼ïÄë%îîñïîŷÄŷ§ŷÄŷ-¹âiü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]        EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]        EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]      EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]      EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]       EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ          EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ïð³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLîî¼è¹ç•è²îOKïð³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;è¼ŷ¼. ŷî;¼ŷÉ;èïðîäï¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2011-09-03 10:42:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2011-09-03 10:42:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2011-09-03 10:42:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-09-03 10:42:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´úÄîÄî»ö¼ŷïèÄðï¹èDCBC•x²è *****
0276 C. (¼ä°îŷÖŷÄŷÉŷŷŷÉŷáŷçŷèè¼ïð¼Ä»Ûï¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîñèË´Øï¹èDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-262:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2011-09-03 10:42:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 80 80 08 08)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 20)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 20 08)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 80 80 08 20)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0f 80 80 06 06)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 10 80 80 08 08)
0172 + DC 07-F0 MDP_XRT_FLD_ENA
0173 BC (d8)
0174 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0175 BC (c8)
0176 +. DC 07-F0 MDP_XRT_AEC_RESET
0177 BC (d0)
0178 + DC 07-F0 MDP_XRT_ARS_DIS
0179 BC (d5)
0180 +. DC 07-F0 MDP_XRT_FLD_RESET
0181 BC (da)
0182 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0183 BC (c4 12)
0184 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0185 BC (c5 0d)
0186 . C. ----- Success Verify ? OK / NG ____
0187 C.
0188 C.
0189 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0190 C.
0191 +. DC 07-F0 MDP_XRT_MODE_OBSV
0192 BC (c2)
0193 +. TI 2011-09-03 10:42:02.0

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0194 DC 07-F0 MDP_XRT_MODE_OBSV
0195 BC (c2)
0196 . C. ----- Success Verify ? OK / NG ____
0197 C.
0198 C. ***** XRT END *****
0199 C.
0200 . C. ***** MDP `úÃîñî»ò¼ŷñÊÂĐñ¹ñèDCBC•x²è *****
0201 C. (¼á°îŷÓŷÃŷÈŷŦŷËŷáŷçŷèñÊ¼ñ¼Ã»Ûñ¹ñè)
0202 . S. DC-BC dcbc-402:DCBC
0203 (MDP_known_event)
0204 C.
0205 C.
0206 . C. ***** ŷĐŷ¹•İ Daily±¿İÑñÊ´Øñ¹ñèDCBC•x²è *****
0207 . S. DC-BC dcbc-153:DCBC
0208 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0209 C.
0210 C.
0211 . C. ¡ãLOSŷÁŷŦŷÃŷ¬¼Ã»Ûñ¹ñè
0212 C.
0213 . C. ***** LOS *****
0214 C.
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\*\*\* OP Sequence for XRT \*\*\*

2011/09/03	10:52:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	10:53:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/09/03	10:55:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/09/03	10:55:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/09/03	10:55:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/09/03	10:55:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/09/03	10:55:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/09/03	10:55:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/03	10:55:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/09/03	10:55:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/09/03	10:56:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/03	14:10:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	14:10:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/03	14:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/03	14:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/03	14:15:00.0	XRT_Custom_418_OG [0x1a2]							
2011/09/03	14:16:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/03	15:42:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	15:42:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/03	15:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/03	15:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/03	16:05:30.0	XRT_Custom_418_OG [0x1a2]							
2011/09/03	16:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/03	17:18:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	17:18:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/03	17:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/03	17:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/03	17:42:30.0	XRT_Custom_418_OG [0x1a2]							
2011/09/03	17:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/03	17:59:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	17:59:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/09/03	17:59:30.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/09/03	17:59:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/09/03	17:59:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/09/03	17:59:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/09/03	18:02:28.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2011/09/03	18:02:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/03	18:09:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	18:09:30.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/09/03	18:11:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/09/03	18:12:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/09/03	18:12:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/09/03	18:12:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/09/03	18:12:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/09/03	18:12:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/03	18:12:26.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/09/03	18:12:28.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				

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2011/09/03	18:12:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/03	18:56:00.5	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	18:56:02.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/03	18:56:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/03	18:59:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/03	19:19:01.0	XRT_Custom_418_OG [0x1a2]							
2011/09/03	19:20:01.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/03	20:33:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	20:33:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/03	20:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/03	20:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/03	20:56:30.0	XRT_Custom_418_OG [0x1a2]							
2011/09/03	20:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/03	22:10:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	22:10:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/03	22:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/03	22:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/03	22:32:30.0	XRT_Custom_418_OG [0x1a2]							
2011/09/03	22:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/03	23:48:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	23:48:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/03	23:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/03	23:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/03	23:58:00.0	XRT_Custom_418_OG [0x1a2]							
2011/09/03	23:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/04	01:14:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/04	01:14:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/04	01:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/04	01:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/04	01:31:00.0	XRT_Custom_418_OG [0x1a2]							
2011/09/04	01:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/04	02:46:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/04	02:46:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/04	02:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/04	02:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/04	03:07:30.0	XRT_Custom_418_OG [0x1a2]							
2011/09/04	03:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/04	04:16:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/04	04:16:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/04	04:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/04	04:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/04	04:45:00.0	XRT_Custom_418_OG [0x1a2]							
2011/09/04	04:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/04	05:28:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/04	05:28:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/09/04	05:29:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/09/04	05:29:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/09/04	05:29:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/09/04	05:29:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/09/04	05:31:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				

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2011/09/04	05:32:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/04	05:38:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/04	05:39:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00
2011/09/04	05:41:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/09/04	05:41:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/09/04	05:41:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/09/04	05:41:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/09/04	05:41:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/09/04	05:41:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/04	05:41:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2011/09/04	05:41:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2011/09/04	05:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/04	05:56:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/04	05:56:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/04	05:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/04	05:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/04	06:22:30.0	XRT_Custom_418_OG [0x1a2]				
2011/09/04	06:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/04	07:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/04	07:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	04 00 00 00 00
2011/09/04	10:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/04	10:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00
2011/09/04	10:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/09/04	10:32:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/09/04	10:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/09/04	10:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/09/04	10:32:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/09/04	10:32:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/04	10:32:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2011/09/04	10:32:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2011/09/04	10:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/04	14:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/04	14:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/04	14:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/04	14:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/04	14:55:00.0	XRT_Custom_418_OG [0x1a2]				
2011/09/04	14:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/04	16:18:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/04	16:18:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/04	16:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/04	16:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/04	16:42:00.0	XRT_Custom_418_OG [0x1a2]				
2011/09/04	16:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/04	17:44:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/04	17:44:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/09/04	17:45:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2011/09/04	17:45:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/09/04	17:45:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9



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2011/09/04	17:45:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/09/04	17:47:58.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2011/09/04	17:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/04	17:54:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/04	17:55:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/09/04	17:57:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/09/04	17:57:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/09/04	17:57:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/09/04	17:57:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/09/04	17:57:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/09/04	17:57:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/04	17:57:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2011/09/04	17:57:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2011/09/04	18:19:00.0	XRT_Custom_418_OG [0x1a2]			
2011/09/04	18:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/04	19:32:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/04	19:32:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/04	19:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/04	19:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/04	19:55:30.0	XRT_Custom_418_OG [0x1a2]			
2011/09/04	19:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/04	21:10:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/04	21:10:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/04	21:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/04	21:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/04	21:33:00.0	XRT_Custom_418_OG [0x1a2]			
2011/09/04	21:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/04	22:47:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/04	22:47:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/04	22:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/04	22:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/04	23:07:30.0	XRT_Custom_418_OG [0x1a2]			
2011/09/04	23:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/05	00:24:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/05	00:24:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/05	00:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/05	00:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/05	00:32:00.0	XRT_Custom_418_OG [0x1a2]			
2011/09/05	00:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/05	01:48:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/05	01:48:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/05	01:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/05	01:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/05	02:07:00.0	XRT_Custom_418_OG [0x1a2]			
2011/09/05	02:08:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/05	03:23:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/05	03:23:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/05	03:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/05	03:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/05	03:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]			

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2011/09/05	06:03:54.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00	00	00	00	00
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2011/09/05	06:04:14.0	XRT_FLD_DIS_402_OG [0x192]								
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2011/09/05	06:04:16.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]								
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2011/09/05	06:04:18.0	XRT_ARS_DIS_438_OG [0x1b6]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2011/09/05	06:06:56.0	XRT_QT_PROG_SET_410_OG [0x19a]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	07			
2011/09/05	06:06:58.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2011/09/05	07:30:00.0	AOCs_OrE-point_Start_4_OG [0x09a]								
		AOCU_NM		5	02-76	01	00	00	00	00
2011/09/05	10:35:30.0	AOCs_OrE-point_Start_5_OG [0x09b]								
		AOCU_NM		5	02-76	00	54	00	00	00
2011/09/05	11:00:30.0	AOCs_OrE-point_Start_6_OG [0x09c]								
		AOCU_NM		5	02-76	00	4b	1a	00	00
2011/09/05	11:25:30.0	AOCs_OrE-point_Start_7_OG [0x09d]								
		AOCU_NM		5	02-76	00	42	35	00	00
2011/09/05	11:50:30.0	AOCs_OrE-point_Start_8_OG [0x09e]								
		AOCU_NM		5	02-76	00	39	58	00	00
2011/09/05	12:15:30.0	AOCs_OrE-point_Start_9_OG [0x09f]								
		AOCU_NM		5	02-76	00	30	72	00	00
2011/09/05	12:40:30.0	AOCs_OrE-point_Start_10_OG [0x0a0]								
		AOCU_NM		5	02-76	00	27	8d	00	00
2011/09/05	13:05:30.0	AOCs_OrE-point_Start_11_OG [0x0a1]								
		AOCU_NM		5	02-76	00	1e	a7	00	00
2011/09/05	13:30:30.0	AOCs_OrE-point_Start_12_OG [0x0a2]								
		AOCU_NM		5	02-76	00	15	ca	00	00
2011/09/05	13:55:30.0	AOCs_OrE-point_Start_13_OG [0x0a3]								
		AOCU_NM		5	02-76	00	0c	e5	00	00
2011/09/05	14:20:24.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2011/09/05	14:20:26.0	XRT_FOCUS_POSITION_401_OG [0x191]								
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2011/09/05	14:20:30.0	AOCs_OrE-point_Start_14_OG [0x0a4]								
		AOCU_NM		5	02-76	00	04	00	00	00
2011/09/05	14:20:46.0	XRT_FLD_DIS_402_OG [0x192]								
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2011/09/05	14:20:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]								
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2011/09/05	14:20:50.0	XRT_ARS_DIS_438_OG [0x1b6]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2011/09/05	14:23:28.0	XRT_QT_PROG_SET_410_OG [0x19a]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	07			
2011/09/05	14:23:30.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2011/09/05	14:45:30.0	AOCs_OrE-point_Start_15_OG [0x0a5]								
		AOCU_NM		5	02-76	00	fc	00	00	00
2011/09/05	15:43:30.0	AOCs_OrE-point_Start_16_OG [0x0a6]								
		AOCU_NM		5	02-76	00	f3	1b	00	00
2011/09/05	16:08:30.0	AOCs_OrE-point_Start_17_OG [0x0a7]								
		AOCU_NM		5	02-76	00	ea	36	00	00
2011/09/05	17:19:30.5	AOCs_OrE-point_Start_18_OG [0x0a8]								
		AOCU_NM		5	02-76	00	e1	59	00	00
2011/09/05	17:44:30.0	AOCs_OrE-point_Start_19_OG [0x0a9]								
		AOCU_NM		5	02-76	00	d8	73	00	00
2011/09/05	18:56:30.0	AOCs_OrE-point_Start_20_OG [0x0aa]								
		AOCU_NM		5	02-76	00	cf	8e	00	00
2011/09/05	19:21:30.0	AOCs_OrE-point_Start_21_OG [0x0ab]								
		AOCU_NM		5	02-76	00	c6	a8	00	00
2011/09/05	20:33:30.0	AOCs_OrE-point_Start_22_OG [0x0ac]								
		AOCU_NM		5	02-76	00	bd	cb	00	00
2011/09/05	20:58:30.0	AOCs_OrE-point_Start_23_OG [0x0ad]								
		AOCU_NM		5	02-76	00	b4	e6	00	00
2011/09/05	22:10:00.0	AOCs_OrE-point_Start_24_OG [0x0ae]								
		AOCU_NM		5	02-76	00	ac	00	00	00
2011/09/05	22:39:54.0	XRT_CTRL_MANU_439_OG [0x1b7]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2011/09/05	22:40:00.0	AOCs_OrE-point_Start_1_OG [0x097]								
		AOCU_NM		5	02-76	03	00	00	00	00
2011/09/05	22:42:26.0	XRT_FOCUS_RECALIBRATE_430_OG [0x1ae]								
		XRT_FOCUS_RECAL		2	07-F8	78	00			
2011/09/05	22:46:26.0	XRT_FOCUS_POSITION_409_OG [0x199]								
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2011/09/05	22:46:46.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2011/09/05	22:46:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2011/09/05	22:46:50.0	XRT_AEC_RESET_443_OG [0x1bb]								
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2011/09/05	22:46:52.0	XRT_ARS_DIS_431_OG [0x1af]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2011/09/05	22:46:54.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2011/09/05	22:46:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	12			
2011/09/05	22:46:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]								
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	0d			
2011/09/05	22:47:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				



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2011/09/06	07:12:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/06	07:12:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/06	07:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/06	07:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/06	07:30:00.0	AOCS_ORe-point_Start_25_OG [0x0af]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2011/09/06	10:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/06	10:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/09/06	10:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/09/06	10:32:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/09/06	10:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/09/06	10:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/09/06	10:32:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/09/06	10:32:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/06	10:32:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/09/06	10:32:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/09/06	10:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/06	10:34:00.5	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/06	10:34:02.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/06	10:34:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/06	10:37:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/06	10:40:30.0	XRT_Custom_418_OG [0x1a2]							
2011/09/06	10:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/06	11:04:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/06	11:05:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				