

# XRT Timeline to be uploaded on 2011/09/06

Period: 2011/09/06 10:55:00 - 2011/09/09 09:31:00

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Normal mode

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## XOB #187C: Synoptic Q95 2x2 - Al/mesh(45/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2897) + Thin-Be(36)

Term	Pointing (x, y)	Comment
09/06 11:08:00 - 09/06 11:14:54	Fixed ( 0.0, 0.0)	OP start + 10min: synoptic
09/07 05:45:00 - 09/07 05:51:54	Fixed ( 0.0, 0.0)	synoptic, shifted -18.0 min
<b>PROG= 07 1-time(s)</b>		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 86 1-time(s) 4.0sec		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 44ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 37 1-time(s) 4.0sec		
└─ Open/Ti-poly	Open/Ti-poly close Safe Norm 86ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/Ti-poly close Safe Norm 2.83s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 32 1-time(s) 2.0sec		
└─ thin-Be/Open	thin-Be/Open close Safe Norm 354ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ thin-Be/Open	thin-Be/Open close Safe Norm 2.83s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 4 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 16ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #18A5: North pole+AR - Ti/poly 512ms, G-band - 2x2-fov2048x1024-center(1024,640)-2min

Term	Pointing (x, y)	Comment
09/06 11:18:00 - 09/07 04:14:54	Fixed ( -9.0, 905.0)	# HOP 101 (for SOT) & 163 (for EIS) - N-pole for 17 hours
<b>PROG= 05 Inf.-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 34 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 12ms	Obs 2x2 2048x1024 (1024, 640) Q=95 0 0 2.0sec
└─ Subr= 2 15-time(s) 2.0sec		
└─ Seqn= 16 2-time(s) 120.0sec		
└─ Open/Ti-poly	Open/thick-Al close Safe Norm 500ms	Obs 2x2 2048x1024 (1024, 640) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #189F: AR Standard-A(Filter-Ratio) with PFB, thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048, 60s cad

Term	Pointing (x, y)	Comment
09/07 04:18:00 - 09/07 05:41:54	Track ( 328.5, 100.9) <sup>09/07 04:15:00</sup>	AR 11283 tracking
<b>PROG= 18 Inf.-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 19 1-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 63ms	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Seqn= 76 4-time(s) 2.0sec		
└─ Al-poly/Open	thin-Be/Open close Safe Norm 500ms	Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close Safe Norm 16.0s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/thick-Al	Open/thick-Al close Safe Norm 16.0s	Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 62 20-time(s) 2.0sec		
└─ thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
└─ thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
└─ Open/Ti-poly	Open/thick-Al close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
└─ thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
└─ Open/Ti-poly	Open/thick-Al close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
└─ thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
└─ Open/Ti-poly	Open/thick-Al close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #18A3: AR dynamics - only Thin-Be - 384x384 - Q95 - 15s - AEC4

Term	Pointing (x, y)	Comment
09/07 05:55:00 - 09/07 06:09:30	Track ( 342.0, 101.5) <sup>09/07 05:52:00</sup>	# AR 11283 tracking
<b>PROG= 10 Inf.-time(s)</b>		
└─ Subr= 1 1-time(s) 15.0sec		
└─ Seqn= 64 1-time(s) 2.0sec		
└─ thin-Be/Open	thin-Be/Open close Safe Norm 1.00s	Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

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### Flare mode

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XOB #1869: Flare standard obs. multifilter (thin-Be,med-Al,thick-Be 384x384 - Al-poly 512x512 2x2)											
Term		Pointing (x, y)					Comment				
09/06 11:18:00 - 09/07 04:14:54		Fixed ( -9.0, 905.0)					# HOP 101 (for SOT) & 163 (for EIS) - N-pole for 17 hours				
09/07 04:18:00 - 09/07 05:41:54		Track ( 328.5, 100.9) <sup>09/07 04:15:00</sup>					AR 11283 tracking				
<b>PROG= 13 1-time(s)</b>											
└─ <b>Subr= 1 4-time(s) 2.0sec</b>											
└─ <b>Seqn= 55 45-time(s) 20.0sec</b>											
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs 2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ <b>Seqn= 90 1-time(s) 2.0sec</b>											
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs 2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ <b>Subr= 2 1-time(s) 600.0sec</b>											
└─ <b>Seqn= 89 1-time(s) 2.0sec</b>											
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs 1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs 1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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### Active Region Search

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NOT USED

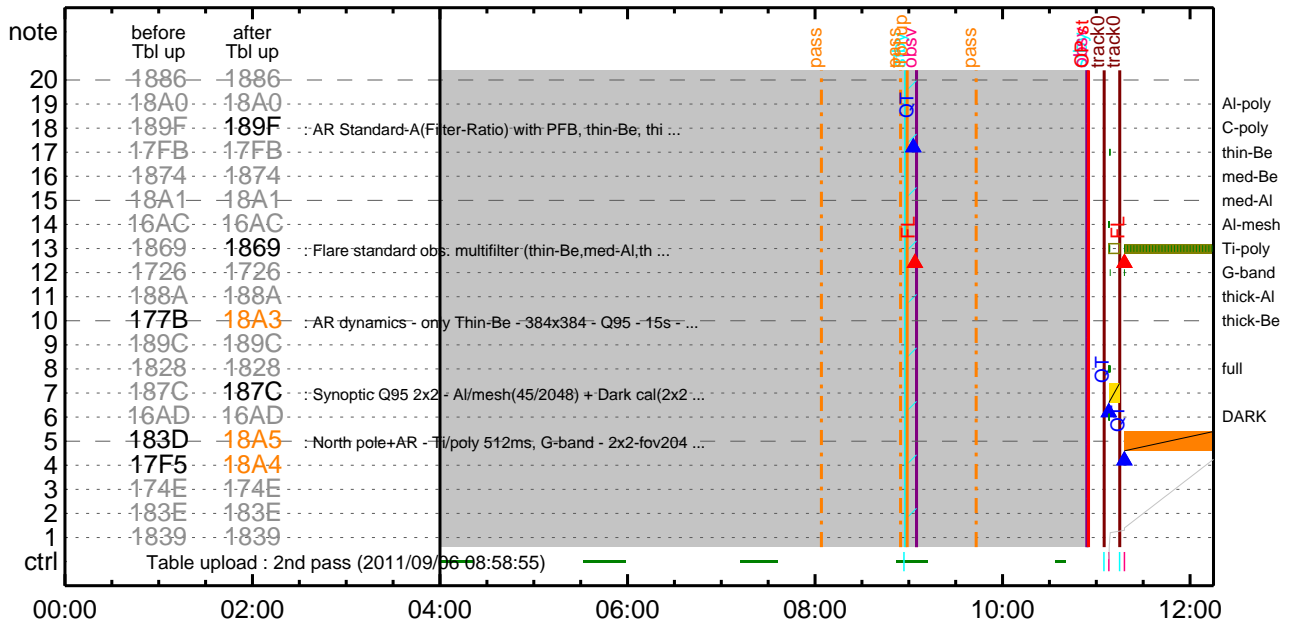
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### Flare Detection

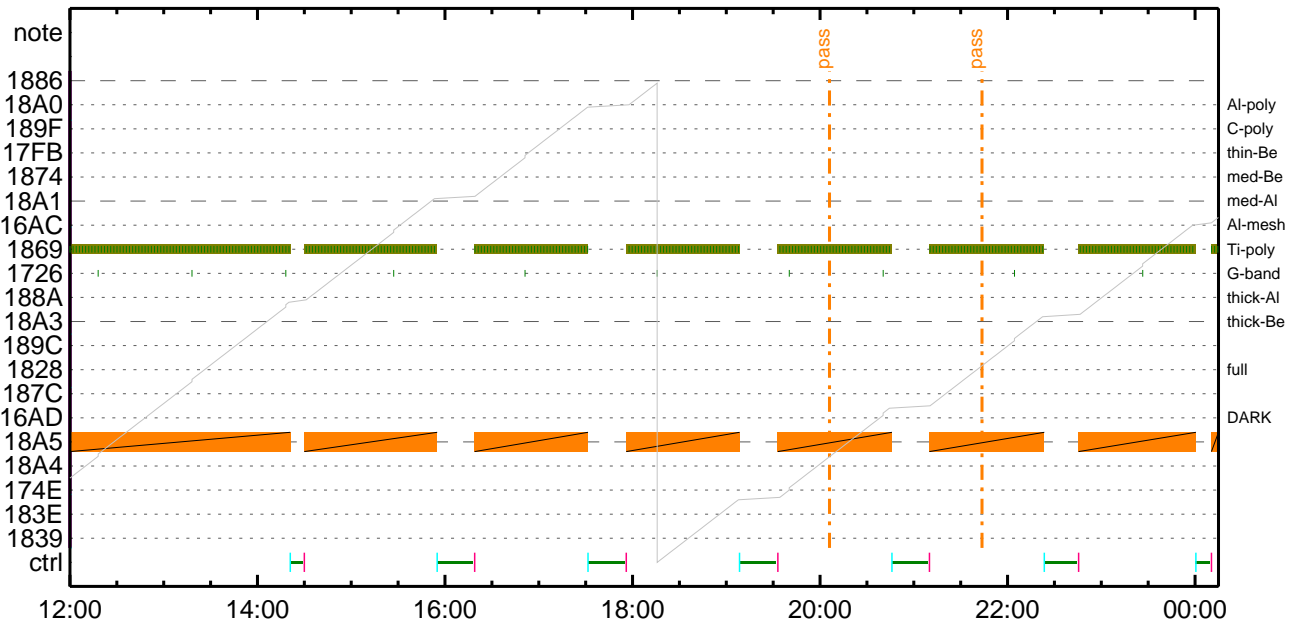
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FLD Patrol											
Term		Pointing (x, y)					Comment				
09/06 11:17:46 - 09/07 05:42:16		Fixed ( -9.0, 905.0)					# HOP 101 (for SOT) & 163 (for EIS) - N-pole for 17 hours				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs 8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

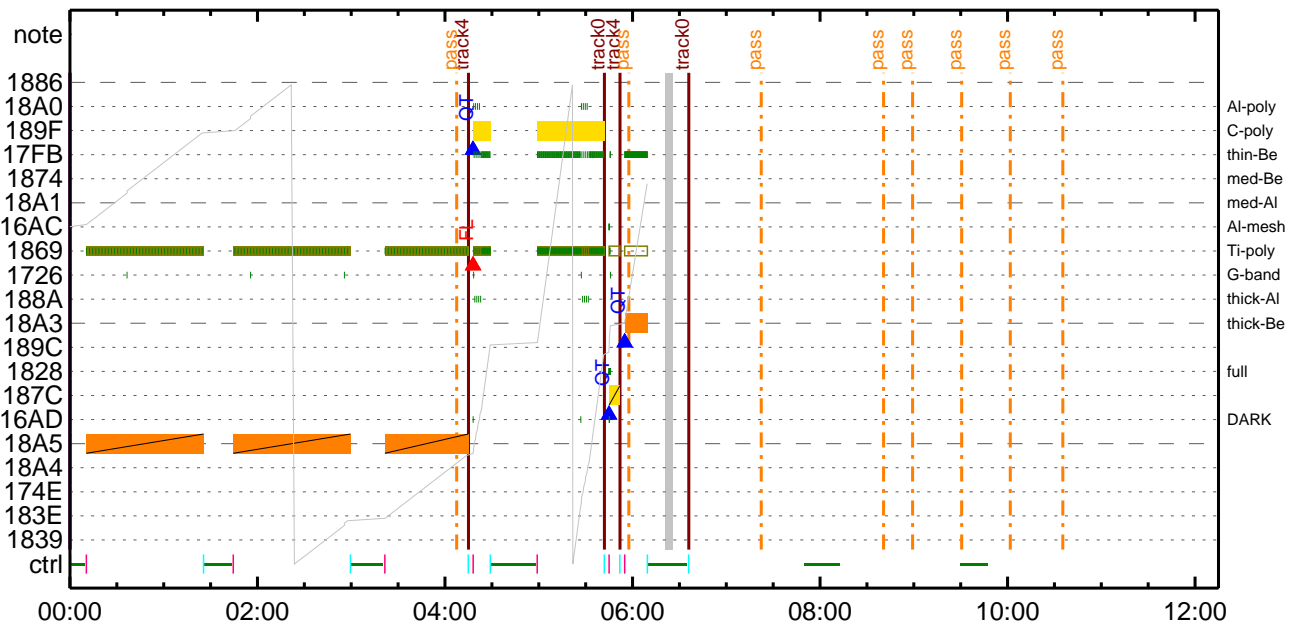
### CMDI #0127 2011/09/06



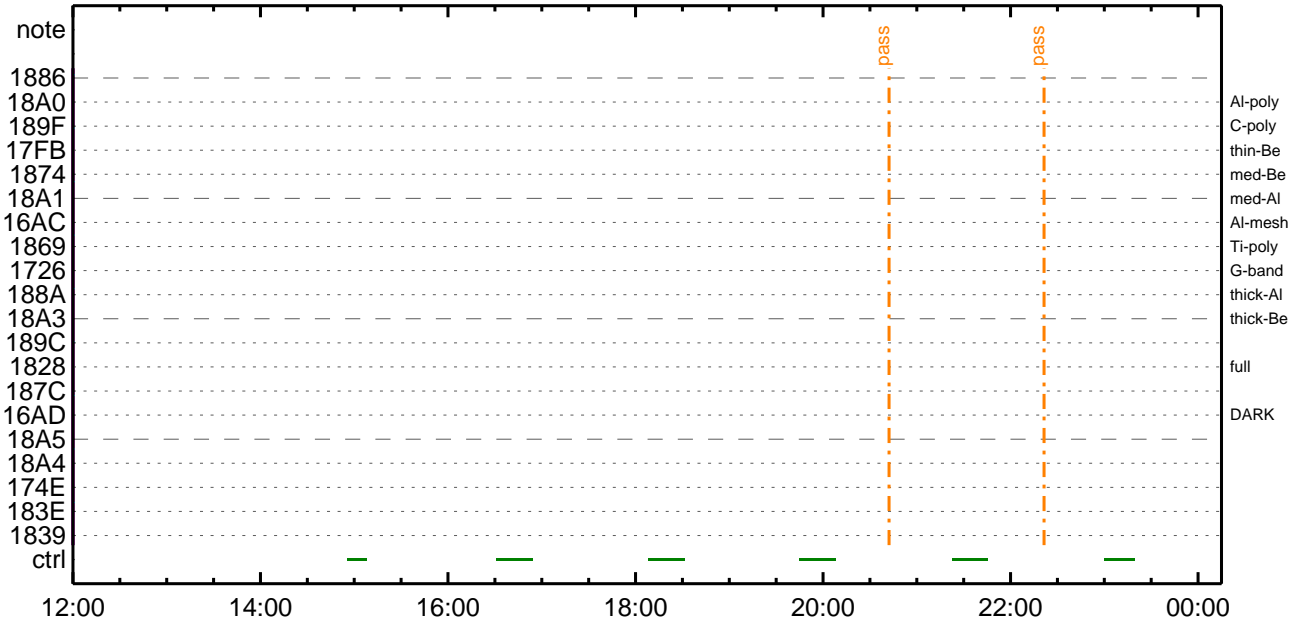
### CMDI #0127 2011/09/06



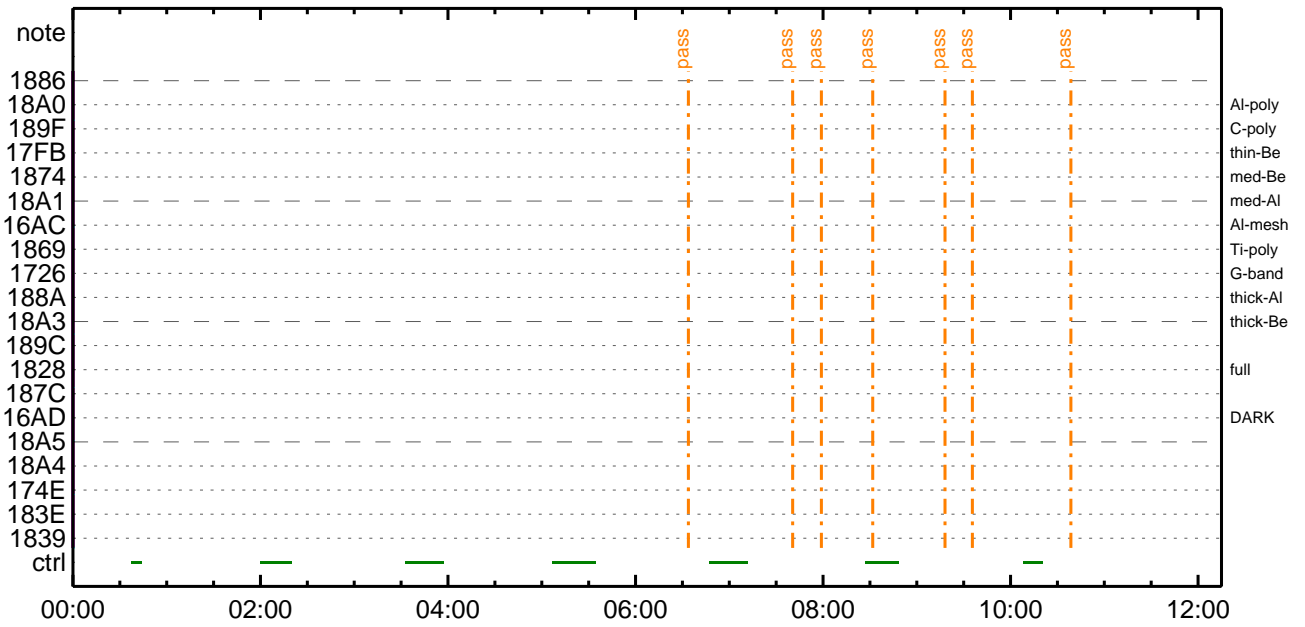
### CMDI #0127 2011/09/07



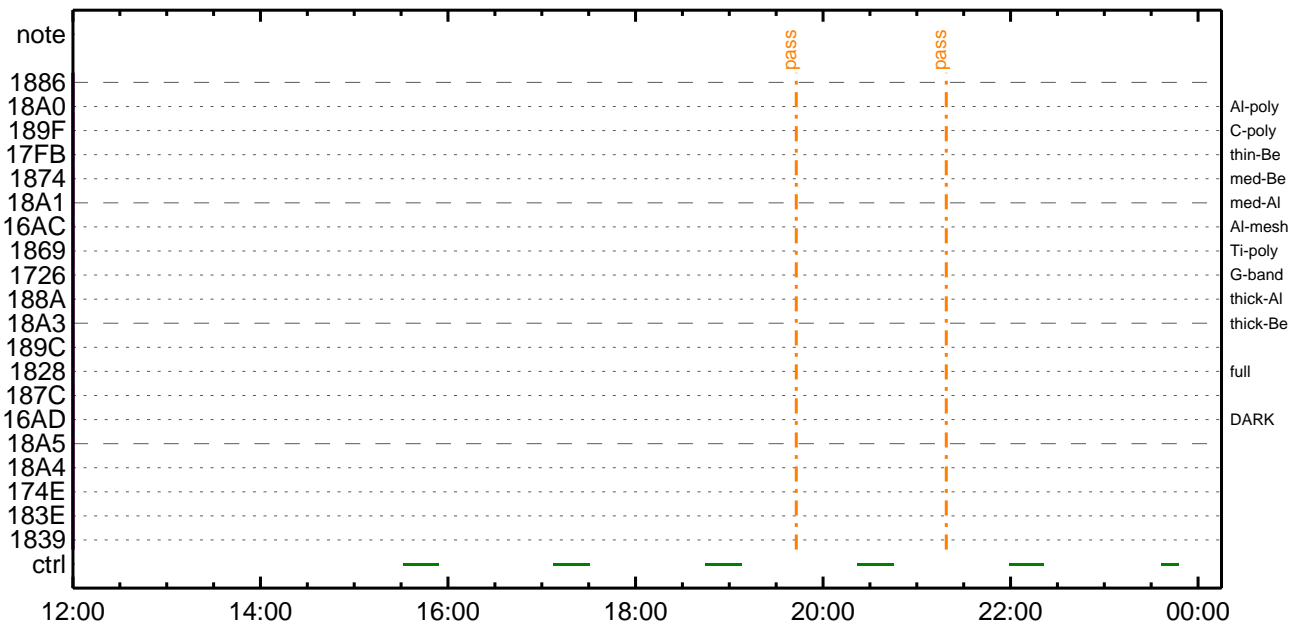
### CMDI #0127 2011/09/07



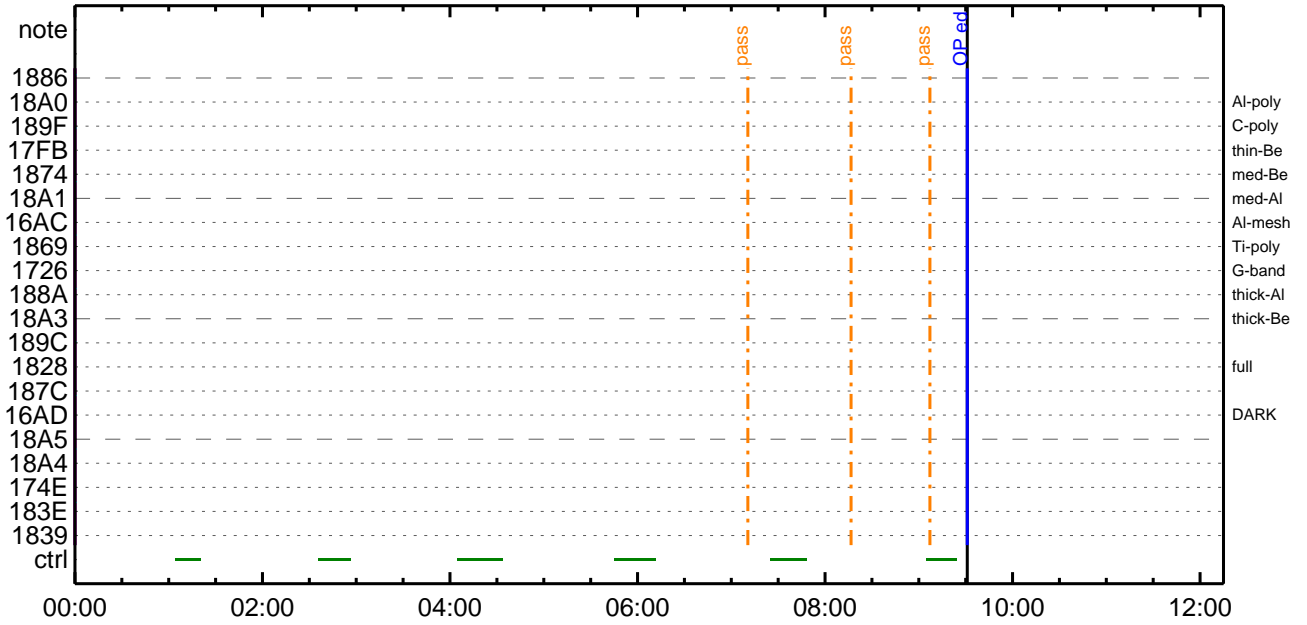
### CMDI #0127 2011/09/08



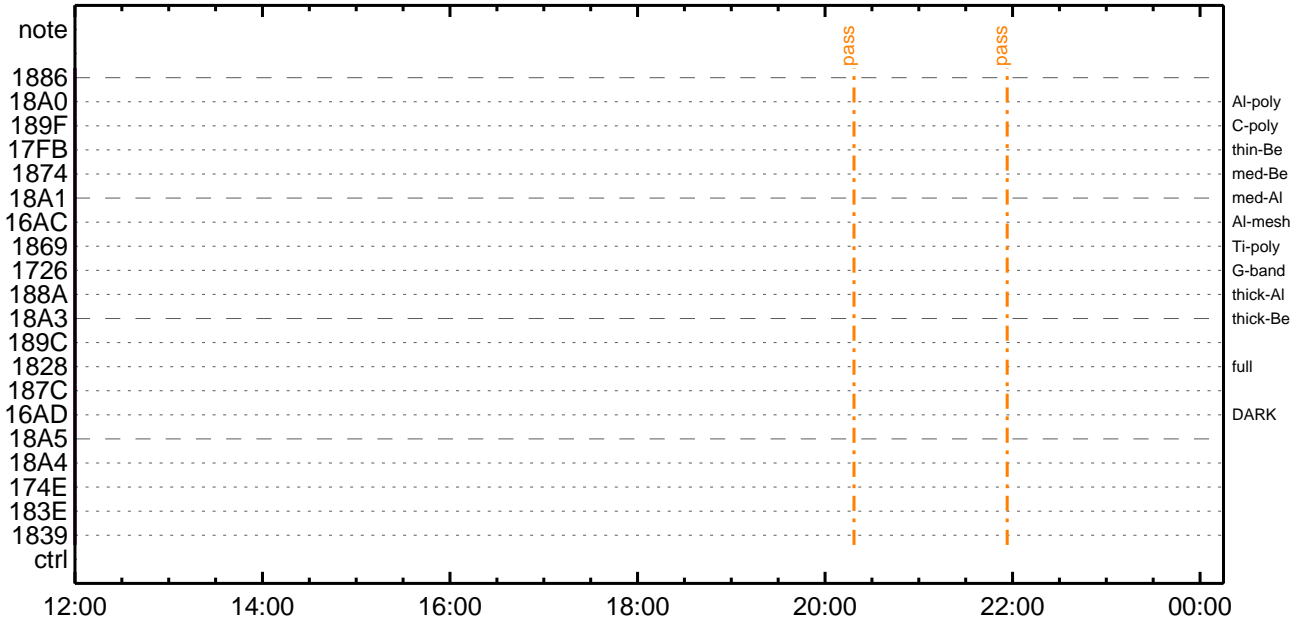
### CMDI #0127 2011/09/08



CMDI #0127 2011/09/09



CMDI #0127 2011/09/09





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-305:OP
0104 ( )
0105 S. OG og-305:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYÅYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YÅYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YÅYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YÅYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YÅYôYx½ê½çòðÁÓæòÇ¼ª°²°è¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ô²°²E²²²³²E;f
0180 C. ²²²²;çSET²E²DUMP²îE²±²îY²¹²ç¹Ô²²²²³²E;f
0181 C.
0182 C. TIY³Y²YôYÉ²òðÁDî¿(UT)
0183 +. TI 2011-09-06 10:50:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-09-06 10:50:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-09-06 10:50:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2011-09-06 10:54:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      çç[HK1_TI_CMD_NUM]                    EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ïÄê%íññîîŷÄŷ§ŷÄŷ-¹àîŰ
0200 C.      çç[HK1_TI_CMD_ENA/DIS]                 EQ      ENA
0201 C.      çç[HK1_TI_CMD_NUM]                    EQ      4
0202 C.      çç[HK1_NEXT_EXEC_PIM]                EQ      DHU
0203 C.      çç[HK1_NEXT_EXEC_DC]                 EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÔŷx
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      çç[HK1_DMP_TOP_ADRS_1]               EQ      07
0213 C.      çç[HK1_DMP_TOP_ADRS_0]               EQ      2B
0214 C.      çç[HK1_DMP_BLOCK_NUM]                EQ      3
0215 C.      çç[HK1_DMP_REPEAT_NUM]              EQ      0
0216 C.      çç[HK1_DMA_DMP_PIM]                  EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      çç[HK1_PKT_FORM_NO]                  EQ      7
0220 C.      çç[HK1_PKT_GEN_TIME]                  EQ      0.25 s
0221 C.      çç[HK1_S_TLM_BIT_RATE]               EQ      32k
0222 C.      çç[HK1_X_TLM_BIT_RATE]               EQ      4M
0223 C.      çç[HK1_DMP_CHK_FLG]                  EQ      EXEC
0224 C.
0225 C. ŷÄŷÔŷx½ªî»ò³îç§
0226 C.      çç[HK1_DMP_CHK_FLG]                  EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLñî¾È¹ç•è²îOKò³îç§
0229 C.
0230 C. DHUŷä;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èòðîää¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      çç[HK1_PKT_FORM_NO]                  EQ      2
0234 C.      çç[HK1_PKT_GEN_TIME]                  EQ      0.5S
0235 C.      çç[HK1_S_TLM_BIT_RATE]               EQ      32K
0236 C.      çç[HK1_X_TLM_BIT_RATE]               EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2011-09-06 10:54:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2011-09-06 10:54:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2011-09-06 10:54:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.      [ ] [HK1_TI_CMD_NUM]                    EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-09-06 10:54:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM]                    EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÄîäî»ö¼ŷòÉÄð¹ñèDCBC•x²è *****
0276 C. (¼á°îŷŷÄŷÈŷŷÈŷäŷçŷèèÈ¼òñ¼Ä»Űñ¹è)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîññè´Øñ¹èèDCBC•x²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ä
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-306 2011-09-06 12:48:50 164 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼ÁÜ;ã
0005 C.
0006 C. YÁYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αÁα•μ°È»Í×ÁÇαÍY¿YÁY×Yí;¼YÉ;ÈÈ%μ•íÉ;ÈÈ¼°ÇÔα•α¿¼í¹¿αÍ;çÀ®, ùα¹αÈαBαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCs Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 4973228.2 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_MANU
0056 BC (21 02)
0057 . C. Verify EIS in MANUAL mode
0058 . C. Estimated OBSTBL upload time is 12s
0059 C. *****
0060 C. EIS START OBSTBL LOAD
0061 C. *****
0062 . S. RAM ram-820:EIS_OBSTBL
0063 ( )
0064 +. DC 07-FC EIS_DUMP_OBSTBL
0065 BC (07 07 07 00 00 70 00)
0066 C.
0067 C. Execute, after the success of OBSTBL upload.
0068 C. Set EIS TI-commands
0069 +. TI 2011-09-06 10:54:50.0
0070 DC 07-FC EIS_MODE_CHG_ENA
0071 BC (20)
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0073 C. *****
0074 C. EIS END OBSTBL LOAD
0075 C. *****
0076 C.
0077 C. ***** XRT START *****
0078 C.
0079 +. DC 07-F0 MDP_XRT_CTRL_MANU
0080 BC (c1)
0081 +. DC 07-F0 MDP_XRT_MODE_STBY
0082 BC (c3)
0083 . C. ----- Success Verify ? OK / NG____
0084 C.
0085 C. XRT Obs. Table Upload
0086 . S. RAM ram-291:MDP_OBS_X
0087 ( )
0088 C.
0089 +. DC 07-F0 MDP_DUMP_XRTTBL
0090 BC (84 07 00 00 00 3a d4)
0091 . C. ----- Comparison Check ? OK / ERR ____
0092 C.
0093 C.
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 01 b1 b1 04 04)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 02 b1 b1 08 08)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 03 b1 b1 08 08)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 04 b1 b1 06 06)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 05 85 83 06 06)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 06 85 83 06 06)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 07 80 80 08 08)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 08 80 80 20 20)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 09 80 80 20 08)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 0a 80 80 08 20)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 0b 80 50 20 10)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 0c 80 80 06 06)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 0f 80 80 06 06)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 10 80 80 08 08)
0122 + DC 07-F0 MDP_XRT_FLD_ENA
0123 BC (d8)
0124 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0125 BC (c8)
0126 + DC 07-F0 MDP_XRT_AEC_RESET
0127 BC (d0)
0128 + DC 07-F0 MDP_XRT_ARS_DIS
0129 BC (d5)
0130 + DC 07-F0 MDP_XRT_FLD_RESET
0131 BC (da)
0132 + DC 07-F0 MDP_XRT_QT_PROG_SET
0133 BC (c4 12)
0134 + DC 07-F0 MDP_XRT_FL_PROG_SET
0135 BC (c5 0d)
0136 . C. ----- Success Verify ? OK / NG ____
0137 C.
0138 C.
0139 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0140 C.
0141 + DC 07-F0 MDP_XRT_MODE_OBSV
0142 BC (c2)
0143 + TI 2011-09-06 10:54:02.0
0144 DC 07-F0 MDP_XRT_MODE_OBSV
0145 BC (c2)
0146 . C. ----- Success Verify ? OK / NG ____
0147 C.
0148 C. ***** XRT END *****
0149 C.
0150 . C. ***** MDP `uãîï»ö%ýðÊâð¹ðêDCBC•x²è *****
0151 C. (%ã°îÿÓYÁYBYBYÿYáYçYèðÊ%¼ðð%Á»Û¹ðé)
0152 . S. DC-BC dcbc-402:DCBC
0153 (MDP_known_event)
0154 C.
0155 C.
0156 . C. ***** YDY¹•İ Daily±¿İÑðÊ´Ø¹ðêDCBC•x²è *****
0157 . S. DC-BC dcbc-153:DCBC
0158 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0159 C.
0160 C.
0161 . C. ;ãLOS¥Á¥$¥Ã¥¹¼Á»Û;ä
0162 C.
0163 . C. ***** LOS *****
0164 C.
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\*\*\* OP Sequence for XRT \*\*\*

2011/09/06	11:04:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/06	11:04:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/09/06	11:05:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/09/06	11:05:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/09/06	11:05:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/09/06	11:05:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/09/06	11:07:58.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2011/09/06	11:08:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/06	11:14:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/06	11:15:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 af 8e 00 cc
2011/09/06	11:17:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/09/06	11:17:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/09/06	11:17:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/09/06	11:17:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/09/06	11:17:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/09/06	11:17:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/06	11:17:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2011/09/06	11:17:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2011/09/06	11:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/06	14:21:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/06	14:21:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/06	14:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/06	14:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/06	14:29:00.0	XRT_Custom_418_OG [0x1a2]			
2011/09/06	14:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/06	15:55:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/06	15:55:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/06	15:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/06	15:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/06	16:18:00.0	XRT_Custom_418_OG [0x1a2]			
2011/09/06	16:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/06	17:31:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/06	17:31:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/06	17:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/06	17:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/06	17:55:00.0	XRT_Custom_418_OG [0x1a2]			
2011/09/06	17:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/06	19:08:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/06	19:08:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/06	19:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/06	19:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/06	19:32:00.0	XRT_Custom_418_OG [0x1a2]			
2011/09/06	19:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/06	20:46:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/06	20:46:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/06	20:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/06	20:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/06	21:09:00.0	XRT_Custom_418_OG [0x1a2]			

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2011/09/06	21:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/06	22:23:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/06	22:23:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/06	22:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/06	22:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/06	22:44:30.0	XRT_Custom_418_OG [0x1a2]							
2011/09/06	22:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/07	00:00:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/07	00:00:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/07	00:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/07	00:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/07	00:09:30.0	XRT_Custom_418_OG [0x1a2]							
2011/09/07	00:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/07	01:25:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/07	01:25:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/07	01:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/07	01:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/07	01:43:30.0	XRT_Custom_418_OG [0x1a2]							
2011/09/07	01:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/07	02:59:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/07	02:59:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/07	02:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/07	03:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/07	03:20:30.0	XRT_Custom_418_OG [0x1a2]							
2011/09/07	03:21:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/07	04:14:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/07	04:15:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/09/07	04:17:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/09/07	04:17:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/09/07	04:17:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/09/07	04:17:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/09/07	04:17:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/09/07	04:17:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/07	04:17:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/09/07	04:17:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/09/07	04:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/07	04:29:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/07	04:29:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/07	04:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/07	04:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/07	04:58:00.0	XRT_Custom_418_OG [0x1a2]							
2011/09/07	04:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/07	05:41:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/07	05:41:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/09/07	05:42:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/09/07	05:42:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/09/07	05:42:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/09/07	05:42:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/09/07	05:44:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				

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2011/09/07	05:45:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/07	05:51:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/07	05:52:00.0	AOCS_Orpoint_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	04 00 00 00 00
2011/09/07	05:54:32.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/09/07	05:54:52.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/09/07	05:54:54.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/09/07	05:54:56.0	XRT_ARS_DIS_449_OG [0x1c1]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/09/07	05:54:58.0	XRT_QT_PROG_SET_404_OG [0x194]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2011/09/07	05:55:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/09/07	06:09:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/07	06:09:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/09/07	06:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/09/07	06:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/09/07	06:35:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/09/07	06:36:00.0	AOCS_Orpoint_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 00 00 00 00