

XRT Timeline to be uploaded on 2011/09/29

Period: 2011/09/29 10:02:00 - 2011/10/04 09:50:00

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Normal mode

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XOB #18B2: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 512x512 at 1064 1048 (all), 105s cad

Term	Pointing (x, y)		Comment									
09/29 10:15:00 - 09/29 17:59:54	Track (176.8,	80.8)	# OP start + 10min, AR 11302.									
09/29 18:13:00 - 09/30 06:12:24	Track (247.3,	82.9)	# Cont.									
10/01 07:37:00 - 10/01 09:28:00	Track (551.9,	101.1)	* AR 11302.									
PROG= 19 1-time(s) 2.0sec												
Subr= 1 1-time(s) 2.0sec												
Seqn= 17 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 53 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 81 25-time(s) 105.0sec												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18AD: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be(12)

Term	Pointing (x, y)		Comment									
09/29 18:03:00 - 09/29 18:09:54	Fixed (0.0,	0.0)	synoptic									
09/30 06:15:30 - 09/30 06:22:30	Fixed (0.0,	0.0)	synoptic, shifted 12.5 min									
10/01 07:27:00 - 10/01 07:33:54	Fixed (0.0,	0.0)	* Synoptic, shifted manually.									
PROG= 04 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 7 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 8 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long

Term	Pointing (x, y)		Comment									
10/01 06:43:00 - 10/01 06:49:54	Fixed (-528.4,	-528.4)	* XRT post-bakeout four-quadrant pointings.									
PROG= 07 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 38 1-time(s) 12.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 93 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly -long

Term	Pointing (x, y)		Comment									
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PROG= 18 1-time(s)													
Subr= 1 1-time(s) 12.0sec													
Seqn= 36 1-time(s) 12.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 93 2-time(s) 2.0sec													
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
10/01 07:03:00 - 10/01 07:09:54	Fixed (528.4, 528.4)	

PROG= 08 1-time(s)													
Subr= 1 1-time(s) 12.0sec													
Seqn= 39 1-time(s) 12.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 93 2-time(s) 2.0sec													
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
10/01 07:13:00 - 10/01 07:23:54	Fixed (-528.4, 528.4)	* Last four-quadrant pointing.

PROG= 01 1-time(s)													
Subr= 1 1-time(s) 12.0sec													
Seqn= 40 1-time(s) 12.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(1536, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 93 2-time(s) 2.0sec													
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1869: Flare standard obs. multifilter (thin-Be,med-Al,thick-Be 384x384 - Al-poly 512x512 2x2)

Term	Pointing (x, y)	Comment
09/29 10:15:00 - 09/29 17:59:54	Track (176.8, 80.8) @ 09/29 10:12:00	# OP start + 10min, AR 11302.
09/29 18:13:00 - 09/30 06:12:24	Track (247.3, 82.9) @ 09/29 18:10:00	# Cont.
10/01 07:37:00 - 10/01 09:28:00	Track (551.9, 101.1) @ 10/01 07:34:00	* AR 11302.

PROG= 13 1-time(s)													
Subr= 1 4-time(s) 2.0sec													
Seqn= 55 45-time(s) 20.0sec													
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec
Seqn= 90 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 600.0sec													
Seqn= 89 1-time(s) 2.0sec													
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

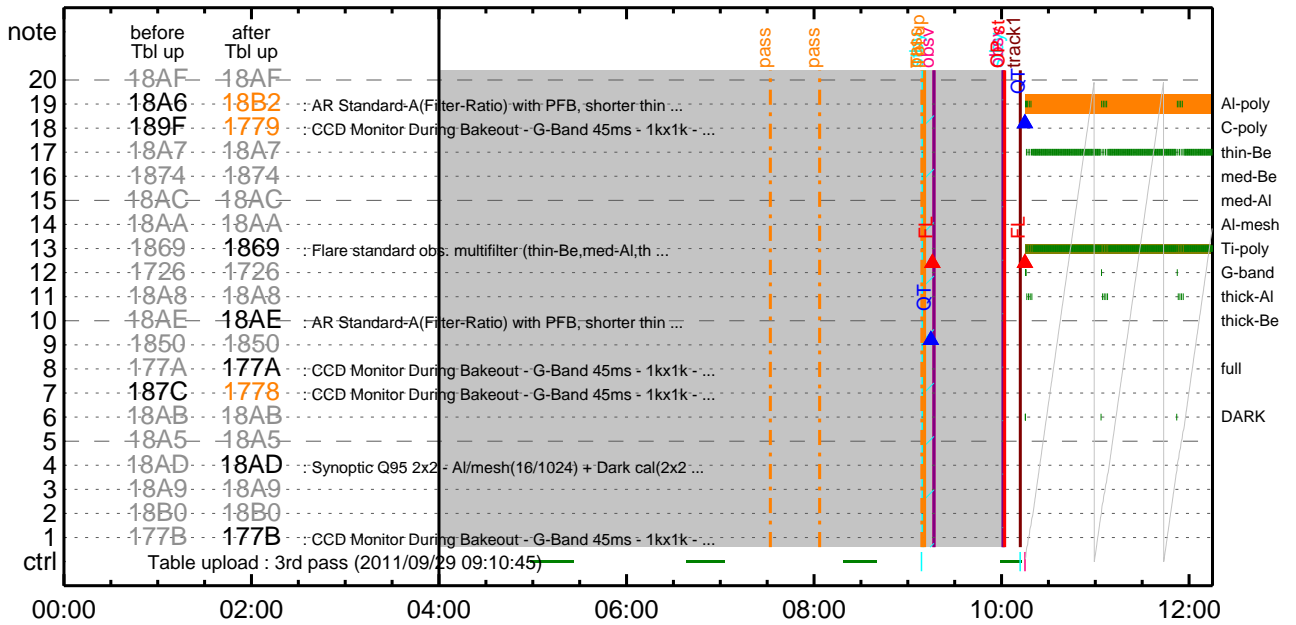
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Flare Detection

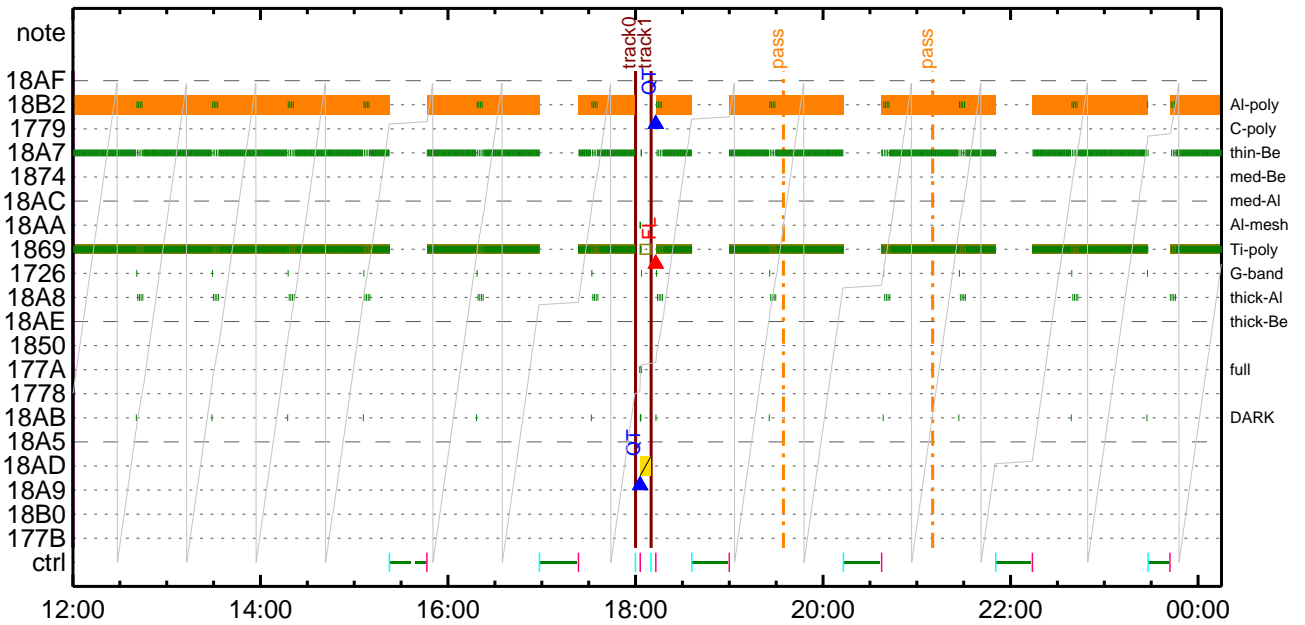
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FLD Patrol											
Term		Pointing (x, y)						Comment			
09/29 18:10:16 - 09/30 06:12:46		Track (247.3, 82.9) @ 09/29 18:10:00						# Cont.			
10/01 07:34:16 - 10/04 09:50:00		Track (551.9, 101.1) @ 10/01 07:34:00						* AR 11302.			
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

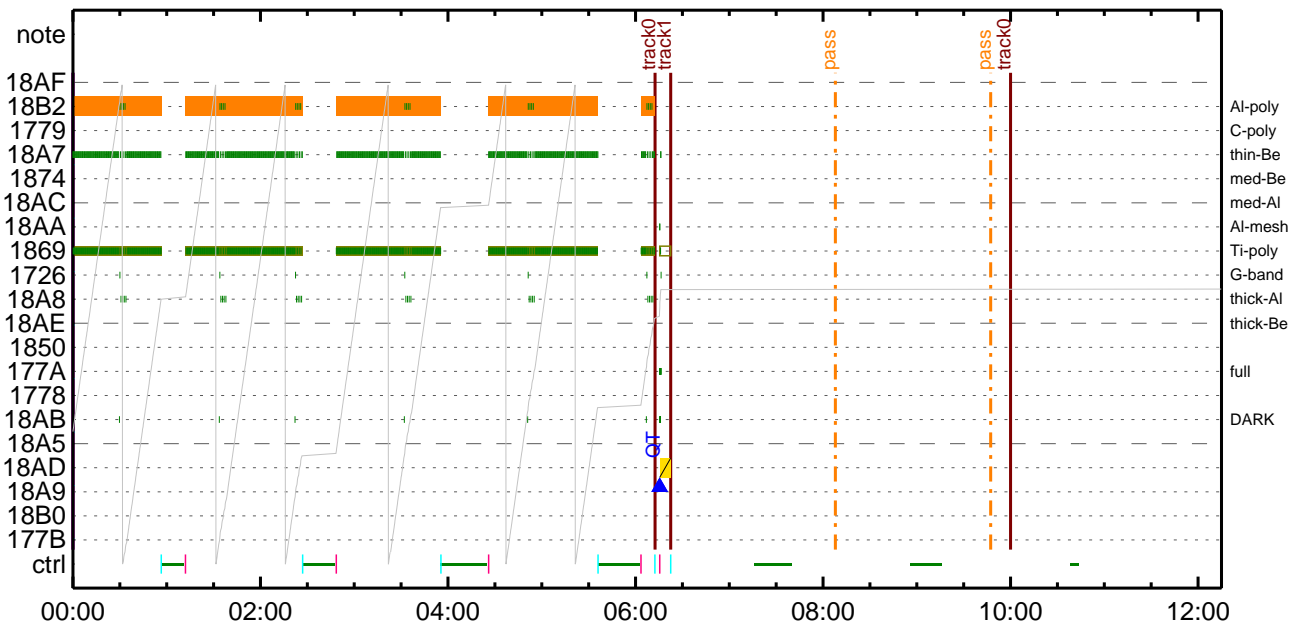
CMDI #0179 2011/09/29



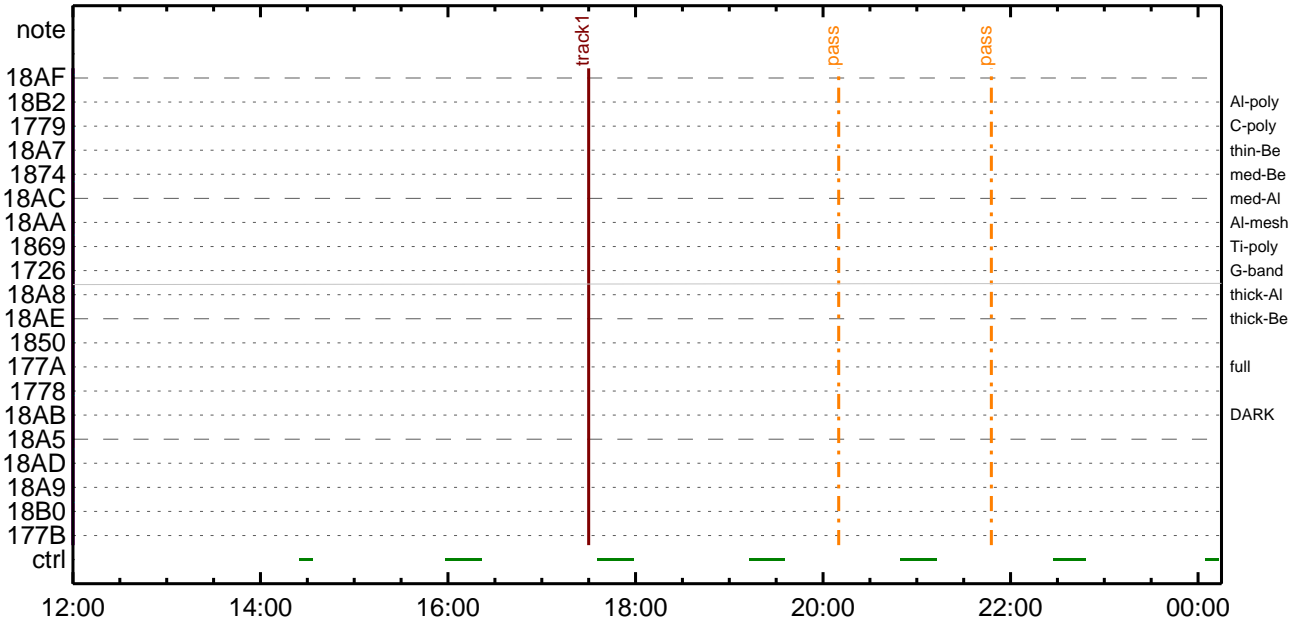
CMDI #0179 2011/09/29



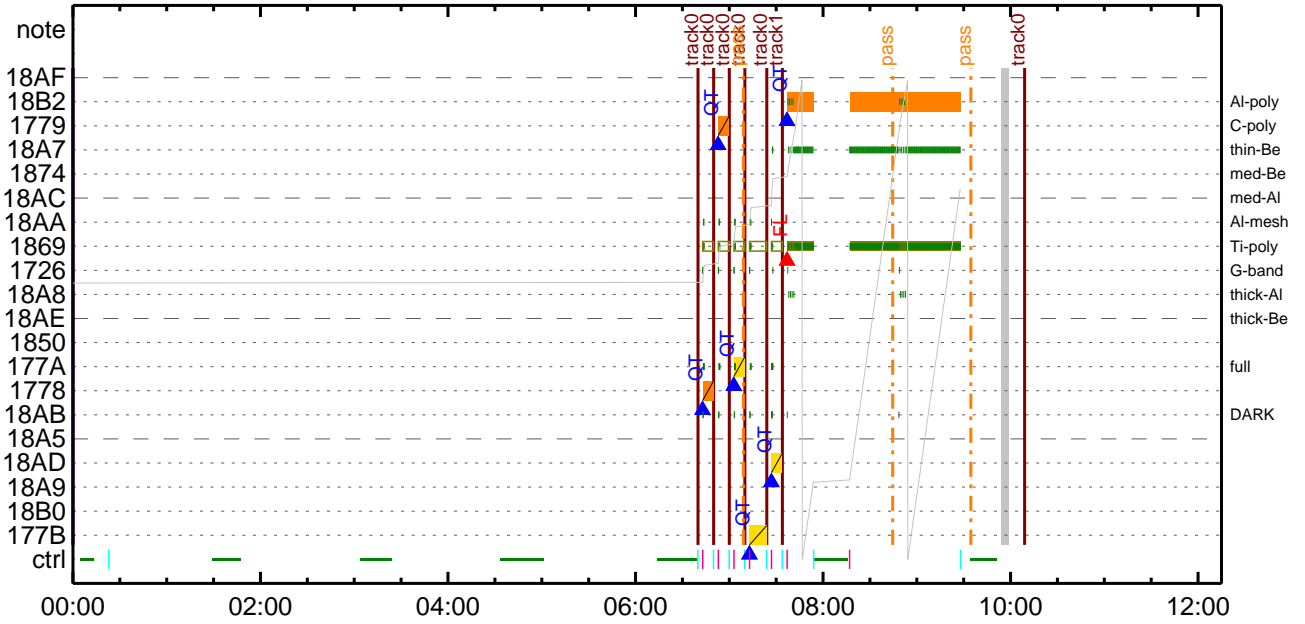
CMDI #0179 2011/09/30



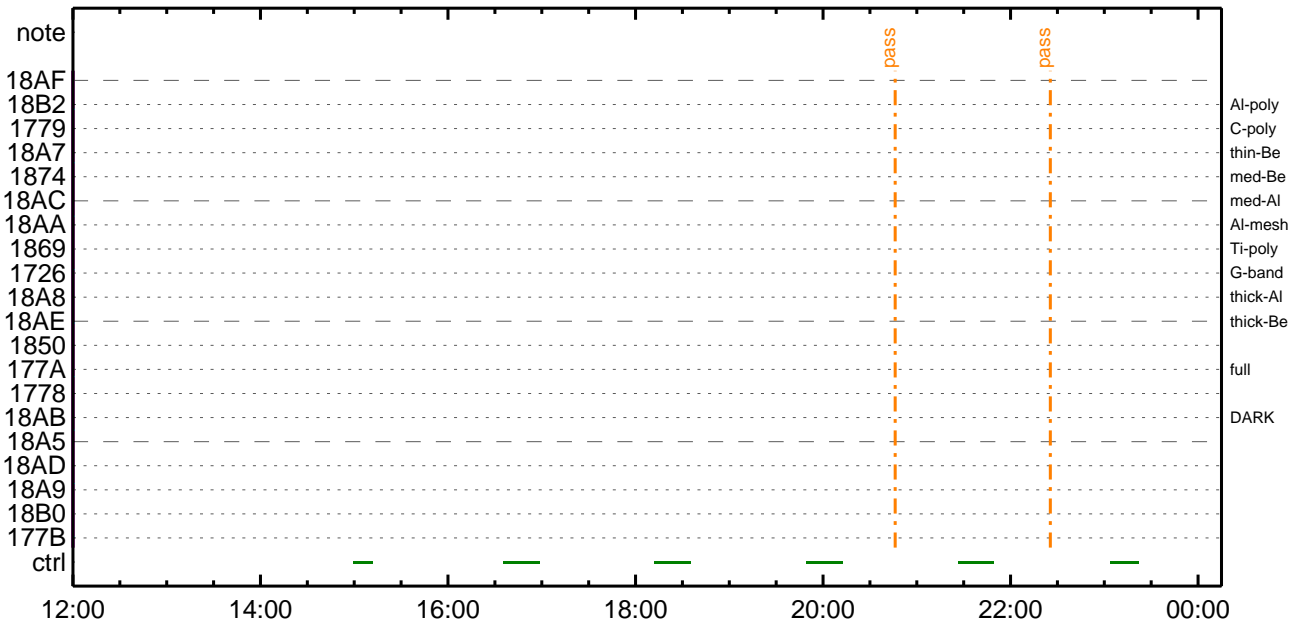
CMDI #0179 2011/09/30



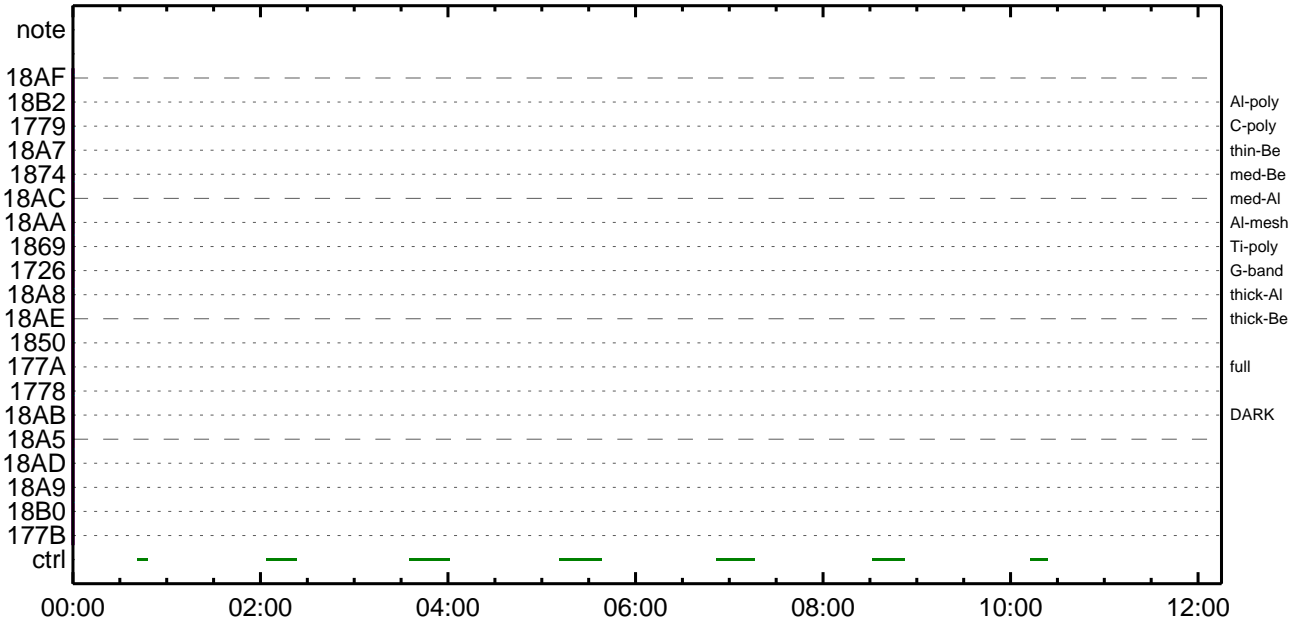
CMDI #0179 2011/10/01



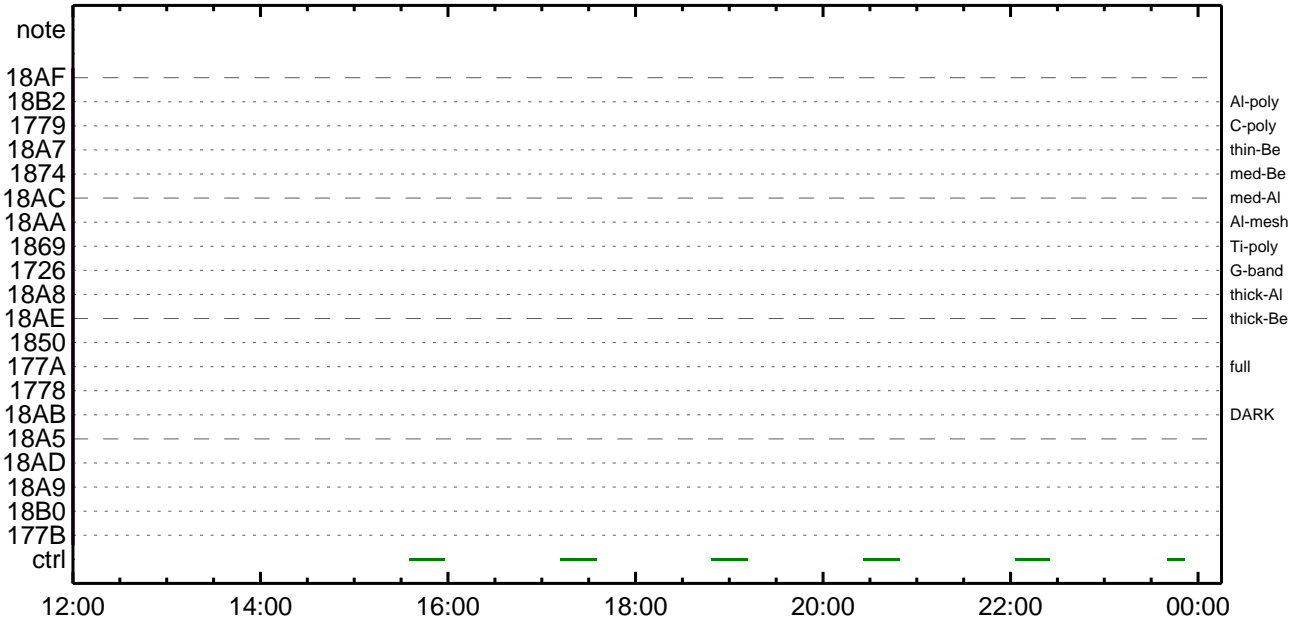
CMDI #0179 2011/10/01



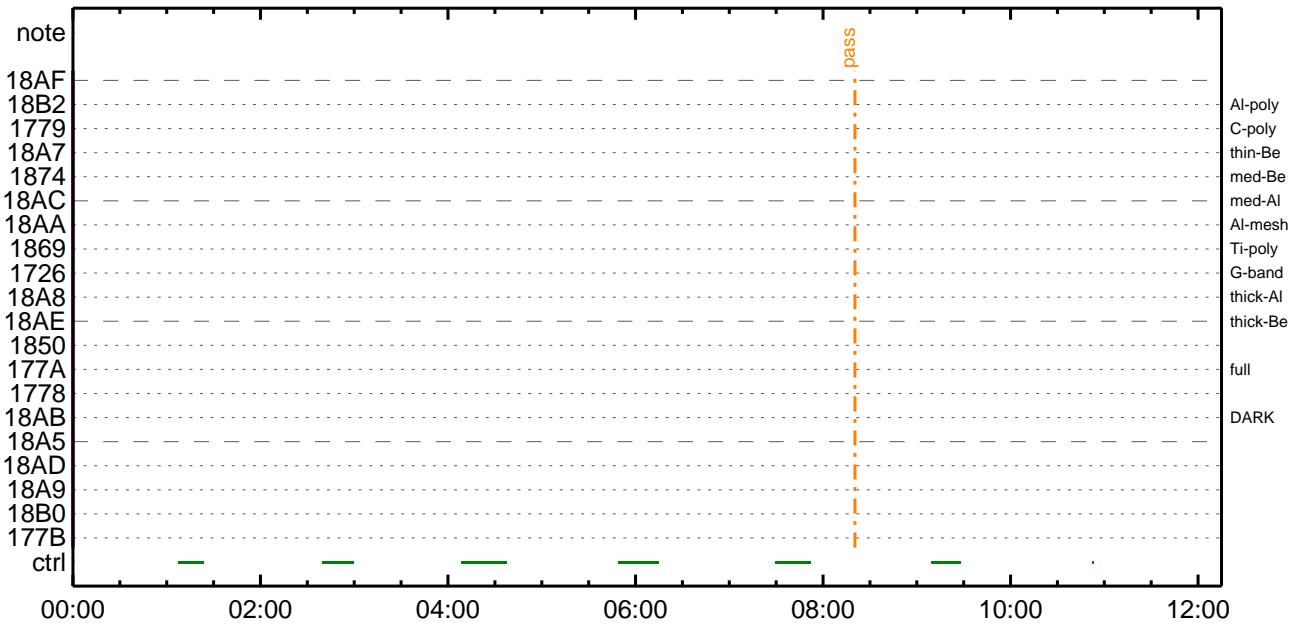
CMDI #0179 2011/10/02



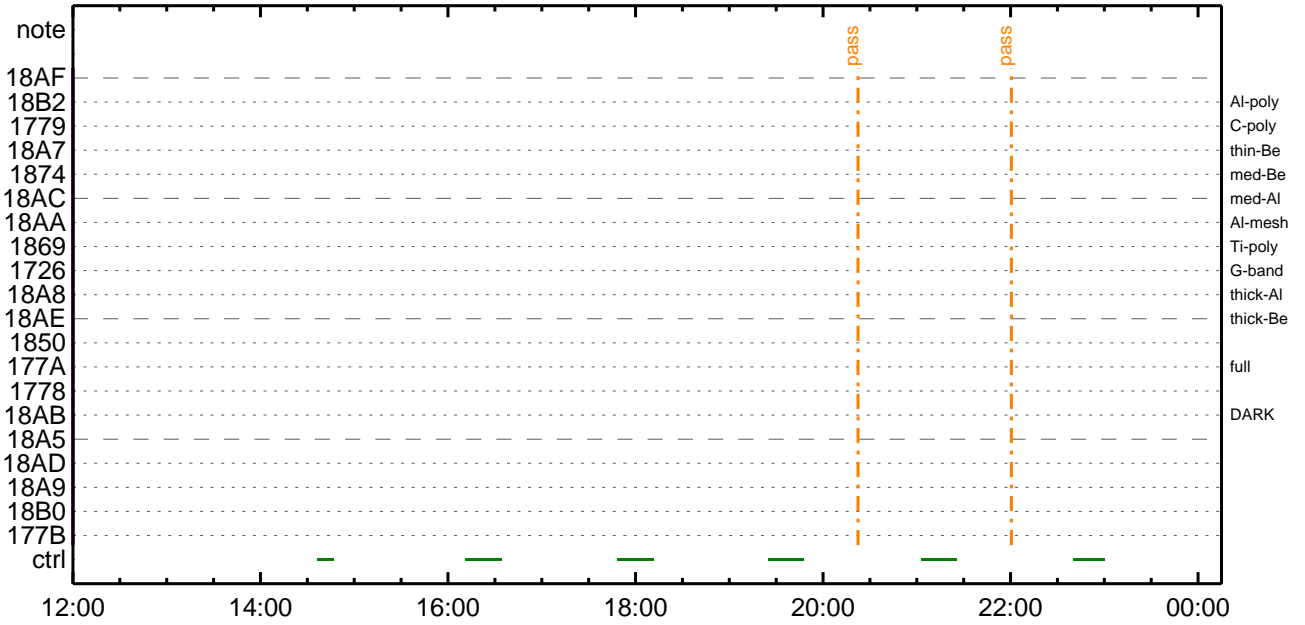
CMDI #0179 2011/10/02



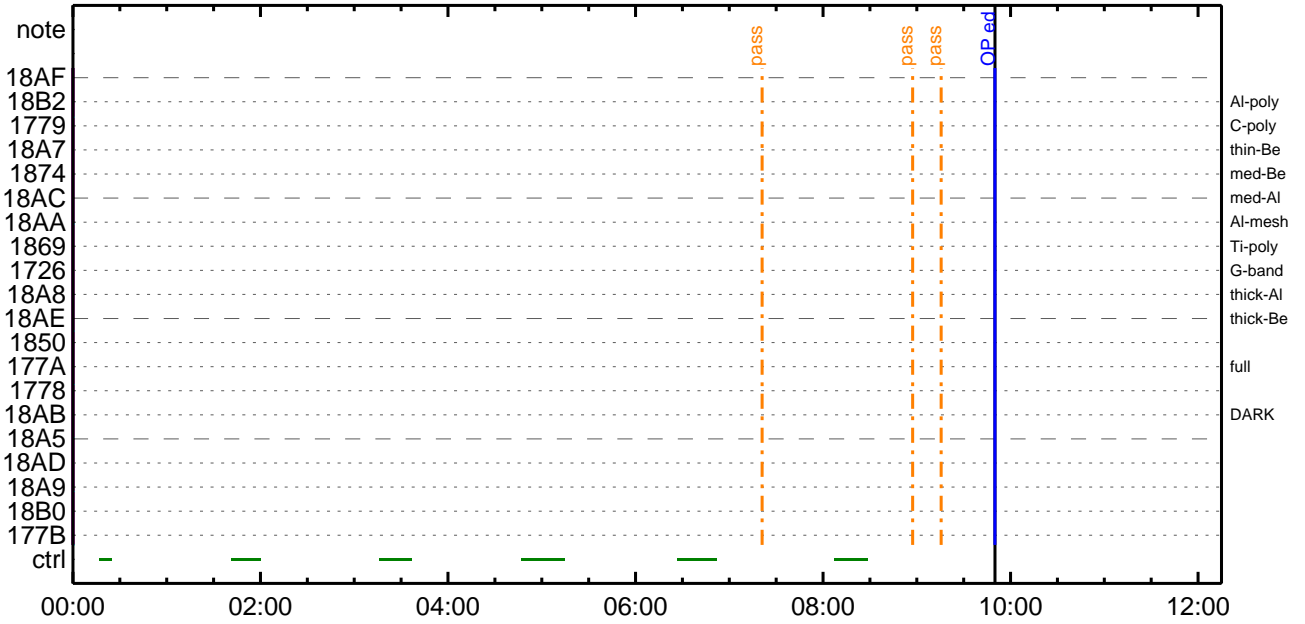
CMDI #0179 2011/10/03



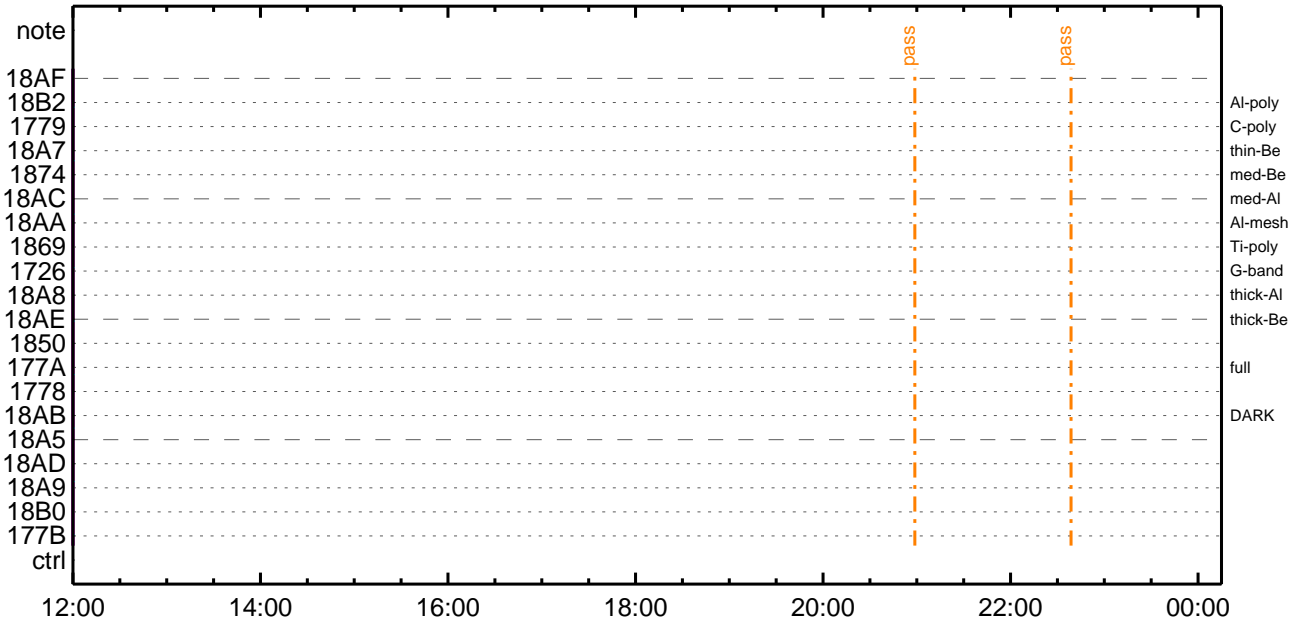
CMDI #0179 2011/10/03



CMDI #0179 2011/10/04



CMDI #0179 2011/10/04




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;YÉi;YAYóYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;YÉi;ã
0103 S. OP op-365:OP
0104 ( )
0105 S. OG og-365:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYóYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYóYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGîî°è¹ç.ë²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYóYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGîî°è¹ç.ë²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYóYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OPîî°è¹ç.ë²îOKò³îÇ§
0165 C.
0166 C. ***** °ê²¼òîî°è¹ç.ë²îOKò³îÇ§ *****
0167 C. DHUYâ;YÉi;YAYóYx½ªî»ò³îÇ§
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADò-Á÷ç@NGîî°è¹ç.ë²îOKò³îÇ§
0180 C. çç[HK1_PKT_FORM_NO] EQ 2
0181 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0182 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0183 +. TI 2011-09-29 09:57:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-09-29 09:57:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-09-29 09:57:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-366 2011-09-29 12:46:02 82 33 SOLAR-B MAIN //
0001   C.
0002   . C. ***** AOS *****
0003   C.
0004   . C. ;ãAOSYÁY$YÁY-¼Ä»Û;ã
0005   C.
0006   C. YÀYB;¼Y³YFÝÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008   C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010   C.   Áí;È□¿□Á□•µ°È»Í×ÁÇ□ÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•ííÈ;È□È¼°ÇÖ□•□¿¼í¹ç□Í;çÄ®, ù□¹□è□È□□ÇÁ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012   C.
0013   C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015   C. Upload the Orbit Element and the Target Attitude
0016   C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018   ( )
0019   C.
0020   C.
0021   C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023   BC       (07 00 00 00 18 00)
0024   C.
0025   C.   <A_ST$1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026   C.
0027   C.
0028   C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030   BC       (04 0b f8)
0031   C.
0032   C. Wait for AOCS DUMP to end
0033   C.
0034   . C. Check the dump memory
0035   C.
0036   C.                               Result = OK [ ]
0037   C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039   BC       (02 0a f8)
0040   C.
0041   C.                               <A_***>[TLM STS] FMT = 2 [ ]
0042   C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047   BC       (21 02)
0048   . C. Verify EIS in MANUAL mode
0049   . C. Estimated OBSTBL upload time is 17s
0050   C. *****
0051   C. EIS START OBSTBL LOAD
0052   C. *****
0053   . S. RAM   ram-820:EIS_OBSTBL
0054   ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056   BC       (07 07 07 00 00 70 00)
0057   C.
0058   C. Execute, after the success of OBSTBL upload.
0059   C. Set EIS TI-commands
0060 +. TI 2011-09-29 10:01:50.0
0061   DC 07-FC EIS_MODE_CHG_ENA
0062   BC       (20)
0063   . C.                               [ ] [HK1_TI_CMD_NUM]      EQ      1 COUNTUP
0064   C. *****
0065   C. EIS END OBSTBL LOAD
0066   C. *****
0067   C.
0068   . C. ***** MDP `ûÁí□í»ö¼Y□ÈÄ□¹□èDCBC•x²è *****
0069   C. (¼á°íYÓYÁYÈYÈYÈYáYçYÈ□È¼□¼Ä»Û□¹□è)
0070   . S. DC-BC dcbc-402:DCBC
0071   (MDP_known_event)
0072   C.
0073   C.
0074   . C. ***** YDY¹.İ Daily±¿İÑ□È`Ø□¹□èDCBC•x²è *****
0075   . S. DC-BC dcbc-153:DCBC
0076   (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077   C.
0078   C.
0079   . C. ;ãLOSÁY$YÁY-¼Ä»Û;ã
0080   C.
0081   . C. ***** LOS *****
0082   C.
```

(a) Spacecraft Operation Procedure (real-commands)

```

main-367 2011-09-29 12:46:02 237 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YÞYÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èµ¿µÄµ•µ°Æ»Í×ÁÇµÍYÇYÄY×YÍ;¼YÉ;ÈÈÈµ•íÉ;ÈµÈ¼°ÇÖµ•µ¿¼í¹ÇµÍ;¿À®, ùµ¹µÈµµÇÁ+¿®µ•µÈµµµ³µÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. °ÆÀ, Í×ÈYµäLÒSµµÇµÍ»p´Öµµ¹ÍÍ, µ•; ¿ÉÖÍ×µÈXÁÖONµÍ¹ÖµÈµÍµÈµµ³µÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. µ¿[HK1_XPA_ON/OFF] EQ ON
0025 C. µ¿[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. µ¿[HK1_XMOD_ON/OFF] EQ ON
0027 C. µ¿[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÄY-¾ÖÄÖµ-µÄÄµ•µ¿µÉ; ¿°È²¼µÍ°ÆÀ, ¼É¾Çµµµ¹Öµ¹µÈ; f
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÍ¼í°ÆÀ,
0033 C. *****
0034 C. ° RESTART; ÈPT1; Èµ•µ¿µµ¼í¹ÇµÍ; ¿°È²¼µÍ°ÆÀ¹Öµ»µ°; ¿DCBC-150µµ¿¿Èµà; f
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. µ¿[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. µ¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. µ¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÆYÉÄÜÁÏ; ÈÁ•Ä°²óÈø; È, áµÍ°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. µ¿[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. µ¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. µ¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, µ-¼«Æ°Äá»ßµ•µ¿, á; ¿°È²¼µµ¹Öµ¹µÈ; f
0055 C. YÇYÓYÆYÉÄÜÁÏµÁ•Ä°²óÈøµ-¶áµµ¼í¹ÇµÍ´°Í´°Í»µ¹µÈµµÇÁÖµÄ; f
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÍ¼í°ÆÀ,
0059 C. *****
0060 C. ° RESTART; ÈPT2; Èµ•µ¿µµ¼í¹ÇµÍ; ¿°È²¼µÍ°ÆÀ¹Öµ»µ°; ¿DCBC-151µµ¿¿Èµà; f
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. µ¿[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. µ¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. µ¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÆYÉÄÜÁÏ; ÈÁ•Ä°²óÈø; È, áµÍ°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. µ¿[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. µ¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. µ¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Äá»ß; ¿XÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Äá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. µ¿[HK1_REP_STA/STP] EQ STOP
0087 C. µ¿[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. µ¿[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. µ¿[HK1_XMOD_ON/OFF] EQ OFF
0095 C. µ¿[HK1_XPA_ON/OFF] EQ OFF

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b c0 c0 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c 40 c0 10 10)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d 40 40 10 10)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0e c0 40 10 10)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0f 80 80 06 06)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 10 80 80 08 08)
0148 + DC 07-F0 MDP_XRT_FLD_ENA
0149 BC (d8)
0150 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151 BC (c8)
0152 +. DC 07-F0 MDP_XRT_AEC_RESET
0153 BC (d0)
0154 + DC 07-F0 MDP_XRT_ARS_DIS
0155 BC (d5)
0156 +. DC 07-F0 MDP_XRT_FLD_RESET
0157 BC (da)
0158 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0159 BC (c4 0a)
0160 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0161 BC (c5 0d)
0162 . C. ----- Success Verify ? OK / NG ____
0163 C.
0164 C.
0165 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0166 C.
0167 +. DC 07-F0 MDP_XRT_MODE_OBSV
0168 BC (c2)
0169 +. TI 2011-09-29 10:01:02.0
0170 DC 07-F0 MDP_XRT_MODE_OBSV
0171 BC (c2)
0172 . C. ----- Success Verify ? OK / NG ____
0173 C.
0174 C. ***** XRT END *****
0175 . C. *****
0176 C. SOT table upload
0177 C. *****
0178 . C. < Stop FG table >
0179 +. DC 07-F0 MDP_FG_CTRL_MANU
0180 BC (51)
0181 . C. -----
0182 C. MDP_FG_CTRL_MODE = MANU [ ]
0183 C. -----
0184 C.
0185 . C. <Upload FG Observation Table>
0186 . S. RAM ram-268:MDP_OBS_F
0187 ( )
0188 C.
0189 . C. < Dump RAMID=MDP_OBS_F >
0190 +. DC 07-F0 MDP_DUMP_FGTBL
0191 BC (82 07 00 00 00 38 b8)
0192 C. -----
0193 C. MDP_OBS_F verify = OK/NG [ ]

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0194 C. -----
0195 C.
0196 . C. < Resume FG table (auto mode) >
0197 +. DC 07-F0 MDP_FG_CTRL_AUTO
0198 BC (50)
0199 . C. -----
0200 C. MDP_FG_CTRL_MODE = AUTO [ ]
0201 C. -----
0202 C.
0203 C. *****
0204 C. SOT TI command set
0205 C. *****
0206 C. Execute, after the success of TBL upload.
0207 +. TI 2011-09-29 10:01:18.0
0208 DC 07-F0 MDP_SOT_MODE_OBSV
0209 BC (40)
0210 . C. -----
0211 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0212 C. -----
0213 C.
0214 C. Only when FG_CTRL_AUTO is used in RT.
0215 +. TI 2011-09-29 10:01:20.0
0216 DC 07-F0 MDP_FG_CTRL_AUTO
0217 BC (50)
0218 . C. -----
0219 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0220 C. -----
0221 C. ***** SOT END *****
0222 C.
0223 . C. ***** MDP `úÃîâî»ö¼ÝðĚÂĐð¹æDCBC•x²è *****
0224 C. (%ãºîÿÓŸÄŸĚŸßŸĚŸâŸçŸĚæð¼æ¼Ä»Ûð¹æè)
0225 . S. DC-BC dcbc-402:DCBC
0226 (MDP_known_event)
0227 C.
0228 C.
0229 . C. ***** ŸDŸ¹•İ Daily±¿İÑæĚ´Øæ¹æDCBC•x²è *****
0230 . S. DC-BC dcbc-153:DCBC
0231 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0232 C.
0233 C.
0234 . C. ;ãLOSŸÄŸŸŸÄŸ-¼Ä»Û;ã
0235 C.
0236 . C. ***** LOS *****
0237 C.

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Sep 29, 11 12:46

XRT_OGLIST_0179.chk

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*** OP Sequence for XRT ***

2011/09/29	10:11:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/09/29	10:11:56.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/09/29	10:12:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2011/09/29	10:12:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/09/29	10:12:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/09/29	10:12:20.0	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/09/29	10:12:22.0	XRT_ARS_DIS_427_OG [0x1ab]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/09/29	10:14:54.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/09/29	10:14:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2011/09/29	10:14:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2011/09/29	10:15:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/09/29	15:22:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/09/29	15:22:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/09/29	15:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/09/29	15:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/09/29	15:45:30.0	XRT_Custom_418_OG [0x1a2]						
2011/09/29	15:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/09/29	16:58:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/09/29	16:58:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/09/29	16:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/09/29	17:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/09/29	17:22:30.0	XRT_Custom_418_OG [0x1a2]						
2011/09/29	17:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/09/29	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/09/29	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2011/09/29	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2011/09/29	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/09/29	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/09/29	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/09/29	18:02:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2011/09/29	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/09/29	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/09/29	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/09/29	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2011/09/29	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/09/29	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/09/29	18:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/09/29	18:10:22.0	XRT_ARS_DIS_427_OG [0x1ab]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/09/29	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/09/29	18:12:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2011/09/29	18:12:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2011/09/29	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/09/29	18:36:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/09/29	18:36:02.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/09/29	18:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/09/29	18:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/09/29	18:59:00.0	XRT_Custom_418_OG [0x1a2]						

2011/09/29	19:00:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/29	20:13:00.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/29	20:13:02.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/09/29	20:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/09/29	20:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/09/29	20:36:30.0	XRT_Custom_418_OG [0x1a2]				
2011/09/29	20:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/29	21:50:30.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/29	21:50:32.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/09/29	21:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/09/29	21:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/09/29	22:13:00.0	XRT_Custom_418_OG [0x1a2]				
2011/09/29	22:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/29	23:28:00.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/29	23:28:02.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/09/29	23:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/09/29	23:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/09/29	23:41:00.0	XRT_Custom_418_OG [0x1a2]				
2011/09/29	23:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/30	00:56:30.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/30	00:56:32.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/09/30	00:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/09/30	00:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/09/30	01:11:00.0	XRT_Custom_418_OG [0x1a2]				
2011/09/30	01:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/30	02:27:00.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/30	02:27:02.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/09/30	02:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/09/30	02:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/09/30	02:47:30.0	XRT_Custom_418_OG [0x1a2]				
2011/09/30	02:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/30	03:55:30.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/30	03:55:32.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/09/30	03:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/09/30	03:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/09/30	04:25:01.0	XRT_Custom_418_OG [0x1a2]				
2011/09/30	04:26:01.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/30	05:36:00.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/30	05:36:02.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/09/30	05:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/09/30	05:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/09/30	06:02:30.0	XRT_Custom_418_OG [0x1a2]				
2011/09/30	06:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/30	06:12:24.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/30	06:12:26.0	XRT_FOCUS_POSITION_401_OG [0x191]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/09/30	06:12:30.0	AOCS_Ore-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2011/09/30	06:12:46.0	XRT_FLD_DIS_402_OG [0x192]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/09/30	06:12:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/09/30	06:12:50.0	XRT_ARS_DIS_438_OG [0x1b6]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/09/30	06:15:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	

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2011/09/30	06:15:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/30	06:22:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/09/30	06:22:30.5	XRT_CTRL_MANU_423_OG [0x1a7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/30	06:23:00.5	XRT_TCIB_XRT_S_HTR_A_ENA_434_OG [0x1b2]							
		TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2011/09/30	08:23:00.5	XRT_Custom_440_OG [0x1b8]							
2011/09/30	10:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 b1 cb 00 cc				
2011/09/30	10:23:00.5	XRT_Custom_440_OG [0x1b8]							
2011/09/30	12:23:00.5	XRT_Custom_440_OG [0x1b8]							
2011/09/30	14:23:00.5	XRT_Custom_403_OG [0x193]							
2011/09/30	14:23:10.5	XRT_Custom_440_OG [0x1b8]							
2011/09/30	16:23:10.5	XRT_Custom_440_OG [0x1b8]							
2011/09/30	17:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/09/30	18:23:10.5	XRT_Custom_440_OG [0x1b8]							
2011/09/30	20:23:10.5	XRT_Custom_440_OG [0x1b8]							
2011/09/30	22:23:10.5	XRT_Custom_405_OG [0x195]							
2011/10/01	00:22:48.5	XRT_Custom_403_OG [0x193]							
2011/10/01	00:22:58.5	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/01	00:23:00.5	XRT_TCIB_XRT_S_HTR_A_DIS_414_OG [0x19e]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2011/10/01	06:39:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/01	06:40:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 2e f9 2e f9				
2011/10/01	06:42:32.0	XRT_FOCUS_POSITION_415_OG [0x19f]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/01	06:42:52.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2011/10/01	06:42:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/01	06:42:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/01	06:42:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/01	06:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/01	06:49:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/01	06:50:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 2e f9 d1 07				
2011/10/01	06:52:32.0	XRT_FOCUS_POSITION_415_OG [0x19f]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/01	06:52:52.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/10/01	06:52:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/01	06:52:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/01	06:52:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/01	06:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/01	06:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/01	07:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 d1 07 d1 07				
2011/10/01	07:02:32.0	XRT_FOCUS_POSITION_415_OG [0x19f]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/01	07:02:52.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2011/10/01	07:02:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/01	07:02:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/01	07:02:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/01	07:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/01	07:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/01	07:10:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 d1 07 2e f9				
2011/10/01	07:12:32.0	XRT_FOCUS_POSITION_415_OG [0x19f]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/01	07:12:52.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2011/10/01	07:12:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/01	07:12:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/01	07:12:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/01	07:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/01	07:23:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/01	07:23:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							

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2011/10/01	07:24:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
			AOCU_NM	5	02-76	00	00	00	00
2011/10/01	07:24:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/10/01	07:24:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/10/01	07:24:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/10/01	07:26:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2011/10/01	07:27:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/01	07:33:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/01	07:33:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/10/01	07:34:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01	00	00	00
2011/10/01	07:34:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/10/01	07:34:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/10/01	07:34:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/10/01	07:34:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/10/01	07:36:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/01	07:36:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2011/10/01	07:36:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2011/10/01	07:37:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/01	07:54:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/01	07:54:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/01	07:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/10/01	07:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/01	08:16:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/01	08:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/01	09:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/01	10:09:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00