

XRT Timeline to be uploaded on 2011/10/01

Period: 2011/10/01 09:59:00 - 2011/10/06 10:19:00

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Normal mode

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XOB #18AF: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 4min cad

Term	Pointing (x, y)	Comment
10/01 10:12:00 - 10/01 10:59:54	Fixed (-825.0, 450.0)	# OP start + 10min, HOP 204. with Proba-2 SWAP high cadence.
10/01 22:03:00 - 10/01 22:49:54	Fixed (-825.0, 450.0)	* HOP 204, with SWAP.
10/01 22:53:00 - 10/02 00:59:54	Track (653.1, 121.0) @ 10/01 22:50:00	* HOP 171, target is AR 11302.
10/02 01:03:00 - 10/02 05:59:54	Track (671.6, 113.5) @ 10/02 01:00:00	* Resume default tracking of AR 11302.
10/02 06:13:00 - 10/02 10:29:54	Track (703.6, 117.6) @ 10/02 06:10:00	# Cont.
10/02 10:33:00 - 10/02 11:29:54	Fixed (-825.0, 450.0)	* HOP 204.
10/02 11:33:00 - 10/02 18:06:24	Track (734.7, 122.0) @ 10/02 11:30:00	* AR 11302.
10/02 18:19:30 - 10/02 20:59:54	Track (771.5, 127.9) @ 10/02 18:16:30	# Cont.
10/02 21:03:00 - 10/02 21:59:54	Fixed (-825.0, 450.0)	* HOP 204.
10/02 22:26:00 - 10/03 00:59:54	Track (786.1, 140.1) @ 10/02 22:00:00	* HOP 171, AR 11302 with sunspot pointing.
10/03 01:03:00 - 10/03 06:24:54	Track (804.7, 133.9) @ 10/03 01:00:00	* AR 11302.
10/03 06:38:00 - 10/03 09:59:54	Track (829.6, 139.1) @ 10/03 06:35:00	# Cont.
10/03 18:24:30 - 10/03 22:40:00	Track (874.3, 150.3) @ 10/03 18:21:30	# AR 11302.
10/03 23:03:00 - 10/04 00:59:54	Track (885.4, 163.8) @ 10/03 23:00:00	* HOP 171.
10/04 01:03:00 - 10/04 05:58:24	Track (894.6, 156.9) @ 10/04 01:00:00	* AR 11302 default pointing.
10/04 06:11:30 - 10/04 09:59:54	Track (907.7, 162.1) @ 10/04 06:08:30	# Cont.

PROG= 20 Inf.-time(s)

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 19	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 96	4-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 62	11-time(s)	240.0sec										
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad

Term	Pointing (x, y)	Comment
10/01 11:03:00 - 10/01 17:48:24	Track (576.9, 103.4) @ 10/01 11:00:00	* Track AR 11302.
10/01 18:01:30 - 10/01 21:59:54	Track (625.5, 108.3) @ 10/01 17:58:30	# Cont.

PROG= 10 Inf.-time(s)

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 19	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 96	4-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 62	15-time(s)	150.0sec										
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18AD: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be(12

Term	Pointing (x, y)	Comment
10/01 17:51:30 - 10/01 17:58:24	Fixed (0.0, 0.0)	synoptic, shifted -11.5 min
10/02 06:03:00 - 10/02 06:09:54	Fixed (0.0, 0.0)	synoptic
10/02 18:09:30 - 10/02 18:16:24	Fixed (0.0, 0.0)	synoptic, shifted 6.5 min
10/03 06:28:00 - 10/03 06:34:54	Fixed (0.0, 0.0)	synoptic, shifted 25.0 min

10/03 18:18:30 - 10/03 18:21:24 Fixed (0.0, 0.0) synoptic, shifted 11.5 min
 10/04 06:01:30 - 10/04 06:08:24 Fixed (0.0, 0.0) synoptic, shifted -1.5 min

PROG= 04 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└ Seqn= 7 1-time(s) 4.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└ Seqn= 5 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0 0 2.0sec
└ Seqn= 8 1-time(s) 4.0sec												
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└ Seqn= 75 1-time(s) 2.0sec												
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└ Seqn= 4 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384

Term	Pointing (x, y)	Comment
10/03 10:03:00 - 10/03 17:48:00	Fixed (-9.0, 880.0)	* HOP 81, polar monitoring, N pole.
PROG= 03 Inf.-time(s)		
└ Subr= 1 1-time(s) 2.0sec		
└ Seqn= 51 1-time(s) 2.0sec		
	Open/G-band	Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
└ Subr= 2 30-time(s) 2.0sec		
└ Seqn= 58 2-time(s) 30.0sec		
	Open/Al-mesh	Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
	Open/Ti-poly	Open/Ti-poly close Safe Norm 8.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
	Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1869: Flare standard obs. multifilter (thin-Be,med-Al,thick-Be 384x384 - Al-poly 512x512 2x2)

Term	Pointing (x, y)	Comment
10/01 10:12:00 - 10/01 10:59:54	Fixed (-825.0, 450.0)	# OP start + 10min, HOP 204. with Proba-2 SWAP high cadence.
10/01 11:03:00 - 10/01 17:48:24	Track (576.9, 103.4) @ 10/01 11:00:00	* Track AR 11302.
10/01 18:01:30 - 10/01 21:59:54	Track (625.5, 108.3) @ 10/01 17:58:30	# Cont.
10/01 22:03:00 - 10/01 22:49:54	Fixed (-825.0, 450.0)	* HOP 204, with SWAP.
10/01 22:53:00 - 10/02 00:59:54	Track (653.1, 121.0) @ 10/01 22:50:00	* HOP 171, target is AR 11302.
10/02 01:03:00 - 10/02 05:59:54	Track (671.6, 113.5) @ 10/02 01:00:00	* Resume default tracking of AR 11302.
10/02 06:13:00 - 10/02 10:29:54	Track (703.6, 117.6) @ 10/02 06:10:00	# Cont.
10/02 10:33:00 - 10/02 11:29:54	Fixed (-825.0, 450.0)	* HOP 204.
10/02 11:33:00 - 10/02 18:06:24	Track (734.7, 122.0) @ 10/02 11:30:00	* AR 11302.
10/02 18:19:30 - 10/02 20:59:54	Track (771.5, 127.9) @ 10/02 18:16:30	# Cont.
10/02 21:03:00 - 10/02 21:59:54	Fixed (-825.0, 450.0)	* HOP 204.
10/02 22:26:00 - 10/03 00:59:54	Track (786.1, 140.1) @ 10/02 22:00:00	* HOP 171, AR 11302 with sunspot pointing.
10/03 01:03:00 - 10/03 06:24:54	Track (804.7, 133.9) @ 10/03 01:00:00	* AR 11302.
10/03 06:38:00 - 10/03 09:59:54	Track (829.6, 139.1) @ 10/03 06:35:00	# Cont.
10/03 10:03:00 - 10/03 17:48:00	Fixed (-9.0, 880.0)	* HOP 81, polar monitoring, N pole.
10/03 18:24:30 - 10/03 22:40:00	Track (874.3, 150.3) @ 10/03 18:21:30	# AR 11302.
10/03 23:03:00 - 10/04 00:59:54	Track (885.4, 163.8) @ 10/03 23:00:00	* HOP 171.
10/04 01:03:00 - 10/04 05:58:24	Track (894.6, 156.9) @ 10/04 01:00:00	* AR 11302 default pointing.
10/04 06:11:30 - 10/04 09:59:54	Track (907.7, 162.1) @ 10/04 06:08:30	# Cont.

PROG= 13 1-time(s)

└ Subr= 1 4-time(s) 2.0sec												
└ Seqn= 55 45-time(s) 20.0sec												
	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3 0 2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3 0 2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2 0 2.0sec
└ Seqn= 90 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0 0 2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0 0 2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=98	0 0 2.0sec
└ Subr= 2 1-time(s) 600.0sec												
└ Seqn= 89 1-time(s) 2.0sec												
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	1 0 2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	1 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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Active Region Search

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NOT USED

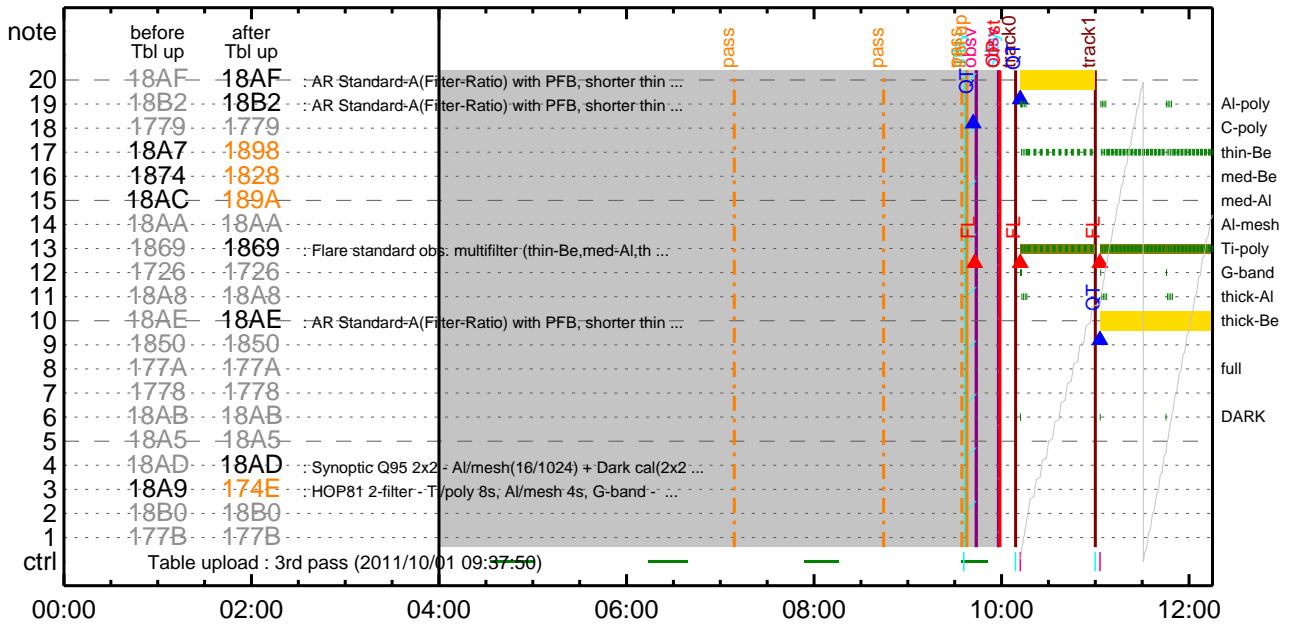
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Flare Detection

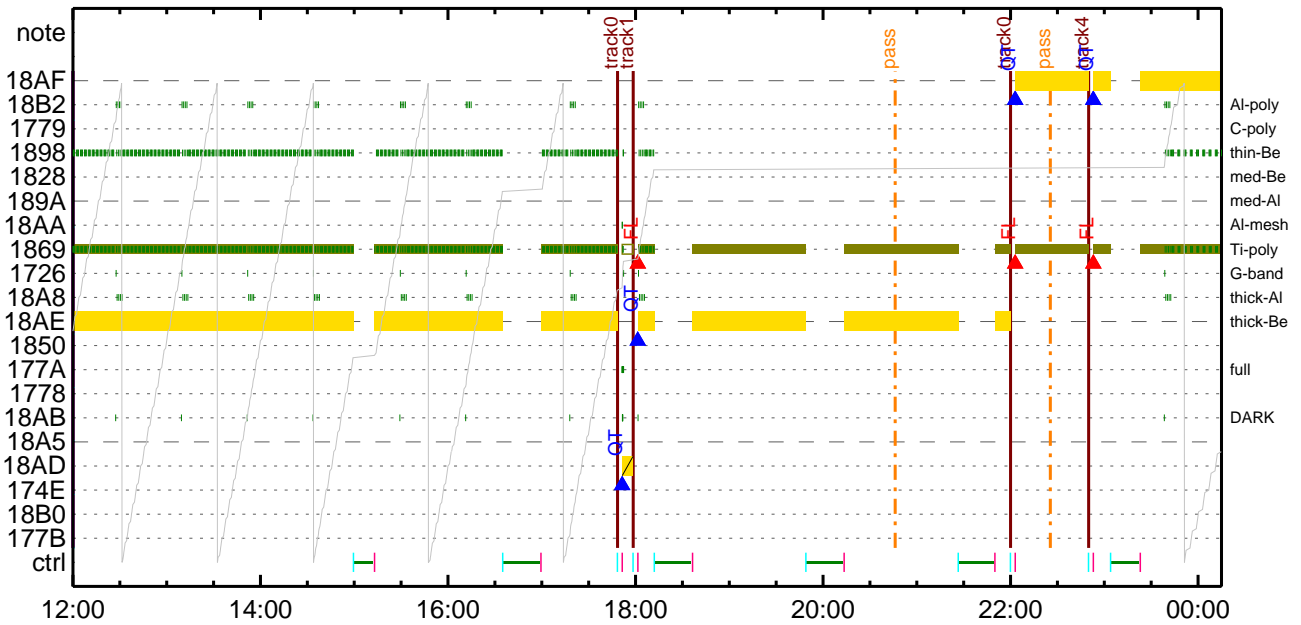
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FLD Patrol										
Term	Pointing (x, y)						Comment			
10/01 17:58:46 - 10/02 06:00:16	Track (625.5, 108.3)	@ 10/01 17:58:30	#	Cont.						
10/02 06:10:16 - 10/02 18:06:46	Track (703.6, 117.6)	@ 10/02 06:10:00	#	Cont.						
10/02 18:16:46 - 10/03 06:25:16	Track (771.5, 127.9)	@ 10/02 18:16:30	#	Cont.						
10/03 06:35:16 - 10/03 18:15:46	Track (829.6, 139.1)	@ 10/03 06:35:00	#	Cont.						
10/03 18:21:46 - 10/04 05:58:46	Track (874.3, 150.3)	@ 10/03 18:21:30	#	AR 11302.						
10/04 06:08:46 - 10/06 10:19:00	Track (907.7, 162.1)	@ 10/04 06:08:30	#	Cont.						
Open/Ti-poly	Open/thick-AI	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

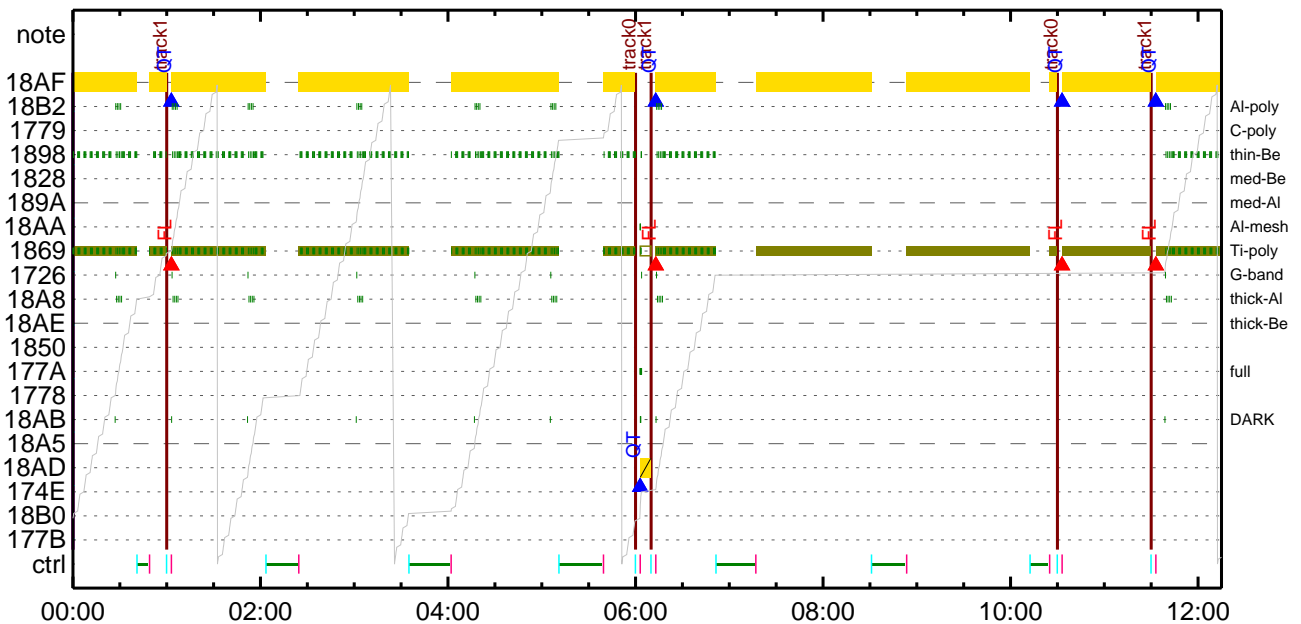
CMDI #0190 2011/10/01



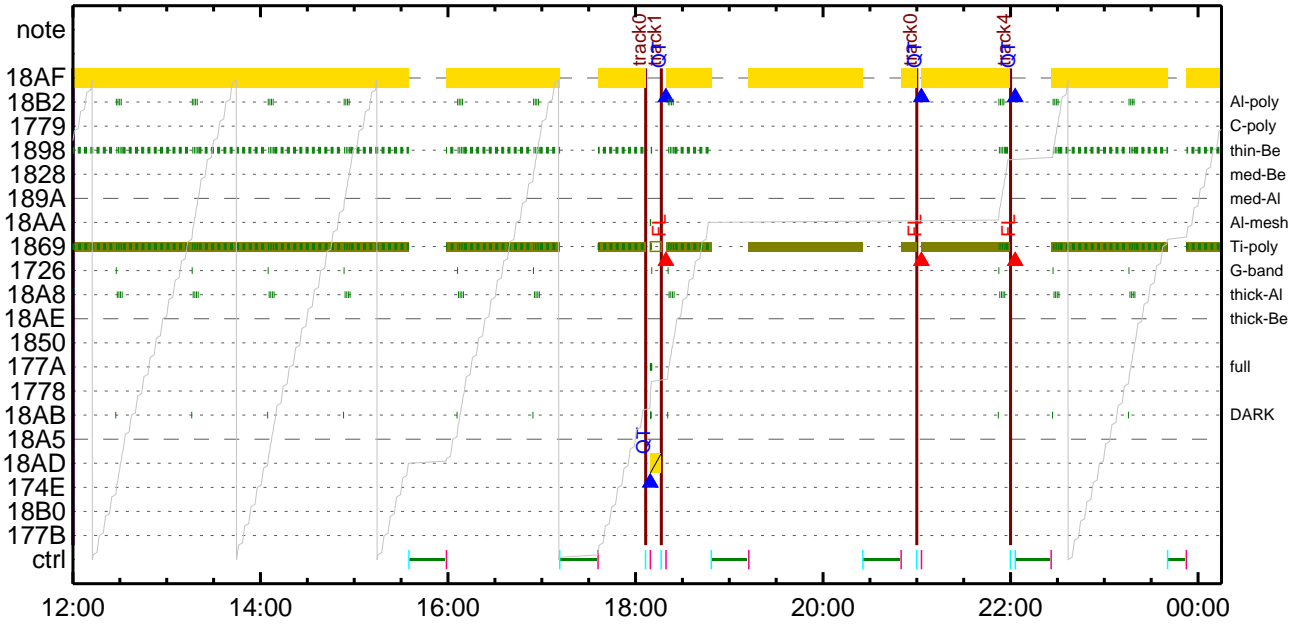
CMDI #0190 2011/10/01



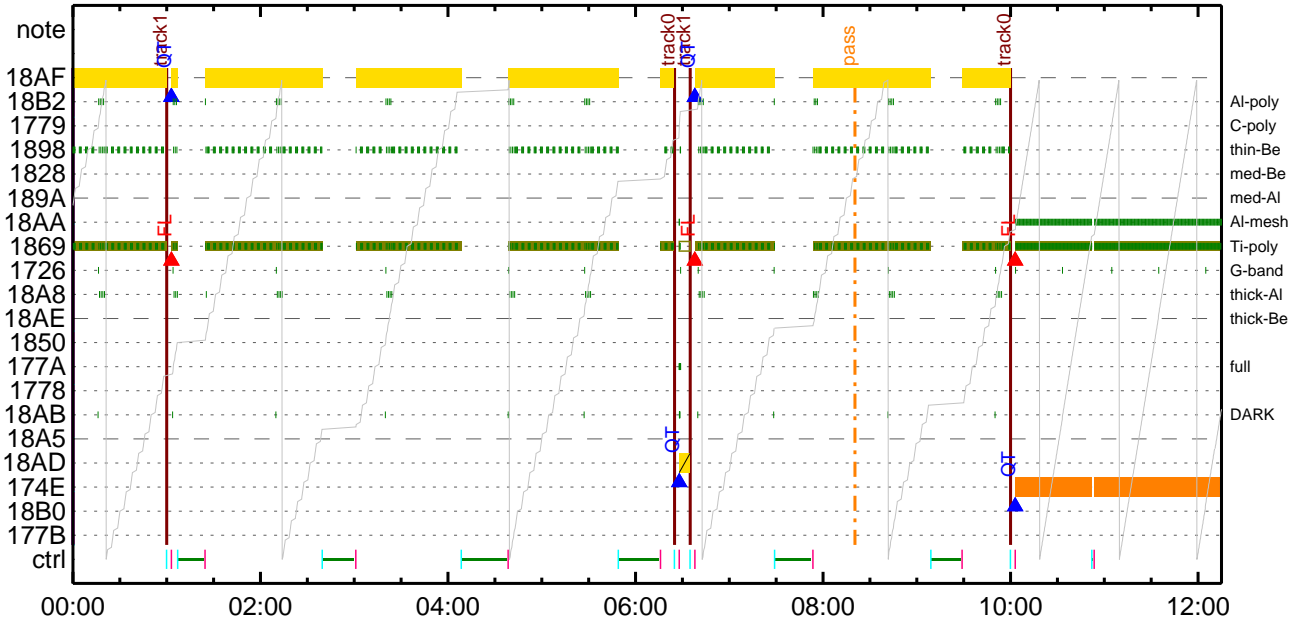
CMDI #0190 2011/10/02



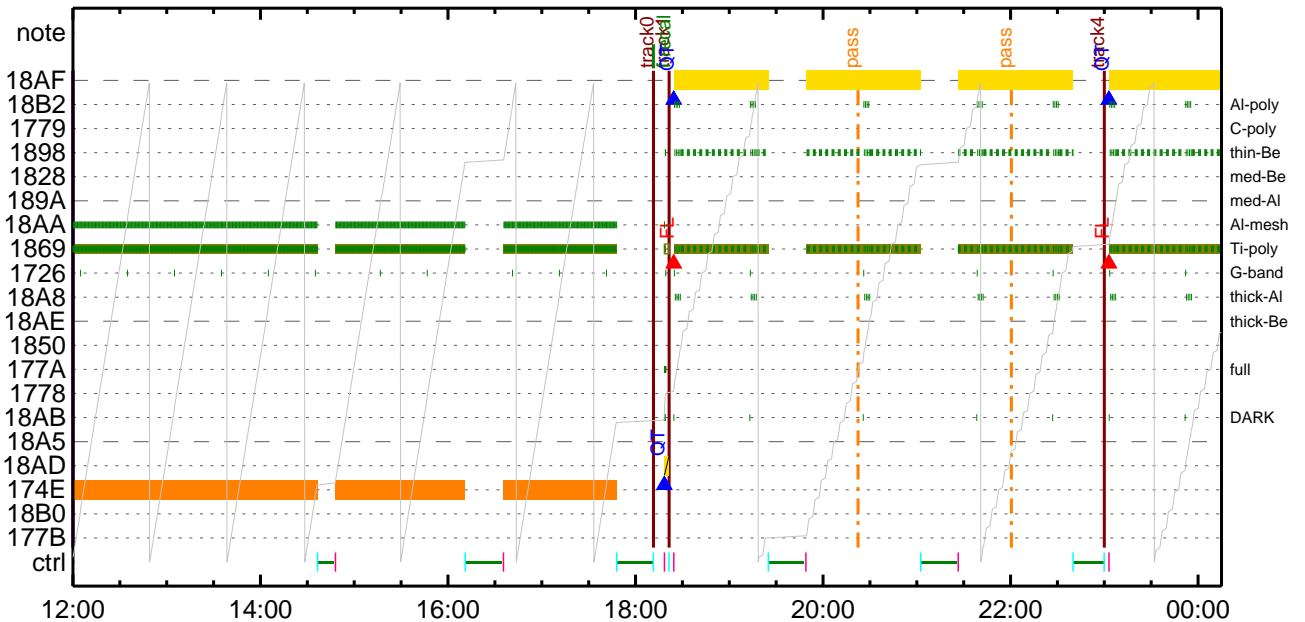
CMDI #0190 2011/10/02



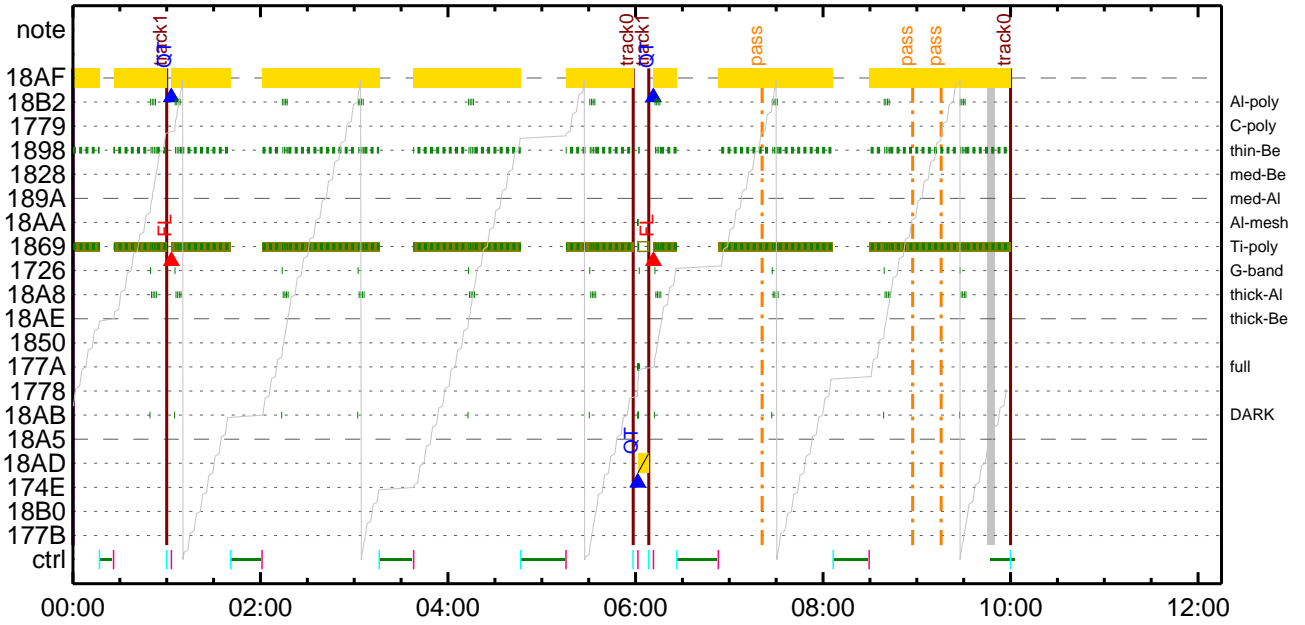
CMDI #0190 2011/10/03



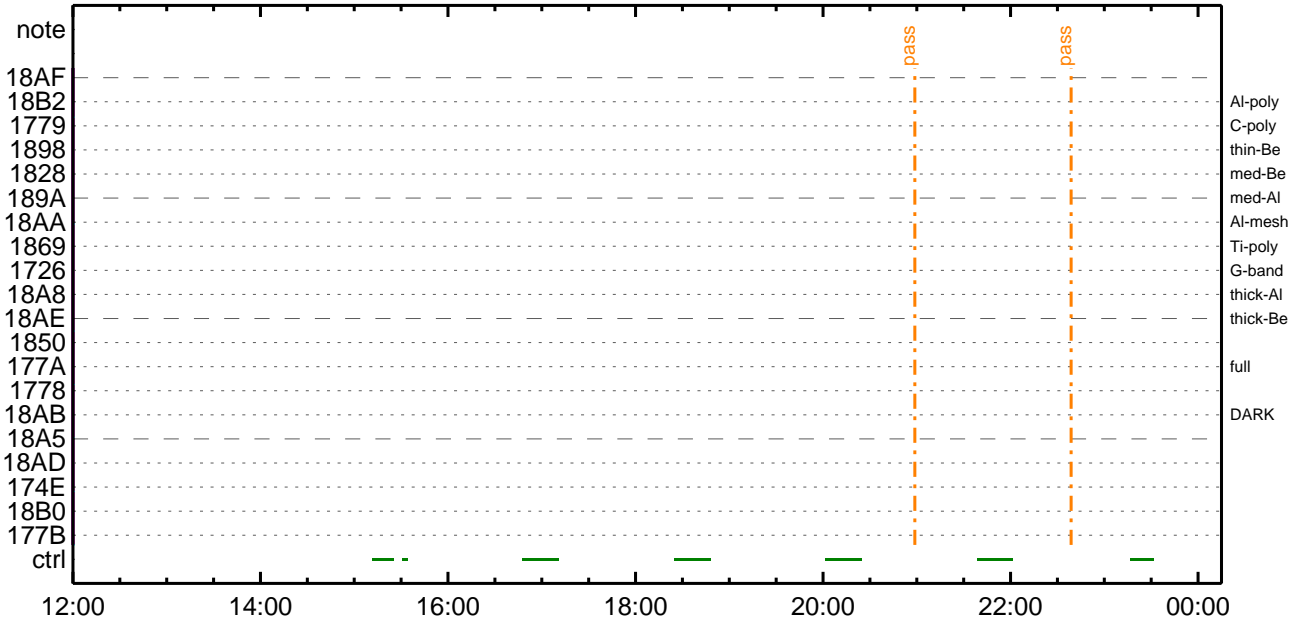
CMDI #0190 2011/10/03



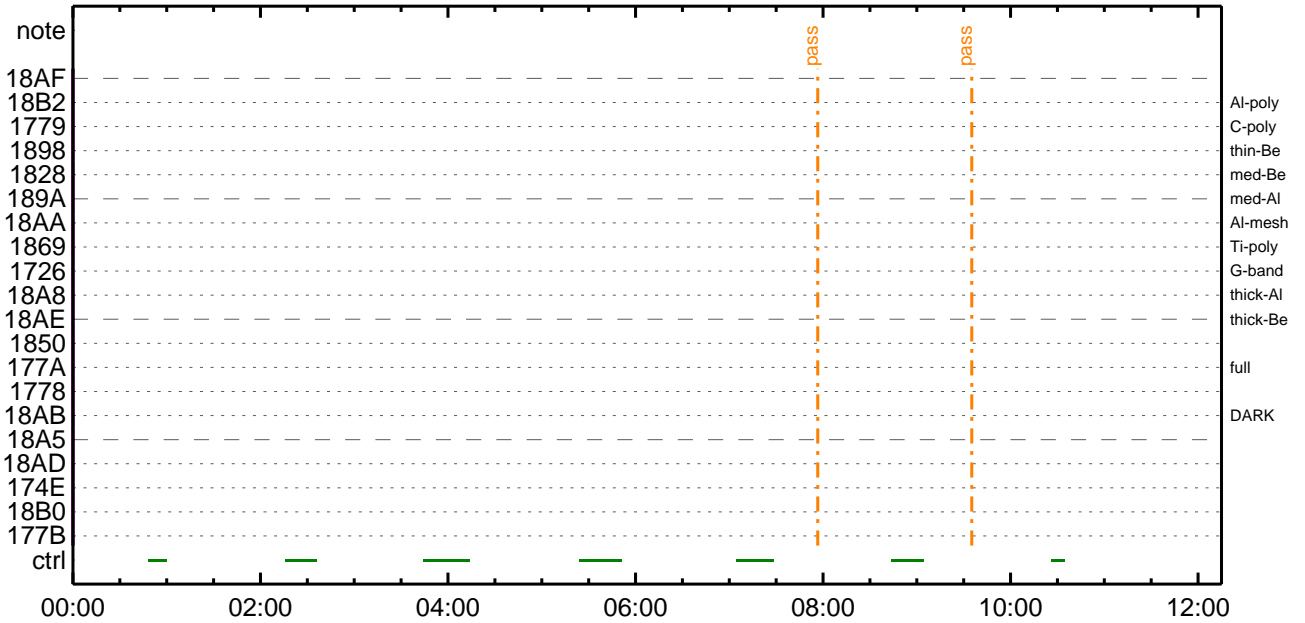
CMDI #0190 2011/10/04



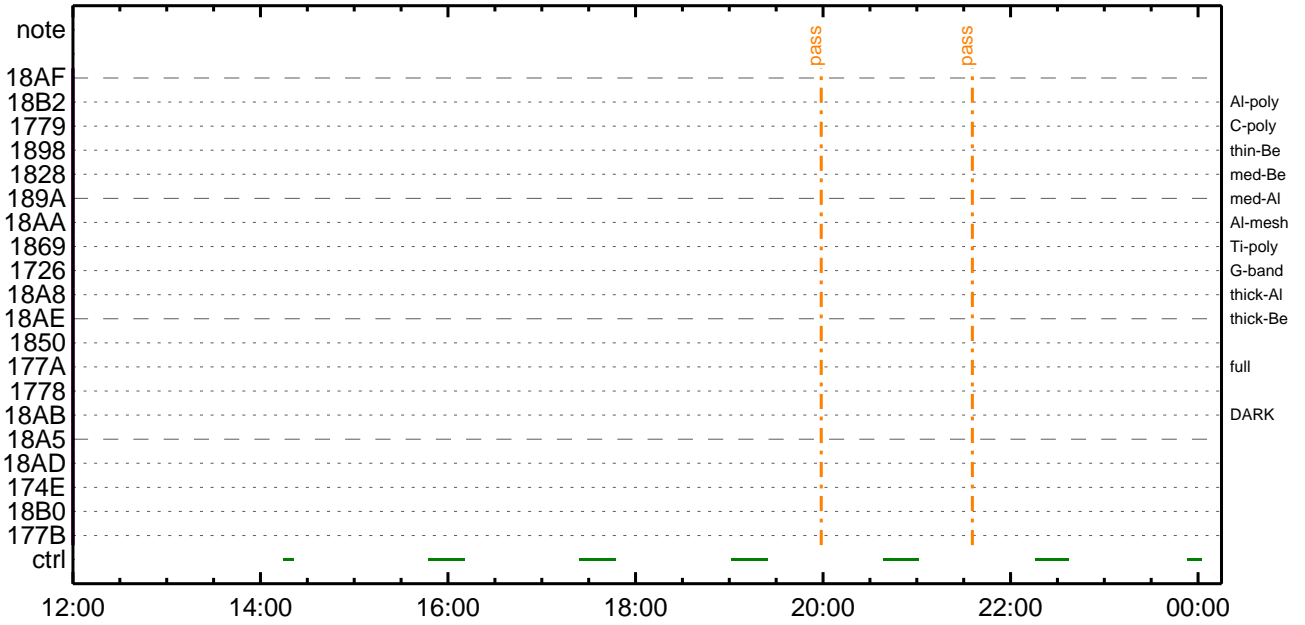
CMDI #0190 2011/10/04



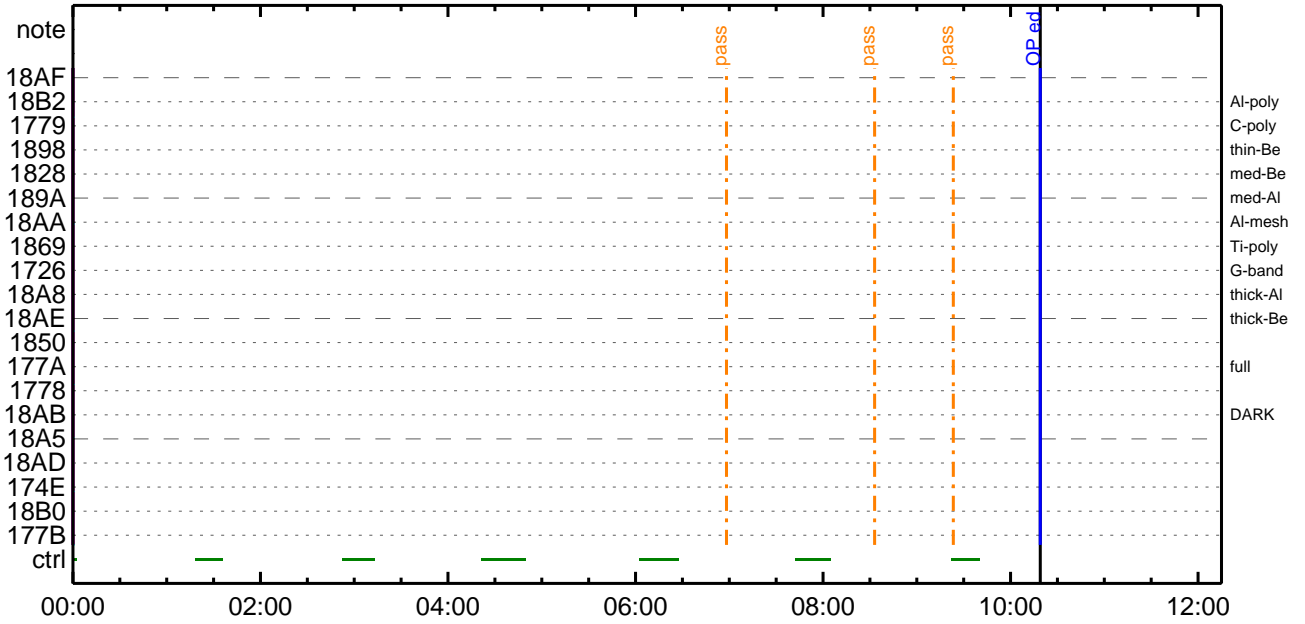
CMDI #0190 2011/10/05



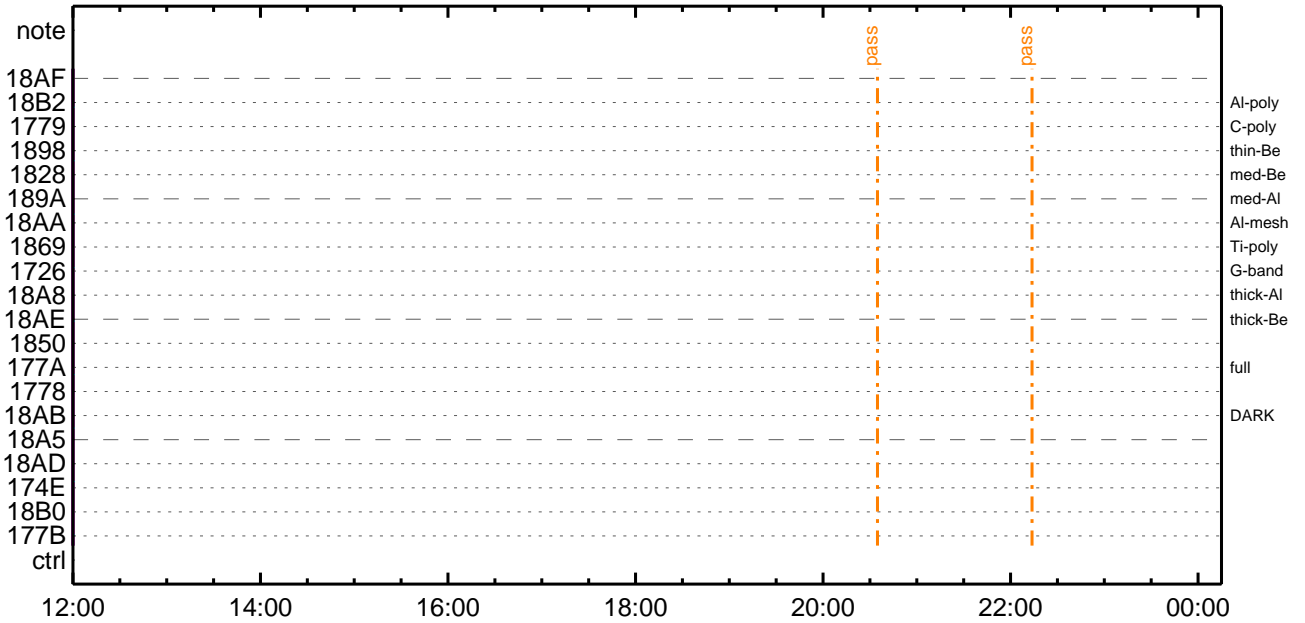
CMDI #0190 2011/10/05



CMDI #0190 2011/10/06



CMDI #0190 2011/10/06




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-371:OP
0104 ( )
0105 S. OG og-371:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°²òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ô²°²E²ò²³òE;f
0180 C. ²²ò¿;çSET²E²DUMP²î½±°îYÑY¹²ç¹Ô²|²³òE;f
0181 C.
0182 C. TIY³Y²YóYÉòðÁDî¿(UT)
0183 +. TI 2011-10-01 09:54:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-10-01 09:54:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-10-01 09:54:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```

0194 C.
0195 +. TI 2011-10-01 09:58:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄè%îîñαîŷÄŷ§ŷÄŷ-¹âiü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]        EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]       EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ          EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¼Ê¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÊ;Ê¼ŷ¼. ŷî;¼ŷÊ;Êαòîâα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2011-10-01 09:58:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2011-10-01 09:58:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2011-10-01 09:58:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-10-01 09:58:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîαî»ö¼ŷαÊÄα¹αèDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÊŷŷŷÄŷçŷèè¼αα¼Ä»Ûα¹è)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîñèË´Øα¹αèDCBC•x²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-373 2011-10-01 13:55:10 148 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Á»Û;ã
0005 C.
0006 C. YÄYŞ;¼Y³YFÿÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αB•µ°E»Í×ÁÇαÍYçYÄY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈBÈ¼°ÇÓα•α¿¼í¹çαÍ;çÀ®, ùα¹αèBαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG _____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR _____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 08 08)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 06 06)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 10 80 80 08 08)
0056 + DC 07-F0 MDP_XRT_FLD_ENA
0057 BC (d8)
0058 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0059 BC (c8)
0060 +. DC 07-F0 MDP_XRT_AEC_RESET
0061 BC (d0)
0062 + DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 +. DC 07-F0 MDP_XRT_FLD_RESET
0065 BC (da)
0066 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0067 BC (c4 13)
0068 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0069 BC (c5 0d)
0070 . C. ----- Success Verify ? OK / NG _____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. TI 2011-10-01 09:58:02.0
0078 DC 07-F0 MDP_XRT_MODE_OBSV
0079 BC (c2)
0080 . C. ----- Success Verify ? OK / NG _____
0081 C.
0082 C. ***** XRT END *****
0083 . C. *****
0084 C. SOT table upload
0085 C. *****
0086 . C. < Stop FG table >
0087 +. DC 07-F0 MDP_FG_CTRL_MANU
0088 BC (51)
0089 . C. -----
0090 C. MDP_FG_CTRL_MODE = MANU [ ]
0091 C. -----
0092 C.
0093 . C. <Upload FG Observation Table>
0094 . S. RAM ram-269:MDP_OBS_F
0095 ( )
```

```

0096      C.
0097      . C. < Dump RAMID=MDP_OBS_F >
0098      +. DC 07-F0 MDP_DUMP_FGTBL
0099      BC      (82 07 00 00 00 38 b8)
0100      C. -----
0101      C. MDP_OBS_F verify = OK/NG      [ ]
0102      C. -----
0103      C.
0104      . C. < Stop SP table >
0105      +. DC 07-F0 MDP_SP_CTRL_MANU
0106      BC      (61)
0107      C. -----
0108      C. MDP_SP_CTRL_MODE          = MANU      [ ]
0109      C. -----
0110      C.
0111      . C. <Upload SP Observation Table>
0112      . S. RAM      ram-283:MDP_OBS_S
0113      ( )
0114      C.
0115      . C. < Dump RAMID=MDP_OBS_S >
0116      +. DC 07-F0 MDP_DUMP_SPTBL
0117      BC      (83 07 00 00 00 38 b8)
0118      C. -----
0119      C. MDP_OBS_S verify = OK/NG      [ ]
0120      C. -----
0121      C.
0122      C. *****
0123      C. SOT TI command set
0124      C. *****
0125      C. Execute, after the success of TBL upload.
0126      +. TI 2011-10-01 09:58:18.0
0127      DC 07-F0 MDP_SOT_MODE_OBSV
0128      BC      (40)
0129      . C. -----
0130      C. HK1_TI_CMD_NUM            = 1 CNTUP [ ]
0131      C. -----
0132      C.
0133      C.
0134      . C. ***** MDP `úÃîªî»ö¼ÝªÈÂªª¹ªèDCBC•x²è *****
0135      C. (%ã°î¸ÝÁÝÈÝßÝÈÝá¥çÝÈªÈ¼ªª¼Ä»Ûª¹ªè)
0136      . S. DC-BC dcbc-402:DCBC
0137      (MDP_known_event)
0138      C.
0139      C.
0140      . C. ***** ÝDÝ¹•İ Daily±¿İÑªÈ´Øª¹ªèDCBC•x²è *****
0141      . S. DC-BC dcbc-153:DCBC
0142      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0143      C.
0144      C.
0145      . C. ;ãLOS¥ÁÝ§¥Ã¥¹¼Ä»Û;ã
0146      C.
0147      . C. ***** LOS *****
0148      C.

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*** OP Sequence for XRT ***

2011/10/01	10:08:54.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/01	10:08:56.0	XRT_FOCUS_POSITION_409_OG [0x199]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/10/01	10:09:00.0	AOCS_OrE-point_Start_1_OG [0x097]				
		AOCU_NM	5	02-76	00 d8 00 49 58	
2011/10/01	10:09:16.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/10/01	10:09:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/10/01	10:09:20.0	XRT_AEC_RESET_443_OG [0x1bb]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/10/01	10:09:22.0	XRT_ARS_DIS_427_OG [0x1ab]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/10/01	10:11:54.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/01	10:11:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2011/10/01	10:11:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2011/10/01	10:12:00.0	XRT_CTRL_AUTO_406_OG [0x196]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/01	10:59:54.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/01	10:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/10/01	11:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	01 00 00 00 00	
2011/10/01	11:00:16.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/10/01	11:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/10/01	11:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/10/01	11:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/10/01	11:02:54.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/01	11:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2011/10/01	11:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2011/10/01	11:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/01	14:59:30.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/01	14:59:32.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/01	14:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/10/01	15:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/10/01	15:12:00.0	XRT_Custom_418_OG [0x1a2]				
2011/10/01	15:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/01	16:35:00.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/01	16:35:02.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/01	16:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/10/01	16:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/10/01	16:58:30.0	XRT_Custom_418_OG [0x1a2]				
2011/10/01	16:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/01	17:48:24.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/01	17:48:26.0	XRT_FOCUS_POSITION_401_OG [0x191]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/10/01	17:48:30.0	AOCS_OrE-point_Start_3_OG [0x099]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2011/10/01	17:48:46.0	XRT_FLD_DIS_402_OG [0x192]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/10/01	17:48:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/10/01	17:48:50.0	XRT_ARS_DIS_438_OG [0x1b6]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/10/01	17:51:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2011/10/01	17:51:30.0	XRT_CTRL_AUTO_406_OG [0x196]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/01	17:58:24.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/01	17:58:26.0	XRT_FOCUS_POSITION_409_OG [0x199]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/10/01	17:58:30.0	AOCS_OrE-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	01 00 00 00 00	
2011/10/01	17:58:46.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/10/01	17:58:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]				

2011/10/01	17:58:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/01	17:58:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/01	18:01:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/01	18:01:26.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a	
2011/10/01	18:01:28.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2011/10/01	18:01:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/01	18:12:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/01	18:12:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/01	18:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/01	18:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/01	18:35:30.0	XRT_Custom_418_OG [0x1a2]						
2011/10/01	18:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/01	19:49:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/01	19:49:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/01	19:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/01	19:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/01	20:12:30.0	XRT_Custom_418_OG [0x1a2]						
2011/10/01	20:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/01	21:26:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/01	21:26:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/01	21:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/01	21:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/01	21:49:00.0	XRT_Custom_418_OG [0x1a2]						
2011/10/01	21:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/01	21:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/01	21:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2011/10/01	22:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 d8 00 49 58		
2011/10/01	22:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/01	22:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/01	22:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/01	22:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/01	22:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/01	22:02:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14	
2011/10/01	22:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2011/10/01	22:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/01	22:49:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/01	22:49:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2011/10/01	22:50:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04 00 00 00 00		
2011/10/01	22:50:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/01	22:50:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/01	22:50:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/01	22:50:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/01	22:52:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/01	22:52:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14	
2011/10/01	22:52:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2011/10/01	22:53:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/01	23:04:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/01	23:04:02.0	XRT_FLD_RESET_412_OG [0x19c]						

2011/10/01	23:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/01	23:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/10/01	23:22:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/01	23:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/02	00:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/02	00:41:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/02	00:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/10/02	00:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/02	00:48:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/02	00:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/02	00:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/10/02	00:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	01 00 00 00 00			
2011/10/02	01:00:00.0	AOCs_Orе-point_Start_2_OG [0x098]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/10/02	01:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/10/02	01:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/10/02	01:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/10/02	01:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/02	01:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2011/10/02	01:02:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2011/10/02	01:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/02	01:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/02	02:03:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/02	02:03:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/10/02	02:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/02	02:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/02	02:23:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/02	02:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/02	03:35:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2011/10/02	03:35:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/02	03:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/02	03:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/10/02	04:01:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/02	04:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/02	05:11:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/02	05:11:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/02	05:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/10/02	05:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/02	05:38:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/02	05:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/02	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2011/10/02	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 00 00 00 00			
2011/10/02	06:00:00.0	AOCs_Orе-point_Start_3_OG [0x099]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/10/02	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/10/02	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/10/02	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2011/10/02	06:02:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/02	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/02	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2011/10/02	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							

2011/10/02	06:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	01	00	00	00
2011/10/02	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/10/02	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/10/02	06:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/10/02	06:10:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/10/02	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/02	06:12:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14		
2011/10/02	06:12:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2011/10/02	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/02	06:51:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/02	06:51:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/02	06:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/10/02	06:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/02	07:16:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/02	07:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/02	08:31:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/02	08:31:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/02	08:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/10/02	08:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/02	08:52:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/02	08:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/02	10:12:30.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/02	10:12:32.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/02	10:12:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/10/02	10:15:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/02	10:24:00.5	XRT_Custom_418_OG [0x1a2]							
2011/10/02	10:25:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/02	10:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/02	10:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/10/02	10:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00	d8	00	49
2011/10/02	10:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/10/02	10:30:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/10/02	10:30:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/10/02	10:30:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/10/02	10:32:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/02	10:32:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14		
2011/10/02	10:32:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2011/10/02	10:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/02	11:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/02	11:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/10/02	11:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01	00	00	00
2011/10/02	11:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/10/02	11:30:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/10/02	11:30:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/10/02	11:30:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/10/02	11:32:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/02	11:32:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14		
2011/10/02	11:32:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							

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2011/10/02	11:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/02	15:35:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/02	15:35:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/02	15:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/02	15:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/02	15:58:00.0	XRT_Custom_418_OG [0x1a2]						
2011/10/02	15:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/02	17:11:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/02	17:11:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/02	17:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/02	17:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/02	17:35:00.0	XRT_Custom_418_OG [0x1a2]						
2011/10/02	17:36:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/02	18:06:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/02	18:06:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00	
2011/10/02	18:06:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00	00 00	
2011/10/02	18:06:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/10/02	18:06:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/10/02	18:06:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/02	18:09:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04	
2011/10/02	18:09:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/02	18:16:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/02	18:16:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00	
2011/10/02	18:16:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00	00 00	
2011/10/02	18:16:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/02	18:16:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/02	18:16:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/02	18:16:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/02	18:19:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/02	18:19:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14	
2011/10/02	18:19:28.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2011/10/02	18:19:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/02	18:48:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/02	18:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/02	18:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/02	18:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/02	19:11:30.0	XRT_Custom_418_OG [0x1a2]						
2011/10/02	19:12:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/02	20:25:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/02	20:25:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/02	20:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/02	20:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/02	20:49:00.0	XRT_Custom_418_OG [0x1a2]						
2011/10/02	20:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/02	20:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/02	20:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00	
2011/10/02	21:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 d8 00 49	58	
2011/10/02	21:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		

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2011/10/02	21:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/02	21:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/02	21:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/02	21:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/02	21:02:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/10/02	21:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/10/02	21:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/02	21:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/02	21:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/10/02	22:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/10/02	22:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/02	22:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/02	22:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/02	22:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/02	22:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/02	22:02:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/10/02	22:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/10/02	22:03:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/02	22:03:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/02	22:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/02	22:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/02	22:25:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/02	22:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/02	23:40:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/02	23:40:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/02	23:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/02	23:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/02	23:51:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/02	23:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	00:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	00:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/10/03	01:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/10/03	01:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/03	01:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/03	01:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/03	01:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/03	01:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/03	01:02:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/10/03	01:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/10/03	01:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	01:07:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	01:07:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/03	01:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/03	01:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/03	01:23:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/03	01:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	02:39:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	02:39:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2011/10/03	02:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/03	02:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/03	03:00:00.5	XRT_Custom_418_OG [0x1a2]							
2011/10/03	03:01:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	04:08:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	04:08:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/03	04:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/03	04:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/03	04:37:30.5	XRT_Custom_418_OG [0x1a2]							
2011/10/03	04:38:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	05:49:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	05:49:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/03	05:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/03	05:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/03	06:15:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/03	06:16:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	06:24:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	06:24:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/03	06:25:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/10/03	06:25:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/03	06:25:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/03	06:25:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/03	06:27:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2011/10/03	06:28:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	06:34:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	06:34:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/10/03	06:35:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/10/03	06:35:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/03	06:35:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/03	06:35:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/03	06:35:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/03	06:37:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/03	06:37:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/10/03	06:37:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/10/03	06:38:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	07:29:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	07:29:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/03	07:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/03	07:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/03	07:52:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/03	07:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	09:09:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	09:09:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/03	09:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/03	09:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/03	09:28:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/03	09:29:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	09:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	10:00:00.0	AOCS_OrE-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 b1 cb 00 cc				

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2011/10/03	10:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/10/03	10:02:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/03	10:02:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/03	10:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/03	10:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/03	10:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/03	10:02:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2011/10/03	10:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2011/10/03	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	10:52:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	10:52:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/03	10:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/03	10:52:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/03	10:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	10:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/03	14:36:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	14:36:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/03	14:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/03	14:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/03	14:47:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/03	14:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	16:11:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	16:11:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/03	16:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/03	16:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/03	16:34:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/03	16:35:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	17:48:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	17:48:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/03	17:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/03	17:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/03	18:11:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	18:11:26.0	XRT_FOCUS_RECALIBRATE_430_OG [0x1ae]							
		XRT_FOCUS_RECAL	2	07-F8	78	00			
2011/10/03	18:11:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2011/10/03	18:15:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/10/03	18:15:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/03	18:15:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/03	18:15:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/03	18:18:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2011/10/03	18:18:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/03	18:21:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/03	18:21:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/10/03	18:21:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	00	00	00	00
2011/10/03	18:21:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/03	18:21:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/03	18:21:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/03	18:21:52.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/03	18:24:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

2011/10/03	18:24:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14
2011/10/03	18:24:28.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2011/10/03	18:24:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/03	19:25:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/03	19:25:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/03	19:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/10/03	19:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/10/03	19:48:00.0	XRT_Custom_418_OG [0x1a2]					
2011/10/03	19:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/03	21:02:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/03	21:02:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/03	21:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/10/03	21:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/10/03	21:25:30.0	XRT_Custom_418_OG [0x1a2]					
2011/10/03	21:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/03	22:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/03	22:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/03	22:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/10/03	22:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/10/03	22:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/03	22:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2011/10/03	23:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04 00 00 00	00 00
2011/10/03	23:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/10/03	23:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/10/03	23:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/10/03	23:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/10/03	23:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/03	23:02:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14
2011/10/03	23:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2011/10/03	23:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/04	00:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/04	00:17:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/04	00:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/10/04	00:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/10/04	00:25:00.0	XRT_Custom_418_OG [0x1a2]					
2011/10/04	00:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/04	00:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/04	00:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2011/10/04	01:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00	00 00
2011/10/04	01:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/10/04	01:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/10/04	01:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/10/04	01:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/10/04	01:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/04	01:02:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14
2011/10/04	01:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2011/10/04	01:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/04	01:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2011/10/04	01:41:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/04	01:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/04	01:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/04	02:00:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/04	02:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/04	03:16:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/04	03:16:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/04	03:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/04	03:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/04	03:37:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/04	03:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/04	04:46:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/04	04:46:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/04	04:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/04	04:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/04	05:14:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/04	05:15:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/04	05:58:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/04	05:58:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/04	05:58:30.0	AOCs_Or-e-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/10/04	05:58:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/04	05:58:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/04	05:58:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/04	06:01:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2011/10/04	06:01:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/04	06:08:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/04	06:08:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/10/04	06:08:30.0	AOCs_Or-e-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/10/04	06:08:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/04	06:08:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/04	06:08:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/04	06:08:52.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/04	06:11:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/04	06:11:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/10/04	06:11:28.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/10/04	06:11:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/04	06:26:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/04	06:26:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/04	06:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/04	06:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/04	06:52:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/04	06:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/04	08:06:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/04	08:06:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/04	08:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/04	08:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/04	08:28:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/04	08:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/04	09:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

2011/10/04 10:00:00.5 AACS_ORe-point_Start_3_OG [0x099]
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