

XRT Timeline to be uploaded on 2011/10/13

Period: 2011/10/13 10:49:00 - 2011/10/18 10:38:00

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Normal mode

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XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad

Term	Pointing (x, y)	Comment
10/13 11:02:00 - 10/13 11:58:54	Track (828.9, 332.4) ^{Ⓢ 10/13 10:59:00}	# OP start + 10min, HOP204
10/13 22:43:00 - 10/13 23:39:54	Fixed (865.0, 342.0)	HOP204
10/14 10:03:00 - 10/14 10:59:54	Fixed (865.0, 342.0)	HOP204
10/14 21:43:00 - 10/14 22:39:54	Fixed (865.0, 342.0)	HOP204
PROG= 10 Inf.-time(s)		
┌ Subr= 1 1-time(s) 2.0sec		
├ Seqn= 19 1-time(s) 2.0sec		
├ Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
├ Open/G-band	Open/G-band open Safe Norm 63ms	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
├ Seqn= 96 4-time(s) 2.0sec		
├ Al-poly/Open	thin-Be/Open close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
├ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
├ thin-Be/Open	med-Be/Open close Safe Norm 5.66s	Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
├ Open/thick-Al	Open/thick-Al close Safe Norm 16.0s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└ Subr= 2 1-time(s) 2.0sec		
├ Seqn= 62 15-time(s) 150.0sec		
├ thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
├ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
├ thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
├ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
├ thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
├ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
├ thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
├ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #18B4: AR Standard-A(Filter-Ratio) with PFB, long thin-Be, thick Al and Al/Poly context, 512x512 at 1064 1048, 100s cad

Term	Pointing (x, y)	Comment
10/13 12:02:00 - 10/13 18:04:24	Track (802.9, -307.2) ^{Ⓢ 10/13 11:59:00}	HOP201
10/14 11:03:00 - 10/14 17:37:54	Track (884.4, -287.1) ^{Ⓢ 10/14 11:00:00}	HOP201
PROG= 09 Inf.-time(s)		
┌ Subr= 1 1-time(s) 2.0sec		
├ Seqn= 17 1-time(s) 2.0sec		
├ Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s	Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec
├ Open/G-band	Open/G-band open Safe Norm 63ms	Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec
├ Seqn= 54 4-time(s) 2.0sec		
├ Al-poly/Open	thin-Be/Open close Safe Norm 500ms	Obs 1x1 512x512 (1064, 1048) Q=95 2 0 2.0sec
├ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 512x512 (1064, 1048) Q=95 2 0 2.0sec
├ thin-Be/Open	med-Be/Open close Safe Norm 16.0s	Obs 1x1 512x512 (1064, 1048) Q=95 2 0 2.0sec
├ Open/thick-Al	Open/thick-Al close Safe Norm 16.0s	Obs 1x1 512x512 (1064, 1048) Q=95 2 0 2.0sec
└ Subr= 2 1-time(s) 2.0sec		
├ Seqn= 79 20-time(s) 2.0sec		
├ thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 512x512 (1064, 1048) Q=95 2 0 2.0sec
├ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 512x512 (1064, 1048) Q=95 2 0 15.0sec
├ thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
├ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
├ thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
├ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
├ thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
├ Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #187C: Synoptic Q95 2x2 - Al/mesh(45/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2897) + Thin-Be(36)

Term	Pointing (x, y)	Comment
10/13 18:07:30 - 10/13 18:14:24	Fixed (0.0, 0.0)	synoptic, shifted 4.5 min
10/14 05:38:30 - 10/14 05:45:24	Fixed (0.0, 0.0)	synoptic, shifted -24.5 min
10/14 17:41:00 - 10/14 17:47:54	Fixed (0.0, 0.0)	synoptic, shifted -22.0 min
10/15 05:59:30 - 10/15 06:06:24	Fixed (0.0, 0.0)	synoptic, shifted -3.5 min
PROG= 12 1-time(s)		
┌ Subr= 1 1-time(s) 12.0sec		
├ Seqn= 86 1-time(s) 4.0sec		
├ Open/Al-mesh	Open/Al-mesh close Safe Norm 44ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
├ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
├ Seqn= 5 1-time(s) 2.0sec		
├ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
├ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
├ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
├ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
├ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└ Seqn= 37 1-time(s) 4.0sec		

		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
		Seqn= 32		1-time(s)		2.0sec									
		thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
		thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
		Seqn= 4		1-time(s)		2.0sec									
		Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #18AF: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 4min cad

Term	Pointing (x, y)	Comment
10/13 18:17:30 - 10/13 20:39:30	Track (-361.7, 335.4) @ 10/13 18:14:30	AR 11314
10/13 23:43:00 - 10/14 02:53:00	Track (-321.2, 333.8) @ 10/13 23:40:00	AR 11314
10/14 05:48:30 - 10/14 07:43:30	Track (-274.7, 332.4) @ 10/14 05:45:30	AR 11314
10/14 17:51:00 - 10/14 19:38:30	Track (-180.3, 330.4) @ 10/14 17:48:00	AR 11314
10/14 22:43:00 - 10/15 05:56:24	Track (-141.4, 329.9) @ 10/14 22:40:00	AR 11314
10/15 06:09:30 - 10/15 10:01:30	Track (-81.3, 329.5) @ 10/15 06:06:30	AR 11314

PROG= 20 Inf.-time(s)

		Subr= 1		1-time(s)		2.0sec									
		Seqn= 19		1-time(s)		2.0sec									
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
		Seqn= 96		4-time(s)		2.0sec									
		Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec	
		thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec	
		Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
		Subr= 2		1-time(s)		2.0sec									
		Seqn= 62		11-time(s)		240.0sec									
		thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec	
		thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec	
		thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	
		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec	
		thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec	
		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

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Flare mode

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XOB #1869: Flare standard obs. multifilter (thin-Be,med-Al,thick-Be 384x384 - Al-poly 512x512 2x2)

Term	Pointing (x, y)	Comment
10/13 11:02:00 - 10/13 11:58:54	Track (828.9, 332.4) @ 10/13 10:59:00	# OP start + 10min, HOP204
10/13 12:02:00 - 10/13 18:04:24	Track (802.9, -307.2) @ 10/13 11:59:00	HOP201
10/13 18:17:30 - 10/13 20:39:30	Track (-361.7, 335.4) @ 10/13 18:14:30	AR 11314
10/13 22:43:00 - 10/13 23:39:54	Fixed (865.0, 342.0)	HOP204
10/13 23:43:00 - 10/14 02:53:00	Track (-321.2, 333.8) @ 10/13 23:40:00	AR 11314
10/14 05:48:30 - 10/14 07:43:30	Track (-274.7, 332.4) @ 10/14 05:45:30	AR 11314
10/14 10:03:00 - 10/14 10:59:54	Fixed (865.0, 342.0)	HOP204
10/14 11:03:00 - 10/14 17:37:54	Track (884.4, -287.1) @ 10/14 11:00:00	HOP201
10/14 17:51:00 - 10/14 19:38:30	Track (-180.3, 330.4) @ 10/14 17:48:00	AR 11314
10/14 21:43:00 - 10/14 22:39:54	Fixed (865.0, 342.0)	HOP204
10/14 22:43:00 - 10/15 05:56:24	Track (-141.4, 329.9) @ 10/14 22:40:00	AR 11314
10/15 06:09:30 - 10/15 10:01:30	Track (-81.3, 329.5) @ 10/15 06:06:30	AR 11314

PROG= 13 1-time(s)

		Subr= 1		4-time(s)		2.0sec									
		Seqn= 55		45-time(s)		20.0sec									
		thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
		med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
		Seqn= 90		1-time(s)		2.0sec									
		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
		Subr= 2		1-time(s)		600.0sec									
		Seqn= 89		1-time(s)		2.0sec									
		Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec	
		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec	
		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
		Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

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Active Region Search

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NOT USED

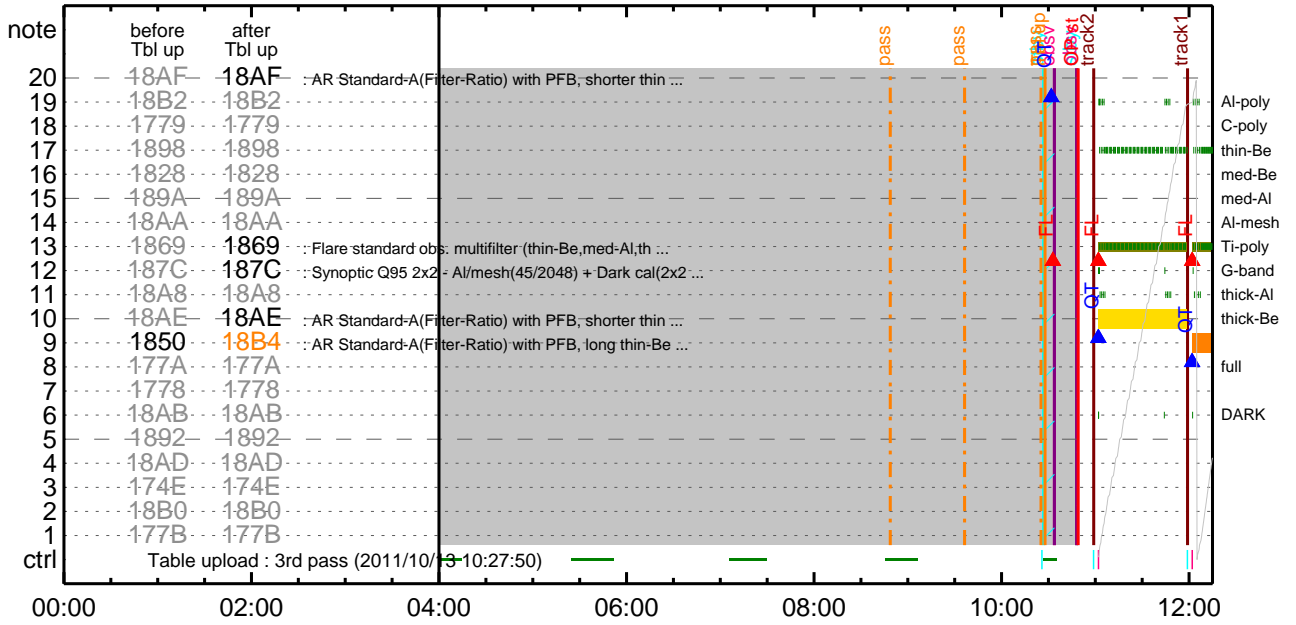
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Flare Detection

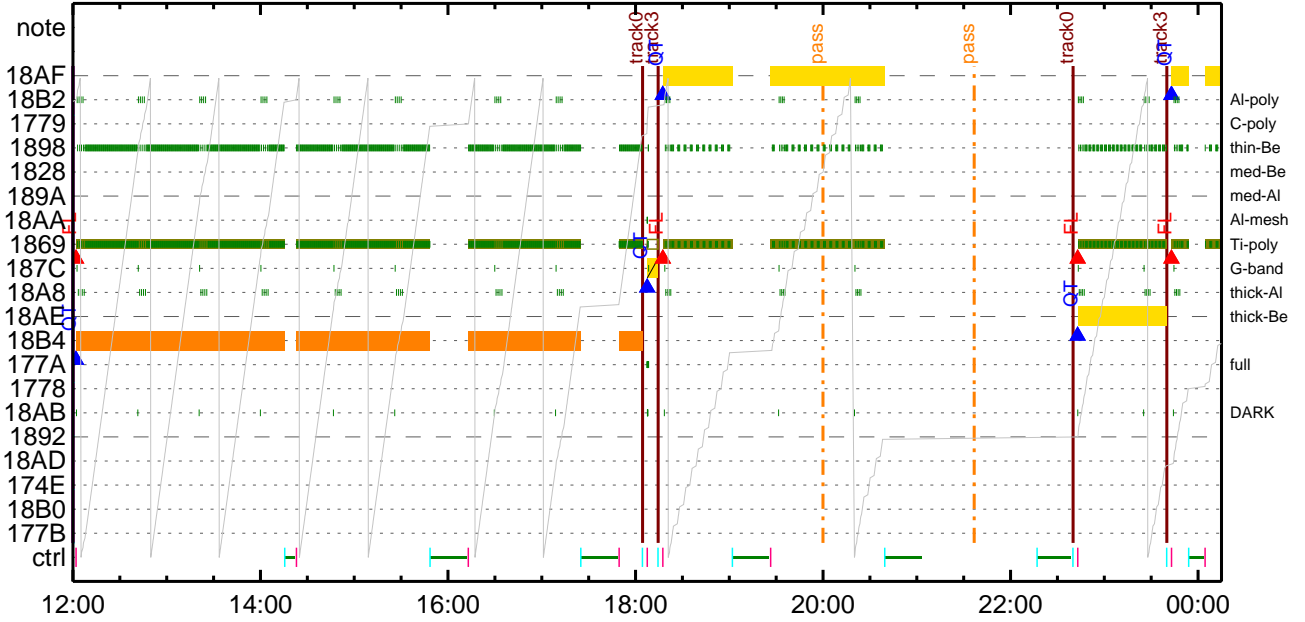
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FLD Patrol											
Term		Pointing (x, y)				Comment					
10/13 18:14:46 - 10/14 05:35:46		Track (-361.7, 335.4) @ 10/13 18:14:30				AR 11314					
10/14 05:45:46 - 10/14 17:38:16		Track (-274.7, 332.4) @ 10/14 05:45:30				AR 11314					
10/14 17:48:16 - 10/15 05:56:46		Track (-180.3, 330.4) @ 10/14 17:48:00				AR 11314					
10/15 06:06:46 - 10/18 10:38:00		Track (-81.3, 329.5) @ 10/15 06:06:30				AR 11314					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

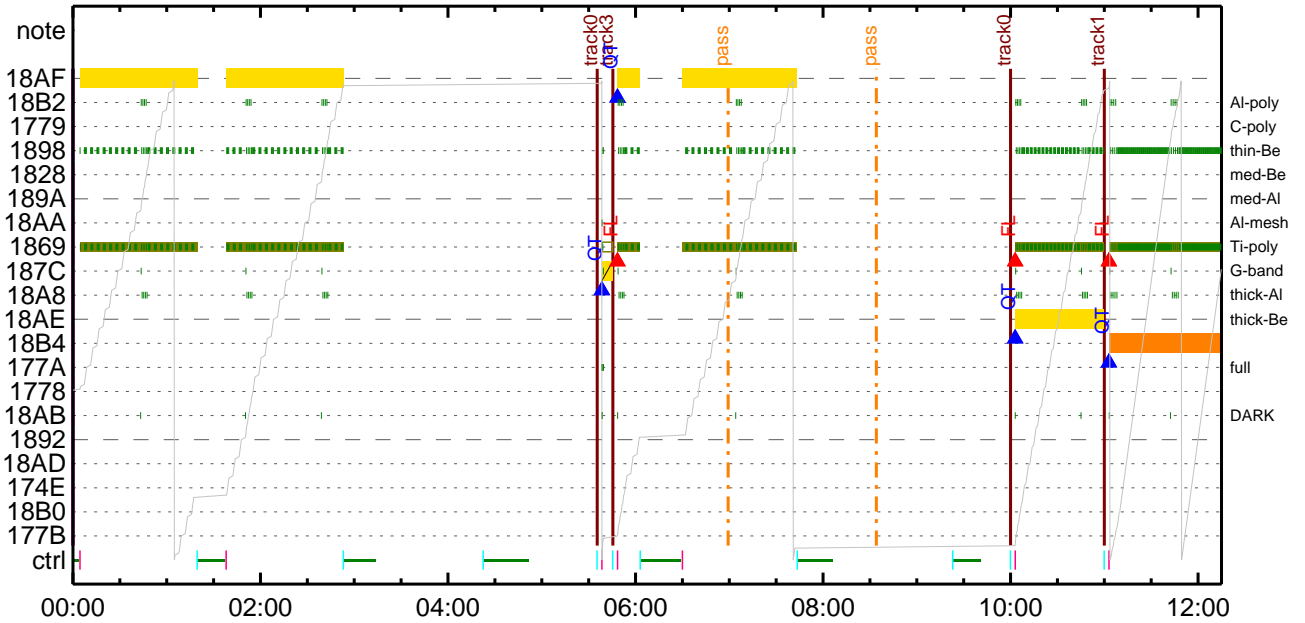
CMDI #0206 2011/10/13



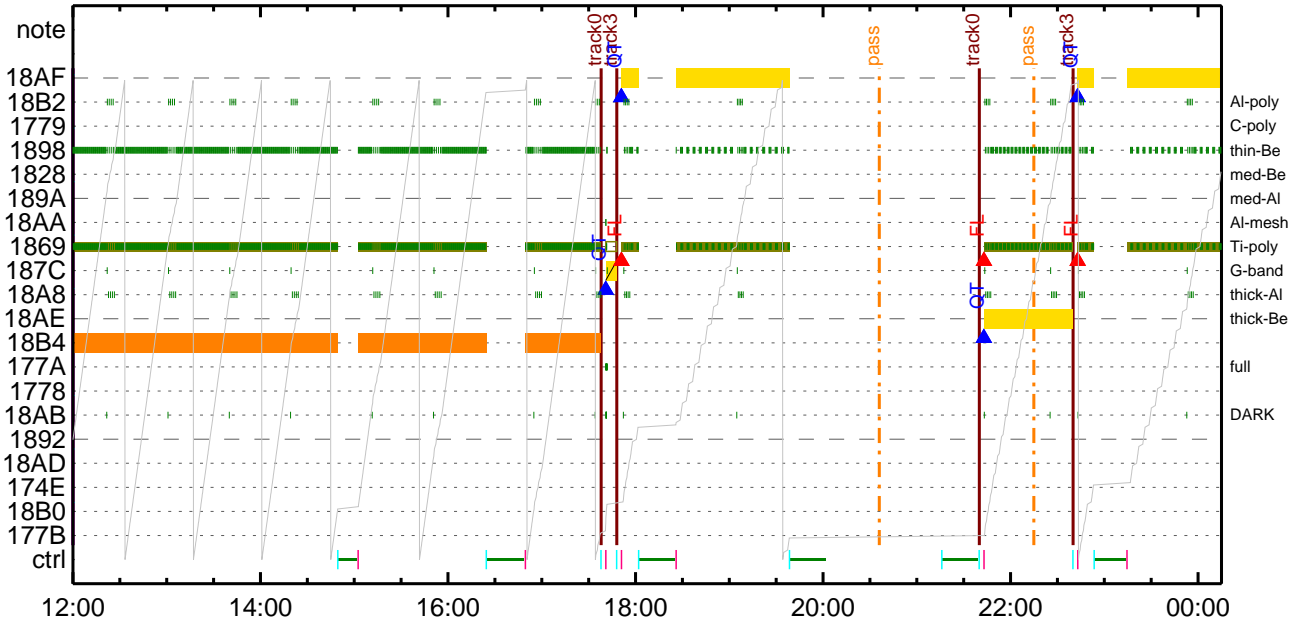
CMDI #0206 2011/10/13



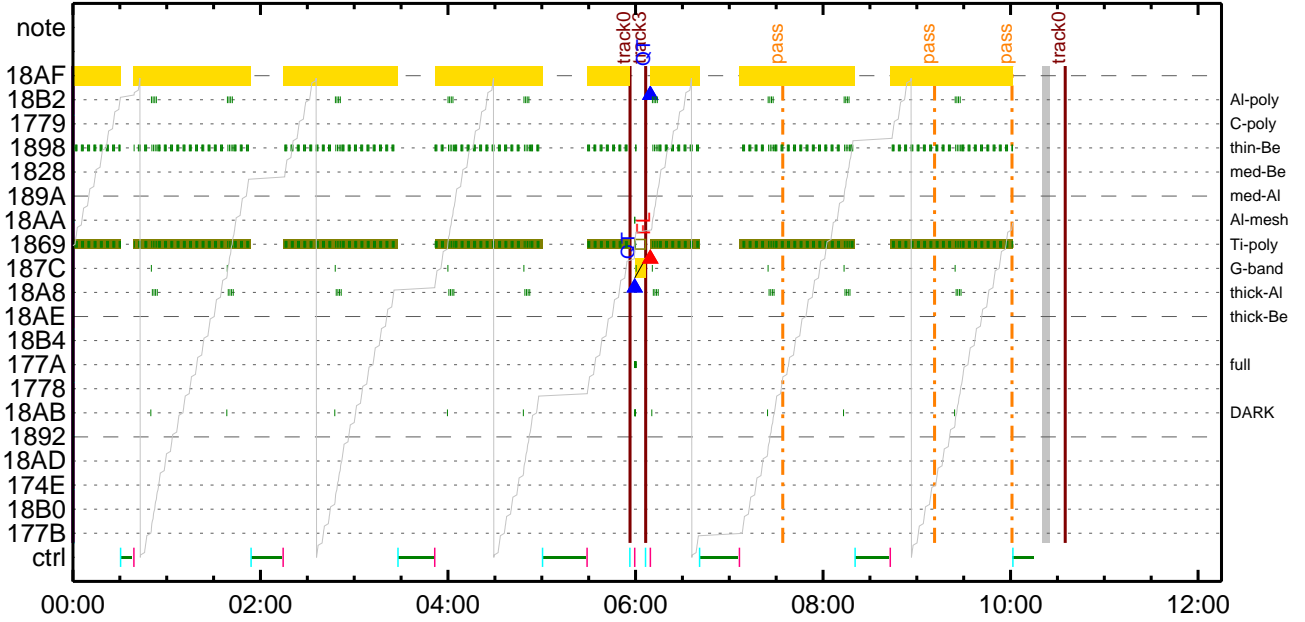
CMDI #0206 2011/10/14



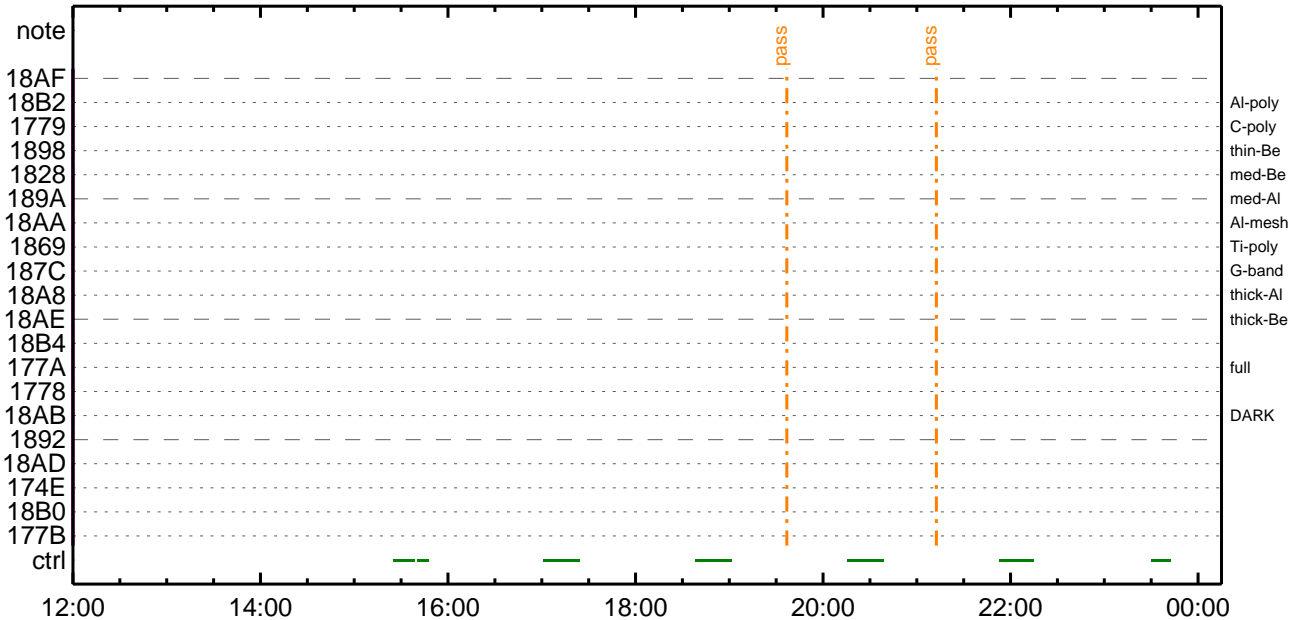
CMDI #0206 2011/10/14



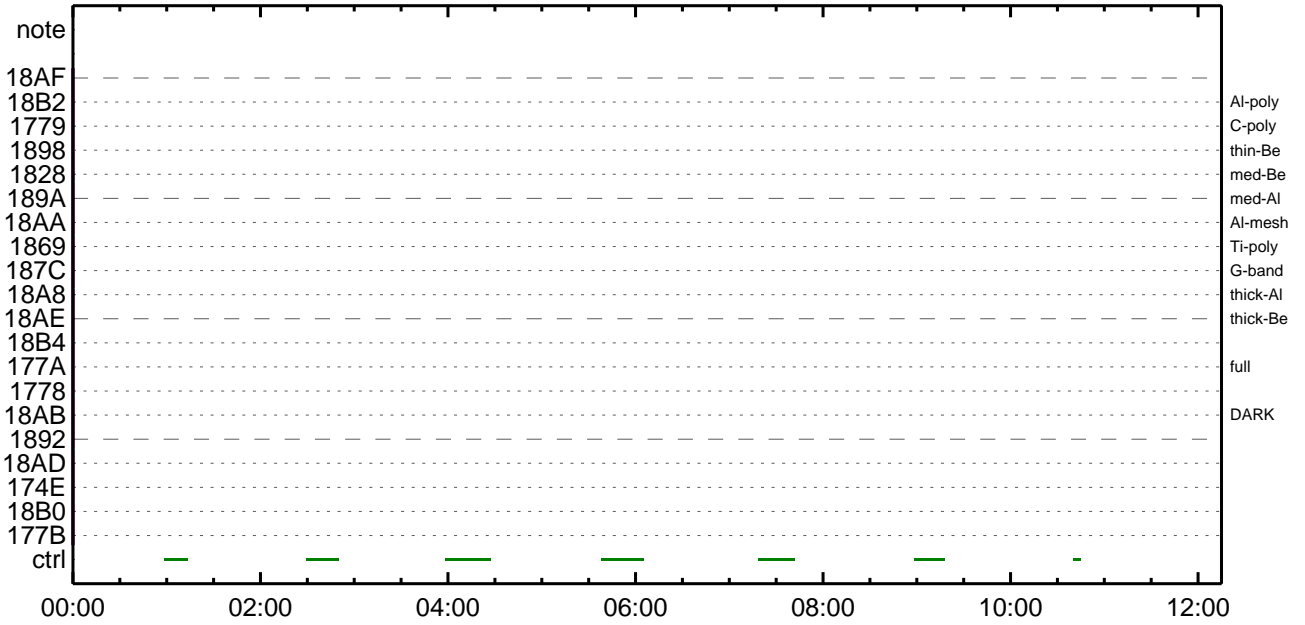
CMDI #0206 2011/10/15



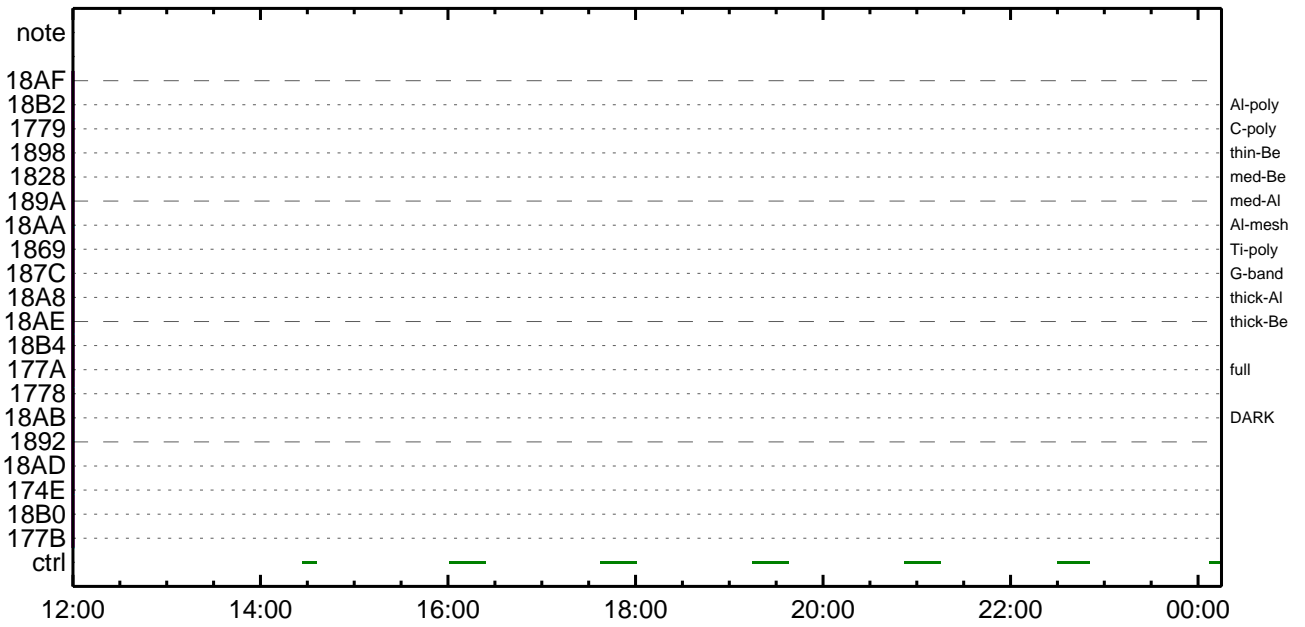
CMDI #0206 2011/10/15



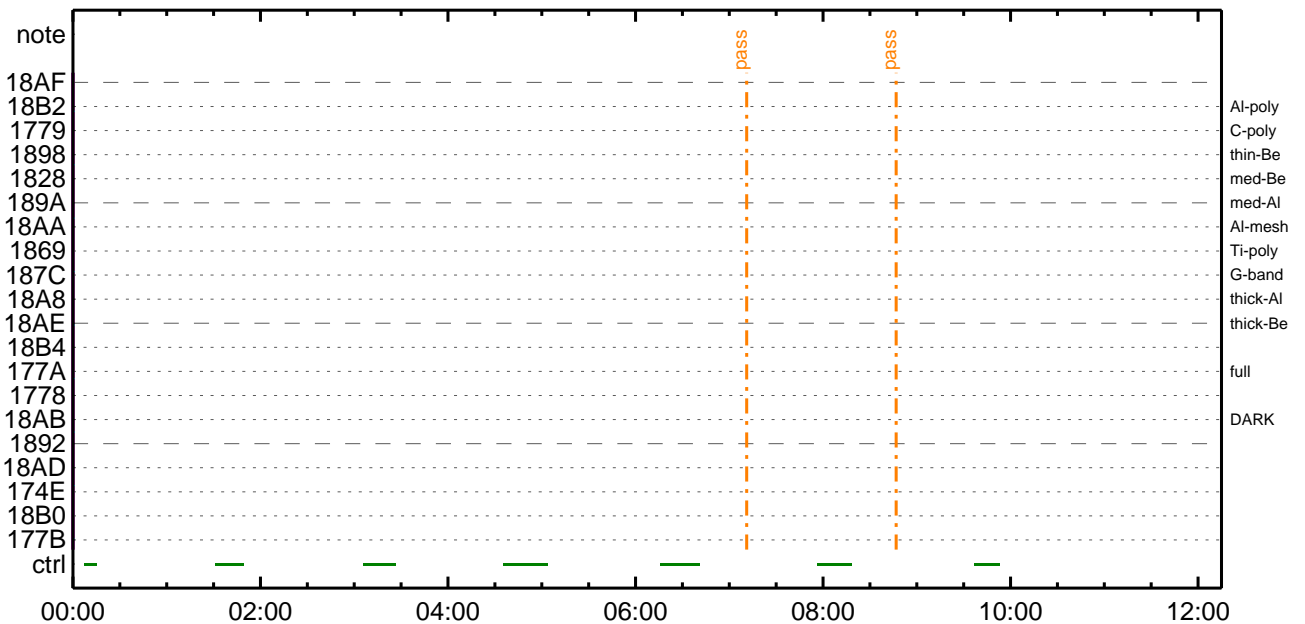
CMDI #0206 2011/10/16



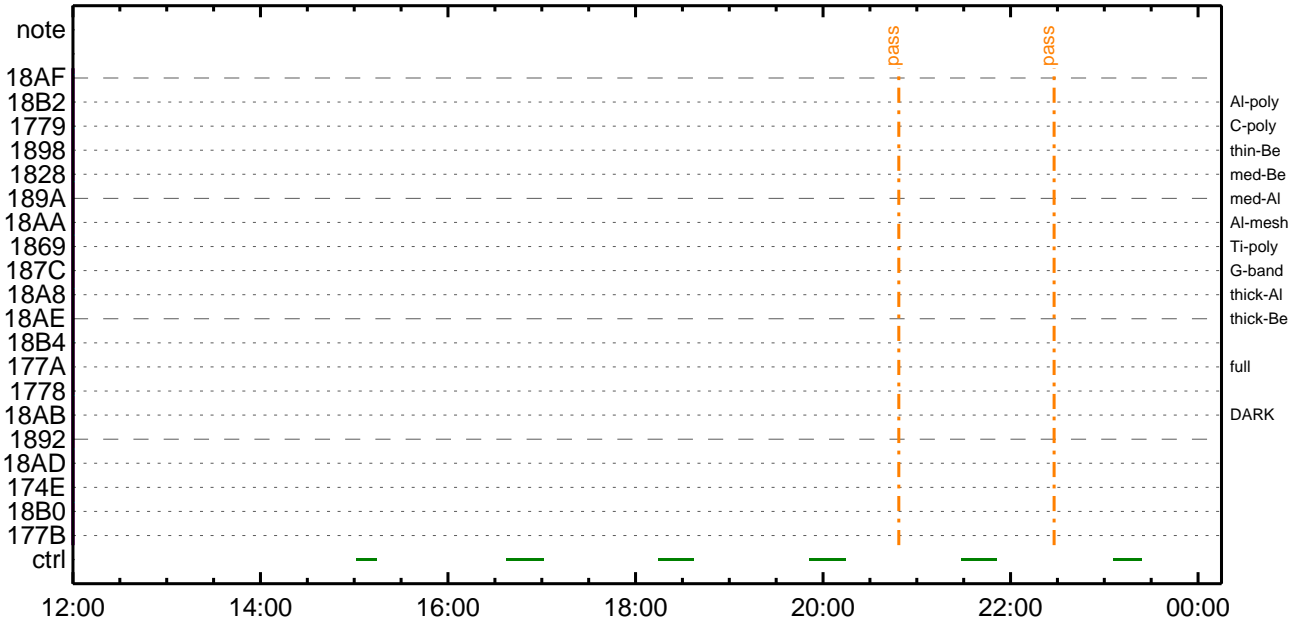
CMDI #0206 2011/10/16



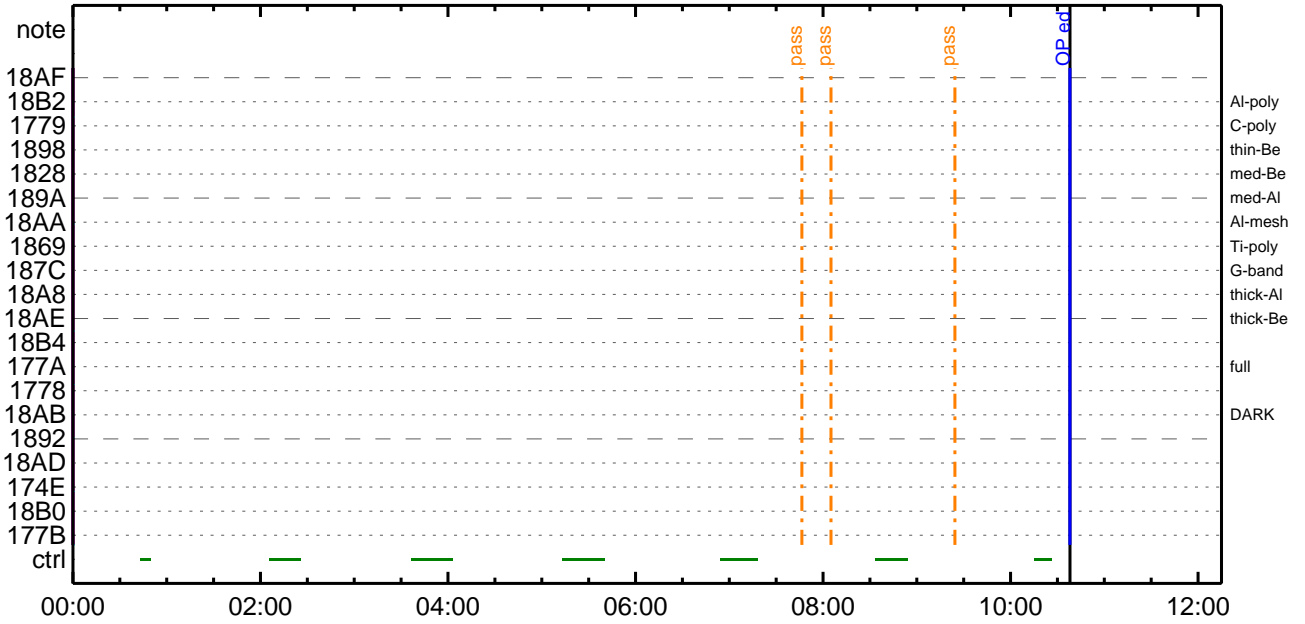
CMDI #0206 2011/10/17



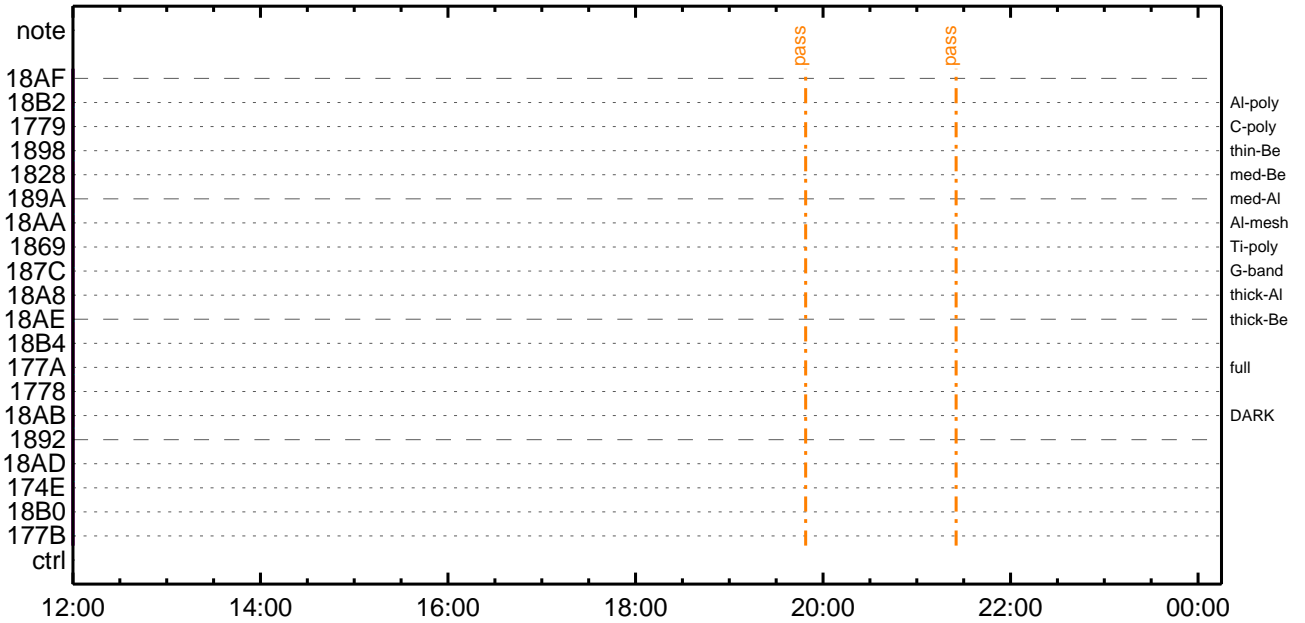
CMDI #0206 2011/10/17



CMDI #0206 2011/10/18



CMDI #0206 2011/10/18



(a) Spacecraft Operation Procedure (real-commands)

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main-395 2011-10-13 12:02:57 205 33 SOLAR-B MAIN //
0001 . C.
0002 . C. ***** AOS *****
0003 . C.
0004 . C. ;ãAOSÿÁÿ$ÿÄÿ-¼Á»Û;ä
0005 . C.
0006 . C. ÿÀÿß;¼ÿ³ÿFÿóÿÉÁ÷ç©
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 . C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 . C. Áí;Èçèçãð•µ°«»Í×ÁççíÿçÿÄÿ×ÿÿí;¼ÿÿÉ;ÈÈèµ•ííÉ;Èèè¼°ççðã•øç¼í¹ççí;çÁ®,ù¹¹èèèèèççÁ÷çç©ã•èèèèèèèèèè;é
0011 +. DC 02-8E AOCU_ORB_UPD
0012 . C.
0013 . C.
0014 . C. *****
0015 . C. OP/OGÿí;¼ÿÿÉ;|ÿÄÿóÿÿ×
0016 . C. *****
0017 . C.
0018 . C. ;ãOP/OGÿí;¼ÿÿÉ;ä
0019 . S. OP op-395:OP
0020 . C.
0021 . S. OG og-395:OG
0022 . C.
0023 . C.
0024 . C. ;ãNMOG&OPí°èÿÄÿóÿÿ×;ä
0025 . C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 . BC (20 00 7f 01 02)
0028 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 . C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 . BC (07 0b f8)
0035 . C. çç[HK1_PKT_FORM_NO] EQ 7
0036 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ÿÀÿóÿÿ×¼ªªî»ðð³îç§
0041 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGðî¼è¹ç•è²îOKðð³îç§
0043 . C.
0044 . C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 . BC (20 80 7f 01 02)
0047 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 . C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 . BC (07 0b f8)
0054 . C. çç[HK1_PKT_FORM_NO] EQ 7
0055 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ÿÀÿóÿÿ×¼ªªî»ðð³îç§
0060 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGðî¼è¹ç•è²îOKðð³îç§
0062 . C.
0063 . C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 . BC (21 00 41 01 02)
0066 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 . C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 . BC (07 0b f8)
0073 . C. çç[HK1_PKT_FORM_NO] EQ 7
0074 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ÿÀÿóÿÿ×¼ªªî»ðð³îç§
0079 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPðî¼è¹ç•è²îOKðð³îç§
0081 . C.
0082 . C. ***** øÈ²¼ðî¼ÿÄ'¶í°èÈÈ-ðòÀ÷çç© (¼äµ-ÿÄÿóÿÿ×¼è¼çððððÖÿççªºãð²¼ð²¼¼í¹ççççã) *****
0083 . C. DHUÿâ;¼ÿÿÉ;È¼ÿ¼;ÿí;¼ÿÿÉ;Èèðíá¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 . BC (02 0a f8)
0086 . C. çç[HK1_PKT_FORM_NO] EQ 2
0087 . C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 . C.
0091 . C. *****
0092 . C. TI-CMD SET (OPOG STOP/COPY/START)
0093 . C. *****
0094 . C.
0095 . C. NOTICE |§ OPOG UPLOADã-Ã÷çç©NGòî¼í¹çç;ç°è²¼ðî¼TI-CMDÁ÷çç©ðî¼ÿÄ¹òã•èèèèèèèèèèè;é
```


0096 C. ...
0097 C.
0098 . C. TI... (UT)
0099 +. TI 2011-10-13 10:44:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C. ... [HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2011-10-13 10:44:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C. ... [HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2011-10-13 10:44:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C. ... [HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2011-10-13 10:48:59.5
0112 DC 01-B2 DHU_OP_START
0113 C. ... [HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C. ...
0116 C. ... [HK1_TI_CMD_ENA/DIS] EQ ENA
0117 C. ... [HK1_TI_CMD_NUM] EQ 4
0118 C. ... [HK1_NEXT_EXEC_PIM] EQ DHU
0119 C. ... [HK1_NEXT_EXEC_DC] EQ 0xB3
0120 C.
0121 . C. *****
0122 C. TI...
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C. ... [HK1_DMP_TOP_ADRS_1] EQ 07
0129 C. ... [HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C. ... [HK1_DMP_BLOCK_NUM] EQ 3
0131 C. ... [HK1_DMP_REPEAT_NUM] EQ 0
0132 C. ... [HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C. ... [HK1_PKT_FORM_NO] EQ 7
0136 C. ... [HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C. ... [HK1_S_TLM_BIT_RATE] EQ 32k
0138 C. ... [HK1_X_TLM_BIT_RATE] EQ 4M
0139 C. ... [HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 . C. ...
0142 C. ... [HK1_DMP_CHK_FLG] EQ NON
0143 C.
0144 . C. RAM ID=TI_TBL...
0145 C.
0146 . C. DHU...
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C. ... [HK1_PKT_FORM_NO] EQ 2
0150 C. ... [HK1_PKT_GEN_TIME] EQ 0.5S
0151 C. ... [HK1_S_TLM_BIT_RATE] EQ 32K
0152 C. ... [HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2011-10-13 10:48:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP []
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2011-10-13 10:48:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2011-10-13 10:48:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2011-10-13 10:48:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP ... *****
0192 C. (%ã°iYÖYÄYÉYßYËYáYçYëãE%¼ã¼Ä»Üã¹ãé)
0193 . S. DC-BC dcbc-402:DCBC

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

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main-396 2011-10-13 12:02:57 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Ä»Û;ã
0005 C.
0006 C. YÁYß;¼Y³YB¥ÖYÉÄ+¿@
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;Ë¿µÄµ•µ°Ë»Í×ÁÇµÍYÇYÁY×YÍ;¼YÉ;ËÈÈ½µ•ÍÉ;ÈµÈ¼°ÇÖµ•µ¿¼Í¹ÇµÍ;ÇÄ®, ùµ¹µÈµµÇÄ+¿@µ•µÈµµµ³µÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ+¿µ;ON
0016 C. *****
0017 C. Ç" °ËÄ, Í×ÈYµäLOSµµÇµÍ»p´Öµµ¹ÍÍ, µ•; ÇÉÖÍ×µÈXÄÖONµÍ¹ÖµÈµµ³µÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYD¥ÖYÉYÍYÁY-¾ÖÄÖµµ°ËÄµµ•µ¿µÉ; Ç°È²¼µÍ°ËÄ, ¼È¾Çµµµ¹½Öµµ³µÈ; f
0030 C.
0031 . C. *****
0032 C. DR PT1 ÄÍ¼i°ËÄ,
0033 C. *****
0034 C. Ç" RESTART;ÈPT1;Èµµ•µ¿µµ¼Í¹ÇµÍ; Ç°È²¼µÍ½Ä¹Öµµ»°; ÇDCBC-150µØ¿Èµµ; f
0035 C.
0036 . C. ;ãPT1°ËÄ, ³«»Û;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÖYÉYÈÄÜÄÖ;ÈÄ•Ä°²óÈð;È, áµÍ°ËÄ, °Ë³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ËÄ, µ-¼«Ë°Ää»ßµµµ¿, á; Ç°È²¼µµ½Ä¹Öµµ³µÈ; f
0055 C. YÇYÖYÉYÈÄÜÄÖµµÄÄ•Ä°²óÈðµµ-µµáµµ¼Í¹ÇµÍ´°Í»µ¹µÈµµµÇÄÖµÄ; f
0056 C.
0057 . C. *****
0058 C. DR PT2 ÄÍ¼i°ËÄ,
0059 C. *****
0060 C. Ç" RESTART;ÈPT2;Èµµ•µ¿µµ¼Í¹ÇµÍ; Ç°È²¼µÍ½Ä¹Öµµ»°; ÇDCBC-151µØ¿Èµµ; f
0061 C.
0062 . C. ;ãPT2°ËÄ, ³«»Û;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÖYÉYÈÄÜÄÖ;ÈÄ•Ä°²óÈð;È, áµÍ°ËÄ, °Ë³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ËÄ, Ää»ß;ÇXÄ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ËÄ, Ää»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ+¿µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF

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0096 C.
0097 . C. < Dump RAMID=MDP_OBS_F >
0098 +. DC 07-F0 MDP_DUMP_FGTBL
0099 BC (82 07 00 00 00 38 b8)
0100 C. -----
0101 C. MDP_OBS_F verify = OK/NG []
0102 C. -----
0103 C.
0104 . C. < Stop SP table >
0105 +. DC 07-F0 MDP_SP_CTRL_MANU
0106 BC (61)
0107 C. -----
0108 C. MDP_SP_CTRL_MODE = MANU []
0109 C. -----
0110 C.
0111 . C. <Upload SP Observation Table>
0112 . S. RAM ram-284:MDP_OBS_S
0113 ()
0114 C.
0115 . C. < Dump RAMID=MDP_OBS_S >
0116 +. DC 07-F0 MDP_DUMP_SPTBL
0117 BC (83 07 00 00 00 38 b8)
0118 C. -----
0119 C. MDP_OBS_S verify = OK/NG []
0120 C. -----
0121 C.
0122 C. *****
0123 C. SOT TI command set
0124 C. *****
0125 C. Execute, after the success of TBL upload.
0126 +. TI 2011-10-13 10:48:18.0
0127 DC 07-F0 MDP_SOT_MODE_OBSV
0128 BC (40)
0129 . C. -----
0130 C. HK1_TI_CMD_NUM = 1 CNTUP []
0131 C. -----
0132 C.
0133 C.
0134 . C. ***** MDP 'ũÃîñî»ö¼ÝñÊÃĐñ¹ñèDCBC•x²è *****
0135 C. (¼á°îÝÖÝÃÝËÝÞÝËÝÁÝ¿ÝèñÊ¼ñ¼Ã»Ûñ¹ñè)
0136 . S. DC-BC dcbc-402:DCBC
0137 (MDP_known_event)
0138 C.
0139 C.
0140 . C. ***** ÝDÝ¹•İ Daily±¿İÑñË´Øñ¹ñèDCBC•x²è *****
0141 . S. DC-BC dcbc-153:DCBC
0142 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0143 C.
0144 C.
0145 . C. ;ãLO\$ÝÁÝ\$ÝÃÝ¹¼Ã»Û;ä
0146 C.
0147 . C. ***** LOS *****
0148 C.

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XRT_OGLIST_0206.chk

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*** OP Sequence for XRT ***

2011/10/13	10:58:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/13	10:58:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/10/13	10:59:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2011/10/13	10:59:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/10/13	10:59:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/10/13	10:59:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/10/13	10:59:22.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/10/13	11:01:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/13	11:01:56.0	XRT_QT_PROG_SET_404_OG [0x194]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2011/10/13	11:01:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2011/10/13	11:02:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/13	11:58:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/13	11:58:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/10/13	11:59:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	01 00 00 00 00
2011/10/13	11:59:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/10/13	11:59:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/10/13	11:59:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/10/13	11:59:22.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/10/13	12:01:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/13	12:01:56.0	XRT_QT_PROG_SET_446_OG [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2011/10/13	12:01:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2011/10/13	12:02:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/13	14:15:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/13	14:15:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/13	14:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/13	14:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/13	14:22:00.0	XRT_Custom_418_OG [0x1a2]			
2011/10/13	14:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/13	15:48:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/13	15:48:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/13	15:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/13	15:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/13	16:12:00.0	XRT_Custom_418_OG [0x1a2]			
2011/10/13	16:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/13	17:25:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/13	17:25:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/13	17:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/13	17:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/13	17:48:30.0	XRT_Custom_418_OG [0x1a2]			
2011/10/13	17:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/13	18:04:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/13	18:04:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/10/13	18:04:30.0	AOCS_OrE-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/10/13	18:04:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/10/13	18:04:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/10/13	18:04:50.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/10/13	18:07:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c

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2011/10/13	18:07:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/13	18:14:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	18:14:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/10/13	18:14:30.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/10/13	18:14:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/13	18:14:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/13	18:14:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/13	18:14:52.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/13	18:17:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/13	18:17:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/10/13	18:17:28.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/10/13	18:17:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/13	19:02:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	19:02:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/13	19:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/13	19:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/13	19:25:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/13	19:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/13	20:39:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	20:39:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/13	20:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/13	20:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/13	22:17:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	22:17:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/13	22:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/13	22:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/13	22:39:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	22:39:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/10/13	22:40:00.0	AOCS_OrE-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 e1 9a b3 1b				
2011/10/13	22:40:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/13	22:40:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/13	22:40:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/13	22:40:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/13	22:42:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/13	22:42:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2011/10/13	22:42:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/10/13	22:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/13	23:39:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	23:39:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/10/13	23:40:00.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/10/13	23:40:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/13	23:40:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/13	23:40:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/13	23:40:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/13	23:42:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/13	23:42:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/10/13	23:42:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				

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2011/10/13	23:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/13	23:54:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/13	23:54:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/13	23:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/13	23:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/14	00:03:30.0	XRT_Custom_418_OG [0x1a2]						
2011/10/14	00:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/14	01:19:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	01:19:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	01:19:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/14	01:22:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/14	01:37:00.0	XRT_Custom_418_OG [0x1a2]						
2011/10/14	01:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/14	02:53:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	02:53:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	02:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/14	02:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/14	04:22:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	04:22:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	04:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/14	04:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/14	05:35:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	05:35:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/10/14	05:35:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2011/10/14	05:35:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/10/14	05:35:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/10/14	05:35:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/14	05:38:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2011/10/14	05:38:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/14	05:45:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	05:45:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/10/14	05:45:30.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00		
2011/10/14	05:45:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/14	05:45:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/14	05:45:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/14	05:45:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/14	05:48:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	05:48:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2011/10/14	05:48:28.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2011/10/14	05:48:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/14	06:03:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	06:03:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	06:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/14	06:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/14	06:29:00.0	XRT_Custom_418_OG [0x1a2]						
2011/10/14	06:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/14	07:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	07:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		

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2011/10/14	07:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/14	07:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/14	09:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	09:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	09:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/14	09:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/14	09:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	09:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/10/14	10:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 e1 9a b3 1b		
2011/10/14	10:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/14	10:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/14	10:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/14	10:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/14	10:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	10:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2011/10/14	10:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2011/10/14	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/14	10:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	10:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/10/14	11:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00		
2011/10/14	11:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/14	11:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/14	11:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/14	11:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/14	11:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	11:02:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2011/10/14	11:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2011/10/14	11:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/14	14:49:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	14:49:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	14:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/14	14:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/14	15:01:30.0	XRT_Custom_418_OG [0x1a2]						
2011/10/14	15:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/14	16:24:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	16:24:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	16:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/14	16:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/14	16:48:30.0	XRT_Custom_418_OG [0x1a2]						
2011/10/14	16:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/14	17:37:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	17:37:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/10/14	17:38:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2011/10/14	17:38:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/10/14	17:38:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/10/14	17:38:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/14	17:40:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2011/10/14	17:41:00.0	XRT_CTRL_AUTO_406_OG [0x196]						

2011/10/14	17:47:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/14	17:47:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	17:48:00.0	AOCs_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/10/14	17:48:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00		
2011/10/14	17:48:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/14	17:48:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/14	17:48:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/14	17:50:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/14	17:50:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	17:50:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2011/10/14	17:51:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2011/10/14	18:02:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/14	18:02:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	18:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	18:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/14	18:25:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/14	18:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]					
2011/10/14	19:38:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/14	19:38:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	19:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	19:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/14	21:16:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/14	21:16:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	21:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	21:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/14	21:39:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/14	21:39:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	21:40:00.0	AOCs_OrE-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/10/14	21:40:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 e1 9a b3 1b		
2011/10/14	21:40:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/14	21:40:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/14	21:40:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/14	21:42:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/14	21:42:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	21:42:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2011/10/14	21:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2011/10/14	22:39:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/14	22:39:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/14	22:40:00.0	AOCs_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/10/14	22:40:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00		
2011/10/14	22:40:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/14	22:40:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/14	22:40:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/14	22:42:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/14	22:42:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/14	22:42:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2011/10/14	22:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		

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2011/10/14	22:53:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/14	22:53:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/14	22:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/14	22:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/14	23:13:30.0	XRT_Custom_418_OG [0x1a2]								
2011/10/14	23:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/15	00:30:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/15	00:30:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/15	00:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/15	00:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/15	00:38:00.0	XRT_Custom_418_OG [0x1a2]								
2011/10/15	00:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/15	01:54:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/15	01:54:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/15	01:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/15	01:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/15	02:13:30.0	XRT_Custom_418_OG [0x1a2]								
2011/10/15	02:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/15	03:28:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/15	03:28:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/15	03:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/15	03:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/15	03:50:30.0	XRT_Custom_418_OG [0x1a2]								
2011/10/15	03:51:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/15	05:00:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/15	05:00:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/15	05:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/15	05:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/15	05:28:00.5	XRT_Custom_418_OG [0x1a2]								
2011/10/15	05:29:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/15	05:56:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/15	05:56:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/15	05:56:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
2011/10/15	05:56:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/15	05:56:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/15	05:56:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/15	05:59:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2011/10/15	05:59:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/15	06:06:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/15	06:06:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/10/15	06:06:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00				
2011/10/15	06:06:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/15	06:06:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/15	06:06:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/15	06:06:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/15	06:09:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/15	06:09:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/10/15	06:09:28.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/10/15	06:09:30.0	XRT_CTRL_AUTO_406_OG [0x196]								

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2011/10/15	06:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/15	06:41:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/15	06:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/15	06:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/15	07:05:30.0	XRT_Custom_418_OG [0x1a2]				
2011/10/15	07:06:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/15	08:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/15	08:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/15	08:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/15	08:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/15	08:42:00.0	XRT_Custom_418_OG [0x1a2]				
2011/10/15	08:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/15	10:01:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/15	10:01:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/15	10:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/15	10:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/15	10:35:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00