

XRT Timeline to be uploaded on 2011/10/18

Period: 2011/10/18 10:38:00 - 2011/10/22 09:49:00

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Normal mode

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XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad

Term	Pointing (x, y)	Comment
10/18 10:51:00 - 10/18 13:59:54	Track (505.2, 359.1) @ 10/18 10:48:00	# OP start + 10min, AR 11314 observations.
10/20 07:58:00 - 10/20 09:48:00	Track (747.6, 386.4) @ 10/20 07:55:00	# AR 11314 observations.
PROG= 10 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 19 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open Safe Norm 63ms	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 96 4-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 5.66s	Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Norm 16.0s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 62 15-time(s) 150.0sec		
thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

XOB #18B6: HOP203 - Ti/Poly - FOV384 - Q95 - 1min cadence

Term	Pointing (x, y)	Comment
10/18 14:03:00 - 10/18 17:54:24	Track (26.2, 66.9) @ 10/18 14:00:00	# HOP-203, bipolar region.
PROG= 14 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 25 1-time(s) 2.0sec		
Open/Ti-poly	Open/Ti-poly close Safe Norm 1.00s	Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Subr= 2 10-time(s) 2.0sec		
Seqn= 19 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open Safe Norm 63ms	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 47 30-time(s) 60.0sec		
Open/Ti-poly	Open/Ti-poly close Safe Norm 4.00s	Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

XOB #18AD: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be(12)

Term	Pointing (x, y)	Comment
10/18 17:57:30 - 10/18 18:04:24	Fixed (0.0, 0.0)	synoptic, shifted -5.5 min
10/19 06:30:30 - 10/19 06:37:30	Fixed (0.0, 0.0)	synoptic, shifted 27.5 min
10/20 07:48:00 - 10/20 07:54:54	Fixed (0.0, 0.0)	synoptic
PROG= 04 1-time(s)		
Subr= 1 1-time(s) 12.0sec		
Seqn= 7 1-time(s) 4.0sec		
Open/Al-mesh	Open/Al-mesh close Safe Norm 16ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close Safe Norm 1.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 8 1-time(s) 4.0sec		
Open/Ti-poly	Open/Ti-poly close Safe Norm 32ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close Safe Norm 2.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 75 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close Safe Norm 125ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close Safe Norm 2.83s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 4 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 16ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

XOB #18B5: HOP201 thin-Be, Ti/Poly, 512x512 at 1064 1048, 120s cad

Term	Pointing (x, y)	Comment
10/18 18:07:30 - 10/19 06:27:24	Fixed (-850.0, 350.0)	# HOP-201, East limb activity.
PROG= 11 Inf.-time(s)		

Subr= 1 1-time(s) 2.0sec													
Seqn= 17 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 28 30-time(s) 120.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long													
Term		Pointing (x, y)						Comment					
10/20 07:03:00 - 10/20 07:09:54		Fixed (-528.4, -528.4)						# XRT post-bakeout quadrant pointings 1/4.					
PROG= 07 1-time(s)													
Subr= 1 1-time(s) 12.0sec													
Seqn= 38 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly -long													
Term		Pointing (x, y)						Comment					
10/20 07:13:00 - 10/20 07:19:54		Fixed (528.4, -528.4)						# 2/4					
PROG= 18 1-time(s)													
Subr= 1 1-time(s) 12.0sec													
Seqn= 36 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long													
Term		Pointing (x, y)						Comment					
10/20 07:23:00 - 10/20 07:29:54		Fixed (528.4, 528.4)						# 3/4					
PROG= 08 1-time(s)													
Subr= 1 1-time(s) 12.0sec													
Seqn= 39 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long													
Term		Pointing (x, y)						Comment					
10/20 07:33:00 - 10/20 07:44:54		Fixed (-528.4, 528.4)						# 4/4					
PROG= 01 1-time(s)													
Subr= 1 1-time(s) 12.0sec													
Seqn= 40 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1869: Flare standard obs. multifilter (thin-Be,med-Al,thick-Be 384x384 - Al-poly 512x512 2x2)

Term	Pointing (x, y)	Comment
10/18 10:51:00 - 10/18 13:59:54	Track (505.2, 359.1) @ 10/18 10:48:00	# OP start + 10min, AR 11314 observations.
10/18 14:03:00 - 10/18 17:54:24	Track (26.2, 66.9) @ 10/18 14:00:00	# HOP-203, bipolar region.
10/18 18:07:30 - 10/19 06:27:24	Fixed (-850.0, 350.0)	# HOP-201, East limb activity.
10/20 07:58:00 - 10/20 09:48:00	Track (747.6, 386.4) @ 10/20 07:55:00	# AR 11314 observations.

PROG= 13 1-time(s)

Subr= 1	4-time(s)	2.0sec
Seqn= 55	45-time(s)	20.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 90	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Subr= 2	1-time(s)	600.0sec
Seqn= 89	1-time(s)	2.0sec
Open/Al-mesh	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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Active Region Search

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NOT USED

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Flare Detection

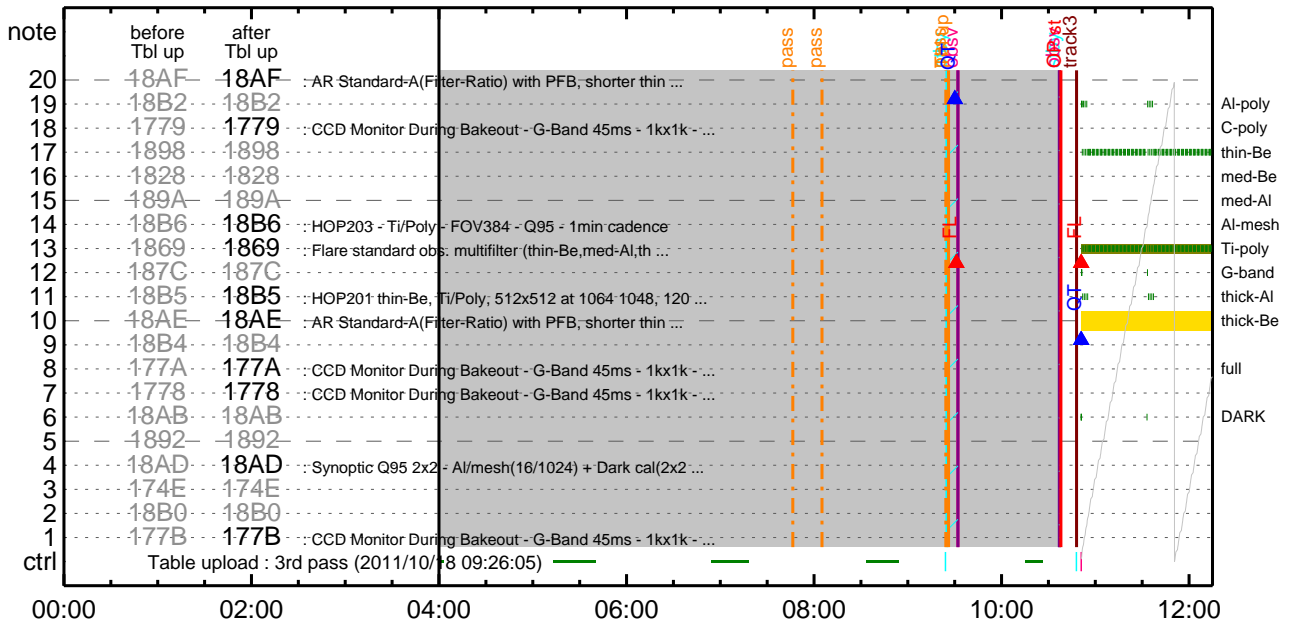
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FLD Patrol

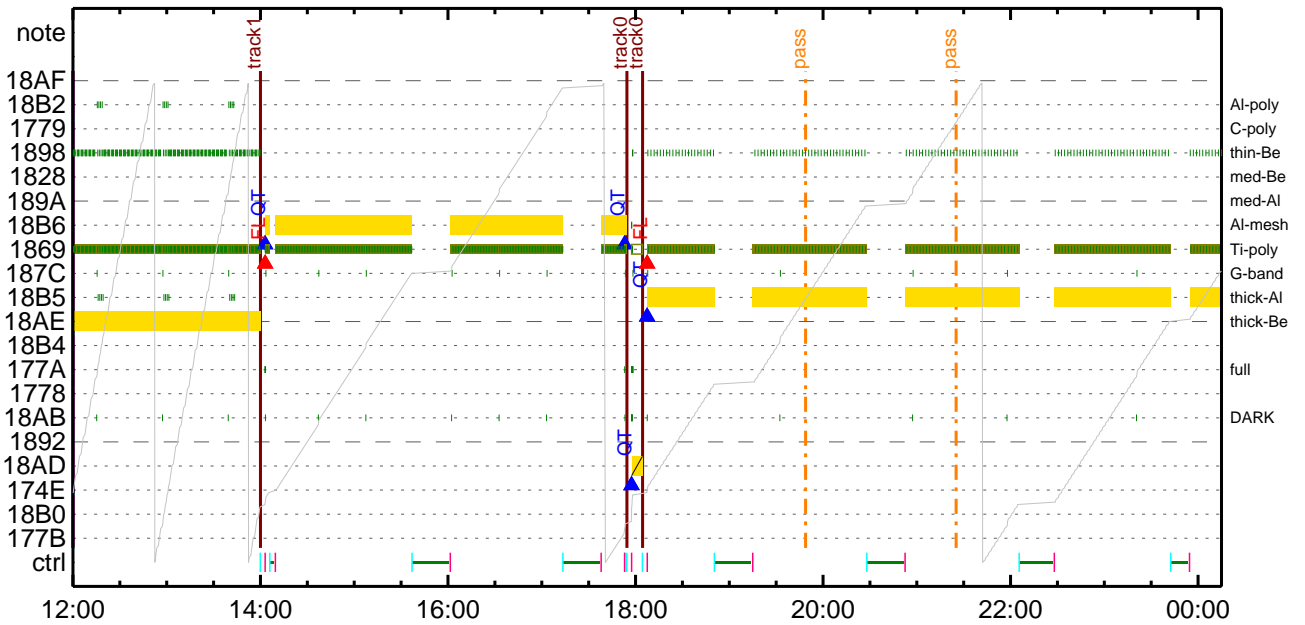
Term	Pointing (x, y)	Comment
10/18 18:04:46 - 10/19 06:27:46	Fixed (-850.0, 350.0)	# HOP-201, East limb activity.
10/20 07:55:16 - 10/22 09:49:00	Track (747.6, 386.4) @ 10/20 07:55:00	# AR 11314 observations.
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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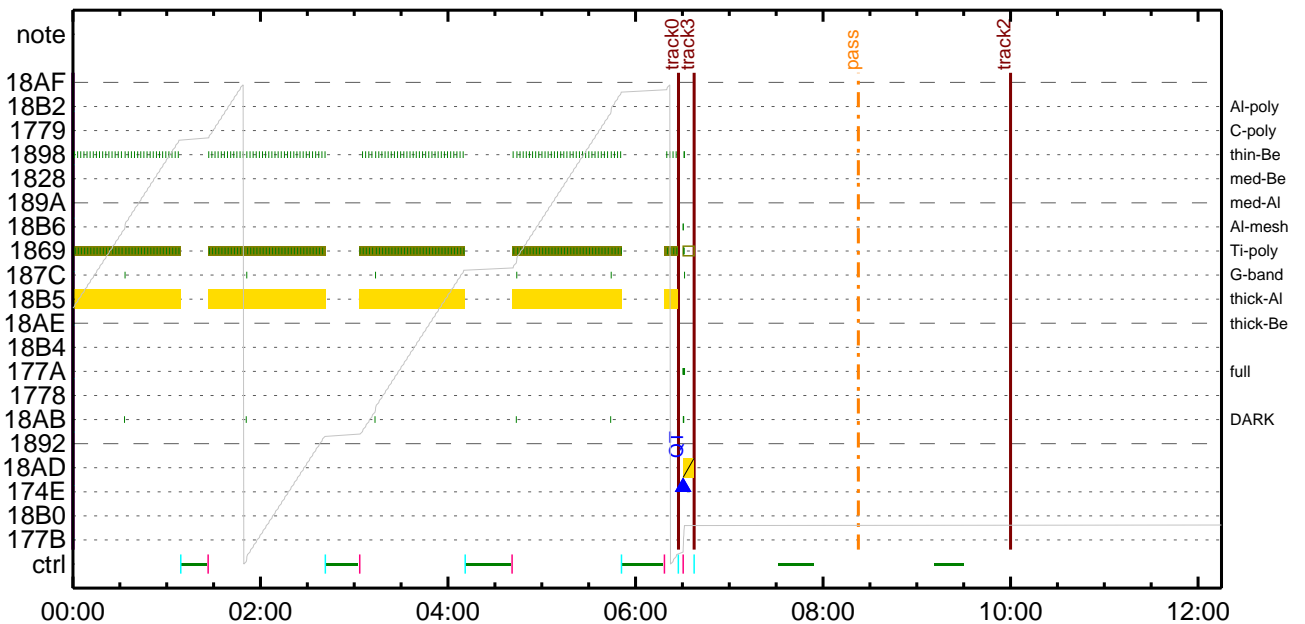
CMDI #0212 2011/10/18



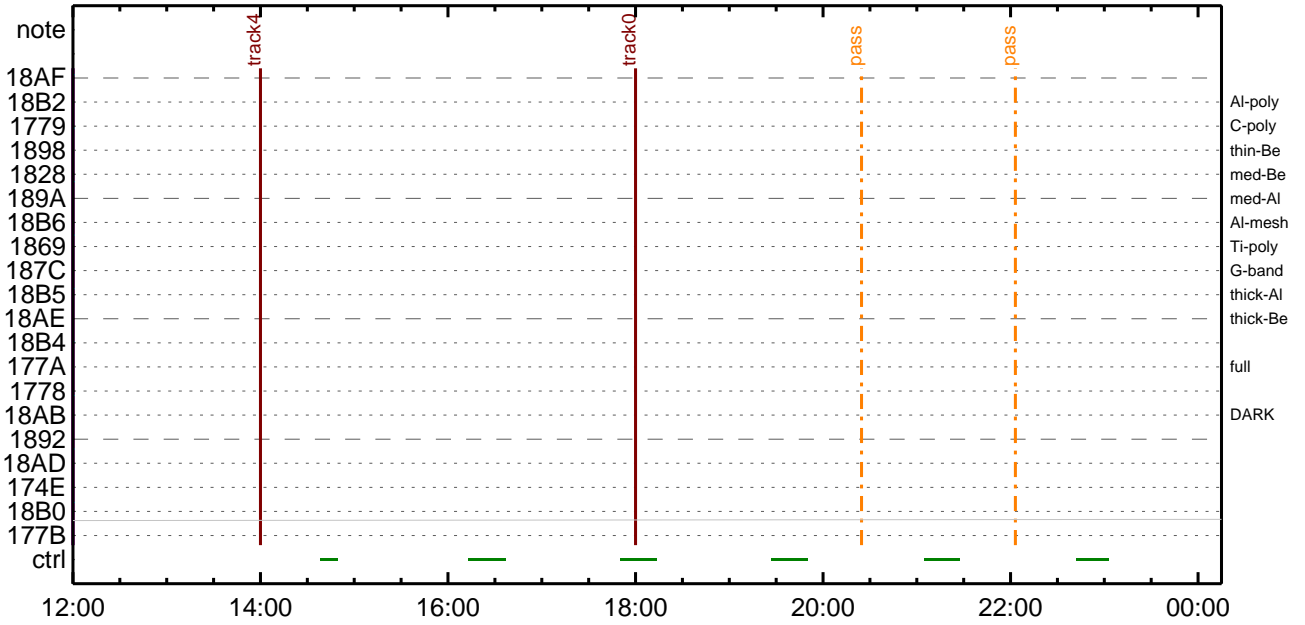
CMDI #0212 2011/10/18



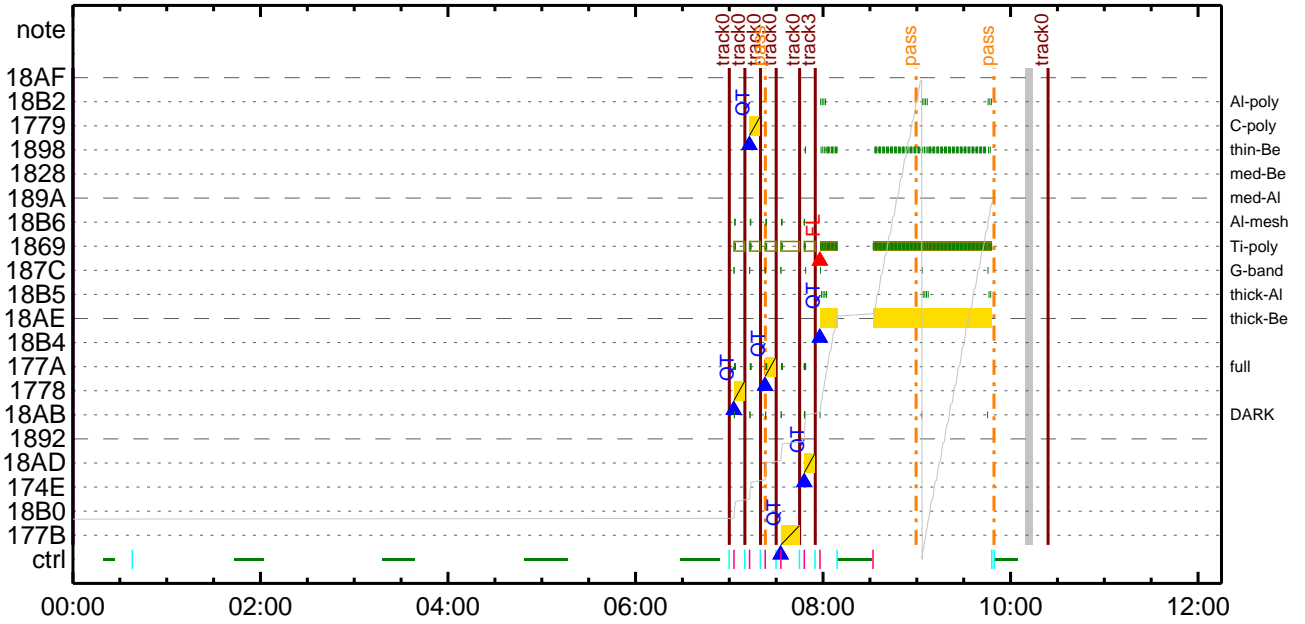
CMDI #0212 2011/10/19



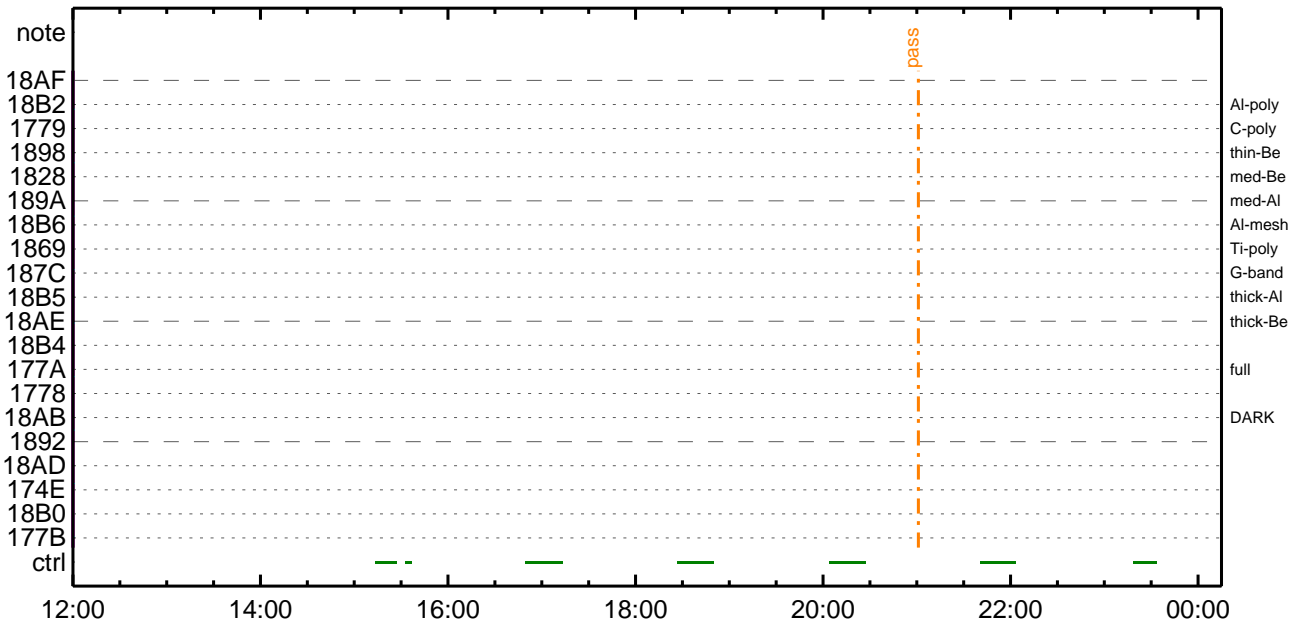
CMDI #0212 2011/10/19



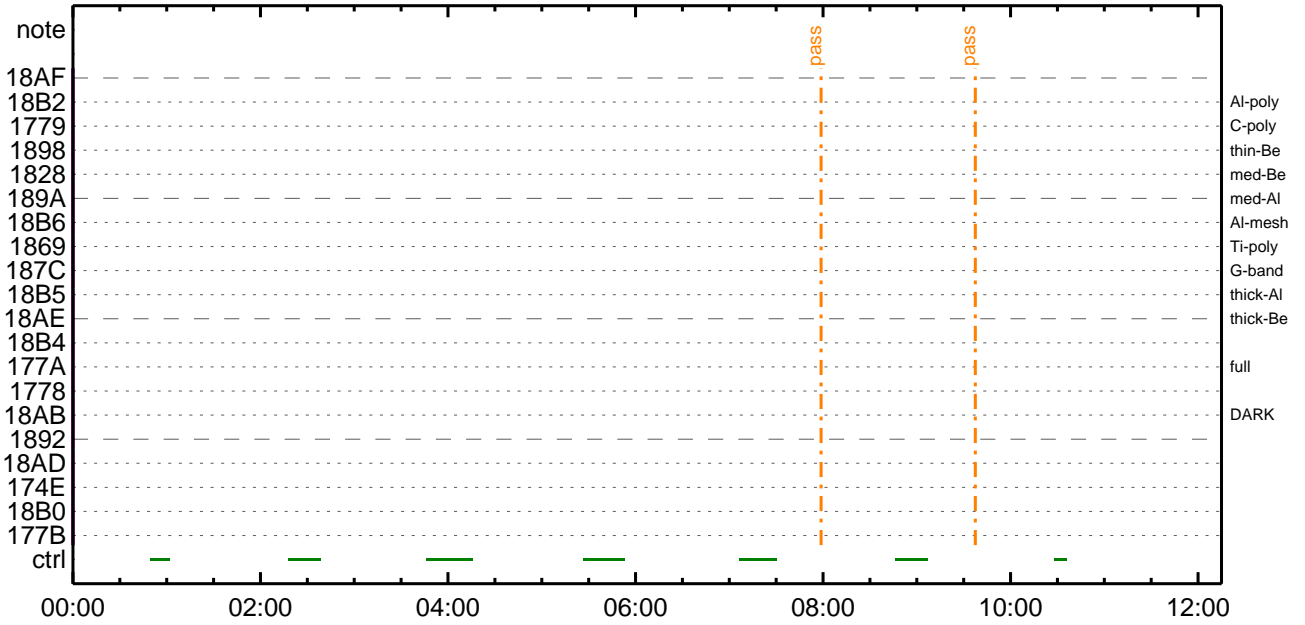
CMDI #0212 2011/10/20



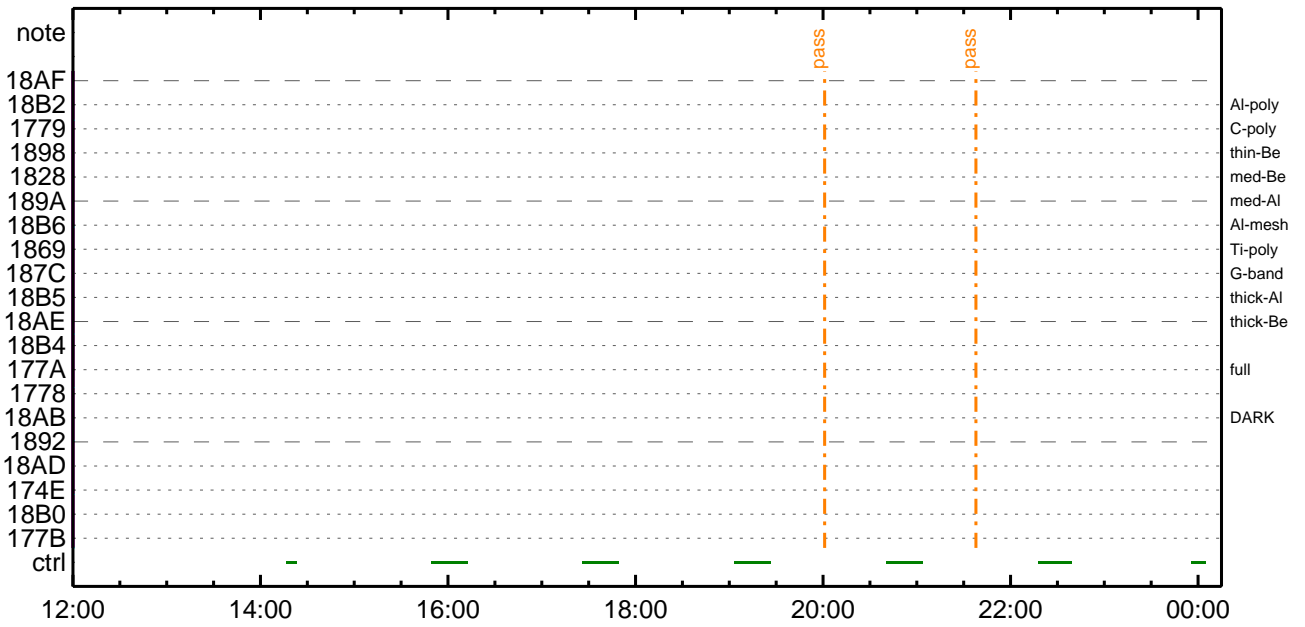
CMDI #0212 2011/10/20



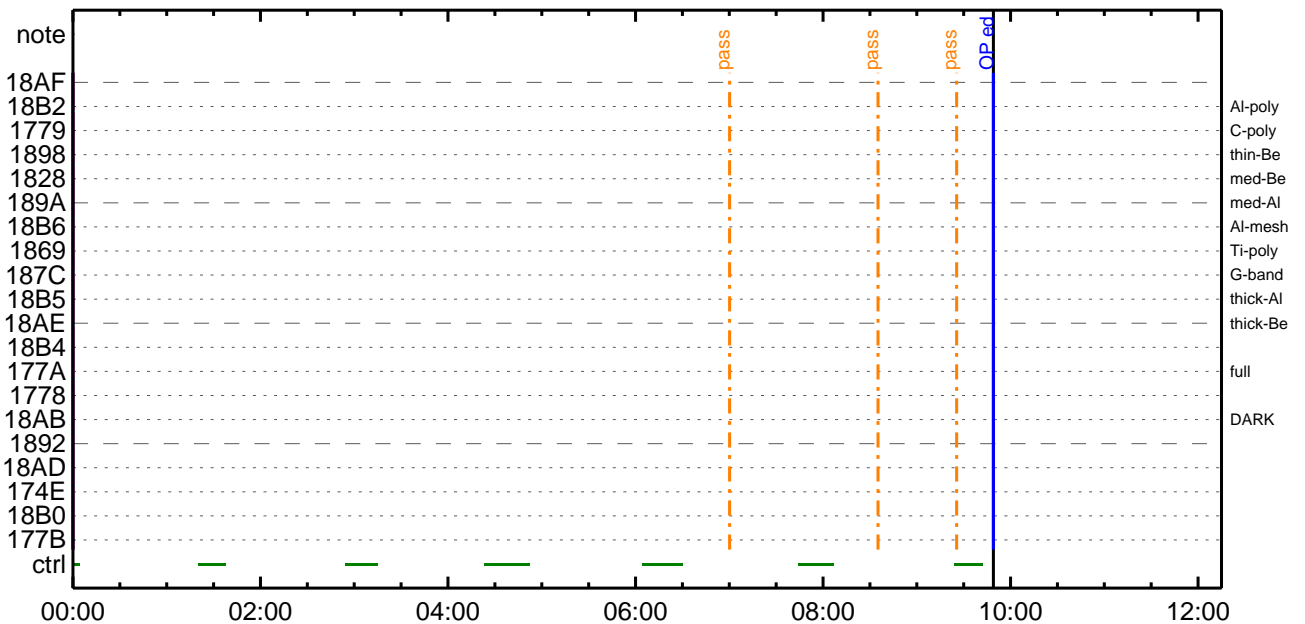
CMDI #0212 2011/10/21



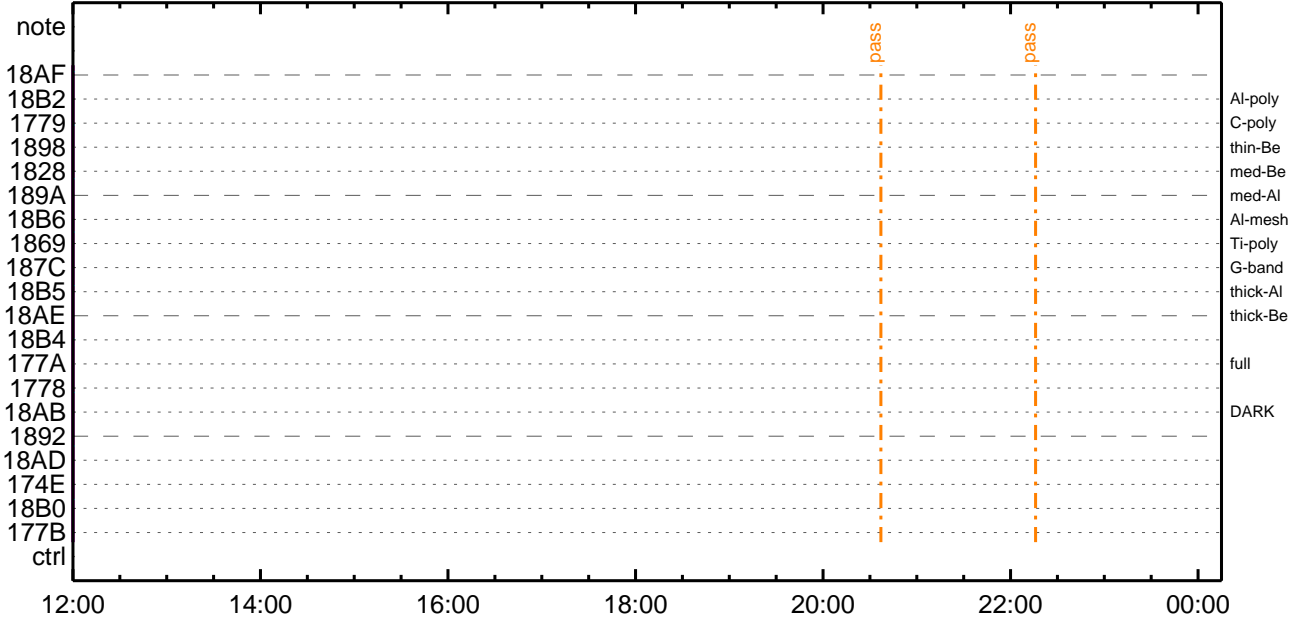
CMDI #0212 2011/10/21



CMDI #0212 2011/10/22



CMDI #0212 2011/10/22




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-405:OP
0104 ( )
0105 S. OG og-405:OG
0106 ( )
0107 C.
0108 C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ;[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ;[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ;[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ;[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ;[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ;[HK1_PKT_FORM_NO] EQ 7
0120 C. ;[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ;[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ;[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ;[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx1/2^a^I»oð³IÇ§
0125 C. ;[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGqf^E¹ç•ë²IOKqð³IÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ;[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ;[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ;[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ;[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ;[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ;[HK1_PKT_FORM_NO] EQ 7
0139 C. ;[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ;[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ;[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ;[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx1/2^a^I»oð³IÇ§
0144 C. ;[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGqf^E¹ç•ë²IOKqð³IÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ;[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ;[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ;[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ;[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ;[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ;[HK1_PKT_FORM_NO] EQ 7
0158 C. ;[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ;[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ;[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ;[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx1/2^a^I»oð³IÇ§
0163 C. ;[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPqf^E¹ç•ë²IOKqð³IÇ§
0165 C.
0166 C. ***** °E²¼oI%Ä´¶Á°EÉ¬oÁ÷¿@ (%âµ-YAYOXx1/2^e1/2çoðÁÓæoÇ¼^a¬°e%î¹çoçqâ) *****
0167 C. DHUYâ;4YE;E1/2Y1;4YE;EoðIâ¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ;[HK1_PKT_FORM_NO] EQ 2
0171 C. ;[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ;[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ;[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADq¬Á÷¿@NGuI%î¹ç;ç°E²¼oI¹TI-CMDÁ÷¿@qI%Ä¹Ôq•qEoq³qE;f
0180 C. qPq¿;çSETqEDUMPqIÆ±°iYNY¹qÇ¹Ôq|q³qE;f
0181 C.
0182 C. TIY³YpYóYEoðÁD¿¿(UT)
0183 +. TI 2011-10-18 10:33:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ;[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-10-18 10:33:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ;[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-10-18 10:33:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ;[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2011-10-18 10:37:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      ÷÷[HK1_TI_CMD_NUM]             EQ      1COUNTUP
0198 C.
0199 C.  °Ê²¼□îÄê%ííñ□îîŷÄŷ$ŷÄŷ⁻¹àîü
0200 C.      ÷÷[HK1_TI_CMD_ENA/DIS]         EQ      ENA
0201 C.      ÷÷[HK1_TI_CMD_NUM]             EQ      4
0202 C.      ÷÷[HK1_NEXT_EXEC_PIM]          EQ      DHU
0203 C.      ÷÷[HK1_NEXT_EXEC_DC]           EQ      0xB3
0204 C.
0205 C.  *****
0206 C.  Tîîî°èŷÄŷÖŷ×
0207 C.  *****
0208 C.
0209 C.  TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      ÷÷[HK1_DMP_TOP_ADRS_1]         EQ      07
0213 C.      ÷÷[HK1_DMP_TOP_ADRS_0]         EQ      2B
0214 C.      ÷÷[HK1_DMP_BLOCK_NUM]          EQ      3
0215 C.      ÷÷[HK1_DMP_REPEAT_NUM]        EQ      0
0216 C.      ÷÷[HK1_DMA_DMP_PIM]           EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      ÷÷[HK1_PKT_FORM_NO]            EQ      7
0220 C.      ÷÷[HK1_PKT_GEN_TIME]           EQ      0.25 s
0221 C.      ÷÷[HK1_S_TLM_BIT_RATE]        EQ      32k
0222 C.      ÷÷[HK1_X_TLM_BIT_RATE]        EQ      4M
0223 C.      ÷÷[HK1_DMP_CHK_FLG]           EQ      EXEC
0224 C.
0225 C.  ŷÄŷÖŷ×½ª î»□ð³îÇ§
0226 C.      ÷÷[HK1_DMP_CHK_FLG]           EQ      NON
0227 C.
0228 C.  RAM ID=TI_TBL□îî³Ê¹ç.è²îOK□ð³îÇ§
0229 C.
0230 C.  DHUŷâ;¼ŷÊ;Ê¼ŷ½.ŷî;¼ŷË;Ë□ðîä□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      ÷÷[HK1_PKT_FORM_NO]            EQ      2
0234 C.      ÷÷[HK1_PKT_GEN_TIME]           EQ      0.5S
0235 C.      ÷÷[HK1_S_TLM_BIT_RATE]        EQ      32K
0236 C.      ÷÷[HK1_X_TLM_BIT_RATE]        EQ      4M
0237 C.
0238 C.  *****
0239 C.  SOT TI command set
0240 C.  *****
0241 C.  Execute, after the success of OP upload.
0242 +. TI 2011-10-18 10:37:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C.  -----
0246 C.  HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C.  -----
0248 C.  ***** SOT END *****
0249 C.  Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C.  ***** Start EIS operation (TI set) *****
0253 C.  Execute, after the success of OP upload.
0254 C.  Set EIS TI-commands
0255 +. TI 2011-10-18 10:37:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2011-10-18 10:37:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.      [ ] [HK1_TI_CMD_NUM]           EQ      2 COUNTUP
0262 C.  ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C.  ***** XRT START *****
0267 C.  Execute, after the success of OP upload.
0268 +. TI 2011-10-18 10:37:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM]           EQ      1COUNTUP
0272 C.
0273 C.  ***** XRT END *****
0274 C.
0275 C.  ***** MDP `ûÄî□î»ö¼ŷ□èÄð□¹èDCBC.x²è *****
0276 C.  (¼ª°îîŷÄŷËŷËŷŷËŷäŷçŷèèÈ¼□□¼Ä»Ü□¹è)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C.  ***** ŷĐŷ¹.î Daily±çîñ□è'Ø□¹èèDCBC.x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C.  ;ãLOSŷÄŷ$ŷÄŷ⁻¹¼Ä»Ü;ä
0287 C.
0288 C.  ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b c0 c0 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c 40 c0 10 10)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d 40 40 10 10)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0e c0 40 10 10)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0f 80 80 06 06)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 10 80 80 08 08)
0148 + DC 07-F0 MDP_XRT_FLD_ENA
0149 BC (d8)
0150 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151 BC (c8)
0152 + DC 07-F0 MDP_XRT_AEC_RESET
0153 BC (d0)
0154 + DC 07-F0 MDP_XRT_ARS_DIS
0155 BC (d5)
0156 + DC 07-F0 MDP_XRT_FLD_RESET
0157 BC (da)
0158 + DC 07-F0 MDP_XRT_QT_PROG_SET
0159 BC (c4 14)
0160 + DC 07-F0 MDP_XRT_FL_PROG_SET
0161 BC (c5 0d)
0162 . C. ----- Success Verify ? OK / NG ____
0163 C.
0164 C.
0165 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0166 C.
0167 +. DC 07-F0 MDP_XRT_MODE_OBSV
0168 BC (c2)
0169 +. TI 2011-10-18 10:37:02.0
0170 DC 07-F0 MDP_XRT_MODE_OBSV
0171 BC (c2)
0172 . C. ----- Success Verify ? OK / NG ____
0173 C.
0174 C. ***** XRT END *****
0175 . C. *****
0176 C. SOT table upload
0177 C. *****
0178 . C. < Stop FG table >
0179 +. DC 07-F0 MDP_FG_CTRL_MANU
0180 BC (51)
0181 . C. -----
0182 C. MDP_FG_CTRL_MODE = MANU [ ]
0183 C. -----
0184 C.
0185 . C. <Upload FG Observation Table>
0186 . S. RAM ram-266:MDP_OBS_F
0187 ( )
0188 C.
0189 . C. < Dump RAMID=MDP_OBS_F >
0190 +. DC 07-F0 MDP_DUMP_FGTBL
0191 BC (82 07 00 00 00 38 b8)
0192 C. -----
0193 C. MDP_OBS_F verify = OK/NG [ ]

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0194 C. -----
0195 C.
0196 . C. < Stop SP table >
0197 +. DC 07-F0 MDP_SP_CTRL_MANU
0198 BC (61)
0199 C. -----
0200 C. MDP_SP_CTRL_MODE = MANU [ ]
0201 C. -----
0202 C.
0203 . C. <Upload SP Observation Table>
0204 . S. RAM ram-285:MDP_OBS_S
0205 ( )
0206 C.
0207 . C. < Dump RAMID=MDP_OBS_S >
0208 +. DC 07-F0 MDP_DUMP_SPTBL
0209 BC (83 07 00 00 00 38 b8)
0210 C. -----
0211 C. MDP_OBS_S verify = OK/NG [ ]
0212 C. -----
0213 C.
0214 . C. < Upload DPL table >
0215 C.
0216 C. ¥¢¥Ã¥×¥í;¼¥É°îÁ°ªÈSTS_CHKªðOFFªÈª¹ªë
0217 C.
0218 . S. RAM ram-271:MDP_DPL
0219 ( )
0220 C.
0221 . C. < Dump RAMID=MDP_DPL >
0222 +. DC 07-F0 MDP_DUMP_FGTBL
0223 BC (82 07 00 38 b8 00 40)
0224 C. -----
0225 C. MDP_DPL verify = OK [ ]
0226 C. -----
0227 C.
0228 C. STS_CHKªðONªÈª¹ªë
0229 C.
0230 . C. < Update MDP DSC PAR1 >
0231 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0232 BC (4c)
0233 C. MDP_CMD_CODE = F04C0700[ ]
0234 C. MDP_CMD_CNT (count-up 1) [ ]
0235 C. -----
0236 C.
0237 . C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of TBL upload.
0242 +. TI 2011-10-18 10:37:18.0
0243 DC 07-F0 MDP_SOT_MODE_OBSV
0244 BC (40)
0245 C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0247 C. -----
0248 C.
0249 C.
0250 . C. ***** MDP ´ûÃîî»ò¼ÝªÈª¹ªëDCBC•×²è *****
0251 C. (¼ª°î¼Ó¥Ã¥È¥ª¥È¥ª¥¢¥Èª¼ªª¼ª»Ûª¹ªë)
0252 . S. DC-BC dcbc-402:DCBC
0253 (MDP_known_event)
0254 C.
0255 C.
0256 . C. ***** ¥Ð¥¹•î Daily±¿îÑªÈ´Øª¹ªëDCBC•×²è *****
0257 . S. DC-BC dcbc-153:DCBC
0258 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0259 C.
0260 C.
0261 . C. ;ãLOS¥Ã¥§¥Ã¥-¼ª»Û;ã
0262 C.
0263 . C. ***** LOS *****
0264 C.

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Oct 18, 11 12:49

XRT_OGLIST_0212.chk

Page 1/5

*** OP Sequence for XRT ***

2011/10/18	10:47:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/18	10:47:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/10/18	10:48:00.5	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/10/18	10:48:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/10/18	10:48:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/10/18	10:48:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/10/18	10:48:22.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/10/18	10:50:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/18	10:50:56.0	XRT_QT_PROG_SET_404_OG [0x194]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2011/10/18	10:50:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2011/10/18	10:51:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/18	13:59:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/18	13:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/10/18	14:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	01 00 00 00 00
2011/10/18	14:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/10/18	14:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/10/18	14:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/10/18	14:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/10/18	14:02:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/18	14:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2011/10/18	14:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2011/10/18	14:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/18	14:06:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/18	14:06:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/18	14:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/18	14:08:30.0	XRT_Custom_418_OG [0x1a2]			
2011/10/18	14:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/18	14:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/18	15:37:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/18	15:37:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/18	15:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/18	15:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/18	16:00:30.0	XRT_Custom_418_OG [0x1a2]			
2011/10/18	16:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/18	17:13:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/18	17:13:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/18	17:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/18	17:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/18	17:37:00.0	XRT_Custom_418_OG [0x1a2]			
2011/10/18	17:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/18	17:53:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/18	17:53:02.0	XRT_QT_PROG_SET_437_OG [0x1b5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2011/10/18	17:53:04.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/18	17:54:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/18	17:54:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/10/18	17:54:30.0	AOCS_OrE-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/10/18	17:54:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9

Oct 18, 11 12:49

XRT_OGLIST_0212.chk

Page 2/5

2011/10/18	17:54:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/10/18	17:54:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/10/18	17:57:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2011/10/18	17:57:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/18	18:04:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/18	18:04:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/10/18	18:04:30.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 e0 e6 4b 8d
2011/10/18	18:04:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/10/18	18:04:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/10/18	18:04:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/10/18	18:04:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/10/18	18:07:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/18	18:07:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2011/10/18	18:07:28.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2011/10/18	18:07:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/18	18:50:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/18	18:50:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/18	18:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/18	18:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/18	19:14:00.0	XRT_Custom_418_OG [0x1a2]				
2011/10/18	19:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/18	20:28:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/18	20:28:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/18	20:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/18	20:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/18	20:51:30.0	XRT_Custom_418_OG [0x1a2]				
2011/10/18	20:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/18	22:05:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/18	22:05:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/18	22:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/18	22:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/18	22:27:00.0	XRT_Custom_418_OG [0x1a2]				
2011/10/18	22:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/18	23:42:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/18	23:42:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/18	23:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/18	23:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/18	23:53:30.0	XRT_Custom_418_OG [0x1a2]				
2011/10/18	23:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/19	01:09:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/19	01:09:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/19	01:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/19	01:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/10/19	01:25:30.0	XRT_Custom_418_OG [0x1a2]				
2011/10/19	01:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/10/19	02:41:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/10/19	02:41:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/10/19	02:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/10/19	02:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

2011/10/19	03:02:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/19	03:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/19	04:11:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/19	04:11:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/19	04:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/19	04:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/19	04:40:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/19	04:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/19	05:51:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/19	05:51:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/19	05:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/19	05:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/19	06:17:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/19	06:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/19	06:27:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/19	06:27:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/19	06:27:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/10/19	06:27:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/19	06:27:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/19	06:27:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/19	06:30:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2011/10/19	06:30:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/19	06:37:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/10/19	06:37:30.5	XRT_CTRL_MANU_423_OG [0x1a7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/19	06:38:00.5	XRT_TCIB_XRT_S_HTR_A_ENA_434_OG [0x1b2]							
		TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2011/10/19	08:38:00.5	XRT_Custom_440_OG [0x1b8]							
2011/10/19	10:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2011/10/19	10:38:00.5	XRT_Custom_440_OG [0x1b8]							
2011/10/19	12:38:00.5	XRT_Custom_440_OG [0x1b8]							
2011/10/19	14:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/10/19	14:38:00.5	XRT_Custom_414_OG [0x19e]							
2011/10/19	14:38:10.5	XRT_Custom_440_OG [0x1b8]							
2011/10/19	16:38:10.5	XRT_Custom_440_OG [0x1b8]							
2011/10/19	18:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 e0 e6 4b 8d				
2011/10/19	18:38:10.5	XRT_Custom_440_OG [0x1b8]							
2011/10/19	20:38:10.5	XRT_Custom_440_OG [0x1b8]							
2011/10/19	22:38:10.5	XRT_Custom_415_OG [0x19f]							
2011/10/20	00:37:48.5	XRT_Custom_414_OG [0x19e]							
2011/10/20	00:37:58.5	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/20	00:38:00.5	XRT_TCIB_XRT_S_HTR_A_DIS_416_OG [0x1a0]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2011/10/20	06:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/20	07:00:00.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 2e f9 2e f9				
2011/10/20	07:02:32.0	XRT_FOCUS_POSITION_417_OG [0x1a1]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/20	07:02:52.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2011/10/20	07:02:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/20	07:02:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/20	07:02:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/20	07:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/20	07:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/20	07:10:00.0	AOCS_Ore-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 2e f9 d1 07				
2011/10/20	07:12:32.0	XRT_FOCUS_POSITION_417_OG [0x1a1]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/20	07:12:52.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2011/10/20	07:12:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				

Oct 18, 11 12:49

XRT_OGLIST_0212.chk

Page 4/5

2011/10/20	07:12:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/20	07:12:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/20	07:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/20	07:19:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/20	07:20:00.0	AOCS_Ore-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 d1 07 d1 07				
2011/10/20	07:22:32.0	XRT_FOCUS_POSITION_417_OG [0x1a1]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/20	07:22:52.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2011/10/20	07:22:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/20	07:22:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/20	07:22:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/20	07:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/20	07:29:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/20	07:30:00.0	AOCS_Ore-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 d1 07 2e f9				
2011/10/20	07:32:32.0	XRT_FOCUS_POSITION_417_OG [0x1a1]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/20	07:32:52.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2011/10/20	07:32:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/20	07:32:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/20	07:32:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/20	07:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/20	07:44:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/20	07:44:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/20	07:45:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/10/20	07:45:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/20	07:45:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/20	07:45:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/20	07:47:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2011/10/20	07:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/20	07:54:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/20	07:54:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/10/20	07:55:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/10/20	07:55:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/20	07:55:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/20	07:55:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/20	07:55:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/20	07:57:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/20	07:57:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2011/10/20	07:57:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/10/20	07:58:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/20	08:09:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/20	08:09:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/20	08:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/20	08:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/20	08:31:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/20	08:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/20	09:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/20	09:49:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/20	09:49:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

2011/10/20	09:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]																		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8														
2011/10/20	09:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]																		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2011/10/20	10:24:00.0	AOCs_OrE-point_Start_3_OG [0x099]																		
			AOCU_NM	5	02-76	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00