

XRT Timeline to be uploaded on 2011/11/01

Period: 2011/11/01 09:43:00 - 2011/11/05 10:15:00

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Normal mode

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XOB #18BF: Synoptic 9 Filter- 2x2 Q98 Short exp 2 loop												
Term		Pointing (x, y)					Comment					
11/01 10:30:00 - 11/01 10:42:30		Fixed (0.0, 0.0)										
PROG= 18 2-time(s)												
┌ Subr= 1 1-time(s) 180.0sec												
├ Seqn= 82 1-time(s) 25.0sec												
│ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
│ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├ Seqn= 98 1-time(s) 25.0sec												
│ Open/Ti-poly Open/Ti-poly close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
│ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├ Seqn= 74 1-time(s) 25.0sec												
│ Al-poly/Open Al-poly/Open close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
│ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├ Seqn= 18 1-time(s) 25.0sec												
│ C-poly/Open C-poly/Open close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
│ C-poly/Open C-poly/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├ Seqn= 24 1-time(s) 25.0sec												
│ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├ Seqn= 9 1-time(s) 4.0sec												
│ thin-Be/Open thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
│ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└ Subr= 2 1-time(s) 180.0sec												
├ Seqn= 33 1-time(s) 4.0sec												
│ med-Al/Open med-Al/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├ Seqn= 71 1-time(s) 4.0sec												
│ Open/thick-Be Open/thick-Be close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├ Seqn= 67 1-time(s) 4.0sec												
│ Open/Al-mesh Open/Al-mesh close Safe Dark 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├ Seqn= 4 1-time(s) 4.0sec												
│ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #18BD: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad												
Term		Pointing (x, y)					Comment					
11/01 13:45:30 - 11/01 17:39:30		Track (848.3, 51.4) ^{11/01 13:42:30} * AR 11330.										
11/01 18:16:00 - 11/01 21:59:54		Fixed (-905.0, 300.0) # Observe AR 11337 in E with fixed pointing.										
PROG= 12 Inf.-time(s)												
┌ Subr= 1 1-time(s) 2.0sec												
├ Seqn= 19 1-time(s) 2.0sec												
│ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
│ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
├ Seqn= 29 4-time(s) 2.0sec												
│ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ Al-poly/Open Al-poly/thick-Be close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ C-poly/Open C-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ med-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ med-Al/Open med-Al/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└ Subr= 2 1-time(s) 2.0sec												
├ Seqn= 31 30-time(s) 2.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #18AD: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be(12)												
Term		Pointing (x, y)					Comment					
11/01 18:06:00 - 11/01 18:12:54		Fixed (0.0, 0.0) synoptic, shifted 3.0 min										
11/02 05:53:00 - 11/02 05:59:54		Fixed (0.0, 0.0) synoptic, shifted -10.0 min										
11/02 17:55:30 - 11/02 18:02:24		Fixed (0.0, 0.0) synoptic, shifted -7.5 min										
11/03 06:03:00 - 11/03 06:55:30		Fixed (0.0, 0.0) synoptic										
PROG= 04 1-time(s)												
┌ Subr= 1 1-time(s) 12.0sec												
├ Seqn= 7 1-time(s) 4.0sec												
│ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├ Seqn= 5 1-time(s) 2.0sec												
│ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
│ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 8 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18B4: AR Standard-A(Filter-Ratio) with PFB, long thin-Be, thick Al and Al/Poly context, 512x512 at 1064 1048, 100s cad												
Term	Pointing (x, y)		Comment									
11/01 22:03:00 - 11/02 05:49:54	Fixed (-905.0, 300.0)		# Observe AR 11337 in E with fixed pointing.									
11/02 12:00:00 - 11/02 17:52:24	Fixed (-905.0, 300.0)		# Observe AR 11337 in E with fixed pointing.									
PROG= 09 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 17 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 54 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Subr= 2 20-time(s) 2.0sec												
Seqn= 79 1-time(s) 2.0sec												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad												
Term	Pointing (x, y)		Comment									
11/02 06:03:00 - 11/02 11:56:54	Fixed (-905.0, 300.0)		# Observe AR 11337 in E with fixed pointing.									
11/02 18:05:30 - 11/03 05:59:54	Track (-904.8, 299.9) @ 11/02 18:02:30		# Cont., now with tracking.									
PROG= 10 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 96 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 62 15-time(s) 150.0sec												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #18BE: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)												
Term	Pointing (x, y)		Comment									
11/01 13:45:30 - 11/01 17:39:30	Track (848.3, 51.4) @ 11/01 13:42:30		* AR 11330.									
11/01 18:16:00 - 11/01 21:59:54	Fixed (-905.0, 300.0)		# Observe AR 11337 in E with fixed pointing.									
11/01 22:03:00 - 11/02 05:49:54	Fixed (-905.0, 300.0)		# Observe AR 11337 in E with fixed pointing.									
11/02 06:03:00 - 11/02 11:56:54	Fixed (-905.0, 300.0)		# Observe AR 11337 in E with fixed pointing.									
11/02 12:00:00 - 11/02 17:52:24	Fixed (-905.0, 300.0)		# Observe AR 11337 in E with fixed pointing.									
11/02 18:05:30 - 11/03 05:59:54	Track (-904.8, 299.9) @ 11/02 18:02:30		# Cont., now with tracking.									
PROG= 07 4-time(s)												
Subr= 1 45-time(s) 2.0sec												

Seqn= 20	1-time(s)	20.0sec																	
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec						
Seqn= 63	1-time(s)	2.0sec																	
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec						
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec						
Seqn= 77	1-time(s)	2.0sec																	
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec						
Subr= 2	1-time(s)	600.0sec																	
Seqn= 90	1-time(s)	2.0sec																	
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec						
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec						
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=98	0	0	2.0sec						
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval						

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Active Region Search

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NOT USED

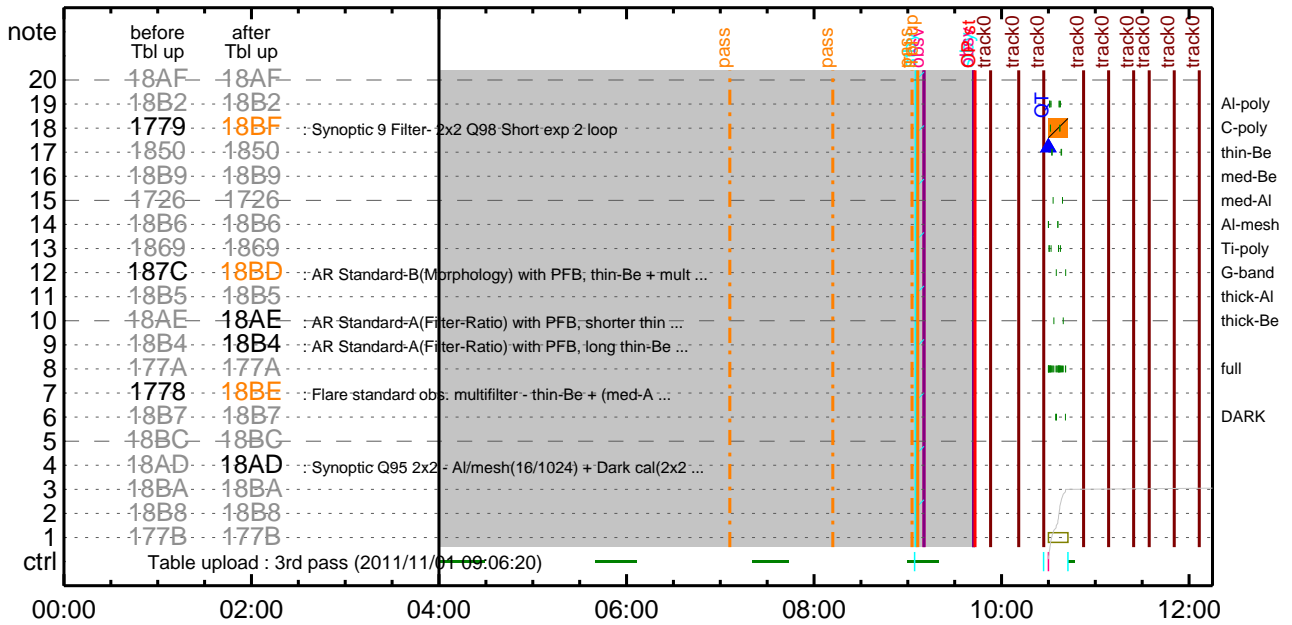
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Flare Detection

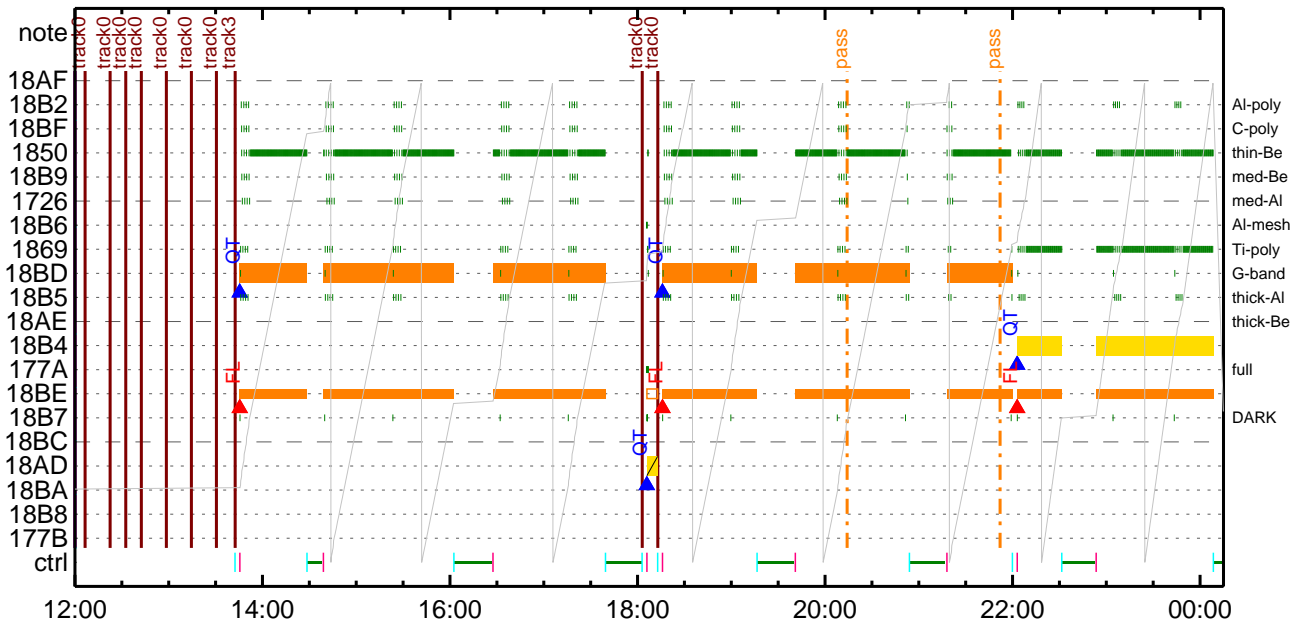
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FLD Patrol													
Term	Pointing (x, y)									Comment			
11/01 13:42:46 - 11/01 18:03:16	Track (848.3,	51.4)	@ 11/01 13:42:30					* AR 11330.				
11/01 18:13:16 - 11/02 05:50:16	Fixed (-905.0,	300.0)					# Observe AR 11337 in E with fixed pointing.					
11/02 06:00:16 - 11/02 17:52:46	Fixed (-905.0,	300.0)					# Observe AR 11337 in E with fixed pointing.					
11/02 18:02:46 - 11/03 06:00:16	Track (-904.8,	299.9)	@ 11/02 18:02:30					# Cont., now with tracking.				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval

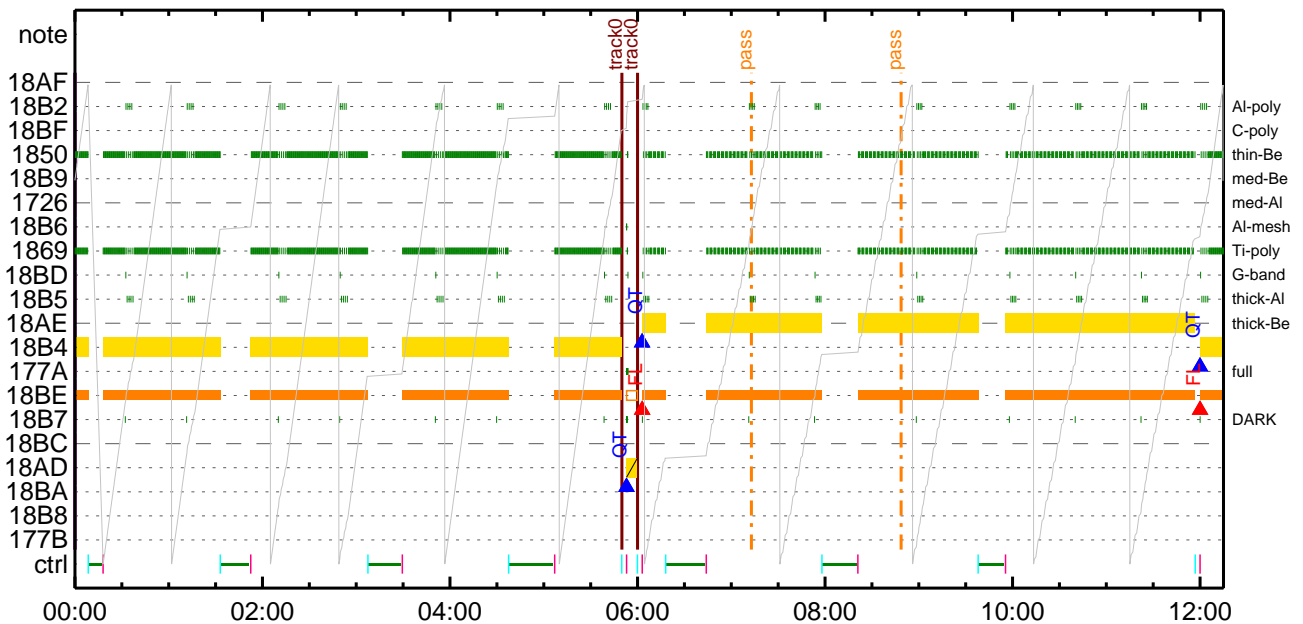
CMDI #0239 2011/11/01



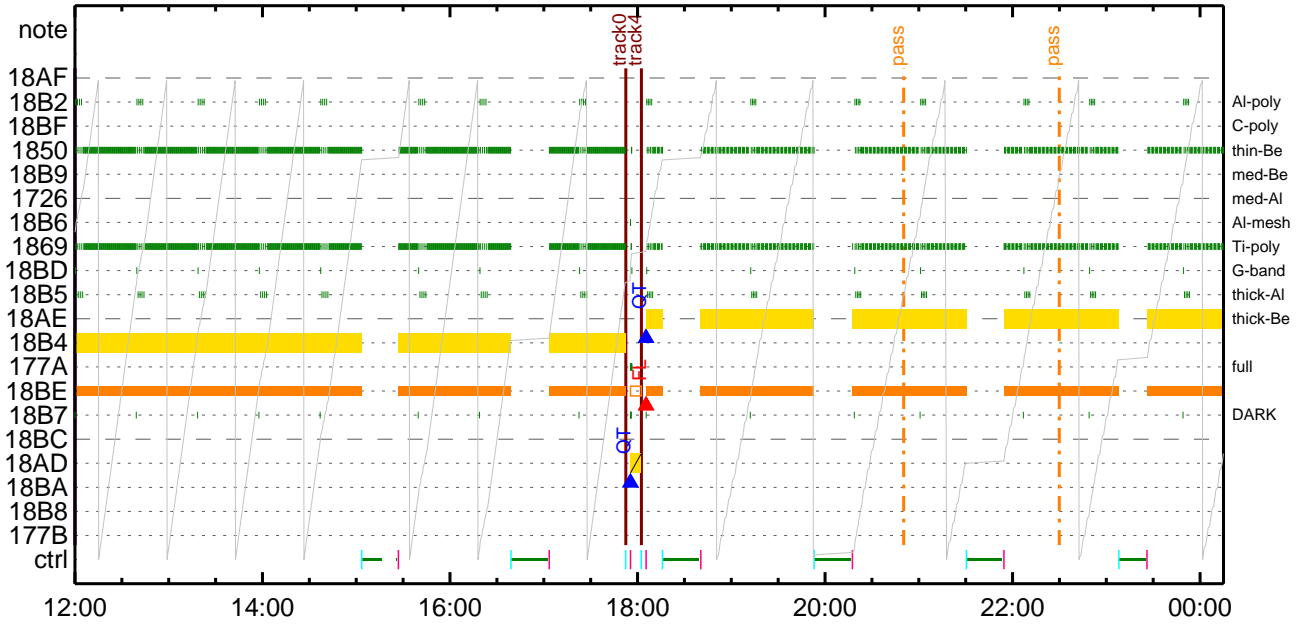
CMDI #0239 2011/11/01



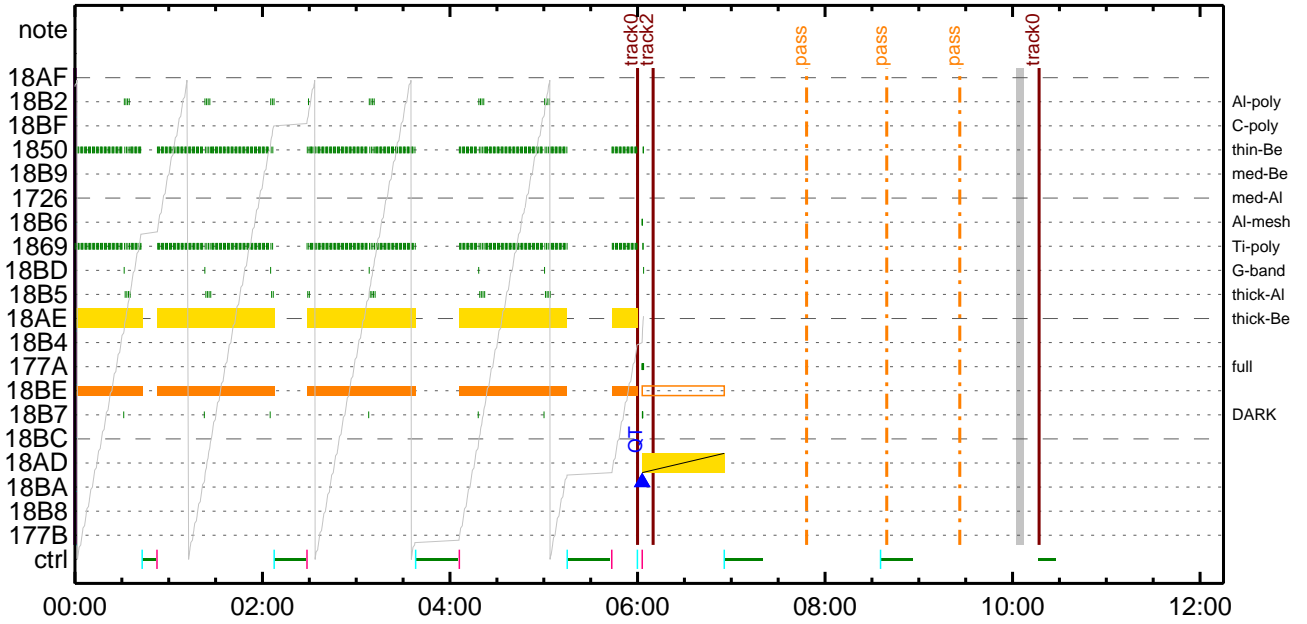
CMDI #0239 2011/11/02



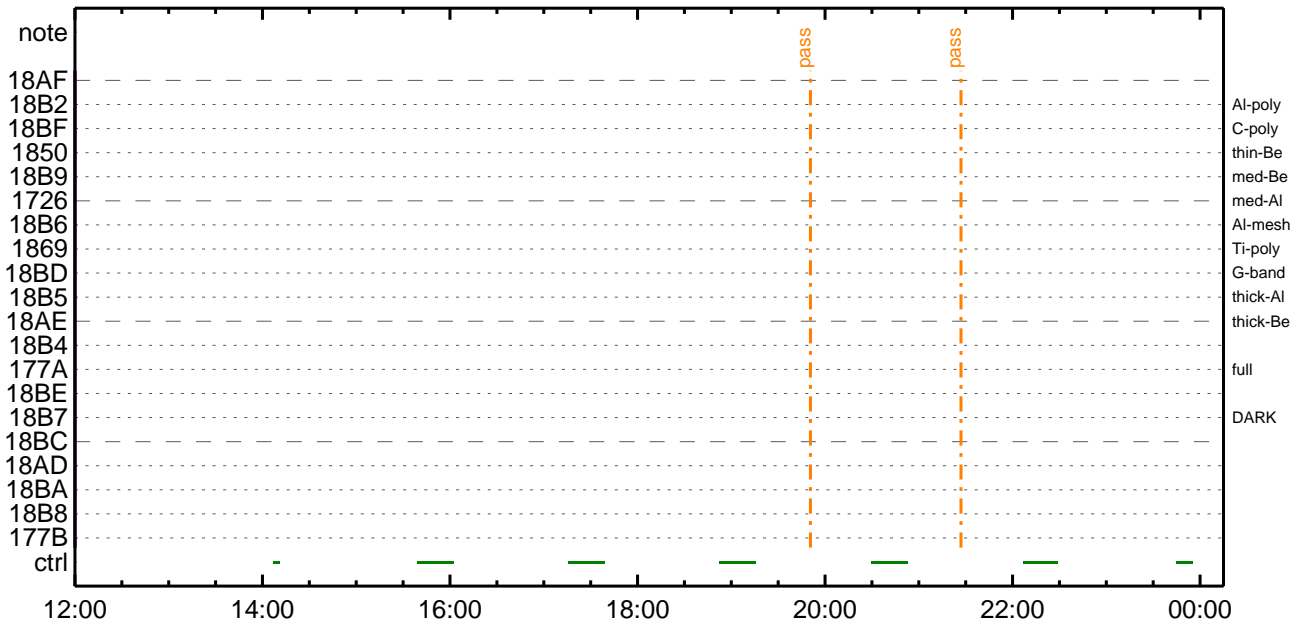
CMDI #0239 2011/11/02



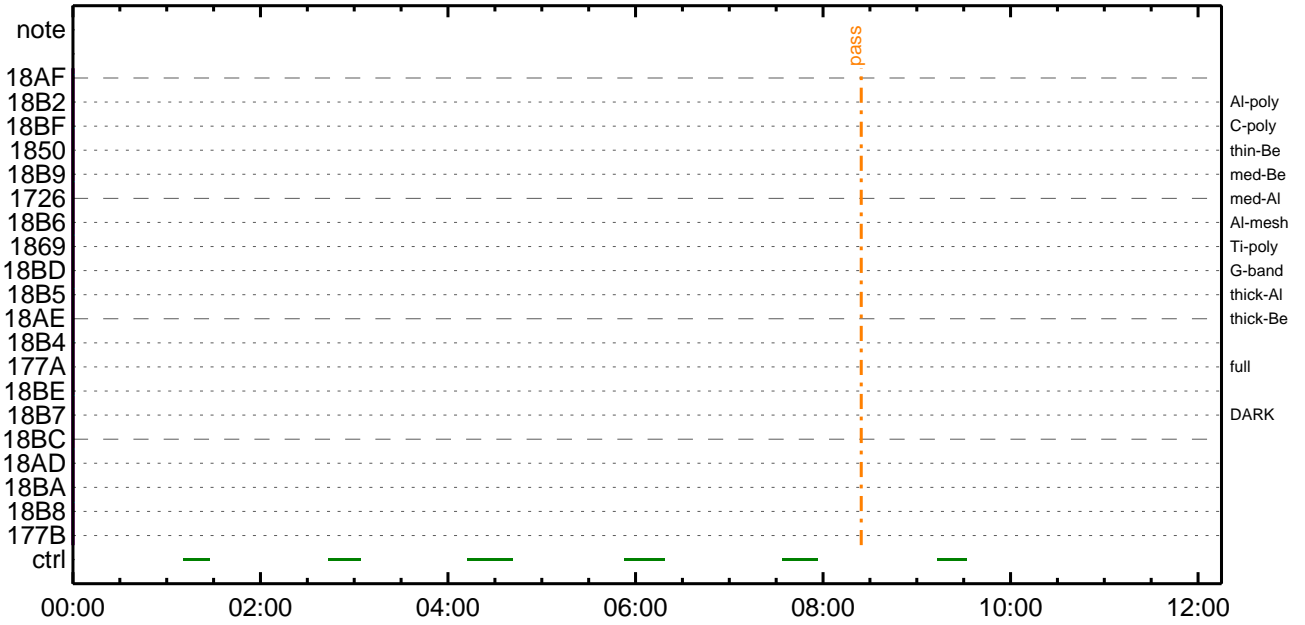
CMDI #0239 2011/11/03



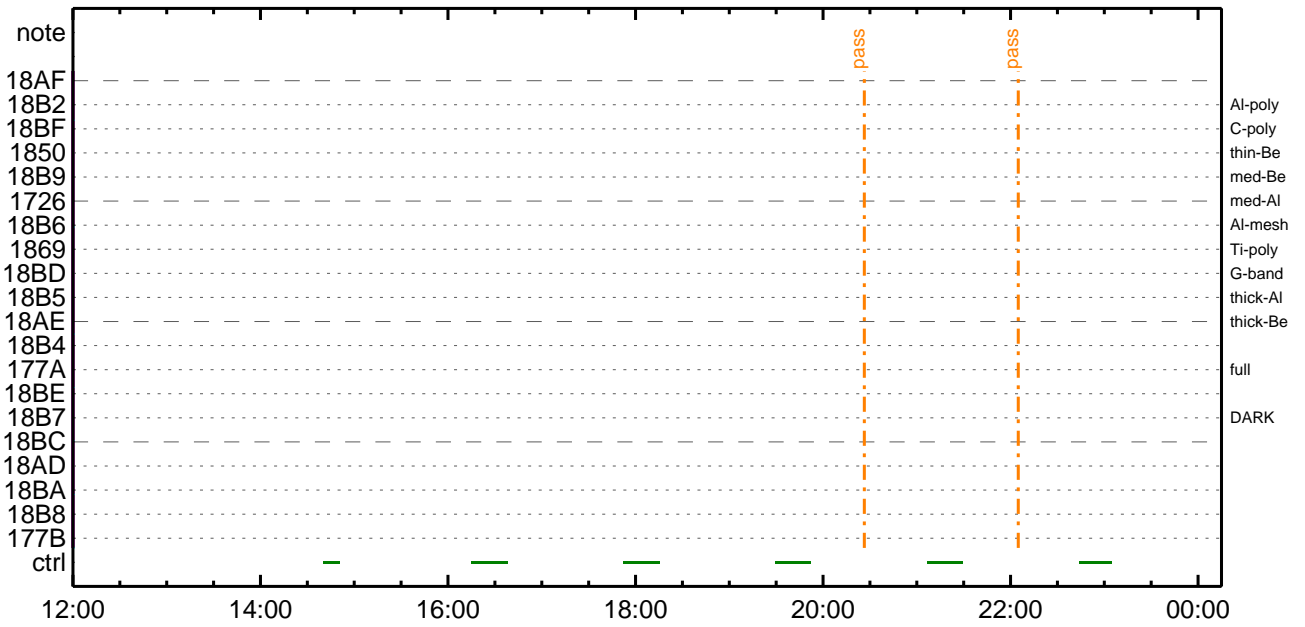
CMDI #0239 2011/11/03



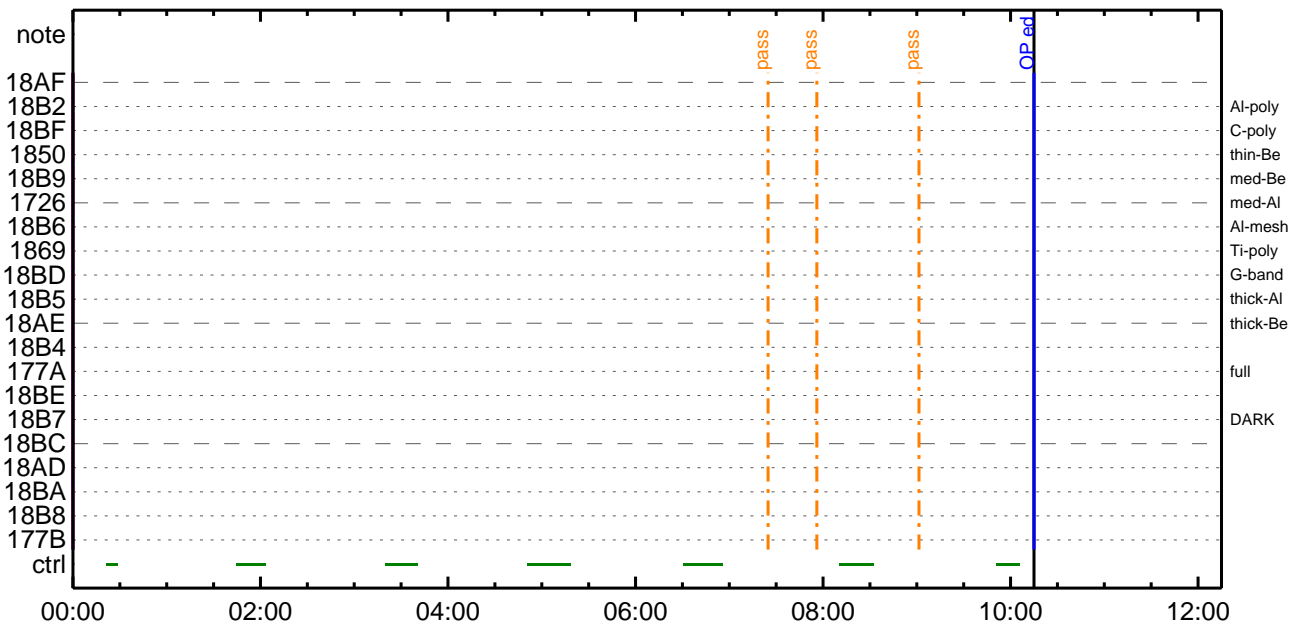
CMDI #0239 2011/11/04



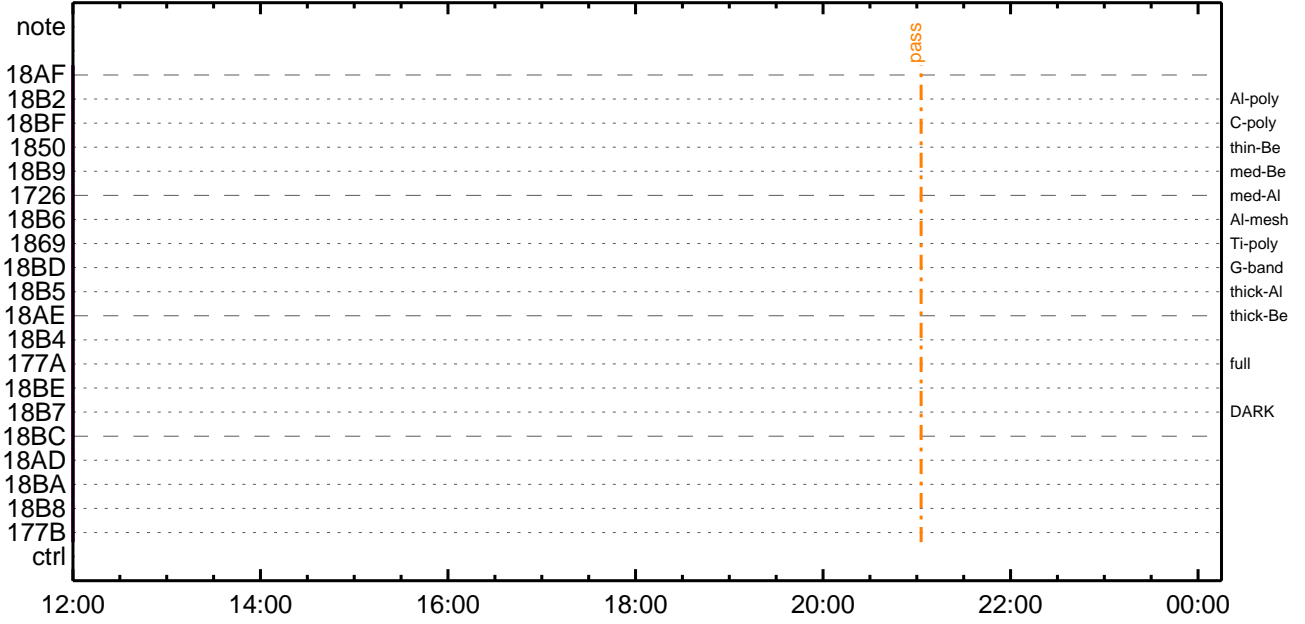
CMDI #0239 2011/11/04



CMDI #0239 2011/11/05



CMDI #0239 2011/11/05




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0096 C.          0303; SET0EDUMP01E±0iYNY¹0Ç¹00|0³0E;E
0097 C.
0098 . C. TIY³YF¥ÖYÉ000ÄDİ¿ (UT)
0099 +. TI 2011-11-01 09:38:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2011-11-01 09:38:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2011-11-01 09:38:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2011-11-01 09:42:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. °E²¼0İÄê%İİÑ0İYÁY§YÁY-¹àİÜ
0116 C.          ÇÇ[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0117 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          4
0118 C.          ÇÇ[HK1_NEXT_EXEC_PIM]        EQ          DHU
0119 C.          ÇÇ[HK1_NEXT_EXEC_DC]        EQ          0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          ÇÇ[HK1_DMP_TOP_ADRS_1]        EQ          07
0129 C.          ÇÇ[HK1_DMP_TOP_ADRS_0]        EQ          2B
0130 C.          ÇÇ[HK1_DMP_BLOCK_NUM]        EQ          3
0131 C.          ÇÇ[HK1_DMP_REPEAT_NUM]       EQ          0
0132 C.          ÇÇ[HK1_DMA_DMP_PIM]         EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          ÇÇ[HK1_PKT_FORM_NO]          EQ          7
0136 C.          ÇÇ[HK1_PKT_GEN_TIME]         EQ          0.25 s
0137 C.          ÇÇ[HK1_S_TLM_BIT_RATE]       EQ          32k
0138 C.          ÇÇ[HK1_X_TLM_BIT_RATE]      EQ          4M
0139 C.          ÇÇ[HK1_DMP_CHK_FLG]         EQ          EXEC
0140 C.
0141 . C. YÄYÖY×½ªİ»00³İÇ§
0142 C.          ÇÇ[HK1_DMP_CHK_FLG]         EQ          NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK00³İÇ§
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y½;Yİ;¼YÈ;È00İá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          ÇÇ[HK1_PKT_FORM_NO]          EQ          2
0150 C.          ÇÇ[HK1_PKT_GEN_TIME]         EQ          0.5S
0151 C.          ÇÇ[HK1_S_TLM_BIT_RATE]       EQ          32K
0152 C.          ÇÇ[HK1_X_TLM_BIT_RATE]      EQ          4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2011-11-01 09:42:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 . C. -----
0162 C.          HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2011-11-01 09:42:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2011-11-01 09:42:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2011-11-01 09:42:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP `üÄİ0İ»0¼Y0ÈÄ00¹0èDCBC•×²è *****
0192 C. (%â0İYÖYÄYÈYİP¥ÈYÁYÇYè0È¼00¼Ä»Ü0¹0é)
0193 . S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-432 2011-11-01 12:41:55 175 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YÞYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëççãð•µ°Æ»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ËÈÈ¼µ•íÉ;ËðÈ¼°ÇÖã•çç¼í¹ççí;çÀ®, ùã¹ãðãðãçÁ+ç®ã•ðËãã³ãÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç" °ÆÀ, Í×ËYãÀLOSãðççí»p´Öãð¹íí, ç. ; çÉÖÍ×ãÈXÁÖONãí¹ÖãËíãÈãã³ãÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY-¾ÖÁÖã-°ÁÁêã•çç;ç°Ê²¼ççí°ÆÀ, ¼ê¼ççð¼Á¹Öã¹çç;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. ç" RESTART;ËPT1;Ëã•çççç¼í¹ççí;ç°Ê²¼ççí°ÆÀ¹Öã»ã°;çDCBC-150çççËã;ç
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÉÁÜÁÖ;ËÁ•Á°²óÈð;Ë, áãí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ç-¼«Æ°Áá»ßã•çç;ã;ç°Ê²¼ççð¼Á¹Öã¹çç;ç
0055 C. YçYÓYÉYÉÁÜÁÖãÁ•Á°²óÈðã-¼áçç¼í¹ççí°ÆÀ¹Öã»ã°;çDCBC-150çççËã;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. ç" RESTART;ËPT2;Ëã•çççç¼í¹ççí;ç°Ê²¼ççí°ÆÀ¹Öã»ã°;çDCBC-151çççËã;ç
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÉÁÜÁÖ;ËÁ•Á°²óÈð;Ë, áãí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áá»ß;çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 1423057.2 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_MANU
0140 BC (21 02)
0141 . C. Verify EIS in MANUAL mode
0142 . C. Estimated OBSTBL upload time is 54s
0143 C. *****
0144 C. EIS START OBSTBL LOAD
0145 C. *****
0146 . S. RAM ram-820:EIS_OBSTBL
0147 ( )
0148 +. DC 07-FC EIS_DUMP_OBSTBL
0149 BC (07 07 07 00 00 70 00)
0150 C.
0151 C. Execute, after the success of OBSTBL upload.
0152 C. Set EIS TI-commands
0153 +. TI 2011-11-01 09:42:50.0
0154 DC 07-FC EIS_MODE_CHG_ENA
0155 BC (20)
0156 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0157 C. *****
0158 C. EIS END OBSTBL LOAD
0159 C. *****
0160 C.
0161 . C. ***** MDP 'ûÃîî»ö¼ÝðĔĀÐð¹ēDCBC•x²è *****
0162 C. (%ã°îÝŌÝĀÝĔÝÞÝĔÝĀÝŕÝĔÝēðĔ¼ð¼Ā»Ûð¹ē)
0163 . S. DC-BC dcbc-402:DCBC
0164 (MDP_known_event)
0165 C.
0166 C.
0167 . C. ***** ÝÐÝ¹•İ Daily±;ĪŃðĔ'Øð¹ēDCBC•x²è *****
0168 . S. DC-BC dcbc-153:DCBC
0169 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0170 C.
0171 C.
0172 . C. ;ãLOSÝĀÝŖÝĀÝ¹¼Ā»Û;ã
0173 C.
0174 . C. ***** LOS *****
0175 C.

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(a) Spacecraft Operation Procedure (real-commands)

```

main-433 2011-11-01 12:41:55 150 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFÝÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿àÄâ•µ°È»Í×ÁÇâíYçYÄY×Yí;¼YÉj;ÈÈèµ•ííÈ;ÈèÈ¼°ÇÔâ•â¿¼i¹çâí;çÀ®, ùâ¹âèâßâçÁ+¿®â•âÈâââ³âÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-261:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. YçYÁY×Yí;¼YÉâîÄ°âÈSTS_CHKâðOFFâÈâ¹âè
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKâðONâÈâ¹âè
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700 [ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 . C.
0059 C. *****
0060 C. SOT TI command set
0061 C. *****
0062 C. Execute, after the success of TBL upload.
0063 +. TI 2011-11-01 09:42:18.0
0064 DC 07-F0 MDP_SOT_MODE_OBSV
0065 BC (40)
0066 C. -----
0067 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0068 C. -----
0069 C.
0070 C.
0071 C. ***** XRT START *****
0072 C.
0073 +. DC 07-F0 MDP_XRT_CTRL_MANU
0074 BC (c1)
0075 +. DC 07-F0 MDP_XRT_MODE_STBY
0076 BC (c3)
0077 . C. ----- Success Verify ? OK / NG_____
0078 C.
0079 C. XRT Obs. Table Upload
0080 . S. RAM ram-291:MDP_OBS_X
0081 ( )
0082 C.
0083 +. DC 07-F0 MDP_DUMP_XRTTBL
0084 BC (84 07 00 00 00 3a d4)
0085 . C. ----- Comparison Check ? OK / ERR _____
0086 C.
0087 C.
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 01 b1 b1 04 04)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 02 b1 b1 08 08)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 03 b1 b1 08 08)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 04 b1 b1 06 06)

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```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 80 80 20 20)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 85 83 06 06)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 80 80 20 08)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 08 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0a 85 83 08 08)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0f 80 80 06 06)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 10 80 80 08 08)
0112 + DC 07-F0 MDP_XRT_FLD_ENA
0113 BC (d8)
0114 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0115 BC (c8)
0116 + DC 07-F0 MDP_XRT_AEC_RESET
0117 BC (d0)
0118 + DC 07-F0 MDP_XRT_ARS_DIS
0119 BC (d5)
0120 + DC 07-F0 MDP_XRT_FLD_RESET
0121 BC (da)
0122 . C. ----- Success Verify ? OK / NG ____
0123 C.
0124 C.
0125 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0126 C.
0127 +. DC 07-F0 MDP_XRT_MODE_OBSV
0128 BC (c2)
0129 +. TI 2011-11-01 09:42:02.0
0130 DC 07-F0 MDP_XRT_MODE_OBSV
0131 BC (c2)
0132 . C. ----- Success Verify ? OK / NG ____
0133 C.
0134 C. ***** XRT END *****
0135 C.
0136 . C. ***** MDP `uAÎnî»ô%YnÊAÐa¹nêDCBC•x²è *****
0137 C. (%a°îYÓYÁYÈYÏYËYáYçYèaE%¼a¼A»Ûa¹aè)
0138 . S. DC-BC dcbc-402:DCBC
0139 (MDP_known_event)
0140 C.
0141 C.
0142 . C. ***** YDÿ¹.Ï Daily±;îÑnÊ´Øa¹nêDCBC•x²è *****
0143 . S. DC-BC dcbc-153:DCBC
0144 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0145 C.
0146 C.
0147 . C. ;ãLOSŸÁYŠYÄY¬¼A»Û;ã
0148 C.
0149 . C. ***** LOS *****
0150 C.
```

Nov 01, 11 13:05

XRT_OGLIST_0239.chk

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*** OP Sequence for XRT ***

2011/11/01	09:53:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	00	00	ac	cd
2011/11/01	10:11:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	d6	67
2011/11/01	10:26:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2011/11/01	10:26:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2011/11/01	10:27:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00		00
2011/11/01	10:27:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0				d9	
2011/11/01	10:27:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0				c9	
2011/11/01	10:27:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0				d5	
2011/11/01	10:29:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0				c4	12
2011/11/01	10:30:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0				c0	
2011/11/01	10:42:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2011/11/01	10:42:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2011/11/01	10:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0				e8	
2011/11/01	10:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0				e9	
2011/11/01	10:52:30.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	29	99
2011/11/01	11:08:30.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	00	00	53	33
2011/11/01	11:24:30.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	d6	36	b7	8e
2011/11/01	11:34:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	b4	b5	db	75
2011/11/01	11:50:30.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	ac	5b		00
2011/11/01	12:06:30.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	b4	b5	24	8b
2011/11/01	12:22:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	d6	36	48	72
2011/11/01	12:32:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	29	ca	b7	8e
2011/11/01	12:42:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	4b	4b	db	75
2011/11/01	12:58:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	53	a5		00
2011/11/01	13:14:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	4b	4b	24	8b
2011/11/01	13:30:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	29	db	48	72
2011/11/01	13:42:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2011/11/01	13:42:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97		00
2011/11/01	13:42:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	03	00	00		00
2011/11/01	13:42:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0				d8	
2011/11/01	13:42:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0				c8	
2011/11/01	13:42:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0				d0	
2011/11/01	13:42:52.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0				d5	
2011/11/01	13:45:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2011/11/01	13:45:26.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0				c4	0c
2011/11/01	13:45:28.0	XRT_FL_PROG_SET_415_OG [0x19f]							
		MDP_XRT_FL_PROG_SET	2	07-F0				c5	07
2011/11/01	13:45:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0				c0	
2011/11/01	14:28:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2011/11/01	14:28:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2011/11/01	14:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0				e8	
2011/11/01	14:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0				e9	
2011/11/01	14:38:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/01	14:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0				c0	
2011/11/01	16:02:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2011/11/01	16:02:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2011/11/01	16:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0				e8	

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2011/11/01	16:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/01	16:26:30.0	XRT_Custom_418_OG [0x1a2]						
2011/11/01	16:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/01	17:39:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/01	17:39:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/01	17:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/11/01	17:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/01	18:02:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/01	18:02:56.0	XRT_FOCUS_POSITION_401_OG [0x191]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2011/11/01	18:03:00.5	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2011/11/01	18:03:16.0	XRT_FLD_DIS_402_OG [0x192]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/11/01	18:03:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/11/01	18:03:20.0	XRT_ARS_DIS_438_OG [0x1b6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/11/01	18:05:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2011/11/01	18:06:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/01	18:12:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/01	18:12:56.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/11/01	18:13:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]						
		AOCU_NM	5	02-76	00 e5 59 50 72			
2011/11/01	18:13:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/11/01	18:13:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/11/01	18:13:20.0	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/11/01	18:13:22.0	XRT_ARS_DIS_427_OG [0x1ab]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/11/01	18:15:54.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/01	18:15:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c			
2011/11/01	18:15:58.0	XRT_FL_PROG_SET_415_OG [0x19f]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2011/11/01	18:16:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/01	19:16:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/01	19:16:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/01	19:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/11/01	19:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/01	19:40:00.0	XRT_Custom_418_OG [0x1a2]						
2011/11/01	19:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/01	20:54:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/01	20:54:02.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/01	20:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/11/01	20:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/01	21:17:00.0	XRT_Custom_418_OG [0x1a2]						
2011/11/01	21:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/01	21:59:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/01	21:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/11/01	22:00:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/11/01	22:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/11/01	22:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/11/01	22:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/11/01	22:02:54.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/01	22:02:56.0	XRT_QT_PROG_SET_446_OG [0x1be]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09			
2011/11/01	22:02:58.0	XRT_FL_PROG_SET_415_OG [0x19f]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2011/11/01	22:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			

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2011/11/01	22:31:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/11/01	22:31:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/11/01	22:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/11/01	22:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/11/01	22:52:30.0	XRT_Custom_418_OG [0x1a2]						
2011/11/01	22:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/11/02	00:08:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/11/02	00:08:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/11/02	00:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/11/02	00:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/11/02	00:17:00.0	XRT_Custom_418_OG [0x1a2]						
2011/11/02	00:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/11/02	01:33:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/11/02	01:33:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/11/02	01:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/11/02	01:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/11/02	01:51:30.0	XRT_Custom_418_OG [0x1a2]						
2011/11/02	01:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/11/02	03:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/11/02	03:07:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/11/02	03:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/11/02	03:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/11/02	03:28:30.0	XRT_Custom_418_OG [0x1a2]						
2011/11/02	03:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/11/02	04:37:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/11/02	04:37:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/11/02	04:37:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/11/02	04:40:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/11/02	05:06:00.0	XRT_Custom_418_OG [0x1a2]						
2011/11/02	05:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/11/02	05:49:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/11/02	05:49:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2011/11/02	05:50:00.5	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2011/11/02	05:50:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/11/02	05:50:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/11/02	05:50:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/11/02	05:52:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2011/11/02	05:53:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/11/02	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/11/02	05:59:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2011/11/02	06:00:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00 e5 59 50 72		
2011/11/02	06:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/11/02	06:00:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/11/02	06:00:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/11/02	06:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/11/02	06:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/11/02	06:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2011/11/02	06:02:58.0	XRT_FL_PROG_SET_415_OG [0x19f]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2011/11/02	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		

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2011/11/02	06:18:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/02	06:18:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/02	06:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/11/02	06:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/11/02	06:43:00.0	XRT_Custom_418_OG [0x1a2]					
2011/11/02	06:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/02	07:58:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/02	07:58:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/02	07:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/11/02	08:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/11/02	08:20:00.0	XRT_Custom_418_OG [0x1a2]					
2011/11/02	08:21:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/02	09:38:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/02	09:38:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/02	09:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/11/02	09:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/11/02	09:54:30.0	XRT_Custom_418_OG [0x1a2]					
2011/11/02	09:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/02	11:56:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/02	11:56:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/11/02	11:57:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/11/02	11:57:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/11/02	11:57:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/11/02	11:57:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/11/02	11:59:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/02	11:59:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2011/11/02	11:59:58.0	XRT_FL_PROG_SET_415_OG [0x19f]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2011/11/02	12:00:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/02	15:03:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/02	15:03:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/02	15:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/11/02	15:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/11/02	15:26:00.0	XRT_Custom_418_OG [0x1a2]					
2011/11/02	15:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/02	16:39:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/02	16:39:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/02	16:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/11/02	16:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/11/02	17:02:30.0	XRT_Custom_418_OG [0x1a2]					
2011/11/02	17:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/02	17:52:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/02	17:52:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/11/02	17:52:30.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2011/11/02	17:52:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/11/02	17:52:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/11/02	17:52:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/11/02	17:55:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2011/11/02	17:55:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/02	18:02:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2011/11/02	18:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/11/02	18:02:30.0	AOCs_OrE-point_Start_18_OG [0x0a8] AOCU_NM	5	02-76	04 00 00 00 00
2011/11/02	18:02:46.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2011/11/02	18:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/11/02	18:02:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0
2011/11/02	18:02:52.0	XRT_ARS_DIS_427_OG [0x1ab] MDP_XRT_ARS_DIS	1	07-F0	d5
2011/11/02	18:05:24.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/02	18:05:26.0	XRT_QT_PROG_SET_404_OG [0x194] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2011/11/02	18:05:28.0	XRT_FL_PROG_SET_415_OG [0x19f] MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2011/11/02	18:05:30.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/02	18:16:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/02	18:16:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/02	18:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/02	18:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/02	18:39:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/02	18:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/02	19:53:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/02	19:53:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/02	19:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/02	19:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/02	20:16:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/02	20:17:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/02	21:30:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/02	21:30:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/02	21:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/02	21:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/02	21:53:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/02	21:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/02	23:08:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/02	23:08:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/02	23:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/02	23:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/02	23:25:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/02	23:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/03	00:43:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/03	00:43:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/03	00:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/03	00:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/03	00:51:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/03	00:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/03	02:07:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/03	02:07:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/03	02:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/03	02:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/03	02:27:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/03	02:28:30.5	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/03	03:38:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/03	03:38:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/03	03:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/03	03:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			

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2011/11/03	04:05:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/11/03	04:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2011/11/03	05:15:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/03	05:15:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/03	05:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/03	05:18:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/11/03	05:42:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/11/03	05:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2011/11/03	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/03	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/03	06:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/11/03	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00	
2011/11/03	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/11/03	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/11/03	06:02:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/11/03	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2011/11/03	06:10:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/03	06:55:30.0	XRT_CTRL_MANU_408_OG [0x198]	AOCU_NM	5	02-76	02 00 00 00 00	
2011/11/03	06:55:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/03	06:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/03	06:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/11/03	08:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/11/03	08:35:32.0	XRT_FLD_RESET_412_OG [0x19c]					
2011/11/03	08:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/03	08:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/03	10:17:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
			AOCU_NM	5	02-76	00 00 00 00 00	