

XRT Timeline to be uploaded on 2011/11/12

Period: 2011/11/12 09:06:00 - 2011/11/17 09:28:00

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Normal mode

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XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384													
Term		Pointing (x, y)					Comment						
11/12 09:35:36 - 11/12 15:16:00		Fixed (0.0, -920.0)					# OP start + 10min, HOP 81, polar monitoring, S pole.						
PROG= 16 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 51 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 2 30-time(s) 2.0sec													
└─ Seqn= 58 2-time(s) 30.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long													
Term		Pointing (x, y)					Comment						
11/13 16:03:00 - 11/13 16:09:54		Fixed (-528.4, -528.4)					* XRT post-bakeout four-quadrant pointing, four pointings.						
PROG= 02 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 38 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly -long													
Term		Pointing (x, y)					Comment						
11/13 16:13:00 - 11/13 16:19:54		Fixed (528.4, -528.4)											
PROG= 05 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 36 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long													
Term		Pointing (x, y)					Comment						
11/13 16:23:00 - 11/13 16:29:54		Fixed (528.4, 528.4)											
PROG= 19 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 39 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long													
Term		Pointing (x, y)					Comment						
11/13 16:33:05 - 11/13 16:39:54		Fixed (-528.4, 528.4)					* Final four-quadrant pointing.						
PROG= 04 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 40 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 93 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18B0: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(24/1443) + Thin-Be(88/2)

Term	Pointing (x, y)	Comment
11/13 16:43:00 - 11/13 16:52:00	Fixed (0.0, 0.0)	* Synoptic, shifted manually.
11/14 06:08:30 - 11/14 06:15:24	Fixed (0.0, 0.0)	synoptic, shifted 5.5 min
11/14 17:57:00 - 11/14 18:03:54	Fixed (0.0, 0.0)	synoptic, shifted -6.0 min
11/15 05:42:00 - 11/15 05:48:54	Fixed (0.0, 0.0)	synoptic, shifted -21.0 min

PROG= 08 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 46 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 69 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 50 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18BD: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 60s-cad

Term	Pointing (x, y)	Comment
11/13 17:23:06 - 11/13 22:59:54	Track (414.8, 220.7) @ 11/13 16:50:00	* AR 11342, near disk center for EIS (repeating region).
11/14 18:07:00 - 11/14 22:59:54	Fixed (892.0, 296.0)	# AR 11339.

PROG= 12 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 29 4-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 31 30-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18C3: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/poly, 512x512 at 1064 1048, 30sec-cad

Term	Pointing (x, y)	Comment
11/13 23:03:00 - 11/14 06:05:24	Fixed (892.0, 296.0)	* HOP 128, with Hida. Fixed pointing of AR 11339 at W limb.
11/14 23:03:00 - 11/15 00:59:54	Track (504.9, 147.2) @ 11/14 23:00:00	* HOP 128, with Hida, AR 11341.

PROG= 18 Inf.-time(s)												
Subr= 2 1-time(s) 2.0sec												
Seqn= 17 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 45 4-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1 1-time(s) 2.0sec												
Seqn= 48 90-time(s) 30.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1838: HOP 176 - Ti/poly - 1x1 - 384x384 - 1min cadence - AEC 1 - Q95 +context												
Term	Pointing (x, y)		Comment									
11/14 06:18:30 - 11/14 17:28:30	Fixed (892.0, 296.0)		# AR 11339, with HOP 176 from 9:30.									
PROG= 17 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 49 1-time(s) 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 2 3-time(s) 2.0sec												
└─┬─ Seqn= 95 1-time(s) 2.0sec												
└─└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─┬─ Seqn= 56 20-time(s) 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 15.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 15.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 15.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 3 15.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17B6: AR multifilter - Al/mesh,Ti/Poly, G-bandFOV 384 AEC2 Q95 (5min)												
Term	Pointing (x, y)		Comment									
11/15 01:03:00 - 11/15 05:38:54	Track (-24.9, 313.0) @ 11/15 01:00:00		* Filament.									
11/15 05:52:00 - 11/15 08:40:00	Track (16.4, 313.4) @ 11/15 05:49:00		# Cont.									
PROG= 09 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─┬─ Seqn= 78 1-time(s) 2.0sec												
└─└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
└─ Subr= 1 9-time(s) 300.0sec												
└─┬─ Seqn= 61 1-time(s) 4.0sec												
└─└─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 1024x1024 (1536, 1536) Q=95 2 0 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 1024x1024 (1536, 1536) Q=95 2 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops												
Term	Pointing (x, y)		Comment									
11/12 09:35:36 - 11/12 15:16:00	Fixed (0.0, -920.0)		# OP start + 10min, HOP 81, polar monitoring, S pole.									
11/13 17:23:06 - 11/13 22:59:54	Track (414.8, 220.7) @ 11/13 16:50:00		* AR 11342, near disk center for EIS (repeating region).									
11/13 23:03:00 - 11/14 06:05:24	Fixed (892.0, 296.0)		* HOP 128, with Hida. Fixed pointing of AR 11339 at W limb.									
11/14 06:18:30 - 11/14 17:28:30	Fixed (892.0, 296.0)		# AR 11339, with HOP 176 from 9:30.									
11/14 18:07:00 - 11/14 22:59:54	Fixed (892.0, 296.0)		# AR 11339.									
11/14 23:03:00 - 11/15 00:59:54	Track (504.9, 147.2) @ 11/14 23:00:00		* HOP 128, with Hida, AR 11341.									
11/15 01:03:00 - 11/15 05:38:54	Track (-24.9, 313.0) @ 11/15 01:00:00		* Filament.									
11/15 05:52:00 - 11/15 08:40:00	Track (16.4, 313.4) @ 11/15 05:49:00		# Cont.									
PROG= 03 12-time(s)												
└─ Subr= 1 45-time(s) 10.0sec												
└─┬─ Seqn= 20 1-time(s) 2.0sec												
└─└─ thin-Be/Open med-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬─ Seqn= 63 1-time(s) 2.0sec												
└─└─ med-Al/Open med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬─ Seqn= 77 1-time(s) 2.0sec												
└─└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└─ Subr= 2 1-time(s) 10.0sec												
└─┬─ Seqn= 90 1-time(s) 2.0sec												
└─└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

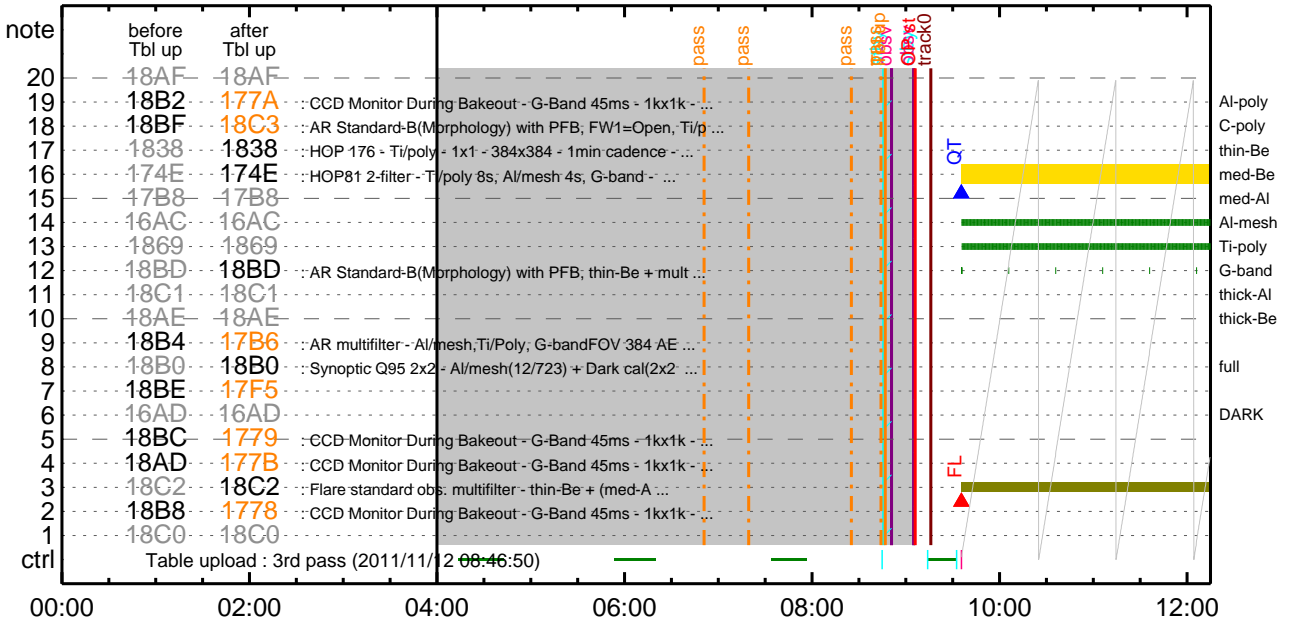
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Flare Detection

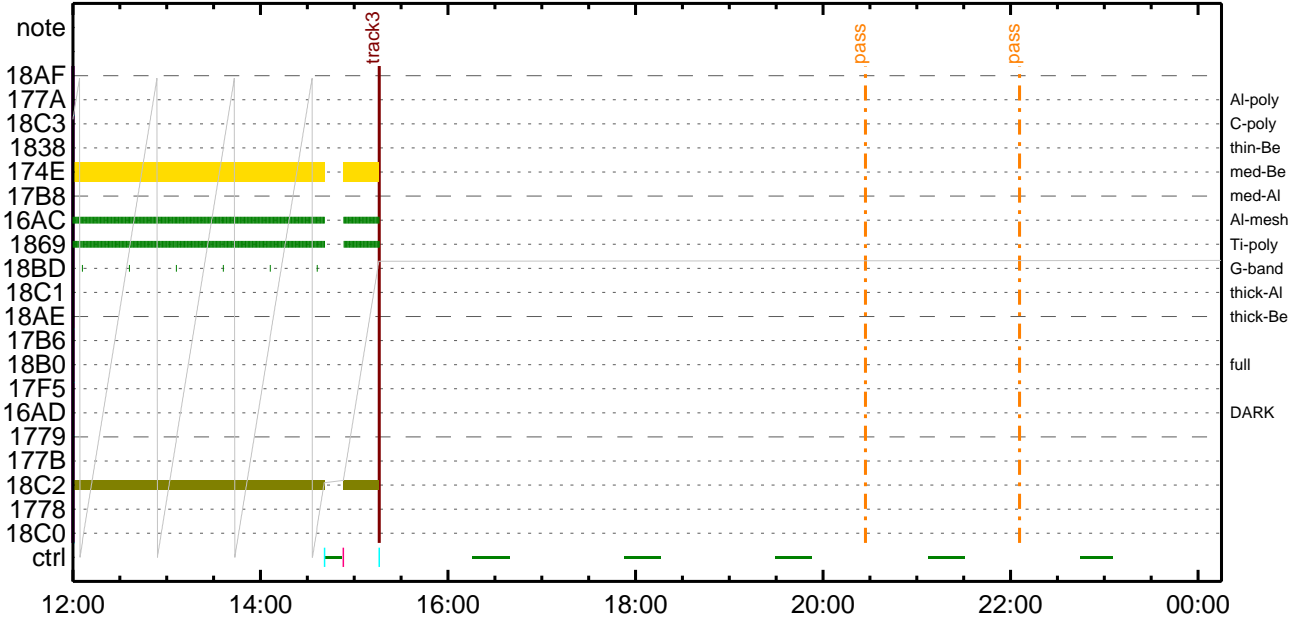
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FLD Patrol												
Term	Pointing (x, y)		Comment									
11/13 17:20:22 - 11/14 06:05:46	Track (414.8, 220.7) @ 11/13 16:50:00		* AR 11342, near disk center for EIS (repeating region).									
11/14 06:15:46 - 11/14 17:54:16	Fixed (892.0, 296.0)		# AR 11339, with HOP 176 from 9:30.									
11/14 18:04:16 - 11/15 05:39:16	Fixed (892.0, 296.0)		# AR 11339.									
11/15 05:51:46 - 11/17 09:28:00	Track (16.4, 313.4) @ 11/15 05:49:00		# Cont.									
Open/Ti-poly Open/thick-Al close Safe Norm 8ms Obs 8x8 Q=50 30sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

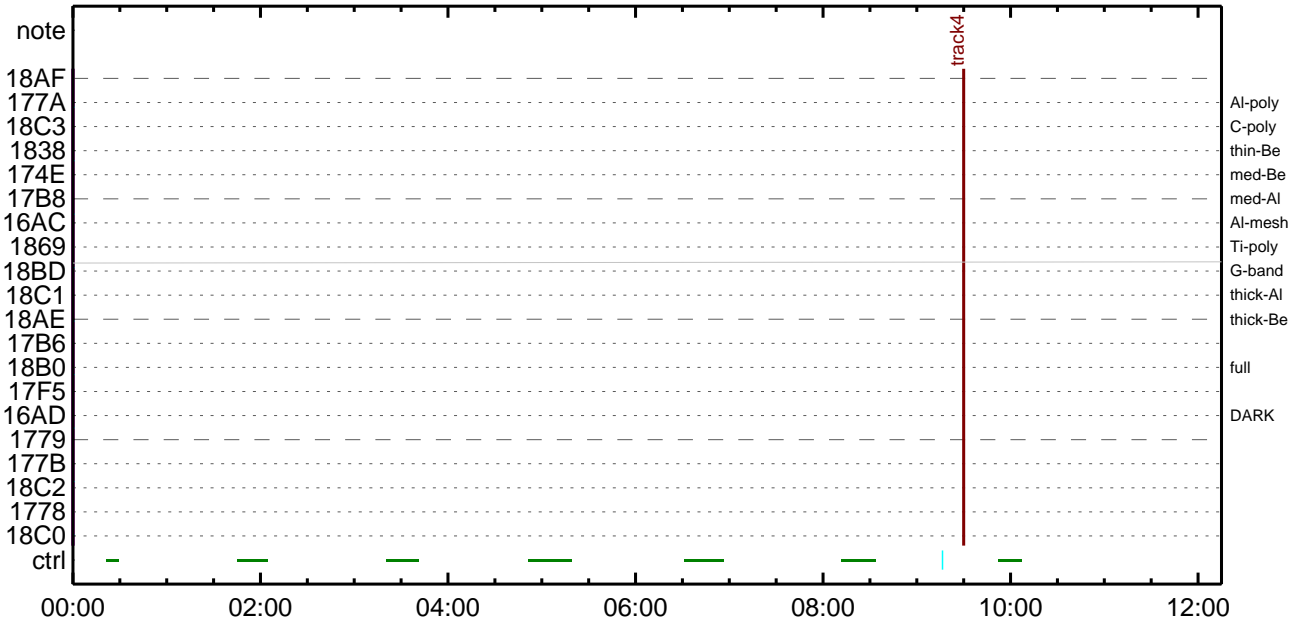
CMDI #0260 2011/11/12



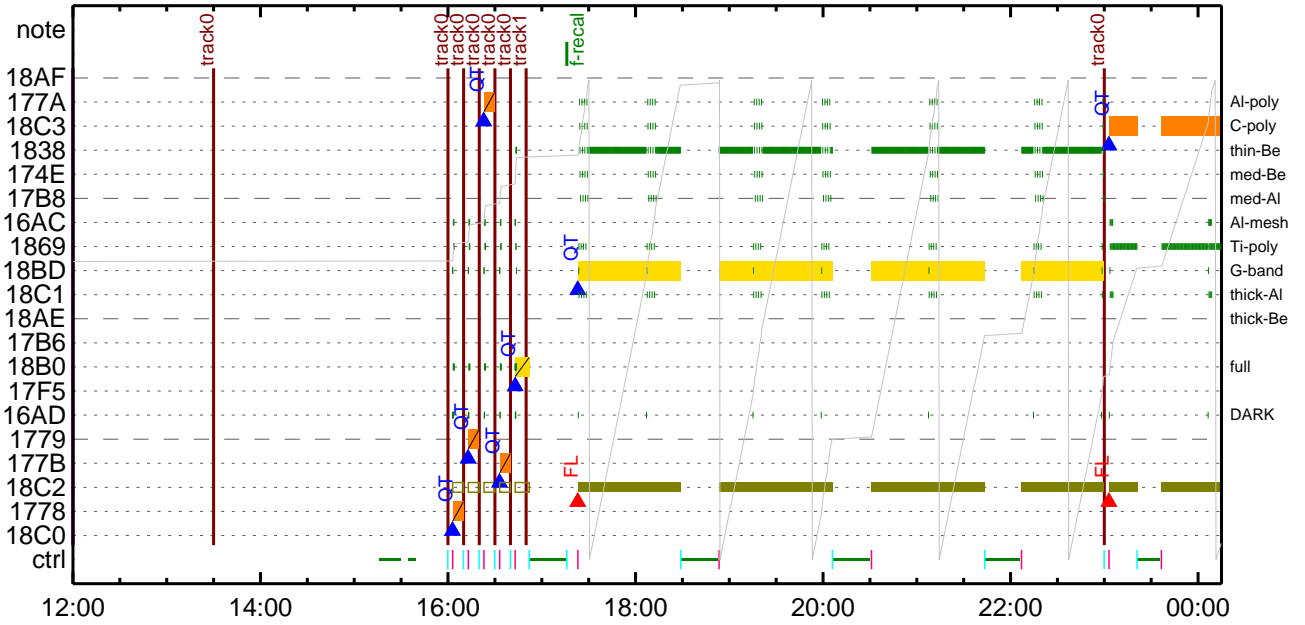
CMDI #0260 2011/11/12



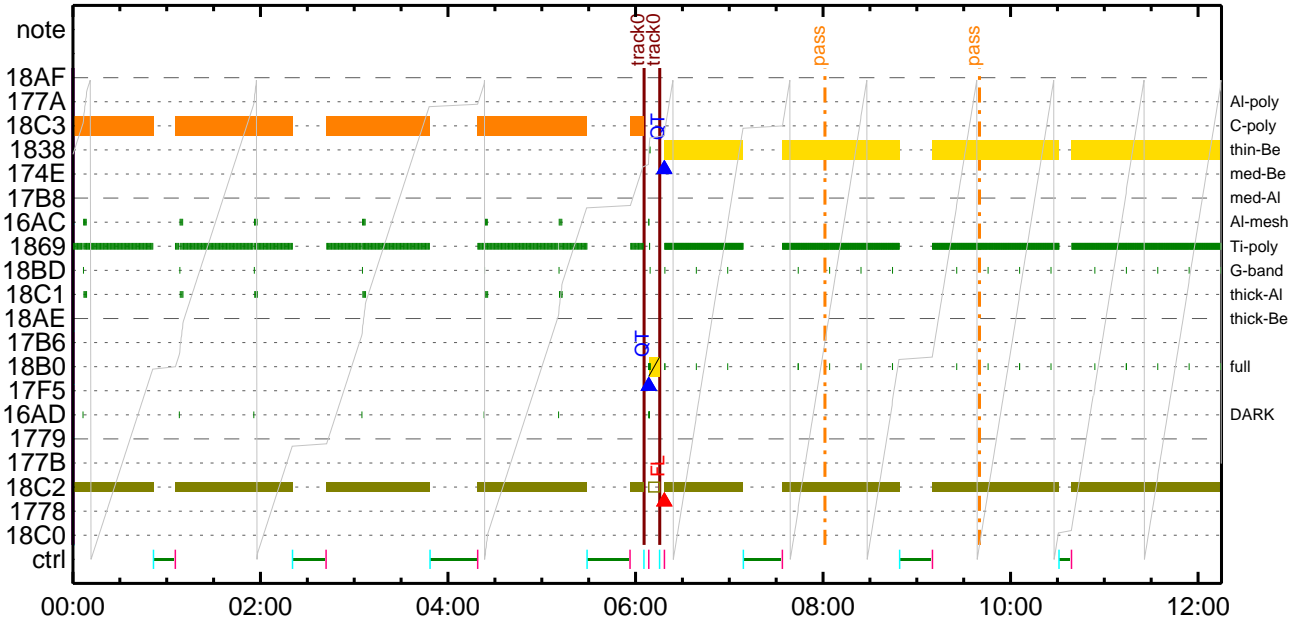
CMDI #0260 2011/11/13



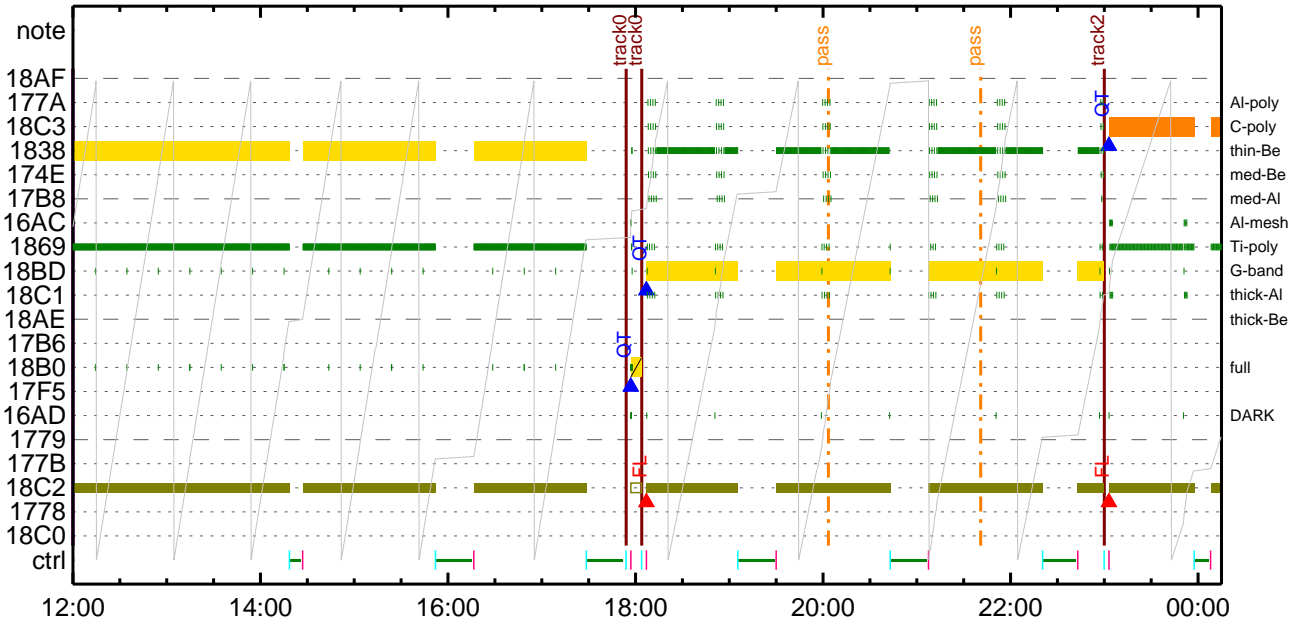
CMDI #0260 2011/11/13



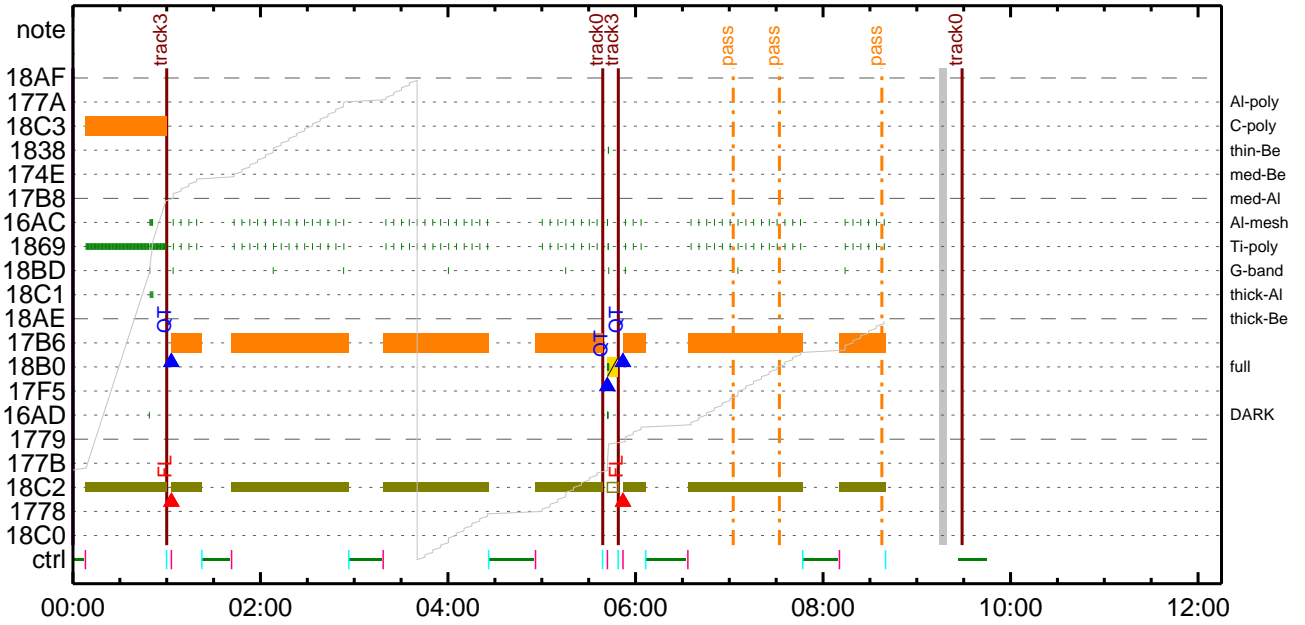
CMDI #0260 2011/11/14



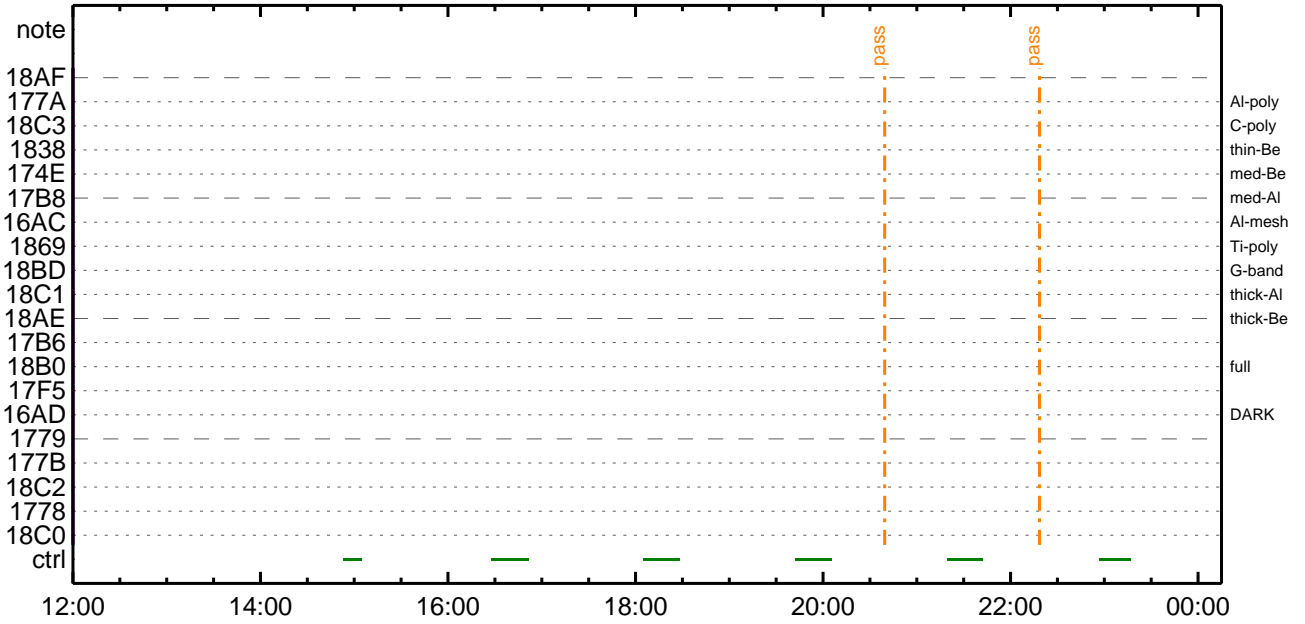
CMDI #0260 2011/11/14



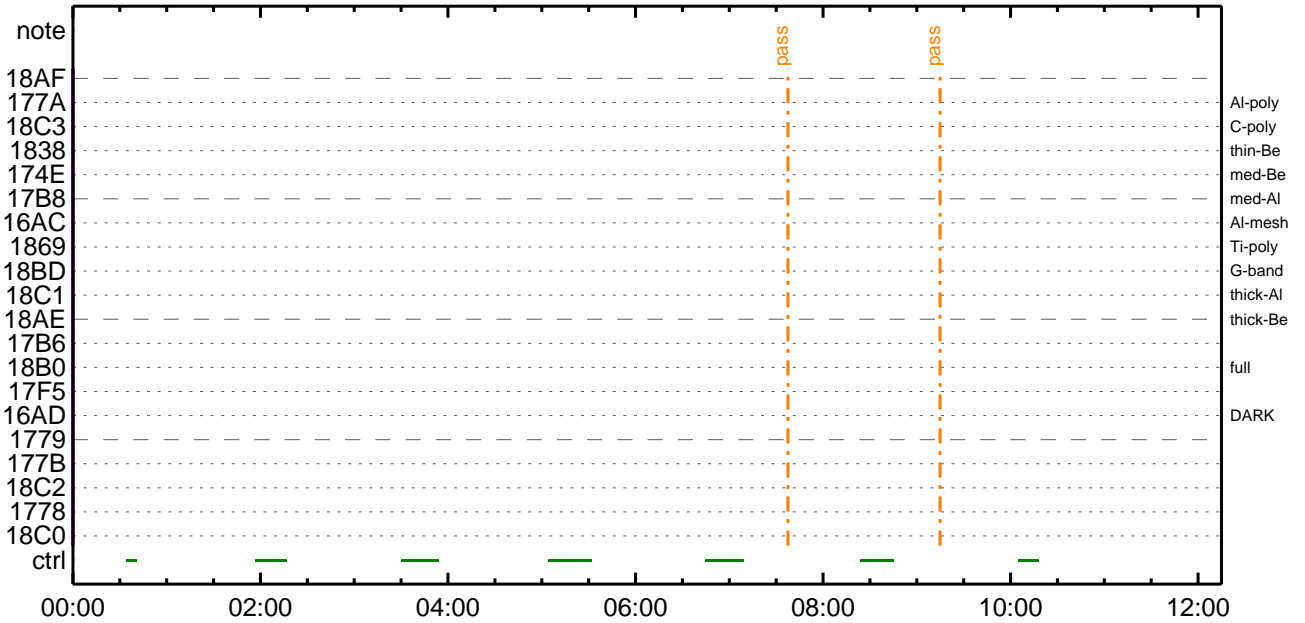
CMDI #0260 2011/11/15



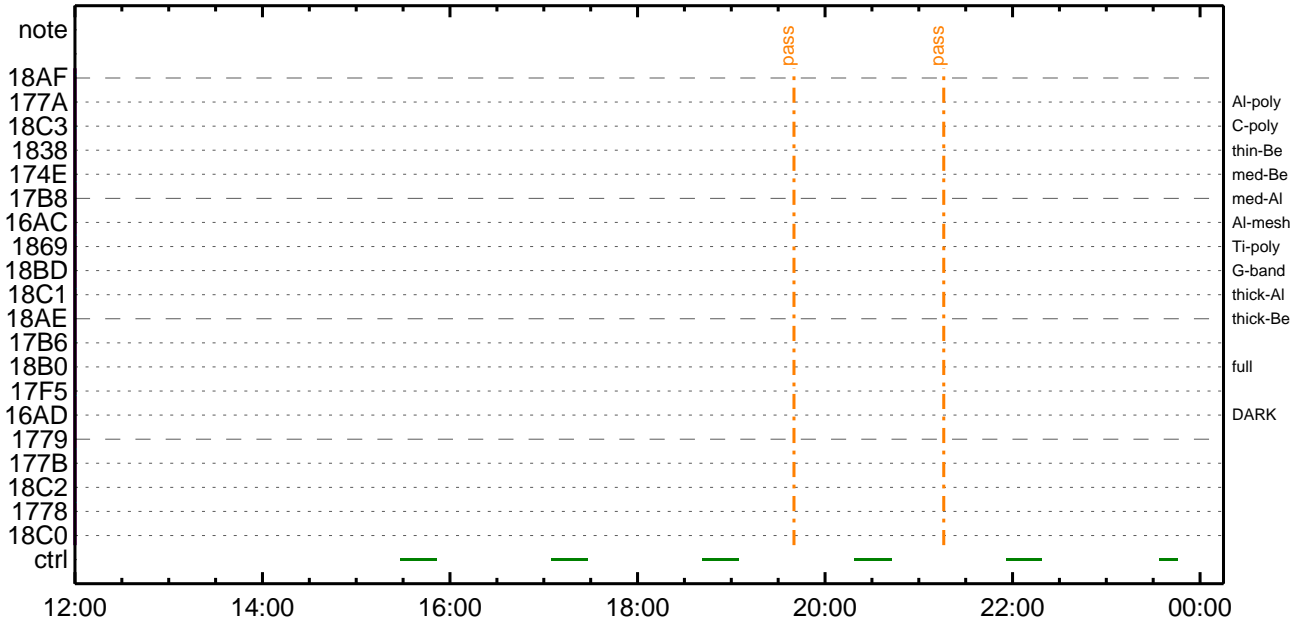
CMDI #0260 2011/11/15



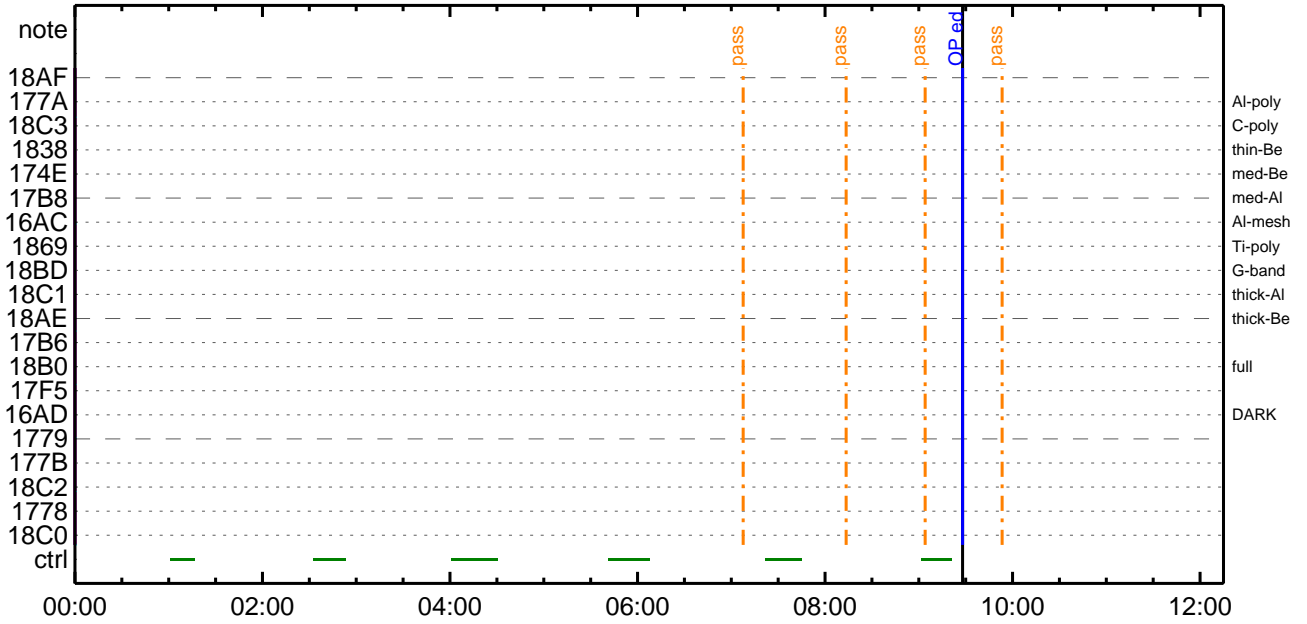
CMDI #0260 2011/11/16



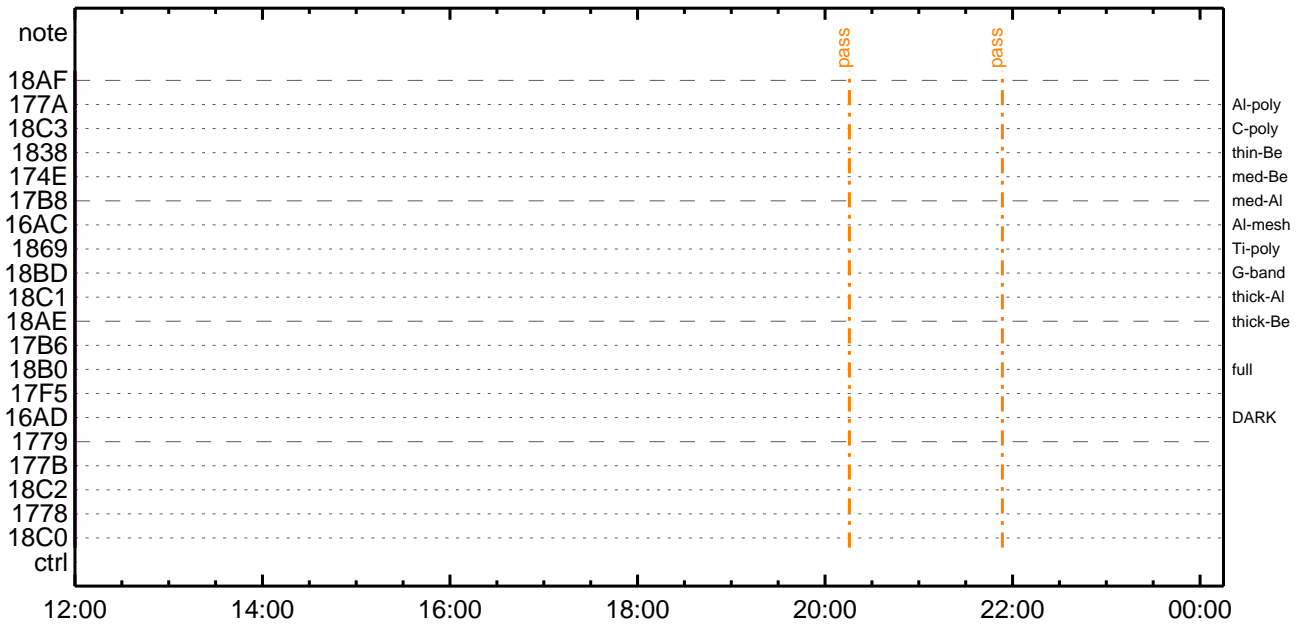
CMDI #0260 2011/11/16



CMDI #0260 2011/11/17



CMDI #0260 2011/11/17



0096 C. ...
0097 C.
0098 C. TI ... (UT)
0099 +. TI 2011-11-12 09:01:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C. ... EQ 1COUNTUP
0102 C.
0103 +. TI 2011-11-12 09:01:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C. ... EQ 1COUNTUP
0106 C.
0107 +. TI 2011-11-12 09:01:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C. ... EQ 1COUNTUP
0110 C.
0111 +. TI 2011-11-12 09:05:59.5
0112 DC 01-B2 DHU_OP_START
0113 C. ... EQ 1COUNTUP
0114 C.
0115 C. ...
0116 C. ... EQ ENA
0117 C. ... EQ 4
0118 C. ... EQ DHU
0119 C. ... EQ 0xB3
0120 C.
0121 C. *****
0122 C. TI ... x
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C. ... EQ 07
0129 C. ... EQ 2B
0130 C. ... EQ 3
0131 C. ... EQ 0
0132 C. ... EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C. ... EQ 7
0136 C. ... EQ 0.25 s
0137 C. ... EQ 32k
0138 C. ... EQ 4M
0139 C. ... EQ EXEC
0140 C.
0141 C. ...
0142 C. ... EQ NON
0143 C.
0144 C. RAM ID=TI_TBL ...
0145 C.
0146 C. DHU ...
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C. ... EQ 2
0150 C. ... EQ 0.5S
0151 C. ... EQ 32K
0152 C. ... EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2011-11-12 09:05:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C.
0162 C. HK1_TI_CMD_NUM = 1 CNTUP []
0163 C.
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2011-11-12 09:05:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2011-11-12 09:05:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2011-11-12 09:05:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP ... *****
0192 C. (...)
0193 S. DC-BC dcbc-402:DCBC

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 22s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2011-11-12 09:05:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'ûÃîðî»ö¼ÝðËÄðð¹æDCBC·×²è *****
0153 C. (%ã°îÿóÿÁÿËÿPÿËÿÿÿÁÿcÿËæðË¼¼¼¼»Û¹æ)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ÝDÿ¹·İ Daily±;İÑðË'ð¹æDCBC·×²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSÿÁÿSÿÿÿÿ-¼Ä»Û;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-456 2011-11-12 14:09:26 134 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È□¿□Á□•µ°È»Í×ÁÇ□íYçYÁY×Yí;¼YÉ;ÈÈ%µ•ííÉ;È□È¼°Ç□□•□¿¼í¹ç□í;çÀ®, ù□¹□È□È□ÇÁ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 c0 c0 10 10)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 40 c0 10 10)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 40 40 10 10)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b c0 40 10 10)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 80 80 20 08)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0d 80 80 08 20)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0e 85 83 08 08)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 0f 80 80 06 06)
0062 + DC 07-F0 MDP_XRT_ROI_SET
0063 BC (cd 10 80 80 08 08)
0064 + DC 07-F0 MDP_XRT_FLD_ENA
0065 BC (d8)
0066 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0067 BC (c8)
0068 + DC 07-F0 MDP_XRT_AEC_RESET
0069 BC (d0)
0070 + DC 07-F0 MDP_XRT_ARS_DIS
0071 BC (d5)
0072 + DC 07-F0 MDP_XRT_FLD_RESET
0073 BC (da)
0074 . C. ----- Success Verify ? OK / NG ____
0075 C.
0076 C.
0077 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0078 C.
0079 +. DC 07-F0 MDP_XRT_MODE_OBSV
0080 BC (c2)
0081 +. TI 2011-11-12 09:05:02.0
0082 DC 07-F0 MDP_XRT_MODE_OBSV
0083 BC (c2)
0084 . C. ----- Success Verify ? OK / NG ____
0085 C.
0086 C. ***** XRT END *****
0087 . C. *****
0088 C. SOT table upload
0089 C. *****
0090 . C. < Stop FG table >
0091 +. DC 07-F0 MDP_FG_CTRL_MANU
0092 BC (51)
0093 . C. -----
0094 C. MDP_FG_CTRL_MODE = MANU [ ]
0095 C. -----
```

```

0096 C.
0097 . C. <Upload FG Observation Table>
0098 . S. RAM ram-266:MDP_OBS_F
0099 ( )
0100 C.
0101 . C. < Dump RAMID=MDP_OBS_F >
0102 +. DC 07-F0 MDP_DUMP_FGTBL
0103 BC (82 07 00 00 00 38 b8)
0104 C. -----
0105 C. MDP_OBS_F verify = OK/NG [ ]
0106 C. -----
0107 C.
0108 C. *****
0109 C. SOT TI command set
0110 C. *****
0111 C. Execute, after the success of TBL upload.
0112 +. TI 2011-11-12 09:05:18.0
0113 DC 07-F0 MDP_SOT_MODE_OBSV
0114 BC (40)
0115 . C. -----
0116 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0117 C. -----
0118 C.
0119 C.
0120 . C. ***** MDP `úÃîñî»ö¼ÝñÊÃÐñ¹ñèDCBC•x²è *****
0121 C. (¼á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèñÊ¼ñ¼Ã»Ûñ¹ñè)
0122 . S. DC-BC dcbc-402:DCBC
0123 (MDP_known_event)
0124 C.
0125 C.
0126 . C. ***** ÝDÝ¹•Ï Daily±¿îññÊ´Øñ¹ñèDCBC•x²è *****
0127 . S. DC-BC dcbc-153:DCBC
0128 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0129 C.
0130 C.
0131 . C. ¡ãLOSÝÁÝ§ÝÃÝ¬¼Ã»Ûñ¹ñè
0132 C.
0133 . C. ***** LOS *****
0134 C.

```

*** OP Sequence for XRT ***

```

2011/11/12 09:14:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/11/12 09:14:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/11/12 09:14:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2011/11/12 09:16:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 51 ca 00 00
2011/11/12 09:17:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2011/11/12 09:32:30.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/11/12 09:35:02.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2011/11/12 09:35:22.0 XRT_FLD_ENA_435_OG [0x1b3]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2011/11/12 09:35:24.0 XRT_FLRCTRL_ENA_403_OG [0x193]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2011/11/12 09:35:26.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2011/11/12 09:35:28.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/11/12 09:35:30.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/11/12 09:35:32.0 XRT_QT_PROG_SET_426_OG [0x1aa]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 10
2011/11/12 09:35:34.0 XRT_FL_PROG_SET_414_OG [0x19e]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 03
2011/11/12 09:35:36.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/11/12 14:41:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/11/12 14:41:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/11/12 14:41:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2011/11/12 14:44:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2011/11/12 14:52:00.0 XRT_Custom_418_OG [0x1a2]
2011/11/12 14:53:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/11/12 15:16:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 03 00 00 00 00
2011/11/12 15:16:00.5 XRT_CTRL_MANU_410_OG [0x19a]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/11/12 15:16:30.5 XRT_TCIB_XRT_S_HTR_A_ENA_405_OG [0x195]
                        TCIB_XRT_S_HTR_A_ENA 0 04-BC
2011/11/12 17:16:30.5 XRT_Custom_442_OG [0x1ba]
2011/11/12 19:16:30.5 XRT_Custom_442_OG [0x1ba]
2011/11/12 21:16:30.5 XRT_Custom_442_OG [0x1ba]
2011/11/12 23:16:30.5 XRT_Custom_429_OG [0x1ad]
2011/11/12 23:16:40.5 XRT_Custom_442_OG [0x1ba]
2011/11/13 01:16:40.5 XRT_Custom_442_OG [0x1ba]
2011/11/13 03:16:40.5 XRT_Custom_442_OG [0x1ba]
2011/11/13 05:16:40.5 XRT_Custom_442_OG [0x1ba]
2011/11/13 07:16:40.5 XRT_Custom_407_OG [0x197]
2011/11/13 09:16:18.5 XRT_Custom_429_OG [0x1ad]
2011/11/13 09:16:28.5 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/11/13 09:16:30.5 XRT_TCIB_XRT_S_HTR_A_DIS_420_OG [0x1a4]
                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2011/11/13 09:30:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 04 00 00 00 00
2011/11/13 13:30:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM 5 02-76 00 3a 7e c1 cb
2011/11/13 15:59:54.0 XRT_CTRL_MANU_444_OG [0x1bc]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/11/13 16:00:00.0 AOCs_OrE-point_Start_5_OG [0x09b]
                        AOCU_NM 5 02-76 00 2e f9 2e f9
2011/11/13 16:02:32.0 XRT_FOCUS_POSITION_415_OG [0x19f]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2011/11/13 16:02:52.0 XRT_QT_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 02
2011/11/13 16:02:54.0 XRT_FLD_DIS_402_OG [0x192]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2011/11/13 16:02:56.0 XRT_FLRCTRL_DIS_433_OG [0x1b1]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2011/11/13 16:02:58.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/11/13 16:03:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/11/13 16:09:54.0 XRT_CTRL_MANU_444_OG [0x1bc]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/11/13 16:10:00.0 AOCs_OrE-point_Start_6_OG [0x09c]
                        AOCU_NM 5 02-76 00 2e f9 d1 07
2011/11/13 16:12:32.0 XRT_FOCUS_POSITION_415_OG [0x19f]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2011/11/13 16:12:52.0 XRT_QT_PROG_SET_446_OG [0x1be]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 05
2011/11/13 16:12:54.0 XRT_FLD_DIS_402_OG [0x192]
                        MDP_XRT_FLD_DIS 1 07-F0 d9

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2011/11/13	17:23:02.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/13	17:23:04.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2011/11/13	17:23:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2011/11/13	18:29:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/13	18:29:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/13	18:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/13	18:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/11/13	18:52:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/13	18:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]						
2011/11/13	20:06:00.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/11/13	20:06:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/13	20:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/13	20:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/13	20:30:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/11/13	20:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/13	21:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_Custom_418_OG [0x1a2]						
2011/11/13	21:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/11/13	21:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/13	21:43:34.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/13	21:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/13	22:06:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/11/13	22:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/13	22:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/11/13	23:00:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/13	23:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/13	23:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	e5 b3 b0 b5		
2011/11/13	23:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2011/11/13	23:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	XRT_FLD_ENA_411_OG [0x19b]						
2011/11/13	23:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/11/13	23:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/11/13	23:02:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/11/13	23:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/13	23:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12		
2011/11/13	23:21:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2011/11/13	23:21:02.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_CTRL_AUTO_406_OG [0x196]						
2011/11/13	23:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/13	23:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/13	23:35:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/13	23:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/11/14	00:51:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/14	00:51:32.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_Custom_418_OG [0x1a2]						
2011/11/14	00:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/11/14	00:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/14	01:04:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/14	01:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/14	02:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/11/14	02:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/14	02:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	XRT_Custom_418_OG [0x1a2]						
2011/11/14	02:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/11/14	02:41:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			

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2011/11/14	02:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	03:48:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	03:48:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	03:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/14	03:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/14	04:18:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/14	04:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	05:29:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	05:29:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	05:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/14	05:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/14	05:55:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/14	05:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	06:05:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	06:05:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/11/14	06:05:30.0	AOCS_Ore-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/11/14	06:05:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/11/14	06:05:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/11/14	06:05:50.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/11/14	06:08:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/11/14	06:08:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	06:15:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	06:15:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/11/14	06:15:30.0	AOCS_Ore-point_Start_11_OG [0x0a1]			
		AOCU_NM	5	02-76	00 e5 b3 b0 b5
2011/11/14	06:15:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/11/14	06:15:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/11/14	06:15:50.0	XRT_ARS_DIS_430_OG [0x1ae]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/11/14	06:18:24.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	06:18:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2011/11/14	06:18:28.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/11/14	06:18:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	07:09:00.5	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	07:09:02.5	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	07:09:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/14	07:12:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/14	07:33:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/14	07:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	08:49:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	08:49:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	08:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/14	08:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/14	09:09:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/14	09:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	10:31:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	10:31:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	10:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/14	10:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/14	10:38:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/14	10:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2011/11/14	14:18:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	14:18:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	14:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/14	14:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/14	14:26:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/14	14:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	15:52:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	15:52:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	15:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/14	15:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/14	16:15:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/14	16:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	17:28:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	17:28:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	17:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/14	17:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/14	17:53:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	17:53:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/11/14	17:54:00.0	AOCS_Ore-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/11/14	17:54:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/11/14	17:54:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/11/14	17:54:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/11/14	17:56:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/11/14	17:57:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	18:03:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	18:03:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/11/14	18:04:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]			
		AOCU_NM	5	02-76	00 e5 b3 b0 b5
2011/11/14	18:04:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/11/14	18:04:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/11/14	18:04:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/11/14	18:04:22.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/11/14	18:06:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	18:06:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2011/11/14	18:06:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/11/14	18:07:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	19:05:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	19:05:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	19:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/14	19:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/14	19:29:00.5	XRT_Custom_418_OG [0x1a2]			
2011/11/14	19:30:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	20:43:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	20:43:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	20:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/14	20:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/14	21:06:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/14	21:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	22:20:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	22:20:32.0	XRT_FLD_RESET_412_OG [0x19c]			

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2011/11/14	22:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	22:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/14	22:42:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/14	22:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	22:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	23:00:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	02 00 00 00 00
2011/11/14	23:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/11/14	23:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/11/14	23:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/11/14	23:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/11/14	23:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/11/14	23:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	23:02:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2011/11/14	23:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/11/14	23:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/14	23:57:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/14	23:57:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/14	23:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/15	00:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/15	00:07:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/15	00:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/15	00:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/15	01:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00
2011/11/15	01:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/11/15	01:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/11/15	01:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/11/15	01:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/11/15	01:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/11/15	01:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/15	01:02:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2011/11/15	01:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/11/15	01:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/15	01:22:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/15	01:22:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/15	01:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/15	01:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/15	01:40:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/15	01:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/15	02:56:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/15	02:56:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/15	02:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/15	02:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/15	03:17:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/15	03:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/15	04:26:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/15	04:26:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/15	04:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/15	04:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2011/11/15	04:55:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/15	04:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/15	05:38:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/15	05:38:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/11/15	05:39:00.5	AOCS_ORe-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/11/15	05:39:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/11/15	05:39:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/11/15	05:39:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/11/15	05:41:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/11/15	05:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/15	05:48:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/15	05:49:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/11/15	05:51:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/11/15	05:51:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/11/15	05:51:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/11/15	05:51:50.5	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/11/15	05:51:52.5	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/11/15	05:51:54.5	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/15	05:51:56.5	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2011/11/15	05:51:58.5	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/11/15	05:52:00.5	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/15	06:06:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/15	06:06:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/15	06:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/15	06:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/15	06:32:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/15	06:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/15	07:47:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/15	07:47:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/15	07:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/15	07:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/15	08:09:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/15	08:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/15	08:40:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/15	09:29:00.0	AOCS_ORe-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 00 00 00 00