

# XRT Timeline to be uploaded on 2011/11/19

Period: 2011/11/19 10:41:00 - 2011/11/24 09:56:00

\* \* \* \* \*

Normal mode

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## XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad

Term	Pointing (x, y)	Comment
11/19 10:54:00 - 11/19 17:57:24	Track ( -767.6, -294.3) <sup>Ⓜ 11/19 10:51:00</sup>	# OP start + 10min, track AR in SE.
11/19 18:10:30 - 11/20 06:29:24	Track ( -729.0, -296.2) <sup>Ⓜ 11/19 18:07:30</sup>	# Cont.
11/20 06:43:10 - 11/20 13:40:24	Track ( 4.2, 204.2) <sup>Ⓜ 11/20 06:40:30</sup>	# AR studies (recurrent old AR remnant, EIS request).
11/20 13:43:30 - 11/20 17:53:30	Track ( -608.1, -300.4) <sup>Ⓜ 11/20 13:40:30</sup>	* AR in SE.
11/20 18:30:00 - 11/21 06:01:24	Track ( -575.1, -301.2) <sup>Ⓜ 11/20 18:27:00</sup>	# Cont.
11/21 06:19:30 - 11/21 09:52:30	Track ( -488.8, -302.8) <sup>Ⓜ 11/21 06:12:30</sup>	# Cont.
11/21 13:43:00 - 11/21 17:59:54	Track ( -430.8, -303.6) <sup>Ⓜ 11/21 13:40:00</sup>	* AR in SE.
11/21 18:13:00 - 11/21 22:59:54	Track ( -394.7, -303.9) <sup>Ⓜ 11/21 18:10:00</sup>	# Cont.

**PROG= 10 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec											
Seqn= 19			1-time(s) 2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec
Seqn= 96			4-time(s) 2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
Subr= 2			1-time(s) 2.0sec										
Seqn= 62			15-time(s) 150.0sec										
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

## XOB #18B0: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(24/1443) + Thin-Be(88/24)

Term	Pointing (x, y)	Comment
11/19 18:00:30 - 11/19 18:07:24	Fixed ( 0.0, 0.0)	synoptic, shifted -2.5 min
11/20 06:32:30 - 11/20 06:40:04	Fixed ( 0.0, 0.0)	synoptic, shifted 30.5 min
11/20 18:20:00 - 11/20 18:26:54	Fixed ( 0.0, 0.0)	synoptic, shifted 17.0 min
11/21 06:04:30 - 11/21 06:12:24	Fixed ( 0.0, 0.0)	synoptic, shifted 2.5 min
11/21 18:03:00 - 11/21 18:09:54	Fixed ( 0.0, 0.0)	synoptic
11/22 06:09:00 - 11/22 06:15:54	Fixed ( 0.0, 0.0)	synoptic, shifted 6.0 min

**PROG= 08 1-time(s)**

Subr= 1	1-time(s)	12.0sec											
Seqn= 46			1-time(s) 4.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5			1-time(s) 2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
Seqn= 69			1-time(s) 4.0sec										
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 50			1-time(s) 2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4			1-time(s) 2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

## XOB #18C3: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/poly, 512x512 at 1064 1048, 30sec-cad

Term	Pointing (x, y)	Comment
11/21 23:03:00 - 11/22 01:59:54	Fixed ( 787.0, 510.0)	* HOP 128, with Hida, prominence at W limb. Continue with same pointing for ToO HOP 128

**PROG= 18 Inf.-time(s)**

Subr= 2	1-time(s)	2.0sec											
Seqn= 17			1-time(s) 2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512	(1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512	(1064, 1048)	Q=98	0	0	2.0sec
Seqn= 45			4-time(s) 2.0sec										
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec

Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 1 1-time(s) 2.0sec</b>												
<b>Seqn= 48 90-time(s) 30.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #17E0: Coronal cavity Al/mesh, Ti/poly, G-band-DPCM 2x2-512x512-FOV -AEC2-3min cadence-SW offset**

Term	Pointing (x, y)	Comment
11/22 02:03:00 - 11/22 06:05:54	Fixed ( 787.0, 510.0)	* HOP 128, with Hida, prominence at W limb. Continue with same pointing for ToO HOP 114
11/22 06:19:00 - 11/22 10:00:00	Fixed ( 787.0, 510.0)	# Cont.

**PROG= 13 Inf.-time(s)**

<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 6 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1152, 1152)	Q=95	0	0	2.0sec
<b>Subr= 1 20-time(s) 180.0sec</b>												
<b>Seqn= 27 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	768x768 (1152, 896)	DPCM	2	0	2.0sec
<b>Seqn= 97 1-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	768x768 (1152, 896)	DPCM	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops**

Term	Pointing (x, y)	Comment
11/19 10:54:00 - 11/19 17:57:24	Track ( -767.6, -294.3) @ 11/19 10:51:00	# OP start + 10min, track AR in SE.
11/19 18:10:30 - 11/20 06:29:24	Track ( -729.0, -296.2) @ 11/19 18:07:30	# Cont.
11/20 06:43:10 - 11/20 13:40:24	Track ( 4.2, 204.2) @ 11/20 06:40:30	# AR studies (recurrent old AR remnant, EIS request).
11/20 13:43:30 - 11/20 17:53:30	Track ( -608.1, -300.4) @ 11/20 13:40:30	* AR in SE.
11/20 18:30:00 - 11/21 06:01:24	Track ( -575.1, -301.2) @ 11/20 18:27:00	# Cont.
11/21 06:19:30 - 11/21 09:52:30	Track ( -488.8, -302.8) @ 11/21 06:12:30	# Cont.
11/21 13:43:00 - 11/21 17:59:54	Track ( -430.8, -303.6) @ 11/21 13:40:00	* AR in SE.
11/21 18:13:00 - 11/21 22:59:54	Track ( -394.7, -303.9) @ 11/21 18:10:00	# Cont.
11/21 23:03:00 - 11/22 01:59:54	Fixed ( 787.0, 510.0)	* HOP 128, with Hida, prominence at W limb. Continue with same pointing for ToO HOP 114
11/22 02:03:00 - 11/22 06:05:54	Fixed ( 787.0, 510.0)	* HOP 128, with Hida, prominence at W limb. Continue with same pointing for ToO HOP 114
11/22 06:19:00 - 11/22 10:00:00	Fixed ( 787.0, 510.0)	# Cont.

**PROG= 03 12-time(s)**

<b>Subr= 1 45-time(s) 10.0sec</b>												
<b>Seqn= 20 1-time(s) 2.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 63 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 77 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2 1-time(s) 10.0sec</b>												
<b>Seqn= 90 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Active Region Search**

\* \* \* \* \*

NOT USED

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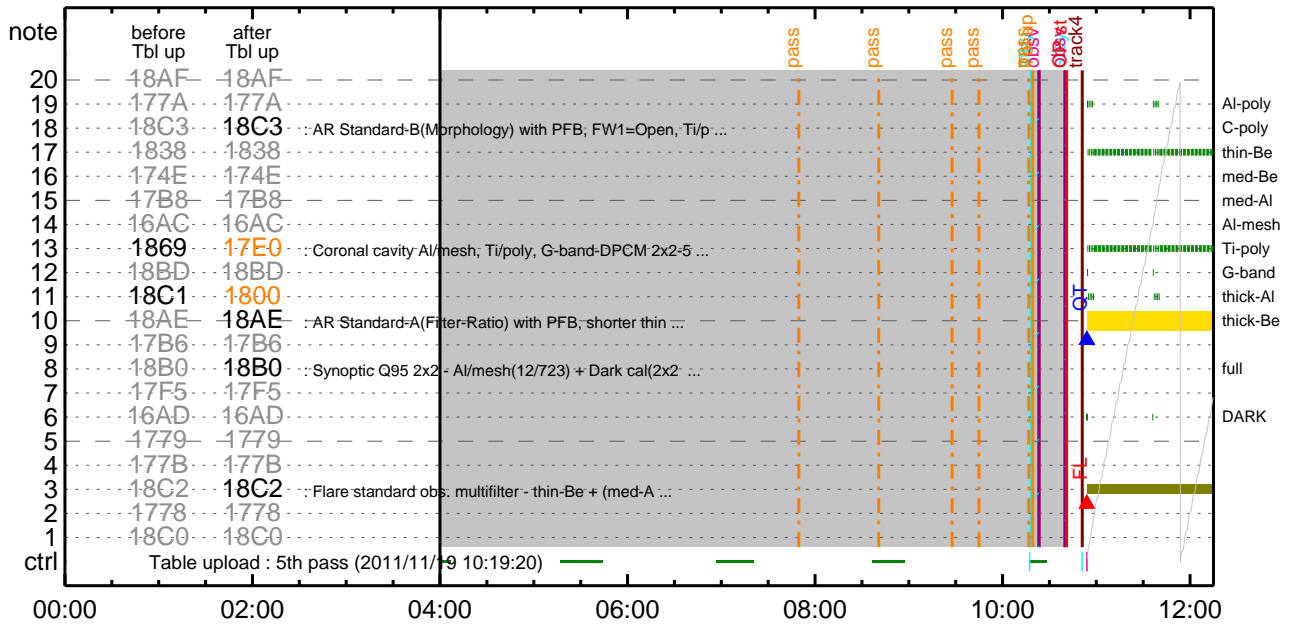
**Flare Detection**

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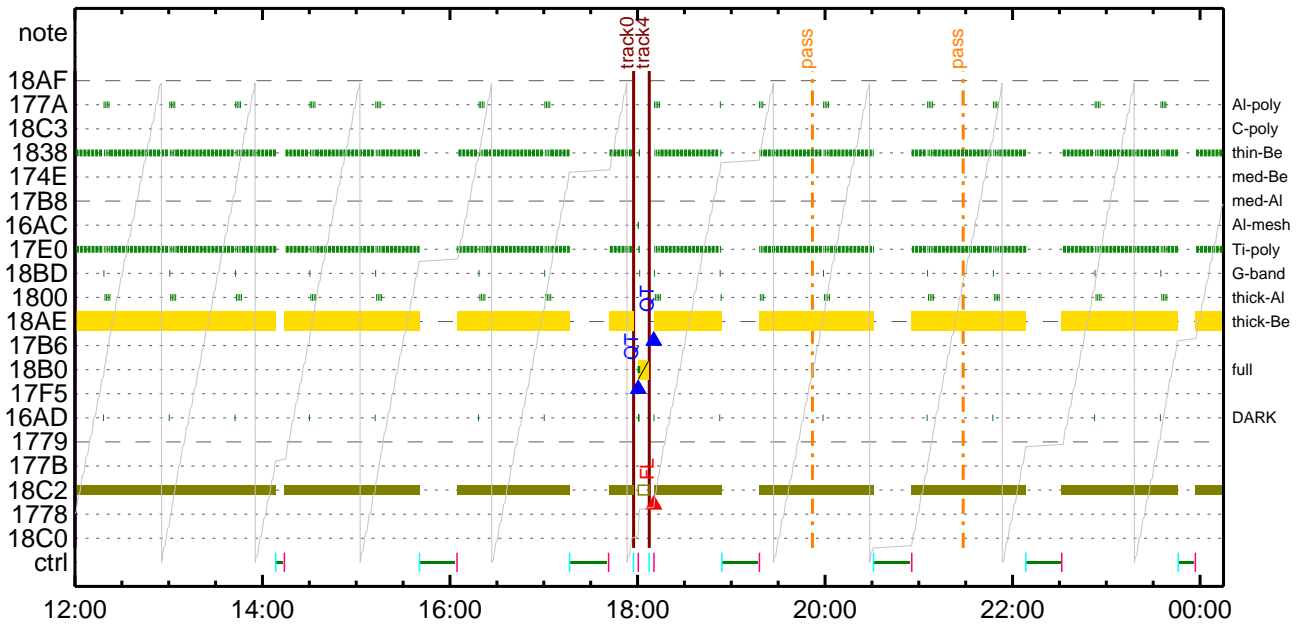
**FLD Patrol**

Term	Pointing (x, y)	Comment									
11/19 18:10:16 - 11/20 06:29:46	Track ( -729.0, -296.2) @ 11/19 18:07:30	# Cont.									
11/20 06:42:56 - 11/20 18:17:16	Track ( 4.2, 204.2) @ 11/20 06:40:30	# AR studies (recurrent old AR remnant, EIS request).									
11/20 18:29:46 - 11/21 06:01:46	Track ( -575.1, -301.2) @ 11/20 18:27:00	# Cont.									
11/21 06:19:16 - 11/21 18:00:16	Track ( -488.8, -302.8) @ 11/21 06:12:30	# Cont.									
11/21 18:12:46 - 11/22 06:06:16	Track ( -394.7, -303.9) @ 11/21 18:10:00	# Cont.									
11/22 06:18:46 - 11/24 09:56:00	Fixed ( 787.0, 510.0)	# Cont.									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

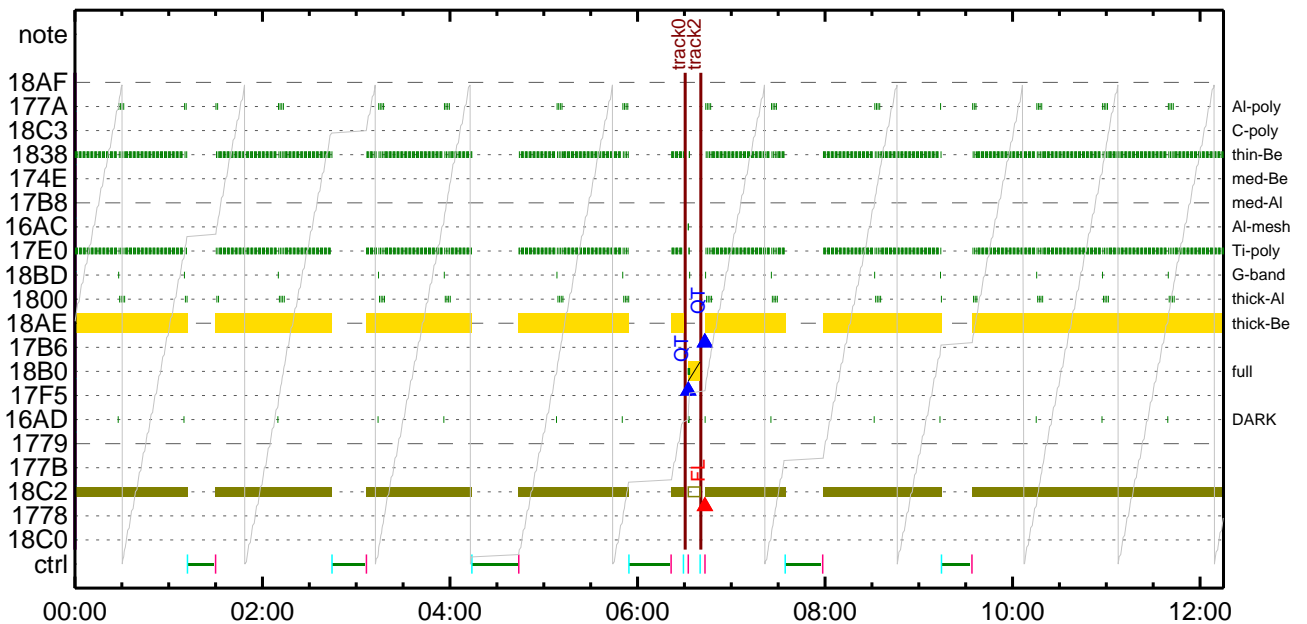
### CMDI #0275 2011/11/19



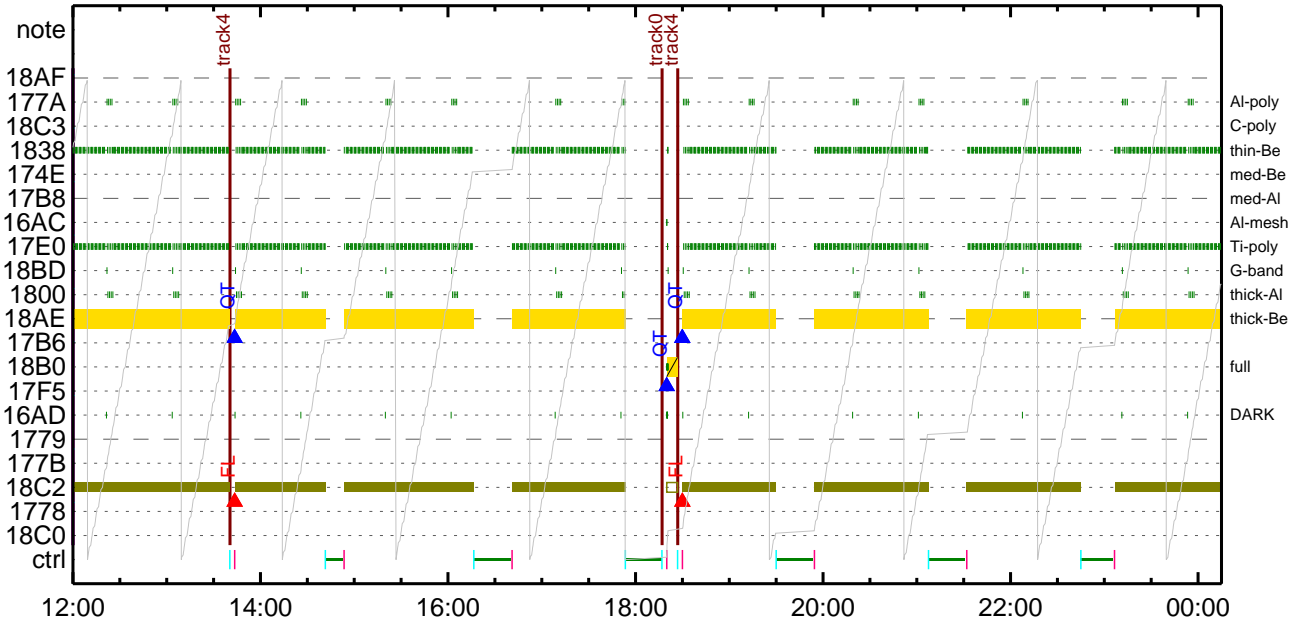
### CMDI #0275 2011/11/19



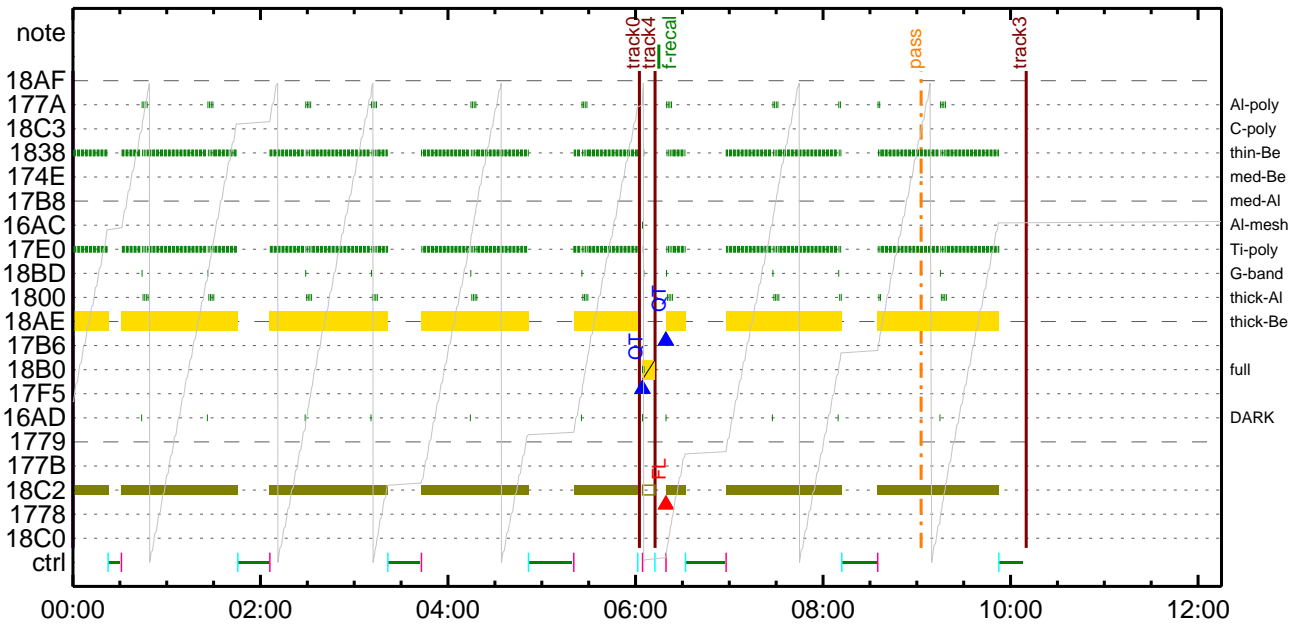
### CMDI #0275 2011/11/20



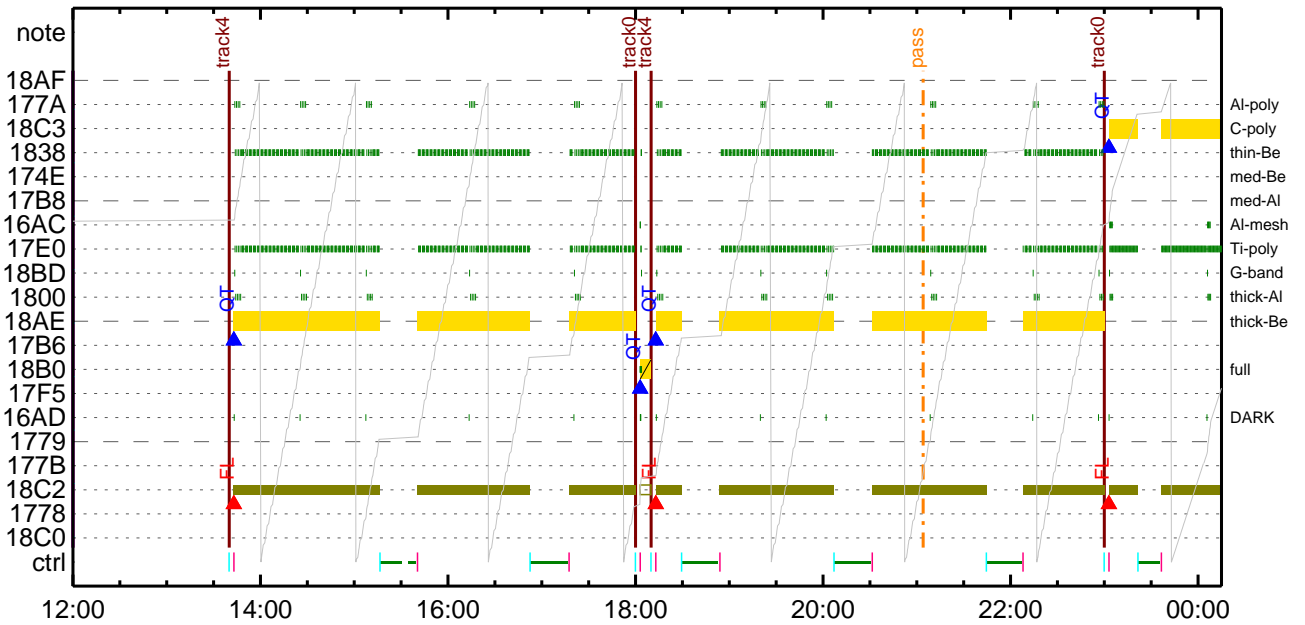
CMDI #0275 2011/11/20



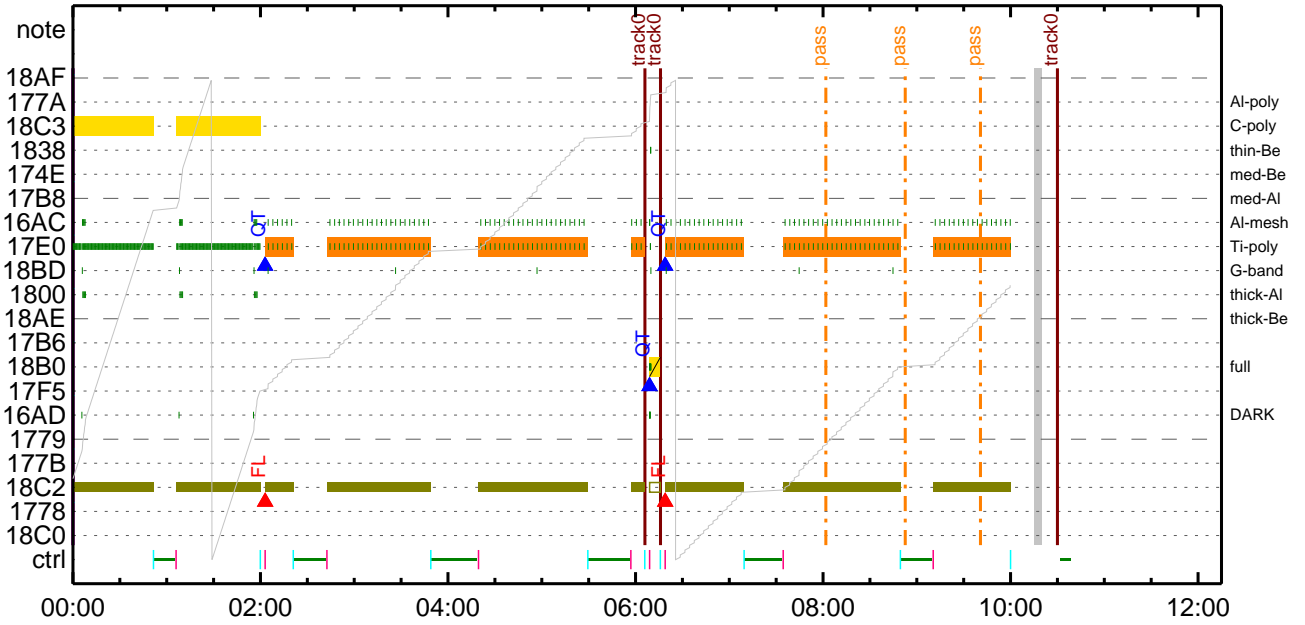
CMDI #0275 2011/11/21



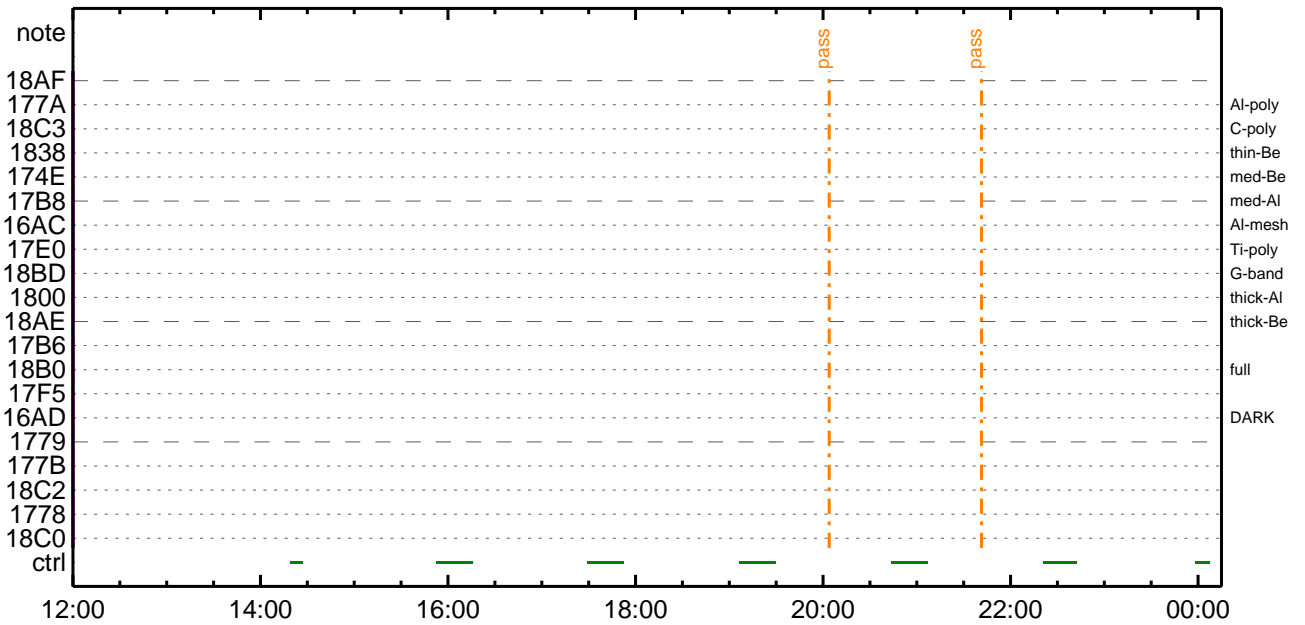
CMDI #0275 2011/11/21



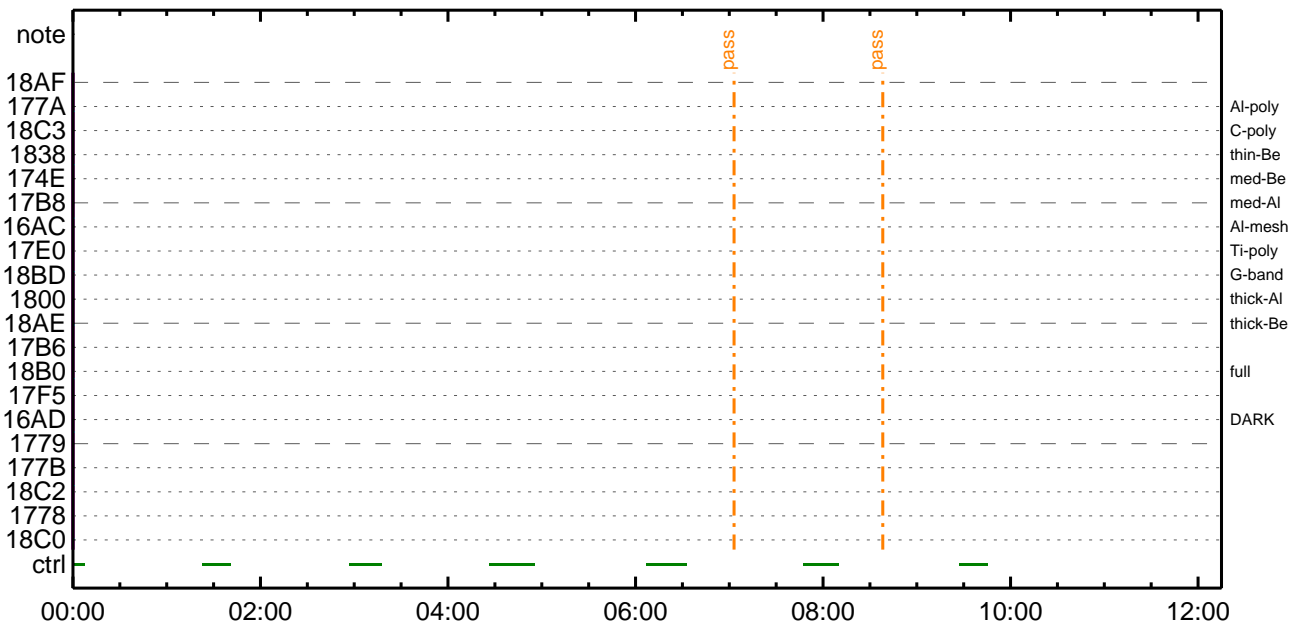
CMDI #0275 2011/11/22



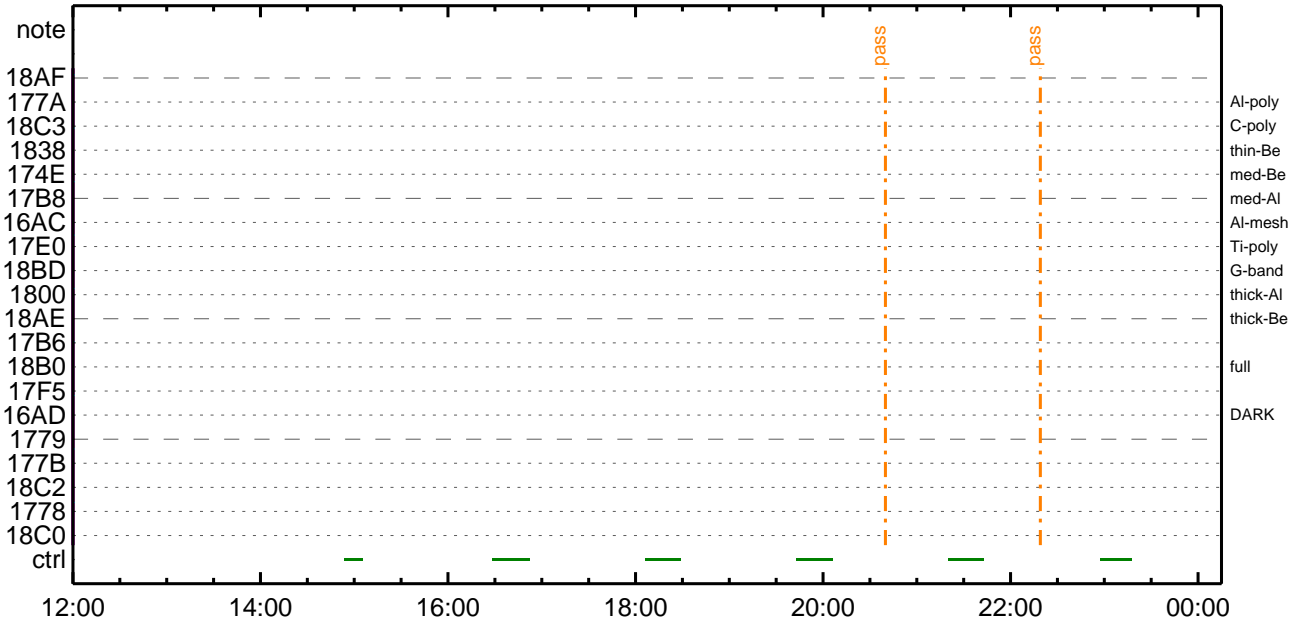
CMDI #0275 2011/11/22



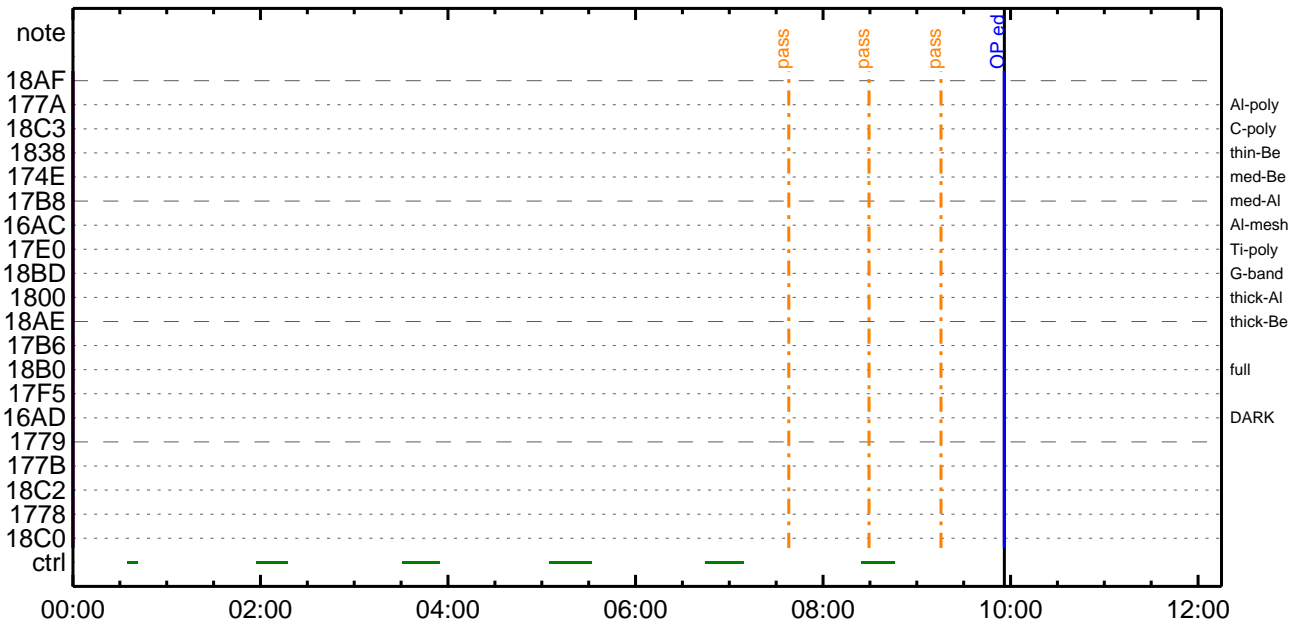
CMDI #0275 2011/11/23



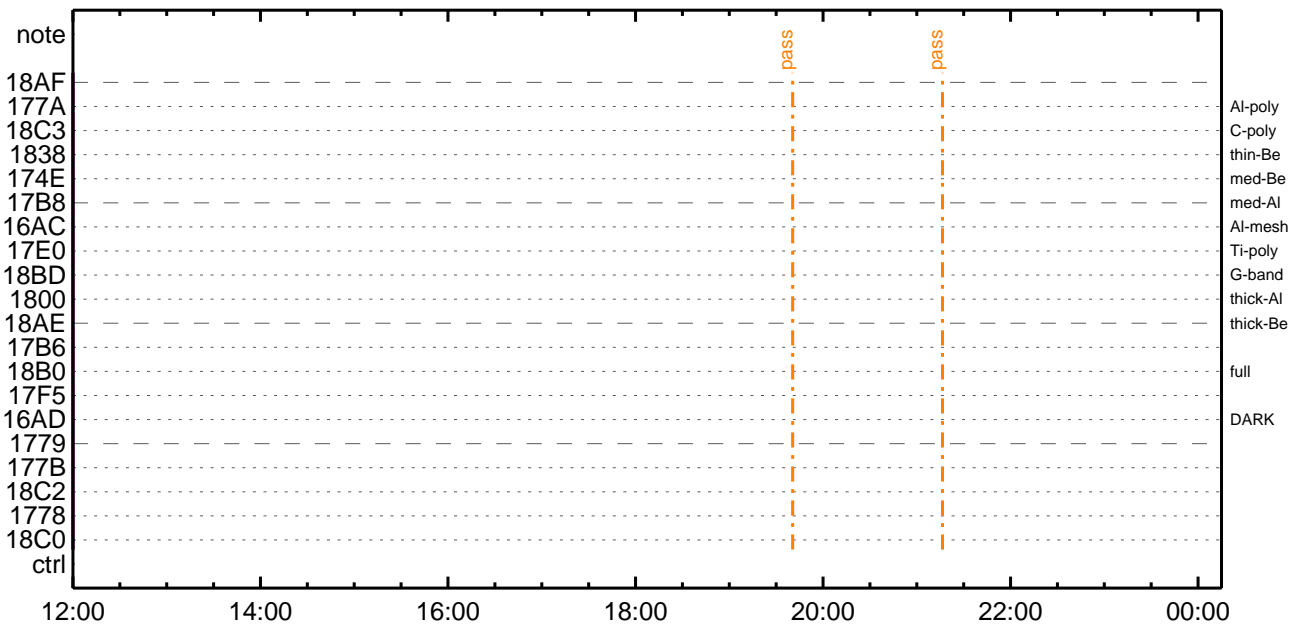
### CMDI #0275 2011/11/23



### CMDI #0275 2011/11/24



### CMDI #0275 2011/11/24





0096 C.  
0097 C.  
0098 C.  
0099 . C. \*\*\*\*\* MDP 'ûÃîñî»ò¼ŸñÊÃĐñ¹ñèDCBC•x²è \*\*\*\*\*  
0100 C. (¼á°îŸÓŸÃŸÈŸŒŸĚŸáŸçŸèñÊ½¼ñ¼Á»Űñ¹ñè)  
0101 . S. DC-BC dcbc-402:DCBC  
0102 (MDP\_known\_event)  
0103 C.  
0104 C.  
0105 . C. \*\*\*\*\* ŸĐŸ¹•Ī Daily±;ĪññÊ´Øñ¹ñèDCBC•x²è \*\*\*\*\*  
0106 . S. DC-BC dcbc-153:DCBC  
0107 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0108 C.  
0109 C.  
0110 . C. ;ãLOSŸÁŸŞŸÃŸ´¼Á»Ű;ã  
0111 C.  
0112 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0113 C.





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0096 C.          0300; SET EDUMP I±°iYNY¹aÇ¹Ôa|³³E; E
0097 C.
0098 C. TIY³YBYÖEððÄDİđ (UT)
0099 +. TI 2011-11-19 10:36:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0102 C.
0103 +. TI 2011-11-19 10:36:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0106 C.
0107 +. TI 2011-11-19 10:36:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0110 C.
0111 +. TI 2011-11-19 10:40:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0114 C.
0115 C. °E²¼aİÄè%îÍÑaİYÁY$YÁY-¹àİÜ
0116 C.          çç[HK1_TI_CMD_ENA/DIS]        EQ      ENA
0117 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0118 C.          çç[HK1_NEXT_EXEC_PIM]        EQ      DHU
0119 C.          çç[HK1_NEXT_EXEC_DC]         EQ      0xB3
0120 C.
0121 C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          çç[HK1_DMP_TOP_ADRS_1]       EQ      07
0129 C.          çç[HK1_DMP_TOP_ADRS_0]       EQ      2B
0130 C.          çç[HK1_DMP_BLOCK_NUM]        EQ      3
0131 C.          çç[HK1_DMP_REPEAT_NUM]       EQ      0
0132 C.          çç[HK1_DMA_DMP_PIM]         EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          çç[HK1_PKT_FORM_NO]          EQ      7
0136 C.          çç[HK1_PKT_GEN_TIME]         EQ      0.25 s
0137 C.          çç[HK1_S_TLM_BIT_RATE]       EQ      32k
0138 C.          çç[HK1_X_TLM_BIT_RATE]      EQ      4M
0139 C.          çç[HK1_DMP_CHK_FLG]         EQ      EXEC
0140 C.
0141 C. YÄYÖY×¼ªİ»ðð³İÇ$
0142 C.          çç[HK1_DMP_CHK_FLG]         EQ      NON
0143 C.
0144 C. RAM ID=TI_TBLaİ%È¹Ç•è²İOKðð³İÇ$
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y¼, Yì;¼YÈ;Èððİáa¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          çç[HK1_PKT_FORM_NO]          EQ      2
0150 C.          çç[HK1_PKT_GEN_TIME]         EQ      0.5S
0151 C.          çç[HK1_S_TLM_BIT_RATE]       EQ      32K
0152 C.          çç[HK1_X_TLM_BIT_RATE]      EQ      4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2011-11-19 10:40:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2011-11-19 10:40:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2011-11-19 10:40:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 C.          [ ] [HK1_TI_CMD_NUM]         EQ      2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2011-11-19 10:40:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 C.          [ ] [HK1_TI_CMD_NUM]         EQ      1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP `üÄİaİ»ö¼YðÈÄð¹aèDCBC•×²è *****
0192 C. (%â°İYÖYÄYÈYB¥ÈYáYçYèaÈ%¼a¼Ä»Üa¹aé)
0193 C. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```
0096 C.
0097 C.
0098 C.
0099 . C. ***** MDP `úÃîñî»ò¼ÿñËÃĐñ¹ñèDCBC•x²è *****
0100 C. (¼á°îÿÓÿÃÿÈÿËÿËÿáÿçÿèñË¼ñ¼Ã»Ûñ¹ñè)
0101 . S. DC-BC dcbc-402:DCBC
0102 (MDP_known_event)
0103 C.
0104 C.
0105 . C. ***** ÿĐÿ¹•Ï Daily±;îÑñË´øñ¹ñèDCBC•x²è *****
0106 . S. DC-BC dcbc-153:DCBC
0107 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0108 C.
0109 C.
0110 . C. ;ãLOSÿÁÿÿÿÿÿÿ¼Ã»Û;ã
0111 C.
0112 . C. ***** LOS *****
0113 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-473 2011-11-19 13:41:11 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;äAOSYÄY$YÄY~¼Ä»Û;ä
0005 C.
0006 C. YÄYB;¼Y³YBYÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ëø¿ðÄø•μ°Ë»Í×ÁÇøÍYçYÄY×Yí;¼YÉ;ËÈè¼μ•íÉ;ËøÈ¼°ÇÖø•ø¿¼í¹çøÍ;çÄ®, ùø¹øÈøðøÇÄ+¿®ø•øËøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 11s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2011-11-19 10:40:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ûÄîøî»ó¼YøÈÄðø¹øÈDCBC•x²è *****
0069 C. (¼á°îYÖYÄYÈYðYÈYáYçYÈøÈ¼ø¼Ä»Ûø¹øÈ)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YDY¹.Ï Daily¿;îÑøÈ`øø¹øÈDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;äLOSŸÄY$YÄY~¼Ä»Û;ä
0080 C.
0081 . C. ***** LOS *****
0082 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-474 2011-11-19 13:41:11 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αÁβ•μ°È»Í×ÁÇαíYçYÁY×Yí;¼YÉ;ÈÈè%μ•ííÉ;ÈβÈ¼°ÇÓα•α¿¼í¹çαÍ;çÁ®, ùα¹αèαβαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 90 70 0c 0c)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 90 90 08 08)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0f 80 80 06 06)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 10 80 80 08 08)
0060 + DC 07-F0 MDP_XRT_FLD_ENA
0061 BC (d8)
0062 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0063 BC (c8)
0064 + DC 07-F0 MDP_XRT_AEC_RESET
0065 BC (d0)
0066 + DC 07-F0 MDP_XRT_ARS_DIS
0067 BC (d5)
0068 + DC 07-F0 MDP_XRT_FLD_RESET
0069 BC (da)
0070 . C. ----- Success Verify ? OK / NG ____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. TI 2011-11-19 10:40:02.0
0078 DC 07-F0 MDP_XRT_MODE_OBSV
0079 BC (c2)
0080 . C. ----- Success Verify ? OK / NG ____
0081 C.
0082 C. ***** XRT END *****
0083 . C. *****
0084 C. SOT table upload
0085 C. *****
0086 . C. < Stop FG table >
0087 +. DC 07-F0 MDP_FG_CTRL_MANU
0088 BC (51)
0089 . C. -----
0090 C. MDP_FG_CTRL_MODE = MANU [ ]
0091 C. -----
0092 C.
0093 . C. <Upload FG Observation Table>
0094 . S. RAM ram-268:MDP_OBS_F
0095 ( )
```





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\*\*\* OP Sequence for XRT \*\*\*

2011/11/19	10:50:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/19	10:51:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/11/19	10:53:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/11/19	10:53:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/11/19	10:53:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/11/19	10:53:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/11/19	10:53:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/19	10:53:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/19	10:53:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2011/11/19	10:53:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/11/19	10:54:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/19	14:08:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/19	14:08:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/19	14:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/19	14:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/19	14:13:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/19	14:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/19	15:40:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/19	15:40:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/19	15:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/19	15:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/19	16:03:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/19	16:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/19	17:16:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/19	17:16:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/19	17:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/19	17:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/19	17:40:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/19	17:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/19	17:57:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/19	17:57:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/11/19	17:57:30.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/11/19	17:57:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/11/19	17:57:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/11/19	17:57:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/19	18:00:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2011/11/19	18:00:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/19	18:07:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/19	18:07:30.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/11/19	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/11/19	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/11/19	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/11/19	18:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/11/19	18:10:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/19	18:10:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/19	18:10:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2011/11/19	18:10:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				

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2011/11/19	18:10:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/19	18:54:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/19	18:54:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/19	18:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/19	18:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/19	19:17:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/19	19:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/19	20:31:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/19	20:31:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/19	20:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/19	20:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/19	20:54:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/19	20:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/19	22:08:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/19	22:08:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/19	22:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/19	22:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/19	22:30:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/19	22:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/19	23:46:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/19	23:46:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/19	23:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/19	23:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/19	23:56:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/19	23:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/20	01:12:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/20	01:12:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/20	01:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/20	01:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/20	01:29:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/20	01:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/20	02:44:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/20	02:44:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/20	02:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/20	02:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/20	03:05:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/20	03:06:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/20	04:14:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/20	04:14:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/20	04:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/20	04:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/20	04:43:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/20	04:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/20	05:54:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/20	05:54:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/20	05:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/20	05:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/20	06:20:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/20	06:21:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/20	06:29:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/20	06:29:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			

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2011/11/20	06:29:46.0	XRT_FLD_DIS_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		MDP_XRT_FLD_DIS		1	07-F0		d9		
2011/11/20	06:29:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9		
2011/11/20	06:29:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2011/11/20	06:30:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00
2011/11/20	06:32:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0		c4		08
2011/11/20	06:32:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2011/11/20	06:40:04.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2011/11/20	06:40:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02	00	00	00
2011/11/20	06:42:36.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/11/20	06:42:56.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0		d8		
2011/11/20	06:42:58.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2011/11/20	06:43:00.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0		d0		
2011/11/20	06:43:02.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2011/11/20	06:43:04.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da		
2011/11/20	06:43:06.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0		c4		0a
2011/11/20	06:43:08.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0		c5		03
2011/11/20	06:43:10.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2011/11/20	07:34:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2011/11/20	07:34:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da		
2011/11/20	07:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2011/11/20	07:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2011/11/20	07:57:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/20	07:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2011/11/20	09:14:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2011/11/20	09:14:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da		
2011/11/20	09:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2011/11/20	09:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2011/11/20	09:33:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/20	09:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2011/11/20	13:40:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2011/11/20	13:40:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04	00	00	00
2011/11/20	13:42:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2011/11/20	13:43:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0		d8		
2011/11/20	13:43:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2011/11/20	13:43:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0		d0		
2011/11/20	13:43:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2011/11/20	13:43:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da		
2011/11/20	13:43:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0		c4		0a
2011/11/20	13:43:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0		c5		03
2011/11/20	13:43:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2011/11/20	14:41:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2011/11/20	14:41:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0		da		
2011/11/20	14:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2011/11/20	14:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2011/11/20	14:52:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/20	14:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2011/11/20	16:16:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2011/11/20	16:16:32.0	XRT_FLD_RESET_412_OG [0x19c]							

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2011/11/20	16:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/11/20	16:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/20	16:40:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/20	16:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/20	17:53:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/20	17:53:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/20	17:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/11/20	17:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/20	18:16:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/20	18:16:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2011/11/20	18:17:00.0	AOCS_Or-e-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2011/11/20	18:17:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2011/11/20	18:17:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2011/11/20	18:17:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/11/20	18:19:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2011/11/20	18:20:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/20	18:26:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/20	18:27:00.0	AOCS_Or-e-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00			
2011/11/20	18:29:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/11/20	18:29:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/11/20	18:29:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/11/20	18:29:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/11/20	18:29:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/11/20	18:29:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/20	18:29:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
2011/11/20	18:29:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2011/11/20	18:30:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/20	19:30:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/20	19:30:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/20	19:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/11/20	19:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/20	19:53:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/20	19:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/20	21:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/20	21:07:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/20	21:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/11/20	21:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/20	21:31:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/20	21:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/20	22:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/20	22:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/20	22:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/11/20	22:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/11/20	23:05:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/20	23:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/11/21	00:22:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/11/21	00:22:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2011/11/21	00:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			

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2011/11/21	00:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/21	00:30:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/21	00:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	01:45:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	01:45:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	01:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/21	01:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/21	02:05:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/21	02:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	03:21:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	03:21:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	03:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/21	03:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/21	03:42:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/21	03:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	04:51:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	04:51:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	04:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/21	04:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/21	05:19:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/21	05:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	06:01:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	06:01:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/11/21	06:01:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/11/21	06:01:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/11/21	06:01:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/21	06:02:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/11/21	06:04:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2011/11/21	06:04:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	06:12:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	06:12:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/11/21	06:14:56.0	XRT_FOCUS_RECALIBRATE_428_OG [0x1ac]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2011/11/21	06:18:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/11/21	06:19:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/11/21	06:19:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/11/21	06:19:20.5	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/11/21	06:19:22.5	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/21	06:19:24.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	06:19:26.5	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2011/11/21	06:19:28.5	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/11/21	06:19:30.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	06:32:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	06:32:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	06:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/21	06:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/21	06:57:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/21	06:58:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	08:12:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	08:12:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2011/11/21	08:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/21	08:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/21	08:34:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/21	08:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	09:52:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	09:52:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	09:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/21	09:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/21	10:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/11/21	13:39:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	13:40:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/11/21	13:42:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/11/21	13:42:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/11/21	13:42:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/11/21	13:42:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/11/21	13:42:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/21	13:42:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	13:42:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2011/11/21	13:42:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/11/21	13:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	15:16:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	15:16:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	15:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/21	15:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/21	15:39:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/21	15:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	16:52:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	16:52:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	16:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/21	16:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/21	17:16:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/21	17:17:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/11/21	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/11/21	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/11/21	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/11/21	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/21	18:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2011/11/21	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	18:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/11/21	18:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/11/21	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/11/21	18:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/11/21	18:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/11/21	18:12:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/21	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2011/11/21	18:12:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2011/11/21	18:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2011/11/21	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	18:29:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	18:29:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	18:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/21	18:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/21	18:53:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/21	18:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	20:07:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	20:07:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	20:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/21	20:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/21	20:30:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/21	20:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	21:44:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	21:44:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	21:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/21	21:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/21	22:07:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/21	22:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	22:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	23:00:00.0	AOCs_OrE-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	d2 a8 ba 0d			
2011/11/21	23:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2011/11/21	23:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/11/21	23:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/11/21	23:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/11/21	23:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/21	23:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	23:02:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2011/11/21	23:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2011/11/21	23:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/21	23:21:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/21	23:21:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/21	23:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/21	23:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/21	23:35:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/21	23:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/22	00:51:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/22	00:51:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/22	00:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/22	00:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/22	01:05:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/22	01:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/22	01:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/22	02:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2011/11/22	02:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/11/22	02:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/11/22	02:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				

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2011/11/22	02:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/11/22	02:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/11/22	02:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d	
2011/11/22	02:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03	
2011/11/22	02:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/11/22	02:21:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/11/22	02:21:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/11/22	02:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/11/22	02:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/11/22	02:41:30.0	XRT_Custom_418_OG [0x1a2]						
2011/11/22	02:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/11/22	03:49:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/11/22	03:49:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/11/22	03:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/11/22	03:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/11/22	04:18:30.0	XRT_Custom_418_OG [0x1a2]						
2011/11/22	04:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/11/22	05:29:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/11/22	05:29:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/11/22	05:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/11/22	05:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/11/22	05:56:00.0	XRT_Custom_418_OG [0x1a2]						
2011/11/22	05:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/11/22	06:05:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/11/22	06:05:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2011/11/22	06:06:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00	
2011/11/22	06:06:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2011/11/22	06:06:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2011/11/22	06:06:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/11/22	06:08:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08	
2011/11/22	06:09:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/11/22	06:15:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/11/22	06:16:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	d2 a8 ba 0d	
2011/11/22	06:18:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2011/11/22	06:18:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/11/22	06:18:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/11/22	06:18:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/11/22	06:18:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/11/22	06:18:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/11/22	06:18:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d	
2011/11/22	06:18:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03	
2011/11/22	06:19:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/11/22	07:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/11/22	07:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/11/22	07:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/11/22	07:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/11/22	07:33:30.0	XRT_Custom_418_OG [0x1a2]						
2011/11/22	07:34:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/11/22	08:49:30.0	XRT_CTRL_MANU_408_OG [0x198]						



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2011/11/22	08:49:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/22	08:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/22	08:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/22	09:09:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/22	09:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/22	10:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/22	10:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00