

# XRT Timeline to be uploaded on 2011/11/22

Period: 2011/11/22 10:20:00 - 2011/11/26 10:05:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

<b>XOB #17BF: Full-disk observation(1x1) Thick_AI(long)-AI/mesh(short,long)-Ti/poly(short,long)-dark-1_loop</b>												
Term	Pointing (x, y)				Comment							
11/22 11:15:00 - 11/22 14:19:00	Fixed ( 0.0, 0.0)				# 3/15							
<b>PROG= 06 1-time(s)</b>												
┌ <b>Subr= 1 1-time(s) 2700.0sec</b>												
├ <b>Seqn= 52 1-time(s) 2.0sec</b>												
│ Open/thick-AI Open/thick-AI close Safe Norm 22.6s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├ <b>Seqn= 84 1-time(s) 2.0sec</b>												
│ Open/AI-mesh Open/AI-mesh close Safe Norm 125ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ Open/AI-mesh Open/AI-mesh close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├ <b>Seqn= 68 1-time(s) 2.0sec</b>												
│ Open/Ti-poly Open/Ti-poly close Safe Norm 250ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ <b>Subr= 2 1-time(s) 2.0sec</b>												
├ <b>Seqn= 92 1-time(s) 2.0sec</b>												
│ Open/Ti-poly Open/Ti-poly close Safe Dark 1.00s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

<b>XOB #18AF: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick AI and AI/Poly context, 384x384 at 1064 1048 (all), 4min cad</b>												
Term	Pointing (x, y)				Comment							
11/22 14:33:00 - 11/22 17:29:00	Track ( -223.3, -304.7) <sup>Ⓜ 11/22 14:30:00</sup>				# Observe AR 11354.							
11/22 18:07:30 - 11/22 23:58:00	Track ( -192.0, -304.6) <sup>Ⓜ 11/22 18:04:30</sup>				# AR 11354.							
11/23 02:03:00 - 11/23 05:39:24	Track ( -122.1, -304.4) <sup>Ⓜ 11/23 02:00:00</sup>				# Return to AR 11354.							
11/23 05:52:30 - 11/23 17:41:54	Track ( -88.0, -304.3) <sup>Ⓜ 11/23 05:49:30</sup>				# AR 11354.							
11/23 17:55:00 - 11/23 23:59:54	Track ( 19.8, -303.4) <sup>Ⓜ 11/23 17:52:00</sup>				# AR 11354.							
11/24 02:20:36 - 11/24 05:59:54	Track ( 92.5, -302.6) <sup>Ⓜ 11/24 02:00:00</sup>				# Return to AR 11354.							
11/24 06:13:00 - 11/24 09:00:00	Track ( 129.6, -302.1) <sup>Ⓜ 11/24 06:10:00</sup>				# AR 11354.							
<b>PROG= 20 Inf.-time(s)</b>												
┌ <b>Subr= 1 1-time(s) 2.0sec</b>												
├ <b>Seqn= 19 1-time(s) 2.0sec</b>												
│ Open/Ti-poly Open/thick-AI close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
│ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
├ <b>Seqn= 96 4-time(s) 2.0sec</b>												
│ AI-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												
│ Open/thick-AI Open/thick-AI close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└ <b>Subr= 2 1-time(s) 2.0sec</b>												
├ <b>Seqn= 62 11-time(s) 240.0sec</b>												
│ thin-Be/Open Open/thick-AI close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec												
│ thin-Be/Open Open/thick-AI close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
│ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec												
│ thin-Be/Open Open/thick-AI close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec												
│ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec												
│ thin-Be/Open Open/thick-AI close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec												
│ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec												
└ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

<b>XOB #18B0: Synoptic Q95 2x2 - AI/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(24/1443) + Thin-Be(88/2)</b>												
Term	Pointing (x, y)				Comment							
11/22 17:57:30 - 11/22 18:04:24	Fixed ( 0.0, 0.0)				synoptic, shifted -5.5 min							
11/23 05:42:30 - 11/23 05:49:24	Fixed ( 0.0, 0.0)				synoptic, shifted -20.5 min							
11/23 17:45:00 - 11/23 17:51:54	Fixed ( 0.0, 0.0)				synoptic, shifted -18.0 min							
11/24 06:03:00 - 11/24 06:09:54	Fixed ( 0.0, 0.0)				synoptic							
<b>PROG= 08 1-time(s)</b>												
┌ <b>Subr= 1 1-time(s) 12.0sec</b>												
├ <b>Seqn= 46 1-time(s) 4.0sec</b>												
│ Open/AI-mesh Open/AI-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ Open/AI-mesh Open/AI-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├ <b>Seqn= 5 1-time(s) 2.0sec</b>												
│ Open/Ti-poly Open/thick-AI close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
│ Open/Ti-poly Open/thick-AI close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
│ Open/Ti-poly Open/thick-AI close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
│ Open/Ti-poly Open/thick-AI close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
│ Open/Ti-poly Open/thick-AI close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
├ <b>Seqn= 69 1-time(s) 4.0sec</b>												
│ Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├ <b>Seqn= 50 1-time(s) 2.0sec</b>												
│ thin-Be/Open thin-Be/Open close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ thin-Be/Open thin-Be/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												

Seqn= 4 1-time(s) 2.0sec  
Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec  
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #18C4: HOP128\_AR Standard-B(Morphology) with NO PFB, FW1=Open, Al/Mesh, 512x512 at 1064 1048, 30sec**

Term	Pointing (x, y)	Comment
11/23 00:10:36 - 11/23 01:59:54	Track ( 188.1, -453.0) @ 11/23 00:00:00	# HOP-128 (AR 11352).
11/24 00:03:00 - 11/24 01:57:30	Track ( 374.3, -449.1) @ 11/24 00:00:00	# HOP-128 (AR 11352).

PROG= 01 Inf.-time(s)

Subr= 2 1-time(s) 2.0sec
Seqn= 17 1-time(s) 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec

Subr= 1 1-time(s) 2.0sec
Seqn= 30 90-time(s) 30.0sec
Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

\* \* \* \* \* Flare mode \* \* \* \* \*

**XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops**

Term	Pointing (x, y)	Comment
11/22 14:33:00 - 11/22 17:29:00	Track ( -223.3, -304.7) @ 11/22 14:30:00	# Observe AR 11354.
11/22 18:07:30 - 11/22 23:58:00	Track ( -192.0, -304.6) @ 11/22 18:04:30	# AR 11354.
11/23 00:10:36 - 11/23 01:59:54	Track ( 188.1, -453.0) @ 11/23 00:00:00	# HOP-128 (AR 11352).
11/23 02:03:00 - 11/23 05:39:24	Track ( -122.1, -304.4) @ 11/23 02:00:00	# Return to AR 11354.
11/23 05:52:30 - 11/23 17:41:54	Track ( -88.0, -304.3) @ 11/23 05:49:30	# AR 11354.
11/23 17:55:00 - 11/23 23:59:54	Track ( 19.8, -303.4) @ 11/23 17:52:00	# AR 11354.
11/24 00:03:00 - 11/24 01:57:30	Track ( 374.3, -449.1) @ 11/24 00:00:00	# HOP-128 (AR 11352).
11/24 02:20:36 - 11/24 05:59:54	Track ( 92.5, -302.6) @ 11/24 02:00:00	# Return to AR 11354.
11/24 06:13:00 - 11/24 09:00:00	Track ( 129.6, -302.1) @ 11/24 06:10:00	# AR 11354.

PROG= 03 12-time(s)

Subr= 1 45-time(s) 10.0sec
Seqn= 20 1-time(s) 2.0sec
thin-Be/Open med-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 63 1-time(s) 2.0sec
med-Al/Open med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 77 1-time(s) 2.0sec
Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec

Subr= 2 1-time(s) 10.0sec
Seqn= 90 1-time(s) 2.0sec
Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

\* \* \* \* \* Active Region Search \* \* \* \* \*

NOT USED

\* \* \* \* \* Flare Detection \* \* \* \* \*

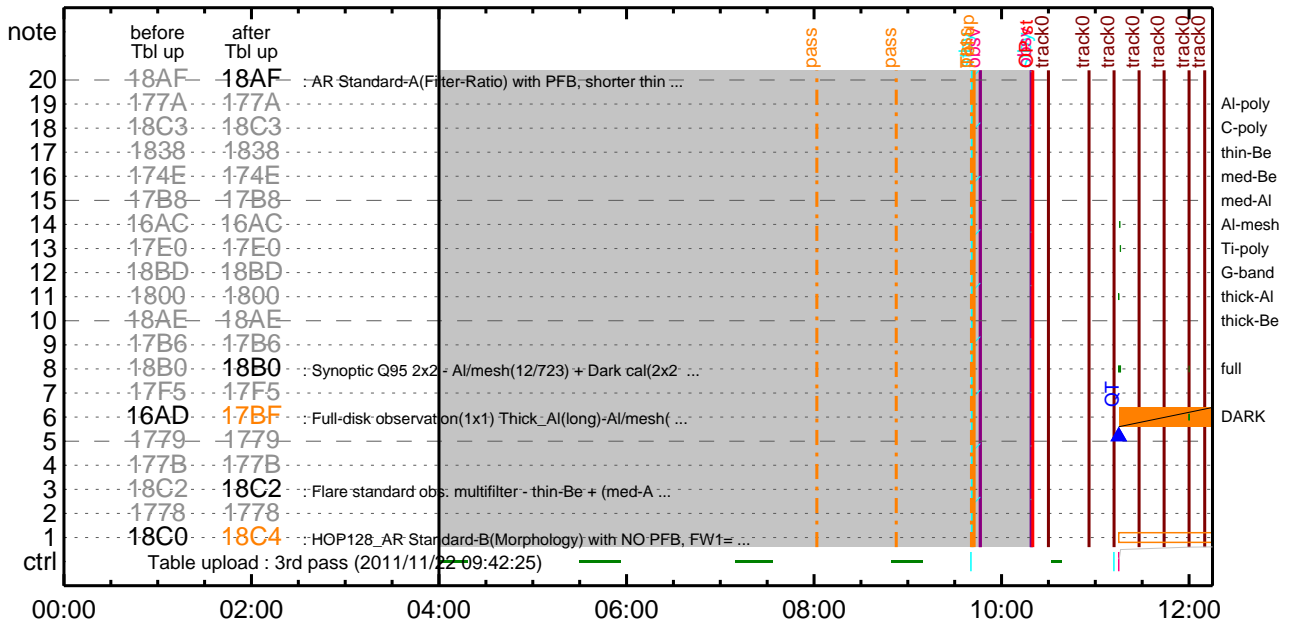
**FLD Patrol**

Term	Pointing (x, y)	Comment
11/22 14:32:46 - 11/22 17:54:46	Track ( -223.3, -304.7) @ 11/22 14:30:00	# Observe AR 11354.
11/22 18:07:16 - 11/23 05:39:46	Track ( -192.0, -304.6) @ 11/22 18:04:30	# AR 11354.
11/23 05:52:16 - 11/23 17:42:16	Track ( -88.0, -304.3) @ 11/23 05:49:30	# AR 11354.
11/23 17:54:46 - 11/24 06:00:16	Track ( 19.8, -303.4) @ 11/23 17:52:00	# AR 11354.
11/24 06:12:46 - 11/26 10:05:00	Track ( 129.6, -302.1) @ 11/24 06:10:00	# AR 11354.

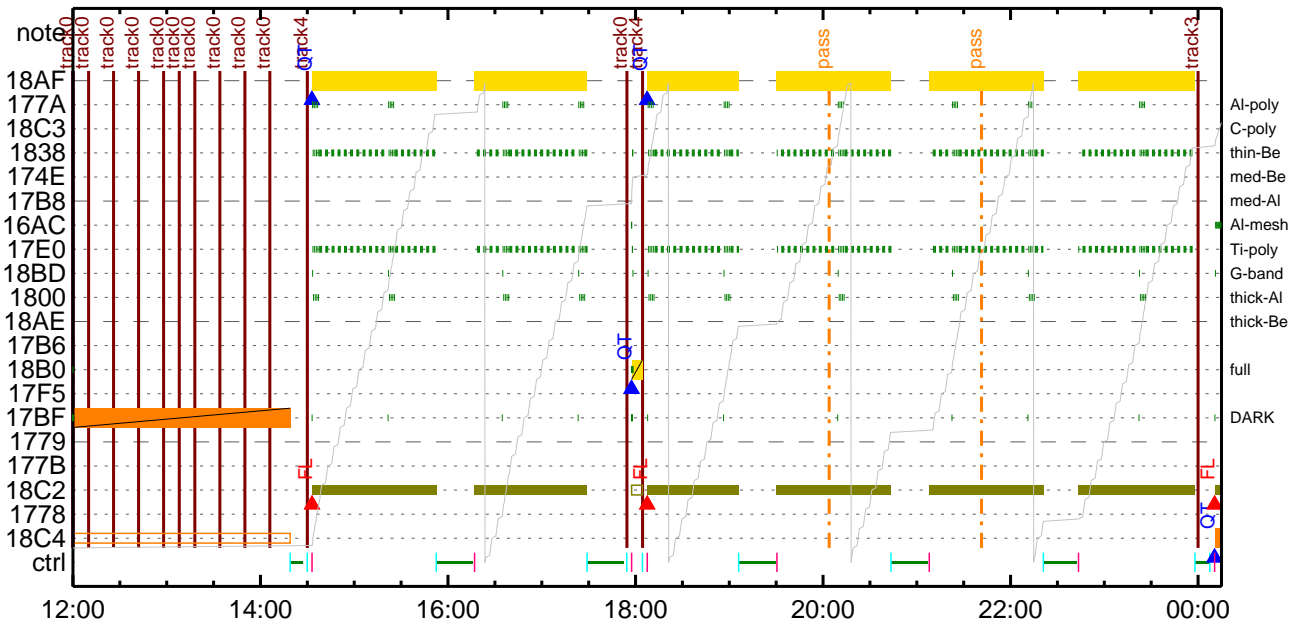
Open/Ti-poly Open/thick-Al close Safe Norm 8ms Obs 8x8 Q=50 30sec
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Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

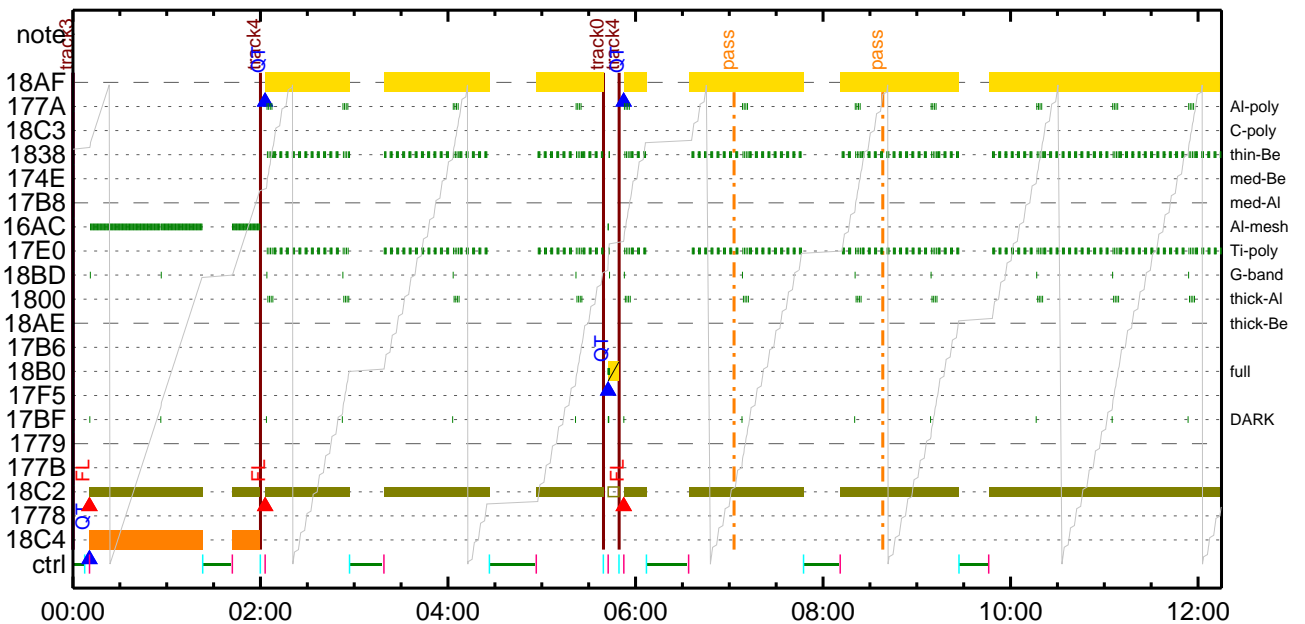
### CMDI #0279 2011/11/22



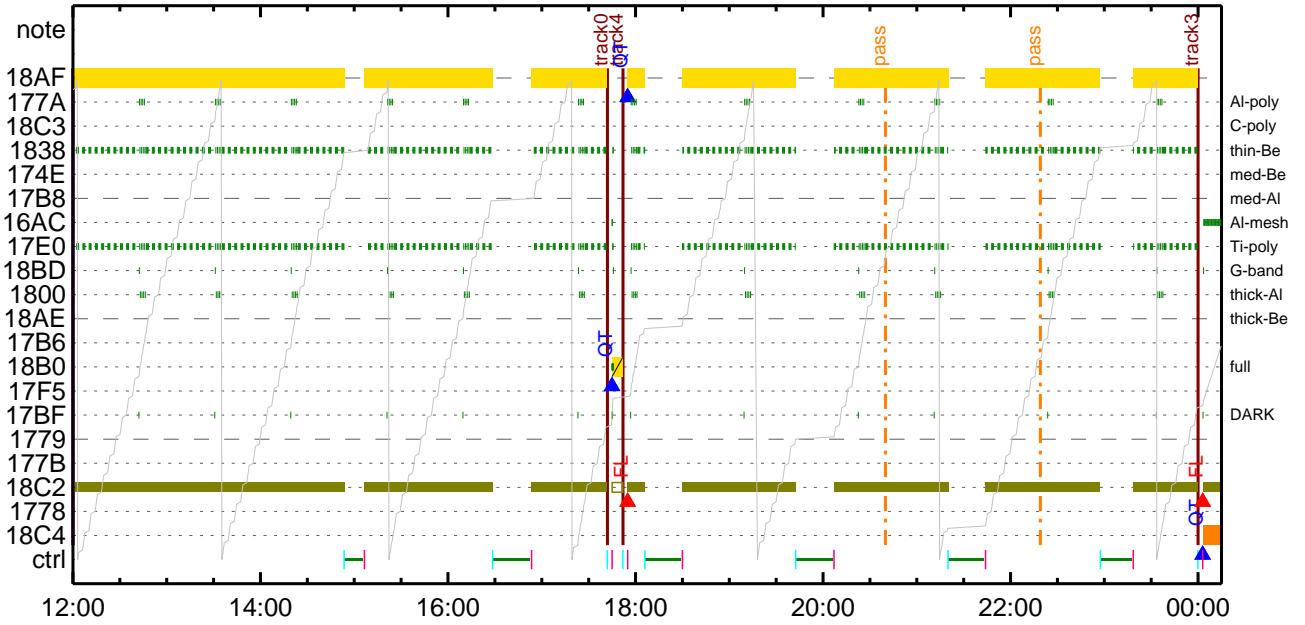
### CMDI #0279 2011/11/22



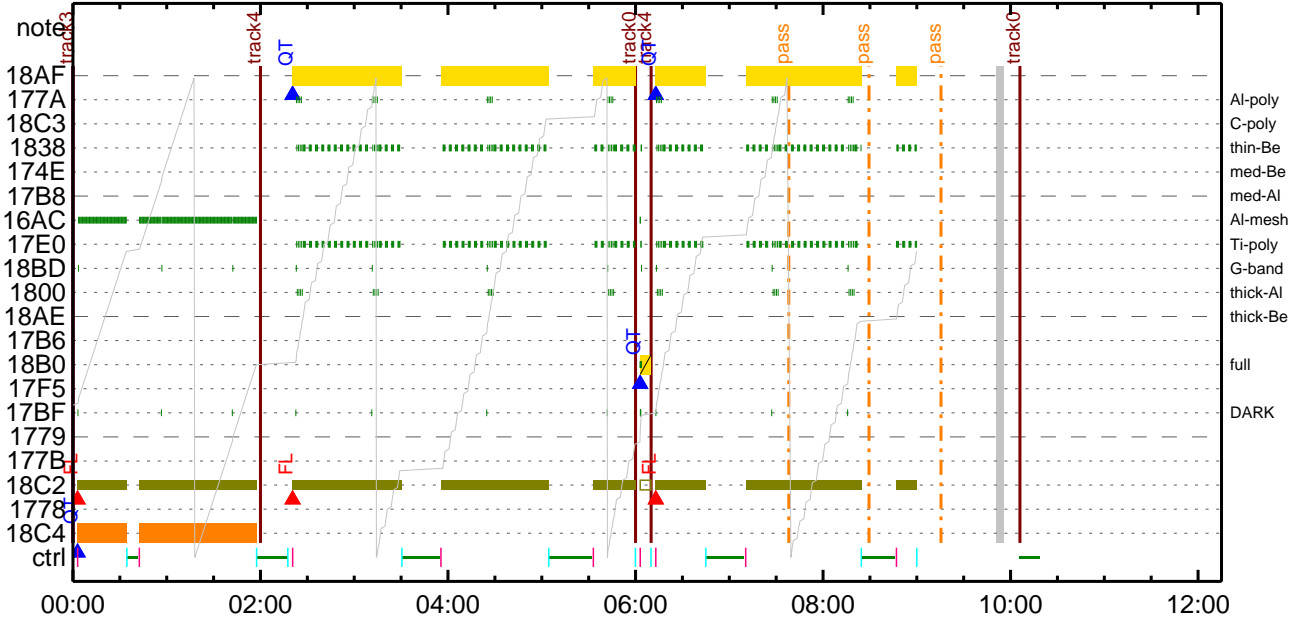
### CMDI #0279 2011/11/23



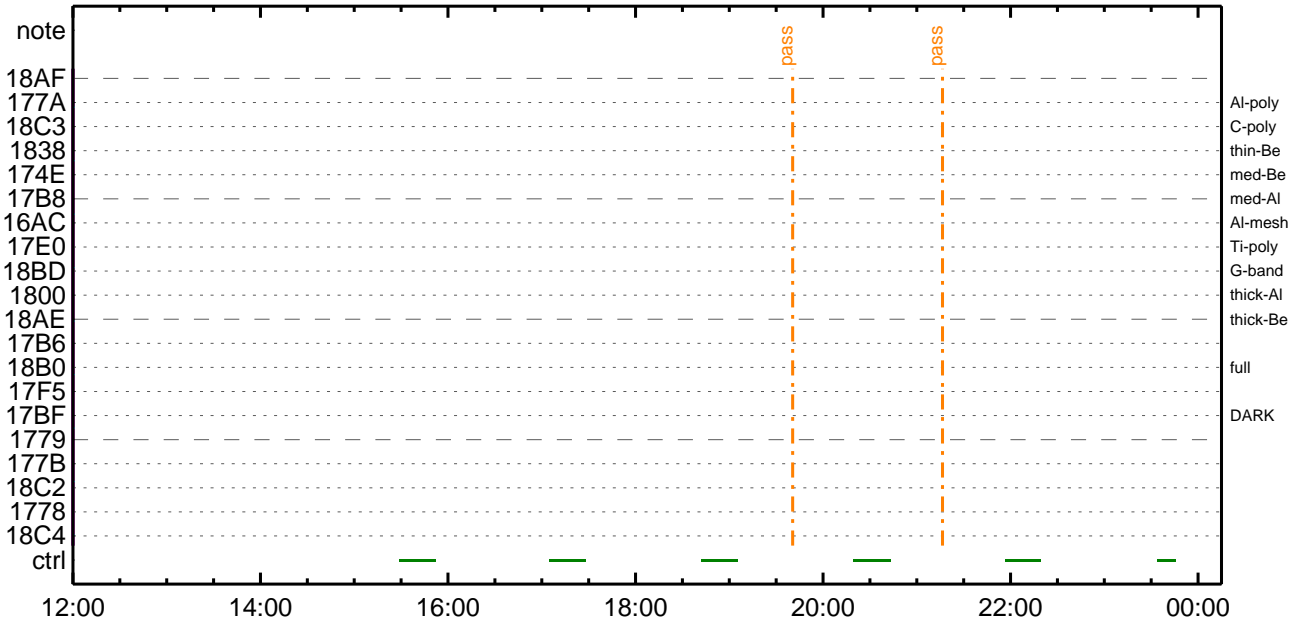
CMDI #0279 2011/11/23



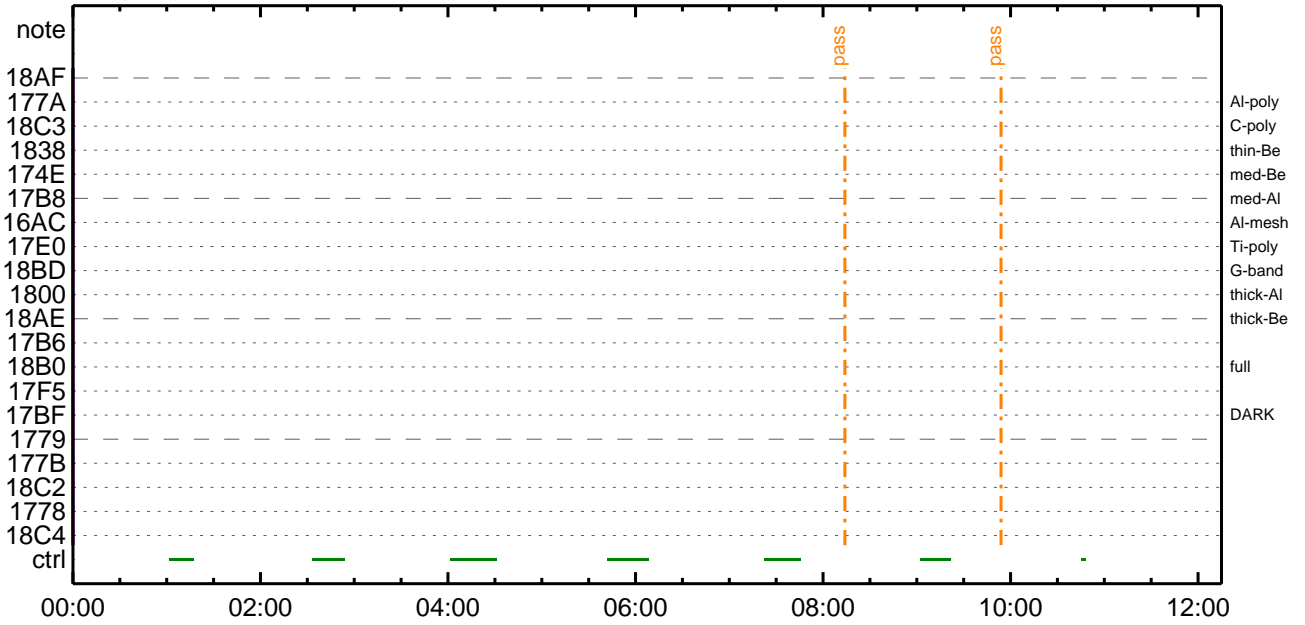
CMDI #0279 2011/11/24



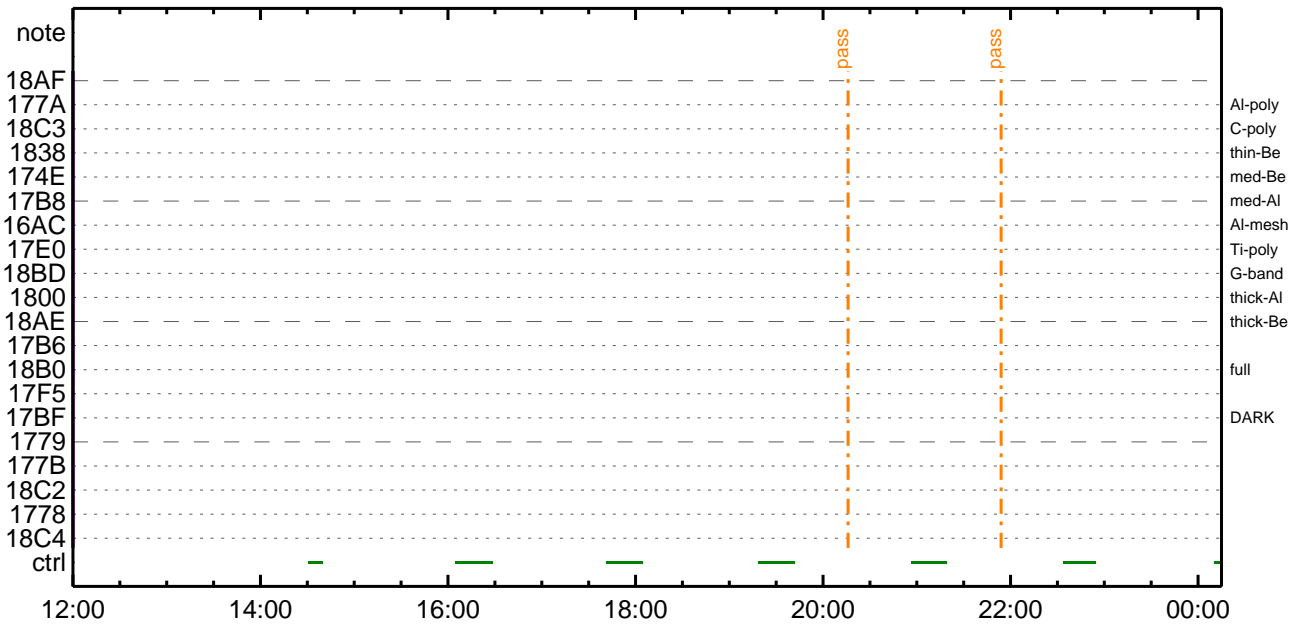
CMDI #0279 2011/11/24



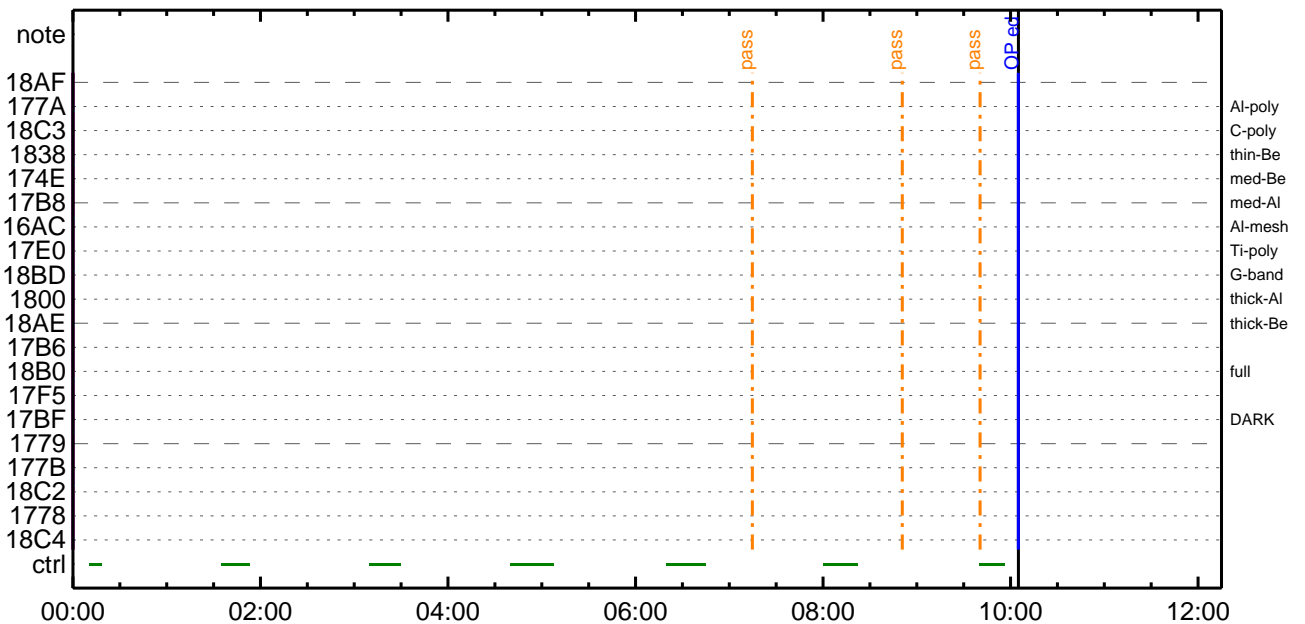
CMDI #0279 2011/11/25



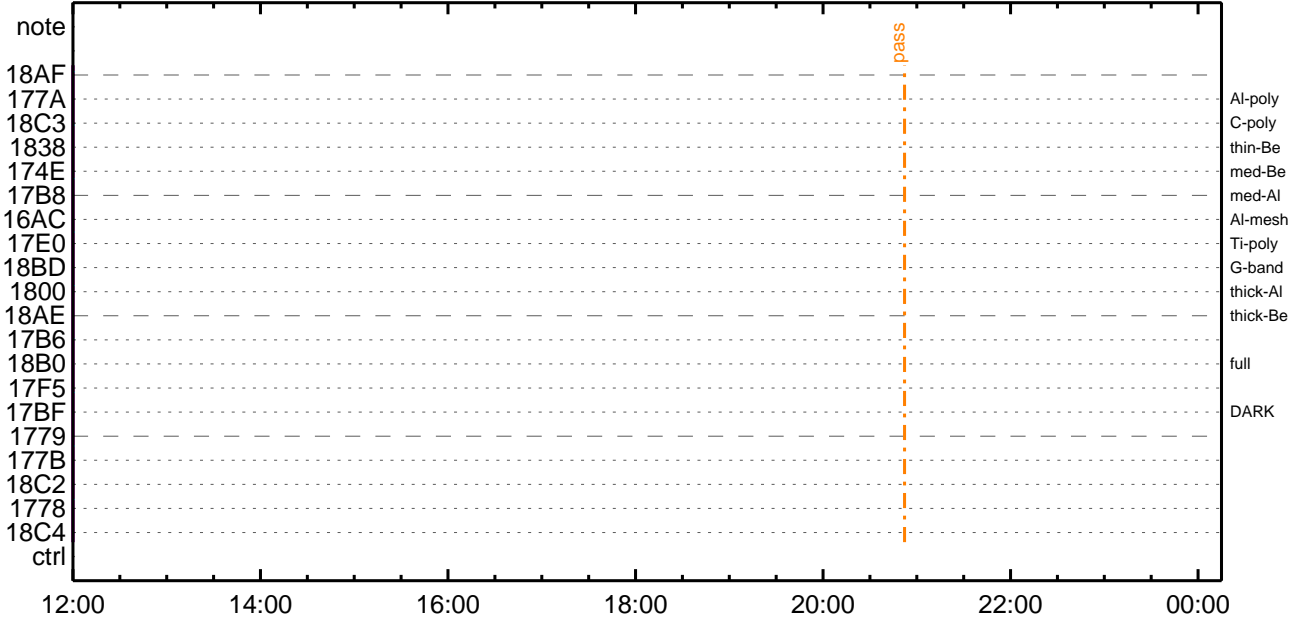
CMDI #0279 2011/11/25



CMDI #0279 2011/11/26



CMDI #0279 2011/11/26





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-476:OP
0104 ( )
0105 S. OG og-476:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼Ê¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼Ê¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼Ê¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °Ê²¼²î¼Ê¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;Ê¼Y½;Yî;4YE;Êòîâ¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î¼Ê¹ç•è²¼²îTI-CMDÁ÷ç²î¼Ê¹Ô²•²Ê²²²³²Ê;f
0180 C. ²²²ç;çSET²ÊDUMP²î¼Ê¹ç•è²¼²î²³²Ê;f
0181 C.
0182 C. TIY³Y²YôYÊ²òðÁîç(UT)
0183 +. TI 2011-11-22 10:15:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-11-22 10:15:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-11-22 10:15:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2011-11-22 10:19:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      ÷÷[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼º¡ĩÄë%ĩĩÑºĩŷÄŷ§ŷÄŷ-¹ãĩŰ
0200 C.      ÷÷[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.      ÷÷[HK1_TI_CMD_NUM]          EQ      4
0202 C.      ÷÷[HK1_NEXT_EXEC_PIM]      EQ      DHU
0203 C.      ÷÷[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIĩŰ°êŷÄŷØŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC     (03 ab 03 01 02)
0212 C.      ÷÷[HK1_DMP_TOP_ADRS_1]    EQ      07
0213 C.      ÷÷[HK1_DMP_TOP_ADRS_0]    EQ      2B
0214 C.      ÷÷[HK1_DMP_BLOCK_NUM]     EQ      3
0215 C.      ÷÷[HK1_DMP_REPEAT_NUM]    EQ      0
0216 C.      ÷÷[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC     (07 0b f8)
0219 C.      ÷÷[HK1_PKT_FORM_NO]       EQ      7
0220 C.      ÷÷[HK1_PKT_GEN_TIME]      EQ      0.25 s
0221 C.      ÷÷[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.      ÷÷[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.      ÷÷[HK1_DMP_CHK_FLG]      EQ      EXEC
0224 C.
0225 C. ŷÄŷØŷ×½ªî»º³ĩÇ§
0226 C.      ÷÷[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLºĩŰºË¹çªë²ìOKºº³ĩÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;Êŷ¼ŷ. ŷì;¼ŷÈ;Ëººĩãº¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC     (02 0a f8)
0233 C.      ÷÷[HK1_PKT_FORM_NO]       EQ      2
0234 C.      ÷÷[HK1_PKT_GEN_TIME]      EQ      0.5S
0235 C.      ÷÷[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.      ÷÷[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2011-11-22 10:19:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC     (21 02)
0247 +. TI 2011-11-22 10:19:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC     (22)
0250 C.      [      ] [HK1_TI_CMD_NUM]  EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2011-11-22 10:19:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC     (41)
0261 C. -----
0262 C.      HK1_TI_CMD_NUM      = 1 CNTUP [      ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-11-22 10:19:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC     (c3)
0271 C.      [      ] [HK1_TI_CMD_NUM]  EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ‘úÃĩº¡Ű»ø¼ŷºÊÄº¹ºëDCBCª²è *****
0276 C. (¼ªºĩŷØŷÄŷËŷŷŷÄŷŷçŷëºÊ¼ººªÄ»Űº¹ºè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹.ĩ Daily±çĩÑºÊ’Øº¹ºëDCBCª²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼ª»Ű;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-477 2011-11-22 11:56:11 91 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFYOYEÁ+¿@
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** APCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αÄα•μ°È»Í×ÁÇαíYçYÁY×Yí;¼YÉ;ÈÈè%μ•fíÈ;ÈαÈ¼°ÇÖα•α¿¼i¹çαÍ;çÄ@, ùα¹αèαBαÇÄ+¿@α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** APCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_ST$1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the APCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** APCS Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 3237470.9 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_MANU
0056 BC (21 02)
0057 . C. Verify EIS in MANUAL mode
0058 C. Estimated OBSTBL upload time is 33s
0059 C. *****
0060 C. EIS START OBSTBL LOAD
0061 C. *****
0062 . S. RAM ram-820:EIS_OBSTBL
0063 ( )
0064 +. DC 07-FC EIS_DUMP_OBSTBL
0065 BC (07 07 07 00 00 70 00)
0066 C.
0067 C. Execute, after the success of OBSTBL upload.
0068 C. Set EIS TI-commands
0069 +. TI 2011-11-22 10:19:50.0
0070 DC 07-FC EIS_MODE_CHG_ENA
0071 BC (20)
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0073 C. *****
0074 C. EIS END OBSTBL LOAD
0075 C. *****
0076 C.
0077 . C. ***** MDP `ûÃÏñÎ»ò¼YαÈÄα¹αèDCBC•×²è *****
0078 C. (¼á°íYÓYÁYÈYÏYÈYáYçYèαÈ¼/αα¼Á»Ûα¹αè)
0079 . S. DC-BC dcbc-402:DCBC
0080 (MDP_known_event)
0081 C.
0082 C.
0083 . C. ***** YDY¹•Ì Daily±¿ÍÑαÈ´Øα¹αèDCBC•×²è *****
0084 . S. DC-BC dcbc-153:DCBC
0085 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086 C.
0087 C.
0088 . C. ;ãLOSYÁY$YÁY-¼Á»Û;ã
0089 C.
0090 . C. ***** LOS *****
0091 C.
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 20 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 85 83 08 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_AEC_RESET
0145 BC (d0)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 . C. ----- Success Verify ? OK / NG ____
0151 C.
0152 C.
0153 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0154 C.
0155 +. DC 07-F0 MDP_XRT_MODE_OBSV
0156 BC (c2)
0157 +. TI 2011-11-22 10:19:02.0
0158 DC 07-F0 MDP_XRT_MODE_OBSV
0159 BC (c2)
0160 . C. ----- Success Verify ? OK / NG ____
0161 C.
0162 C. ***** XRT END *****
0163 . C. *****
0164 C. SOT table upload
0165 C. *****
0166 . C. < Stop FG table >
0167 +. DC 07-F0 MDP_FG_CTRL_MANU
0168 BC (51)
0169 . C. -----
0170 C. MDP_FG_CTRL_MODE = MANU [ ]
0171 C. -----
0172 C.
0173 . C. <Upload FG Observation Table>
0174 . S. RAM ram-269:MDP_OBS_F
0175 ( )
0176 C.
0177 . C. < Dump RAMID=MDP_OBS_F >
0178 +. DC 07-F0 MDP_DUMP_FGTBL
0179 BC (82 07 00 00 00 38 b8)
0180 C. -----
0181 C. MDP_OBS_F verify = OK/NG [ ]
0182 C. -----
0183 C.
0184 . C. < Upload DPL table >
0185 C.
0186 C. ¼ç¥Ã¥×¥í;¼¥É°îÁ°°ÈSTS_CHKαδOFFαÈñ¹ñē
0187 C.
0188 . S. RAM ram-271:MDP_DPL
0189 ( )
0190 C.
0191 . C. < Dump RAMID=MDP_DPL >
0192 +. DC 07-F0 MDP_DUMP_FGTBL
0193 BC (82 07 00 38 b8 00 40)

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0194 C. -----
0195 C. MDP_DPL verify = OK [ ]
0196 C. -----
0197 C.
0198 C. STS_CHKαδONαËα¹αë
0199 C.
0200 . C. < Update MDP DSC PAR1 >
0201 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0202 BC (4c)
0203 C. MDP_CMD_CODE = F04C0700[ ]
0204 C. MDP_CMD_CNT (count-up 1) [ ]
0205 C. -----
0206 C.
0207 . C.
0208 C. *****
0209 C. SOT TI command set
0210 C. *****
0211 C. Execute, after the success of TBL upload.
0212 +. TI 2011-11-22 10:19:18.0
0213 DC 07-F0 MDP_SOT_MODE_OBSV
0214 BC (40)
0215 . C. -----
0216 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0217 C. -----
0218 C.
0219 C.
0220 . C. ***** MDP 'úÃîαî»ö¼ÝαËÊα¹αëDCBC•×²è *****
0221 C. (%ã°îÝÓÝÄÝËÝÞÝËÝäÝçÝèαË¼αα¼Ä»Ûα¹αë)
0222 . S. DC-BC dcbc-402:DCBC
0223 (MDP_known_event)
0224 C.
0225 C.
0226 . C. ***** ÝÐÝ¹•Ï Daily±¿ÎÑαË'Øα¹αëDCBC•×²è *****
0227 . S. DC-BC dcbc-153:DCBC
0228 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0229 C.
0230 C.
0231 . C. ;ãLOSÝÄÝ§ÝÄÝ-¼Ä»Û;ä
0232 C.
0233 . C. ***** LOS *****
0234 C.

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\*\*\* OP Sequence for XRT \*\*\*

2011/11/22	10:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	00	00	ac	cd
2011/11/22	10:56:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	d6	67
2011/11/22	11:11:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2011/11/22	11:11:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/11/22	11:12:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2011/11/22	11:12:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0				d9	
2011/11/22	11:12:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0				c9	
2011/11/22	11:12:20.5	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0				d5	
2011/11/22	11:14:58.5	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0				c4	06
2011/11/22	11:15:00.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0				c0	
2011/11/22	11:28:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	29	99
2011/11/22	11:44:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	00	00	53	33
2011/11/22	12:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	d6	36	b7	8e
2011/11/22	12:10:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	b4	b5	db	75
2011/11/22	12:26:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	ac	5b	00	00
2011/11/22	12:42:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	b4	b5	24	8b
2011/11/22	12:58:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	d6	36	48	72
2011/11/22	13:08:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	29	ca	b7	8e
2011/11/22	13:18:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	4b	4b	db	75
2011/11/22	13:34:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	53	a5	00	00
2011/11/22	13:50:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	4b	4b	24	8b
2011/11/22	14:06:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	29	db	48	72
2011/11/22	14:19:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2011/11/22	14:19:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2011/11/22	14:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0				e8	
2011/11/22	14:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0				e9	
2011/11/22	14:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2011/11/22	14:30:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	04	00	00	00	00
2011/11/22	14:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/11/22	14:32:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0				d8	
2011/11/22	14:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0				c8	
2011/11/22	14:32:50.5	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0				d0	
2011/11/22	14:32:52.5	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0				d5	
2011/11/22	14:32:54.5	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2011/11/22	14:32:56.5	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0				c4	14
2011/11/22	14:32:58.5	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0				c5	03
2011/11/22	14:33:00.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0				c0	
2011/11/22	15:52:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2011/11/22	15:52:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2011/11/22	15:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0				e8	
2011/11/22	15:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0				e9	
2011/11/22	16:16:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/22	16:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0				c0	
2011/11/22	17:29:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2011/11/22	17:29:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2011/11/22	17:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0				e8	

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2011/11/22	17:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/22	17:54:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/22	17:54:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/11/22	17:54:30.0	AOCS_Orе-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/11/22	17:54:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/11/22	17:54:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/11/22	17:54:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/22	17:57:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2011/11/22	17:57:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/22	18:04:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/22	18:04:30.0	AOCS_Orе-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/11/22	18:06:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/11/22	18:07:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/11/22	18:07:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/11/22	18:07:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/11/22	18:07:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/22	18:07:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/22	18:07:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/11/22	18:07:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/11/22	18:07:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/22	19:06:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/22	19:06:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/22	19:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/22	19:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/22	19:29:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/22	19:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/22	20:43:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/22	20:43:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/22	20:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/22	20:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/22	21:07:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/22	21:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/22	22:21:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/22	22:21:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/22	22:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/22	22:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/22	22:42:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/22	22:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/22	23:58:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/22	23:58:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/22	23:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/23	00:00:00.0	AOCS_Orе-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/11/23	00:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/23	00:07:30.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/23	00:10:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/11/23	00:10:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/11/23	00:10:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/11/23	00:10:26.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				

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2011/11/23	00:10:28.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/23	00:10:30.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/23	00:10:32.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2011/11/23	00:10:34.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/11/23	00:10:36.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/23	01:23:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/23	01:23:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/23	01:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/23	01:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/23	01:41:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/23	01:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/23	01:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/23	02:00:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/11/23	02:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/11/23	02:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/11/23	02:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/11/23	02:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/11/23	02:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/23	02:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/23	02:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/11/23	02:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/11/23	02:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/23	02:57:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/23	02:57:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/23	02:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/23	03:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/23	03:18:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/23	03:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/23	04:26:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/23	04:26:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/23	04:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/23	04:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/23	04:55:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/23	04:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/23	05:39:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/23	05:39:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/11/23	05:39:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/11/23	05:39:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/11/23	05:39:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/11/23	05:39:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/23	05:42:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2011/11/23	05:42:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/23	05:49:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/23	05:49:30.0	AOCS_Ore-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/11/23	05:51:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/11/23	05:52:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/11/23	05:52:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/11/23	05:52:20.5	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				



2011/11/23	05:52:22.5	XRT_ARS_DIS_431_OG [0x1af]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/11/23	05:52:24.5	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/23	05:52:26.5	XRT_QT_PROG_SET_449_OG [0x1c1]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2011/11/23	05:52:28.5	XRT_FL_PROG_SET_414_OG [0x19e]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2011/11/23	05:52:30.5	XRT_CTRL_AUTO_406_OG [0x196]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/23	06:07:00.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/23	06:07:02.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/23	06:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/11/23	06:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/11/23	06:33:00.0	XRT_Custom_418_OG [0x1a2]				
2011/11/23	06:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/23	07:47:30.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/23	07:47:32.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/23	07:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/11/23	07:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/11/23	08:10:00.0	XRT_Custom_418_OG [0x1a2]				
2011/11/23	08:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/23	09:27:00.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/23	09:27:02.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/23	09:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/11/23	09:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/11/23	09:45:00.0	XRT_Custom_418_OG [0x1a2]				
2011/11/23	09:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/23	14:53:30.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/23	14:53:32.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/23	14:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/11/23	14:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/11/23	15:05:30.0	XRT_Custom_418_OG [0x1a2]				
2011/11/23	15:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/23	16:28:30.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/23	16:28:32.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/11/23	16:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/11/23	16:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/11/23	16:52:30.0	XRT_Custom_418_OG [0x1a2]				
2011/11/23	16:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/23	17:41:54.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/23	17:41:56.0	XRT_FOCUS_POSITION_401_OG [0x191]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/11/23	17:42:00.0	AOCS_Ore-point_Start_3_OG [0x099]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2011/11/23	17:42:16.0	XRT_FLD_DIS_402_OG [0x192]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/11/23	17:42:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/11/23	17:42:20.0	XRT_ARS_DIS_438_OG [0x1b6]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/11/23	17:44:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2011/11/23	17:45:00.0	XRT_CTRL_AUTO_406_OG [0x196]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/11/23	17:51:54.0	XRT_CTRL_MANU_439_OG [0x1b7]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/11/23	17:52:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]				
		AOCU_NM	5	02-76	04 00 00 00 00	
2011/11/23	17:54:26.0	XRT_FOCUS_POSITION_409_OG [0x199]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/11/23	17:54:46.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/11/23	17:54:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/11/23	17:54:50.0	XRT_AEC_RESET_443_OG [0x1bb]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	

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2011/11/23	17:54:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/11/23	17:54:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/23	17:54:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2011/11/23	17:54:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/11/23	17:55:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/23	18:06:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/23	18:06:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/23	18:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/23	18:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/23	18:29:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/23	18:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/23	19:42:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/23	19:42:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/23	19:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/23	19:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/23	20:06:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/23	20:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/23	21:20:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/23	21:20:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/23	21:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/23	21:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/23	21:43:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/23	21:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/23	22:57:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/23	22:57:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/23	22:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/23	23:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/23	23:17:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/23	23:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/23	23:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/24	00:00:00.0	AOCS_Or-e-point_Start_17_OG [0x0a7]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/11/24	00:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/11/24	00:02:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/11/24	00:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/11/24	00:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/11/24	00:02:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/11/24	00:02:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/24	00:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2011/11/24	00:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/11/24	00:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/24	00:34:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/24	00:34:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/24	00:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/24	00:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/24	00:41:30.0	XRT_Custom_418_OG [0x1a2]			
2011/11/24	00:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/24	01:57:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/24	01:57:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/24	01:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2011/11/24	02:00:00.0	AOCS_ORe-point_Start_16_OG [0x0a6] AOCU_NM	5	02-76	04	00	00	00	00
2011/11/24	02:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/24	02:17:30.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/24	02:20:02.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/11/24	02:20:22.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/11/24	02:20:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/11/24	02:20:26.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/11/24	02:20:28.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/24	02:20:30.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/24	02:20:32.0	XRT_QT_PROG_SET_449_OG [0x1c1] MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2011/11/24	02:20:34.0	XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2011/11/24	02:20:36.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/24	03:30:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/24	03:30:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/24	03:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/24	03:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/24	03:54:30.0	XRT_Custom_418_OG [0x1a2]							
2011/11/24	03:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/24	05:04:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/24	05:04:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/24	05:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/24	05:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/24	05:32:00.0	XRT_Custom_418_OG [0x1a2]							
2011/11/24	05:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/24	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/24	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/11/24	06:00:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00	00	00	00	00
2011/11/24	06:00:16.0	XRT_FLD_DIS_402_OG [0x192] MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/11/24	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/11/24	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/24	06:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1] MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2011/11/24	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/24	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/24	06:10:00.0	AOCS_ORe-point_Start_16_OG [0x0a6] AOCU_NM	5	02-76	04	00	00	00	00
2011/11/24	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/11/24	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/11/24	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/11/24	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/11/24	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/11/24	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/24	06:12:56.0	XRT_QT_PROG_SET_449_OG [0x1c1] MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2011/11/24	06:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2011/11/24	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/11/24	06:45:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/11/24	06:45:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/11/24	06:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/11/24	06:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/11/24	07:09:30.0	XRT_Custom_418_OG [0x1a2]							

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2011/11/24	07:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/24	08:24:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/24	08:24:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/11/24	08:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/11/24	08:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/11/24	08:46:00.0	XRT_Custom_418_OG [0x1a2]			
2011/11/24	08:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/11/24	09:00:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/11/24	10:06:00.5	AOCS_OrE-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00