

# XRT Timeline to be uploaded on 2011/12/01

Period: 2011/12/01 09:20:00 - 2011/12/06 09:41:00

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## Normal mode

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XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly												
Term	Pointing (x, y)		Comment									
12/01 09:49:46 - 12/01 09:56:24	Fixed (	-916.0, -15.0)	# OP start + 10min, HOP 79, irradiance scan, E-W direction.									
12/01 09:59:30 - 12/01 10:06:24	Fixed (	-717.0, -15.0)										
12/01 10:09:30 - 12/01 10:16:24	Fixed (	-517.9, -15.0)										
12/01 10:19:30 - 12/01 10:26:24	Fixed (	-318.8, -15.0)										
12/01 10:29:30 - 12/01 10:36:24	Fixed (	-119.8, -15.0)										
12/01 10:39:30 - 12/01 10:46:24	Fixed (	79.3, -15.0)										
12/01 10:49:30 - 12/01 10:56:24	Fixed (	278.4, -15.0)										
12/01 10:59:30 - 12/01 11:06:24	Fixed (	477.4, -15.0)										
12/01 11:09:30 - 12/01 11:16:24	Fixed (	676.5, -15.0)										
12/01 11:19:30 - 12/01 11:36:24	Fixed (	875.6, -15.0)										
12/01 11:39:30 - 12/01 11:46:24	Fixed (	-914.3, 226.6)										
12/01 11:49:30 - 12/01 11:56:24	Fixed (	-743.6, 227.6)										
12/01 11:59:30 - 12/01 12:06:24	Fixed (	-561.7, 225.0)										
12/01 12:09:30 - 12/01 12:16:24	Fixed (	-375.5, 223.4)										
12/01 12:19:30 - 12/01 12:26:24	Fixed (	-184.9, 222.5)										
12/01 12:29:30 - 12/01 12:36:24	Fixed (	10.0, 222.2)										
12/01 12:39:30 - 12/01 12:46:24	Fixed (	164.9, 222.5)										
12/01 12:49:30 - 12/01 12:56:24	Fixed (	355.5, 223.4)										
12/01 12:59:30 - 12/01 13:06:24	Fixed (	541.7, 225.0)										
12/01 13:09:30 - 12/01 13:16:24	Fixed (	723.6, 227.6)										
12/01 13:19:30 - 12/01 13:26:24	Fixed (	904.3, 211.6)	* HOP 79, final pointing.									
<b>PROG= 04 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 120.0sec												
└─ Seqn= 57 1-time(s) 4.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18C1: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 60s cad												
Term	Pointing (x, y)		Comment									
12/01 13:29:30 - 12/01 17:42:24	Track (	179.7, 290.5)	* Track AR 11361.									
<b>PROG= 11 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 96 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 62 15-time(s) 2.0sec												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18B0: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(24/1443) + Thin-Be(88/2)												
Term	Pointing (x, y)		Comment									
12/01 17:45:30 - 12/01 17:52:24	Fixed (	0.0, 0.0)	synoptic, shifted -17.5 min									
12/02 06:03:00 - 12/02 06:09:54	Fixed (	0.0, 0.0)	synoptic									
12/02 18:03:30 - 12/02 18:10:24	Fixed (	0.0, 0.0)	synoptic, shifted 0.5 min									
12/03 06:22:00 - 12/03 06:28:54	Fixed (	0.0, 0.0)	synoptic, shifted 19.0 min									
<b>PROG= 08 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 46 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 69 1-time(s) 4.0sec												

		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		<b>Seqn= 50</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
		thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		<b>Seqn= 4</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
		Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad**

Term	Pointing (x, y)	Comment
12/01 17:55:30 - 12/02 05:59:54	Track ( 218.0, 291.0) <sup>Ⓜ 12/01 17:52:30</sup>	# Cont.
<b>PROG= 13 Inf.-time(s)</b>		
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 19 1-time(s) 2.0sec</b>		
Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec		
Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec		
<b>Seqn= 96 4-time(s) 2.0sec</b>		
Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec		
thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec		
Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 62 15-time(s) 150.0sec</b>		
thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec		
thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec		
Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec		
thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec		
Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec		
thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec		
Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec		
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #18AF: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 4min cad**

Term	Pointing (x, y)	Comment
12/02 06:13:00 - 12/02 18:00:24	Track ( 322.0, 292.5) <sup>Ⓜ 12/02 06:10:00</sup>	# Cont.
12/02 18:13:30 - 12/03 06:18:54	Track ( 419.4, 293.9) <sup>Ⓜ 12/02 18:10:30</sup>	# Cont.
12/03 06:32:00 - 12/03 09:02:30	Track ( 513.4, 295.5) <sup>Ⓜ 12/03 06:29:00</sup>	# Cont.
<b>PROG= 20 Inf.-time(s)</b>		
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 19 1-time(s) 2.0sec</b>		
Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec		
Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec		
<b>Seqn= 96 4-time(s) 2.0sec</b>		
Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec		
thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec		
Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 62 11-time(s) 240.0sec</b>		
thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec		
thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec		
Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec		
thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec		
Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec		
thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec		
Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec		
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops**

Term	Pointing (x, y)	Comment
12/01 09:49:46 - 12/01 09:56:24	Fixed ( -916.0, -15.0)	# OP start + 10min, HOP 79, irradiance scan, E-W direction.
12/01 09:59:30 - 12/01 10:06:24	Fixed ( -717.0, -15.0)	
12/01 10:09:30 - 12/01 10:16:24	Fixed ( -517.9, -15.0)	
12/01 10:19:30 - 12/01 10:26:24	Fixed ( -318.8, -15.0)	
12/01 10:29:30 - 12/01 10:36:24	Fixed ( -119.8, -15.0)	
12/01 10:39:30 - 12/01 10:46:24	Fixed ( 79.3, -15.0)	
12/01 10:49:30 - 12/01 10:56:24	Fixed ( 278.4, -15.0)	
12/01 10:59:30 - 12/01 11:06:24	Fixed ( 477.4, -15.0)	
12/01 11:09:30 - 12/01 11:16:24	Fixed ( 676.5, -15.0)	
12/01 11:19:30 - 12/01 11:36:24	Fixed ( 875.6, -15.0)	
12/01 11:39:30 - 12/01 11:46:24	Fixed ( -914.3, 226.6)	
12/01 11:49:30 - 12/01 11:56:24	Fixed ( -743.6, 227.6)	

12/01 11:59:30 - 12/01 12:06:24	Fixed ( -561.7, 225.0)	
12/01 12:09:30 - 12/01 12:16:24	Fixed ( -375.5, 223.4)	
12/01 12:19:30 - 12/01 12:26:24	Fixed ( -184.9, 222.5)	
12/01 12:29:30 - 12/01 12:36:24	Fixed ( 10.0, 222.2)	
12/01 12:39:30 - 12/01 12:46:24	Fixed ( 164.9, 222.5)	
12/01 12:49:30 - 12/01 12:56:24	Fixed ( 355.5, 223.4)	
12/01 12:59:30 - 12/01 13:06:24	Fixed ( 541.7, 225.0)	
12/01 13:09:30 - 12/01 13:16:24	Fixed ( 723.6, 227.6)	
12/01 13:19:30 - 12/01 13:26:24	Fixed ( 904.3, 211.6)	* HOP 79, final pointing.
12/01 13:29:30 - 12/01 17:42:24	Track ( 179.7, 290.5) @ 12/01 13:26:30	* Track AR 11361.
12/01 17:55:30 - 12/02 05:59:54	Track ( 218.0, 291.0) @ 12/01 17:52:30	# Cont.
12/02 06:13:00 - 12/02 18:00:24	Track ( 322.0, 292.5) @ 12/02 06:10:00	# Cont.
12/02 18:13:30 - 12/03 06:18:54	Track ( 419.4, 293.9) @ 12/02 18:10:30	# Cont.
12/03 06:32:00 - 12/03 09:02:30	Track ( 513.4, 295.5) @ 12/03 06:29:00	# Cont.

**PROG= 03 12-time(s)**

<b>Subr= 1 45-time(s) 10.0sec</b>												
<b>Seqn= 20 1-time(s) 2.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 63 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 77 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2 1-time(s) 10.0sec</b>												
<b>Seqn= 90 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Active Region Search**

\* \* \* \* \*

NOT USED

\* \* \* \* \*

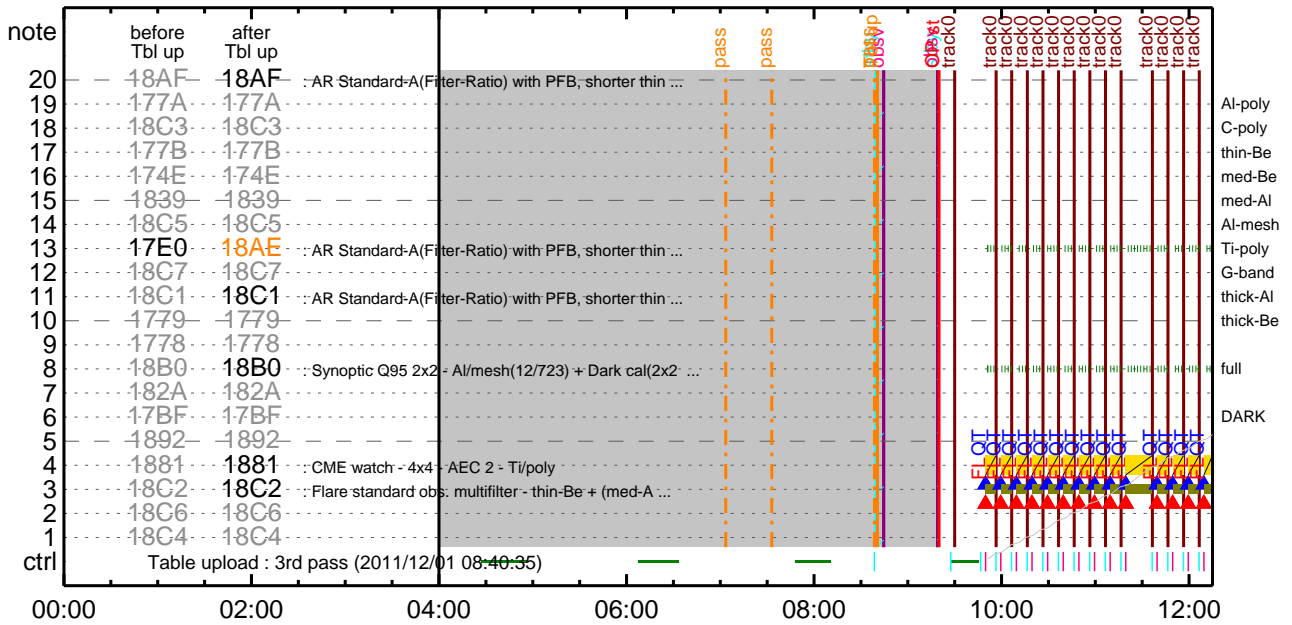
**Flare Detection**

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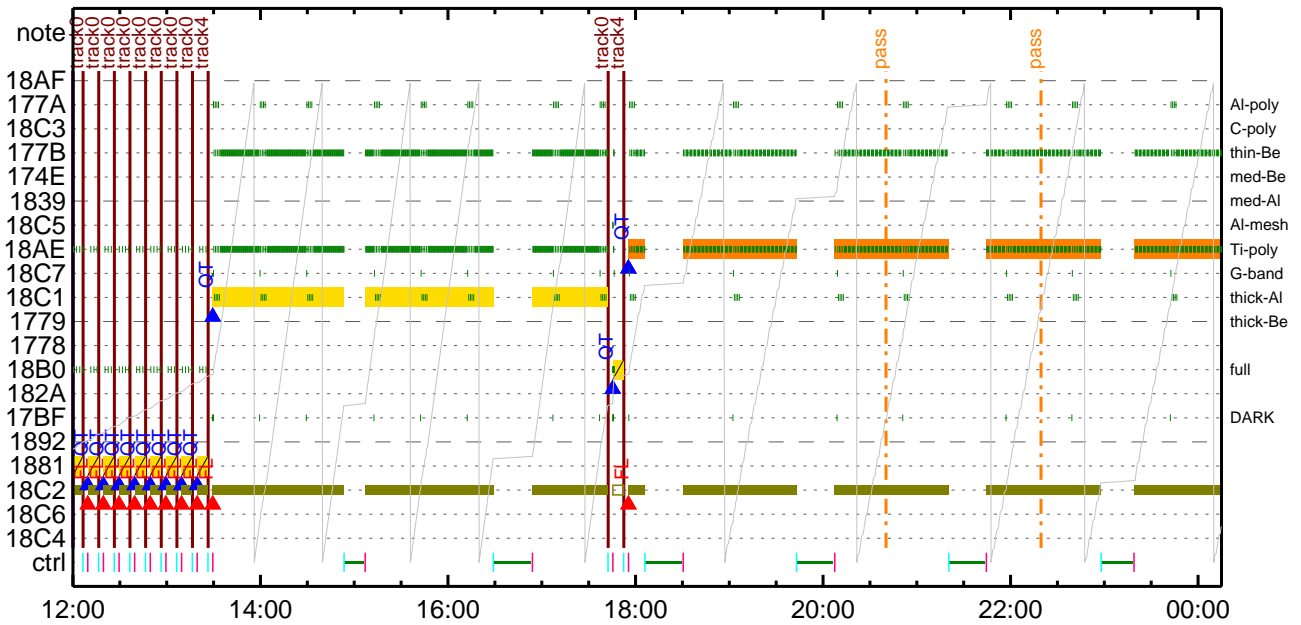
**FLD Patrol**

Term	Pointing (x, y)	Comment
12/01 17:55:16 - 12/02 06:00:16	Track ( 218.0, 291.0) @ 12/01 17:52:30	# Cont.
12/02 06:12:46 - 12/02 18:00:46	Track ( 322.0, 292.5) @ 12/02 06:10:00	# Cont.
12/02 18:13:16 - 12/03 06:19:16	Track ( 419.4, 293.9) @ 12/02 18:10:30	# Cont.
12/03 06:31:46 - 12/06 09:41:00	Track ( 513.4, 295.5) @ 12/03 06:29:00	# Cont.
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

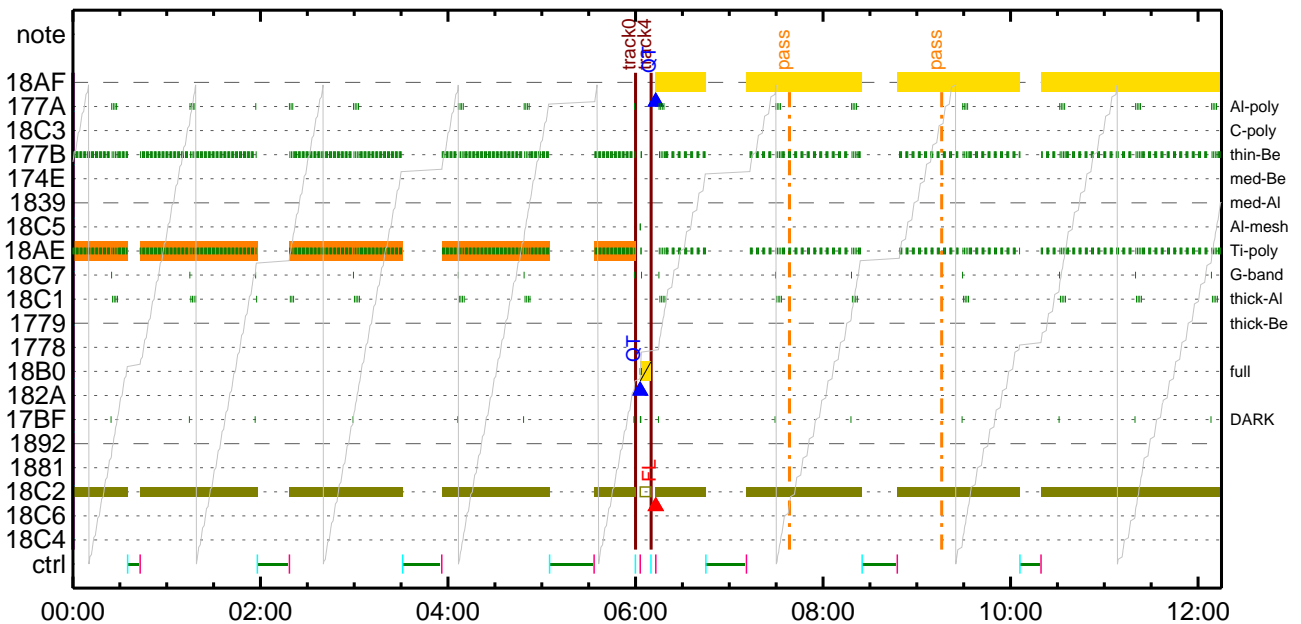
### CMDI #0294 2011/12/01



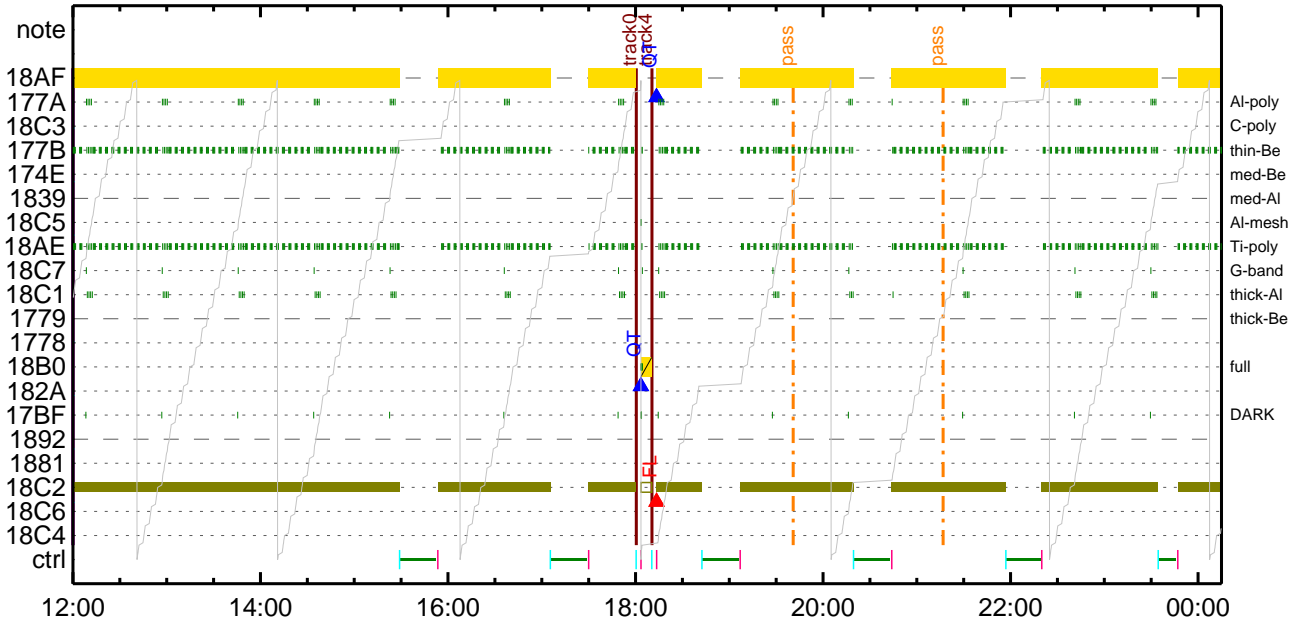
### CMDI #0294 2011/12/01



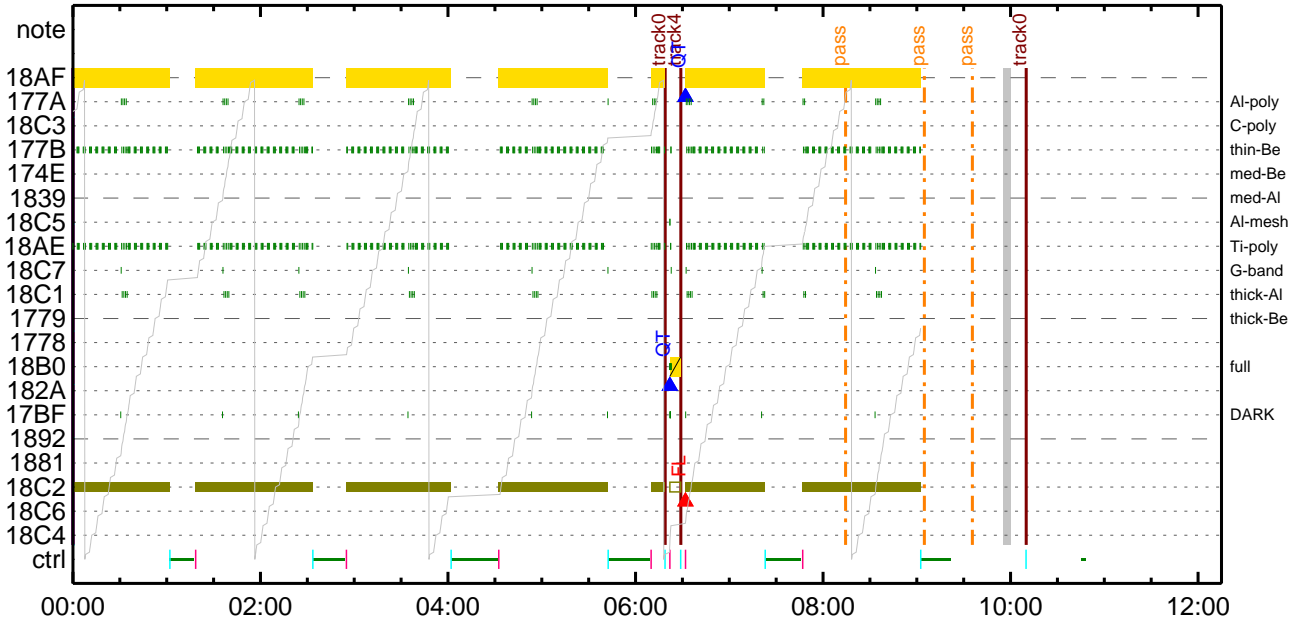
### CMDI #0294 2011/12/02



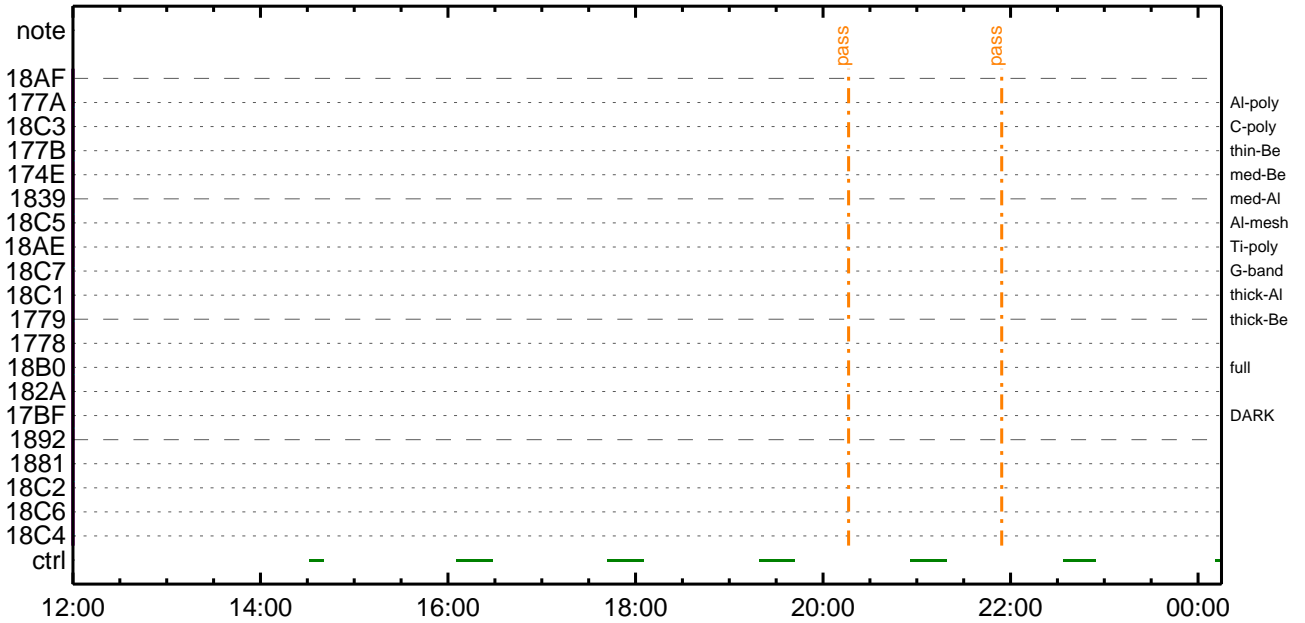
### CMDI #0294 2011/12/02



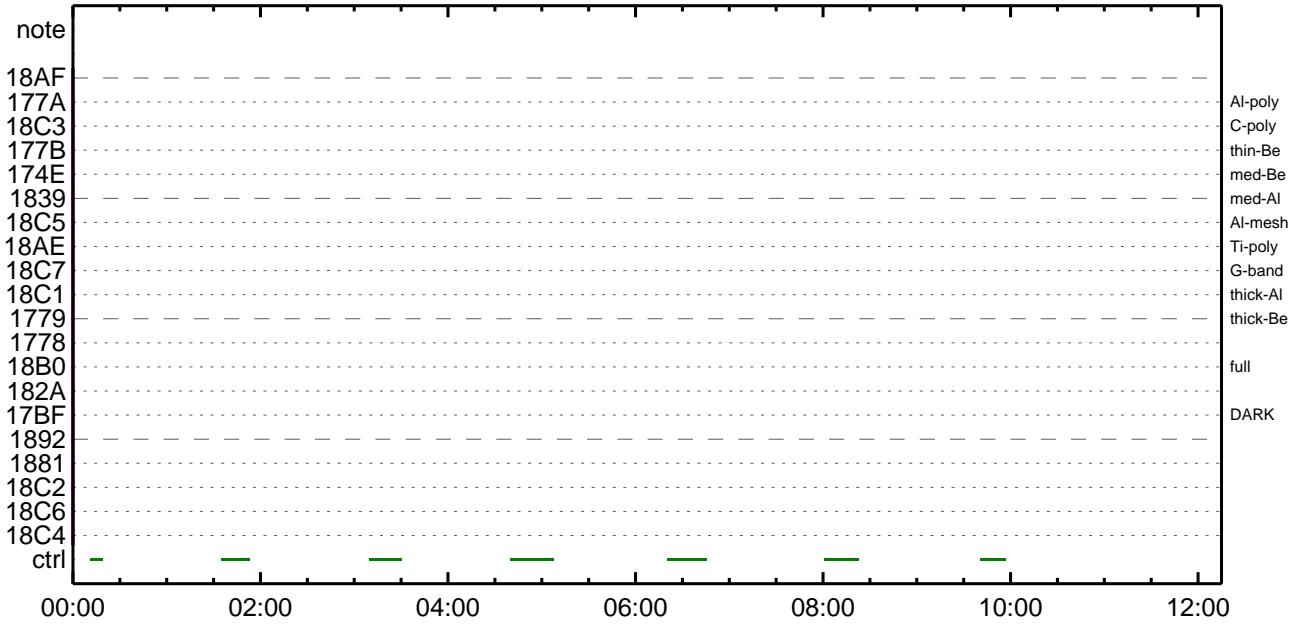
### CMDI #0294 2011/12/03



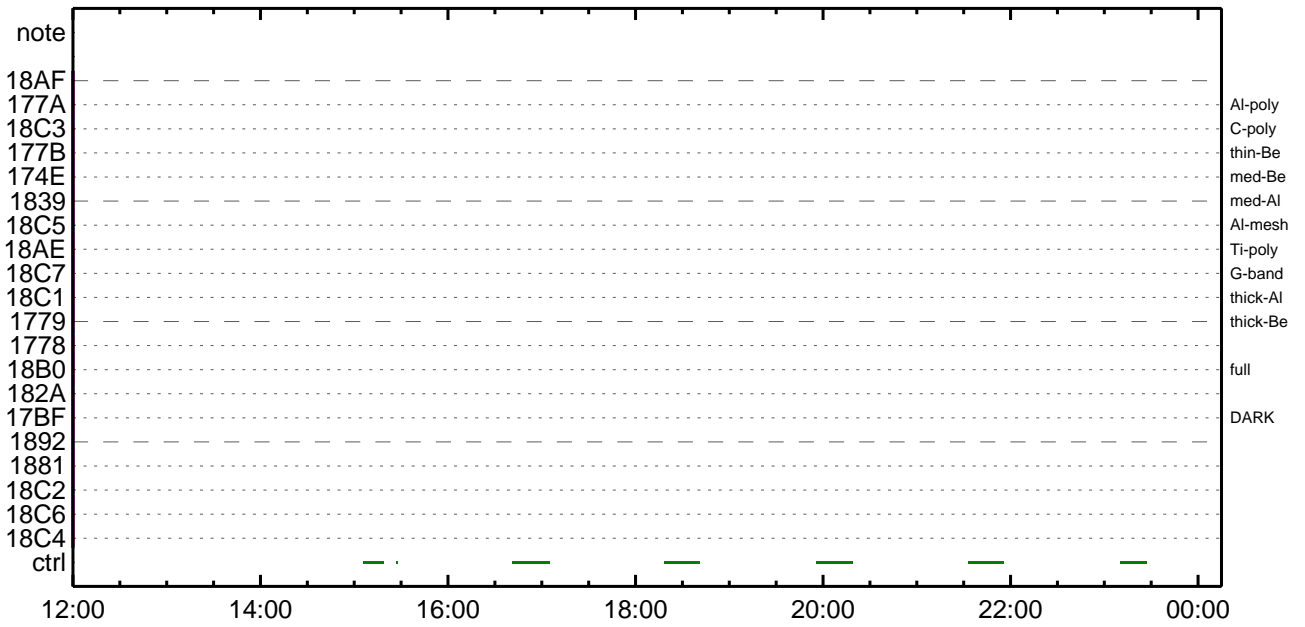
### CMDI #0294 2011/12/03



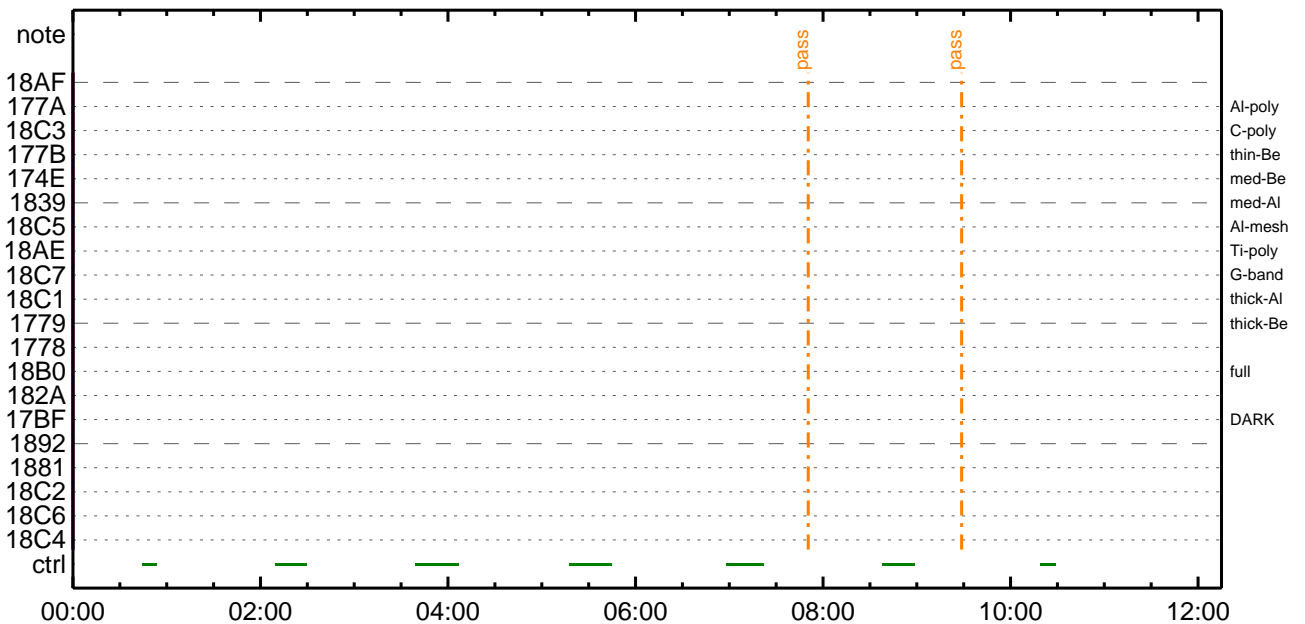
CMDI #0294 2011/12/04



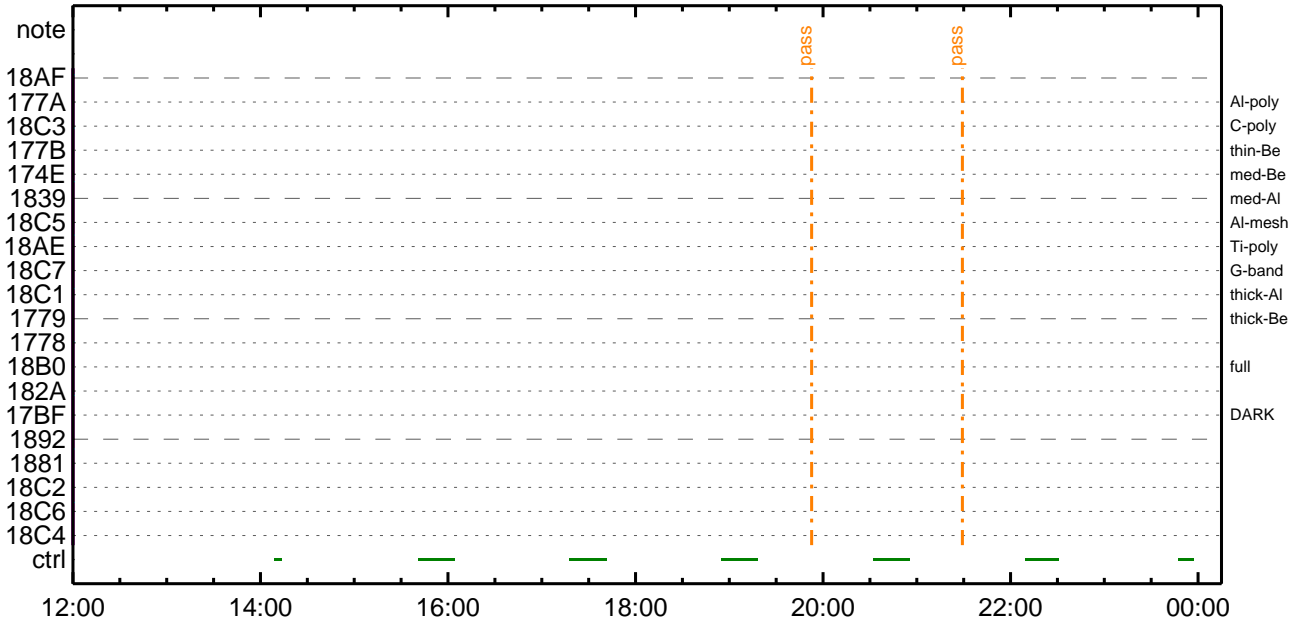
CMDI #0294 2011/12/04



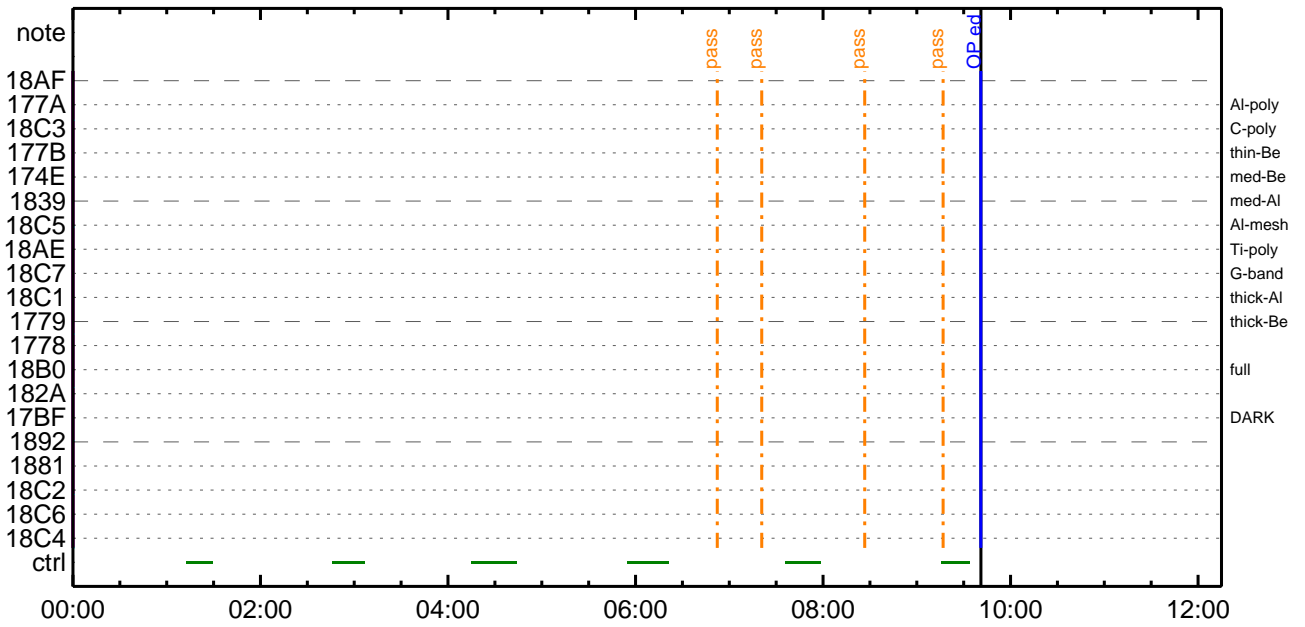
CMDI #0294 2011/12/05



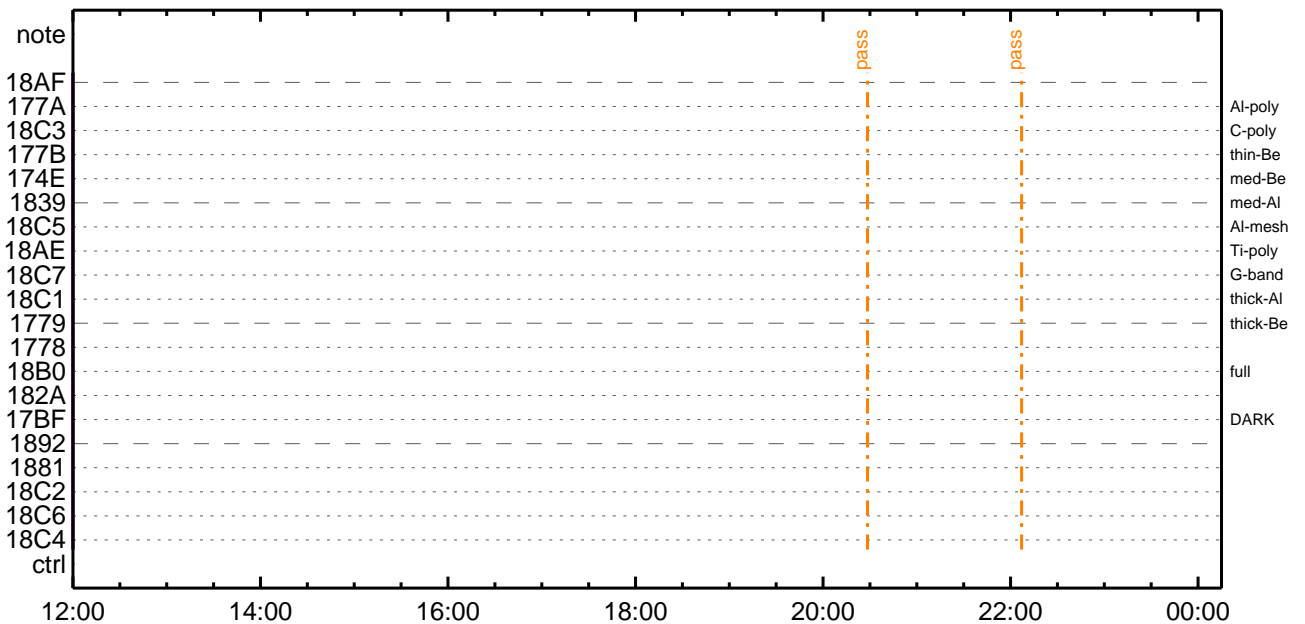
CMDI #0294 2011/12/05



CMDI #0294 2011/12/06



CMDI #0294 2011/12/06









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0194 C.
0195 +. TI 2011-12-01 09:19:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄè%îîñαîŷÄŷ§ŷÄŷ-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TÎîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2011-12-01 09:19:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C.   HK1_TI_CMD_NUM           = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2011-12-01 09:19:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2011-12-01 09:19:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2011-12-01 09:19:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîαî»ö¼ŷαÈÄα¹αèDCBC•x²è *****
0276 C. (¼á°îŷÖŷÄŷÈŷŷÈŷáŷçŷèèÈ¼αα¼Ä»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîñαè'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```

main-495 2011-12-01 14:56:22 82 33 SOLAR-B MAIN //
0001  C.
0002  . C. ***** AOS *****
0003  C.
0004  . C. ;ãAOSYÁYŞYÁY~¼Á»Ü;ã
0005  C.
0006  C. YÀYB;¼Y³YFYÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008  C.
0009  . C. ***** AOCS : Reload orbital element (send every contact) *****
0010  C.  Áí;Èα¿αÁα•µ°È»Í×ÁÇαÍYçYÁY×YÍ;¼YÉ;ÈÈ%µ•ÍÉ;ÈÈE¼°ÇÖα•α¿¼¿i¹çαÍ;çÀ®), ùα¹αÈαBαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012  C.
0013  C.
0014  . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015  C. Upload the Orbit Element and the Target Attitude
0016  C. RAM-ID:TARGET_ATT
0017  . S. RAM ram-150:TARGET_ATT
0018  ( )
0019  C.
0020  C.
0021  C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023  BC      (07 00 00 00 18 00)
0024  C.
0025  C.      <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026  C.
0027  C.
0028  C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030  BC      (04 0b f8)
0031  C.
0032  C. Wait for AOCS DUMP to end
0033  C.
0034  . C. Check the dump memory
0035  C.
0036  C.                               Result = OK [ ]
0037  C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039  BC      (02 0a f8)
0040  C.
0041  C.                               <A_***>[TLM STS] FMT = 2 [ ]
0042  C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044  . C.
0045  C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047  BC      (21 02)
0048  . C. Verify EIS in MANUAL mode
0049  . C. Estimated OBSTBL upload time is 1m15s
0050  C. *****
0051  C. EIS START OBSTBL LOAD
0052  C. *****
0053  . S. RAM ram-820:EIS_OBSTBL
0054  ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056  BC      (07 07 07 00 00 70 00)
0057  C.
0058  C. Execute, after the success of OBSTBL upload.
0059  C. Set EIS TI-commands
0060 +. TI 2011-12-01 09:19:50.0
0061  DC 07-FC EIS_MODE_CHG_ENA
0062  BC      (20)
0063  . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064  C. *****
0065  C. EIS END OBSTBL LOAD
0066  C. *****
0067  C.
0068  . C. ***** MDP `ûÄÎâÍ»ö¼YαÈÄα¹αÈDCBC•×²è *****
0069  C. (¼á°îYÓYÁYÈYBpYÉYáYçYÈαE¾¼α¼Á»Üα¹αè)
0070  . S. DC-BC dcbc-402:DCBC
0071  (MDP_known_event)
0072  C.
0073  C.
0074  . C. ***** YBY¹.İ Daily±¿ÍÑαÈ`Øα¹αÈDCBC•×²è *****
0075  . S. DC-BC dcbc-153:DCBC
0076  (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077  C.
0078  C.
0079  . C. ;ãLOSÁYŞYÁY~¼Á»Ü;ã
0080  C.
0081  . C. ***** LOS *****
0082  C.

```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-289:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2011-12-01 09:19:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 80 80 20 20)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 85 83 06 06)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 85 83 08 08)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 20 08)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 80 80 08 20)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0f 80 80 06 06)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 10 80 80 08 08)
0172 + DC 07-F0 MDP_XRT_FLD_ENA
0173 BC (d8)
0174 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0175 BC (c8)
0176 + DC 07-F0 MDP_XRT_AEC_RESET
0177 BC (d0)
0178 + DC 07-F0 MDP_XRT_ARS_DIS
0179 BC (d5)
0180 + DC 07-F0 MDP_XRT_FLD_RESET
0181 BC (da)
0182 . C. ----- Success Verify ? OK / NG ____
0183 C.
0184 C.
0185 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0186 C.
0187 +. DC 07-F0 MDP_XRT_MODE_OBSV
0188 BC (c2)
0189 +. TI 2011-12-01 09:19:02.0
0190 DC 07-F0 MDP_XRT_MODE_OBSV
0191 BC (c2)
0192 . C. ----- Success Verify ? OK / NG ____
0193 C.

```

```
0194 C. ***** XRT END *****
0195 C.
0196 . C. ***** MDP 'úÃîï»ò¼ýðëDCBC•x²è *****
0197 C. (%ã°îÿÓÿÄÿÈÿÞÿËÿáÿçÿèë¼¼¼¼»Û¹è)
0198 . S. DC-BC dcbc-402:DCBC
0199 (MDP_known_event)
0200 C.
0201 C.
0202 . C. ***** ÿÐÿ¹•ï Daily±¿îñë´ø¹èDCBC•x²è *****
0203 . S. DC-BC dcbc-153:DCBC
0204 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0205 C.
0206 C.
0207 . C. ;ãLOSÿÁÿ§ÿÃÿ¬¼Â»Û;ã
0208 C.
0209 . C. ***** LOS *****
0210 C.
```

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\*\*\* OP Sequence for XRT \*\*\*

2011/12/01	09:27:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/01	09:27:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/01	09:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/01	09:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 01 58 51 70				
2011/12/01	09:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/01	09:46:40.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/01	09:49:12.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/12/01	09:49:32.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/12/01	09:49:34.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/12/01	09:49:36.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/01	09:49:38.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/01	09:49:40.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/01	09:49:42.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2011/12/01	09:49:44.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/12/01	09:49:46.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/01	09:56:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/01	09:56:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 01 58 3f be				
2011/12/01	09:58:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/12/01	09:59:16.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/12/01	09:59:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/12/01	09:59:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/01	09:59:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/01	09:59:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/01	09:59:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2011/12/01	09:59:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/12/01	09:59:30.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/01	10:06:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/01	10:06:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 01 58 2e 0c				
2011/12/01	10:08:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/12/01	10:09:16.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/12/01	10:09:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/12/01	10:09:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/01	10:09:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/01	10:09:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/01	10:09:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2011/12/01	10:09:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/12/01	10:09:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/01	10:16:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/01	10:16:30.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 01 58 1c 5a				
2011/12/01	10:18:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/12/01	10:19:16.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/12/01	10:19:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/12/01	10:19:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/01	10:19:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/01	10:19:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/01	10:19:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]							

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2011/12/01	10:19:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/01	10:19:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/01	10:26:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/01	10:26:30.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 01 58	0a a7
2011/12/01	10:28:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2011/12/01	10:29:16.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/01	10:29:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/01	10:29:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/01	10:29:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/01	10:29:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/01	10:29:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2011/12/01	10:29:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/01	10:29:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/01	10:36:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/01	10:36:30.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 01 58 f8	f6
2011/12/01	10:38:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2011/12/01	10:39:16.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/01	10:39:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/01	10:39:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/01	10:39:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/01	10:39:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/01	10:39:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2011/12/01	10:39:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/01	10:39:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/01	10:46:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/01	10:46:30.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 01 58 e7	44
2011/12/01	10:48:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2011/12/01	10:49:16.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/01	10:49:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/01	10:49:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/01	10:49:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/01	10:49:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/01	10:49:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2011/12/01	10:49:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/01	10:49:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/01	10:56:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/01	10:56:30.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 01 58 d5	92
2011/12/01	10:58:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2011/12/01	10:59:16.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/01	10:59:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/01	10:59:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/01	10:59:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/01	10:59:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/01	10:59:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2011/12/01	10:59:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/01	10:59:30.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	



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2011/12/01	11:06:24.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/01	11:06:30.0	AOCS_ORe-point_Start_9_OG [0x09f] AOCU_NM	5	02-76	00 01 58	c3	e0		
2011/12/01	11:08:56.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22 ff	aa	00		
2011/12/01	11:09:16.0	XRT_FLD_ENA_435_OG [0x1b3] MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/12/01	11:09:18.0	XRT_FLRCTRL_ENA_403_OG [0x193] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/12/01	11:09:20.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/01	11:09:22.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/01	11:09:24.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/01	11:09:26.0	XRT_QT_PROG_SET_416_OG [0x1a0] MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2011/12/01	11:09:28.0	XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2011/12/01	11:09:30.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/01	11:16:24.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/01	11:16:30.0	AOCS_ORe-point_Start_10_OG [0x0a0] AOCU_NM	5	02-76	00 01 58	b2	2e		
2011/12/01	11:18:56.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22 ff	aa	00		
2011/12/01	11:19:16.0	XRT_FLD_ENA_435_OG [0x1b3] MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/12/01	11:19:18.0	XRT_FLRCTRL_ENA_403_OG [0x193] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/12/01	11:19:20.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/01	11:19:22.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/01	11:19:24.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/01	11:19:26.0	XRT_QT_PROG_SET_416_OG [0x1a0] MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2011/12/01	11:19:28.0	XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2011/12/01	11:19:30.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/01	11:36:24.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/01	11:36:30.0	AOCS_ORe-point_Start_11_OG [0x0a1] AOCU_NM	5	02-76	00 eb	d8	51 47		
2011/12/01	11:38:56.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22 ff	aa	00		
2011/12/01	11:39:16.0	XRT_FLD_ENA_435_OG [0x1b3] MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/12/01	11:39:18.0	XRT_FLRCTRL_ENA_403_OG [0x193] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/12/01	11:39:20.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/01	11:39:22.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/01	11:39:24.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/01	11:39:26.0	XRT_QT_PROG_SET_416_OG [0x1a0] MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2011/12/01	11:39:28.0	XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2011/12/01	11:39:30.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/01	11:46:24.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/01	11:46:30.0	AOCS_ORe-point_Start_12_OG [0x0a2] AOCU_NM	5	02-76	00 eb	c7	42 14		
2011/12/01	11:48:56.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22 ff	aa	00		
2011/12/01	11:49:16.0	XRT_FLD_ENA_435_OG [0x1b3] MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/12/01	11:49:18.0	XRT_FLRCTRL_ENA_403_OG [0x193] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/12/01	11:49:20.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/01	11:49:22.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/01	11:49:24.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/01	11:49:26.0	XRT_QT_PROG_SET_416_OG [0x1a0] MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2011/12/01	11:49:28.0	XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2011/12/01	11:49:30.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/01	11:56:24.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/01	11:56:30.0	AOCS_ORe-point_Start_13_OG [0x0a3] AOCU_NM	5	02-76	00 ec	00	31 eb		
2011/12/01	11:58:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							

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2011/12/01	11:59:16.0	XRT_FLD_ENA_435_OG [0x1b3]	4	07-F8	22	ff	aa	00
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/12/01	11:59:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/12/01	11:59:20.0	XRT_AEC_RESET_443_OG [0x1bb]	1	07-F0	d0			
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/12/01	11:59:22.0	XRT_ARS_DIS_431_OG [0x1af]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/12/01	11:59:24.0	XRT_FLD_RESET_412_OG [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/01	11:59:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	2	07-F0	c4	04		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2011/12/01	11:59:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	2	07-F0	c5	03		
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2011/12/01	11:59:30.0	XRT_CTRL_AUTO_406_OG [0x196]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/01	12:06:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/01	12:06:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]	5	02-76	00	ec	29	21 60
		AOCU_NM	5	02-76	00	ec	29	21 60
2011/12/01	12:08:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	4	07-F8	22	ff	aa	00
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2011/12/01	12:09:16.0	XRT_FLD_ENA_435_OG [0x1b3]	1	07-F0	d8			
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/12/01	12:09:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/12/01	12:09:20.0	XRT_AEC_RESET_443_OG [0x1bb]	1	07-F0	d0			
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/12/01	12:09:22.0	XRT_ARS_DIS_431_OG [0x1af]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/12/01	12:09:24.0	XRT_FLD_RESET_412_OG [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/01	12:09:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	2	07-F0	c4	04		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2011/12/01	12:09:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	2	07-F0	c5	03		
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2011/12/01	12:09:30.5	XRT_CTRL_AUTO_406_OG [0x196]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/01	12:16:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/01	12:16:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]	5	02-76	00	ec	3a	10 72
		AOCU_NM	5	02-76	00	ec	3a	10 72
2011/12/01	12:18:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	4	07-F8	22	ff	aa	00
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2011/12/01	12:19:16.0	XRT_FLD_ENA_435_OG [0x1b3]	1	07-F0	d8			
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/12/01	12:19:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/12/01	12:19:20.0	XRT_AEC_RESET_443_OG [0x1bb]	1	07-F0	d0			
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/12/01	12:19:22.0	XRT_ARS_DIS_431_OG [0x1af]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/12/01	12:19:24.0	XRT_FLD_RESET_412_OG [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/01	12:19:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	2	07-F0	c4	04		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2011/12/01	12:19:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	2	07-F0	c5	03		
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2011/12/01	12:19:30.0	XRT_CTRL_AUTO_406_OG [0x196]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/01	12:26:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/01	12:26:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]	5	02-76	00	ec	42	ff 1b
		AOCU_NM	5	02-76	00	ec	42	ff 1b
2011/12/01	12:28:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	4	07-F8	22	ff	aa	00
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2011/12/01	12:29:16.0	XRT_FLD_ENA_435_OG [0x1b3]	1	07-F0	d8			
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/12/01	12:29:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/12/01	12:29:20.0	XRT_AEC_RESET_443_OG [0x1bb]	1	07-F0	d0			
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/12/01	12:29:22.0	XRT_ARS_DIS_431_OG [0x1af]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/12/01	12:29:24.0	XRT_FLD_RESET_412_OG [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/01	12:29:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	2	07-F0	c4	04		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2011/12/01	12:29:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	2	07-F0	c5	03		
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2011/12/01	12:29:30.0	XRT_CTRL_AUTO_406_OG [0x196]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/01	12:36:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/01	12:36:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]	5	02-76	00	ec	3a	f1 59
		AOCU_NM	5	02-76	00	ec	3a	f1 59
2011/12/01	12:38:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	4	07-F8	22	ff	aa	00
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2011/12/01	12:39:16.0	XRT_FLD_ENA_435_OG [0x1b3]	1	07-F0	d8			
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/12/01	12:39:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			

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2011/12/01	12:39:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/12/01	12:39:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/12/01	12:39:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/12/01	12:39:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04	
2011/12/01	12:39:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03	
2011/12/01	12:39:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/12/01	12:46:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/12/01	12:46:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00	ec 29 e0 63	
2011/12/01	12:48:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2011/12/01	12:49:16.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/12/01	12:49:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/12/01	12:49:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/12/01	12:49:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/12/01	12:49:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/12/01	12:49:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04	
2011/12/01	12:49:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03	
2011/12/01	12:49:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/12/01	12:56:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/12/01	12:56:30.0	AOCS_ORe-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00	ec 00 cf d8	
2011/12/01	12:58:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2011/12/01	12:59:16.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/12/01	12:59:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/12/01	12:59:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/12/01	12:59:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/12/01	12:59:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/12/01	12:59:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04	
2011/12/01	12:59:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03	
2011/12/01	12:59:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/12/01	13:06:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/12/01	13:06:30.0	AOCS_ORe-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00	eb c7 bf af	
2011/12/01	13:08:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2011/12/01	13:09:16.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/12/01	13:09:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/12/01	13:09:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/12/01	13:09:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/12/01	13:09:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/12/01	13:09:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04	
2011/12/01	13:09:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03	
2011/12/01	13:09:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/12/01	13:16:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/12/01	13:16:30.0	AOCS_ORe-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00	ed 30 af 9e	
2011/12/01	13:18:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2011/12/01	13:19:16.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/12/01	13:19:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/12/01	13:19:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/12/01	13:19:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/12/01	13:19:24.0	XRT_FLD_RESET_412_OG [0x19c]						

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2011/12/01	13:19:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/01	13:19:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2011/12/01	13:19:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/01	13:26:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/01	13:26:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/01	13:28:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	04 00 00 00 00
2011/12/01	13:29:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/01	13:29:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/01	13:29:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/01	13:29:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/01	13:29:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/01	13:29:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/01	13:29:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2011/12/01	13:29:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/01	14:53:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/01	14:53:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/01	14:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/01	14:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/01	15:06:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/01	15:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/12/01	16:29:00.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/12/01	16:29:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/01	16:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/01	16:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/01	16:53:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/01	16:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/01	17:42:24.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_Custom_418_OG [0x1a2]			
2011/12/01	17:42:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2011/12/01	17:42:30.0	AOCS_ORe-point_Start_23_OG [0x0ad]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/01	17:42:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/01	17:42:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/01	17:42:50.0	XRT_ARS_DIS_438_OG [0x1b6]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/01	17:45:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	AOCU_NM	5	02-76	00 00 00 00 00
2011/12/01	17:45:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/01	17:52:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/12/01	17:52:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/01	17:54:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/12/01	17:55:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/01	17:55:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/01	17:55:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/01	17:55:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/01	17:55:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/01	17:55:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/01	17:55:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/01	17:55:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2011/12/01	18:06:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/01	18:06:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da

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2011/12/01	18:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/01	18:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/01	18:29:30.0	XRT_Custom_418_OG [0x1a2]			
2011/12/01	18:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/01	19:43:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/01	19:43:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/01	19:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/01	19:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/01	20:06:30.0	XRT_Custom_418_OG [0x1a2]			
2011/12/01	20:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/01	21:20:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/01	21:20:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/01	21:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/01	21:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/01	21:43:30.0	XRT_Custom_418_OG [0x1a2]			
2011/12/01	21:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/01	22:58:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/01	22:58:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/01	22:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/01	23:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/01	23:18:00.0	XRT_Custom_418_OG [0x1a2]			
2011/12/01	23:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	00:35:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	00:35:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	00:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/02	00:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/02	00:42:00.0	XRT_Custom_418_OG [0x1a2]			
2011/12/02	00:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	01:58:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	01:58:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	01:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/02	02:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/02	02:17:30.0	XRT_Custom_418_OG [0x1a2]			
2011/12/02	02:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	03:31:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	03:31:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	03:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/02	03:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/02	03:55:00.0	XRT_Custom_418_OG [0x1a2]			
2011/12/02	03:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	05:05:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	05:05:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	05:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/02	05:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/02	05:32:30.0	XRT_Custom_418_OG [0x1a2]			
2011/12/02	05:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/02	06:00:00.0	AOCs_OrE-point_Start_23_OG [0x0ad]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/12/02	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/02	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			

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2011/12/02	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
			MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/02	06:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/12/02	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	06:10:00.0	AOCS_OrE-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	04 00 00 00 00
2011/12/02	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/02	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/02	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/02	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/02	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/02	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	06:12:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2011/12/02	06:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/02	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	06:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	06:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	06:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/02	06:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/02	07:10:00.0	XRT_Custom_418_OG [0x1a2]				
2011/12/02	07:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	08:25:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	08:25:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	08:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/02	08:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/02	08:46:30.0	XRT_Custom_418_OG [0x1a2]				
2011/12/02	08:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	10:06:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	10:06:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	10:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/02	10:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/02	10:18:30.0	XRT_Custom_418_OG [0x1a2]				
2011/12/02	10:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	15:29:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	15:29:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	15:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/02	15:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/02	15:52:30.0	XRT_Custom_418_OG [0x1a2]				
2011/12/02	15:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	17:05:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	17:05:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	17:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/02	17:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/02	17:29:00.0	XRT_Custom_418_OG [0x1a2]				
2011/12/02	17:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	18:00:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	18:00:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/02	18:00:30.0	AOCS_OrE-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00 00 00 00 00
2011/12/02	18:00:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/02	18:00:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]				

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2011/12/02	18:00:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
			MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/02	18:03:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/12/02	18:03:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	18:10:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	18:10:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	04 00 00 00 00
2011/12/02	18:12:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/02	18:13:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/02	18:13:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/02	18:13:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/02	18:13:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/02	18:13:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	18:13:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2011/12/02	18:13:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/02	18:13:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	18:42:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	18:42:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	18:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/02	18:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/02	19:06:00.0	XRT_Custom_418_OG [0x1a2]				
2011/12/02	19:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	20:19:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	20:19:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	20:19:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/02	20:22:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/02	20:43:00.0	XRT_Custom_418_OG [0x1a2]				
2011/12/02	20:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	21:57:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	21:57:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	21:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/02	22:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/02	22:19:00.0	XRT_Custom_418_OG [0x1a2]				
2011/12/02	22:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/02	23:34:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/02	23:34:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/02	23:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/02	23:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/02	23:46:00.0	XRT_Custom_418_OG [0x1a2]				
2011/12/02	23:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/03	01:02:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/03	01:02:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/03	01:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/03	01:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/03	01:17:30.0	XRT_Custom_418_OG [0x1a2]				
2011/12/03	01:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/03	02:33:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/03	02:33:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/03	02:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/03	02:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/03	02:54:00.0	XRT_Custom_418_OG [0x1a2]				

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2011/12/03	02:55:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/03	04:02:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/03	04:02:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/03	04:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/03	04:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/03	04:31:30.5	XRT_Custom_418_OG [0x1a2]							
2011/12/03	04:32:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/03	05:42:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/03	05:42:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/03	05:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/03	05:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/03	06:09:00.0	XRT_Custom_418_OG [0x1a2]							
2011/12/03	06:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/03	06:18:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/03	06:18:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/12/03	06:19:00.5	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/12/03	06:19:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/12/03	06:19:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/12/03	06:19:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/03	06:21:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2011/12/03	06:22:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/03	06:28:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/03	06:29:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/12/03	06:31:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/12/03	06:31:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/12/03	06:31:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/12/03	06:31:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/03	06:31:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/03	06:31:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/03	06:31:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/12/03	06:31:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/12/03	06:32:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/03	07:23:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/03	07:23:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/03	07:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/03	07:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/03	07:46:00.0	XRT_Custom_418_OG [0x1a2]							
2011/12/03	07:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/03	09:02:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/03	09:02:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/03	09:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/03	09:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/03	10:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/03	10:10:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 00 00 00 00				