

Period: 2011/12/13 10:22:00 - 2011/12/17 09:21:00

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XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad																	
Term		Pointing (x, y)						Comment									
12/13 14:17:06 - 12/13 17:58:24		Track (-165.9, -303.1) @ 12/13 14:10:00						# Observe AR 11374									
12/13 18:11:30 - 12/14 06:31:24		Track (-131.3, -302.7) @ 12/13 18:08:30						# AR 11374.									
12/14 06:44:30 - 12/14 09:15:30		Track (-20.9, -301.5) @ 12/14 06:41:30						# AR 11374.									
12/14 13:23:00 - 12/14 17:54:30		Track (37.8, -301.0) @ 12/14 13:20:00						# AR 11374.									
12/14 18:31:00 - 12/15 06:03:24		Track (83.0, -300.6) @ 12/14 18:28:00						# AR 11374.									
PROG= 13 Inf.-time(s)																	
Subr= 1		1-time(s)			2.0sec												
Seqn= 19		1-time(s)			2.0sec												
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)			Q=98	0	0	2.0sec		
└ Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)			Q=98	0	0	2.0sec		
Seqn= 96		4-time(s)			2.0sec												
└ Al-poly/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)			Q=95	3	0	2.0sec		
└ Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)			Q=95	3	0	2.0sec		
└ thin-Be/Open		med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)			Q=95	3	0	2.0sec		
└ Open/thick-Al		Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)			Q=95	3	0	2.0sec		
Subr= 2		1-time(s)			2.0sec												
Seqn= 62		15-time(s)			150.0sec												
└ thin-Be/Open		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)			Q=95	3	0	2.0sec		
└ Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)			Q=95	3	0	15.0sec		
└ thin-Be/Open		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)			Q=95	3	1	2.0sec		
└ Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)			Q=95	3	1	15.0sec		
└ thin-Be/Open		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)			Q=95	3	2	2.0sec		
└ Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)			Q=95	3	2	15.0sec		
└ thin-Be/Open		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)			Q=95	3	3	2.0sec		
└ Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)			Q=95	3	3	15.0sec		
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval			

XOB #18BF: Synoptic 9 Filter- 2x2 Q98 Short exp 2 loop														
Term			Pointing (x, y)					Comment						
12/14 10:15:00 - 12/14 13:19:54			Fixed (0.0, 0.0)					# 3/15						
PROG= 14 2-time(s)														
└ Subr= 1 1-time(s) 180.0sec														
└ Seqn= 82 1-time(s) 25.0sec														
└┐ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└┐ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└ Seqn= 98 1-time(s) 25.0sec														
└┐ Open/Ti-poly Open/Ti-poly close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└┐ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└ Seqn= 74 1-time(s) 25.0sec														
└┐ Al-poly/Open Al-poly/Open close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└┐ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└ Seqn= 18 1-time(s) 25.0sec														

XOB #18AF: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 4min cad															
Term				Pointing (x, y)				Comment							
12/15 06:16:30 - 12/15 08:13:00				Track (185.7, -299.8) @ 12/15 06:13:30				# AR 13374.							
PROG= 20 Inf.-time(s)															
Subr= 1		1-time(s)		2.0sec											
Seqn= 19		1-time(s)		2.0sec											
└─		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
└─		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Seqn= 96		4-time(s)		2.0sec											
└─		Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
└─		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec
└─		thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec
└─		Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Subr= 2		1-time(s)		2.0sec											
Seqn= 62		11-time(s)		240.0sec											
└─		thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec.
└─		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	15.0sec
└─		thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec
└─		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	15.0sec
└─		thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec
└─		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	15.0sec
└─		thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	2.0sec
└─		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	15.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

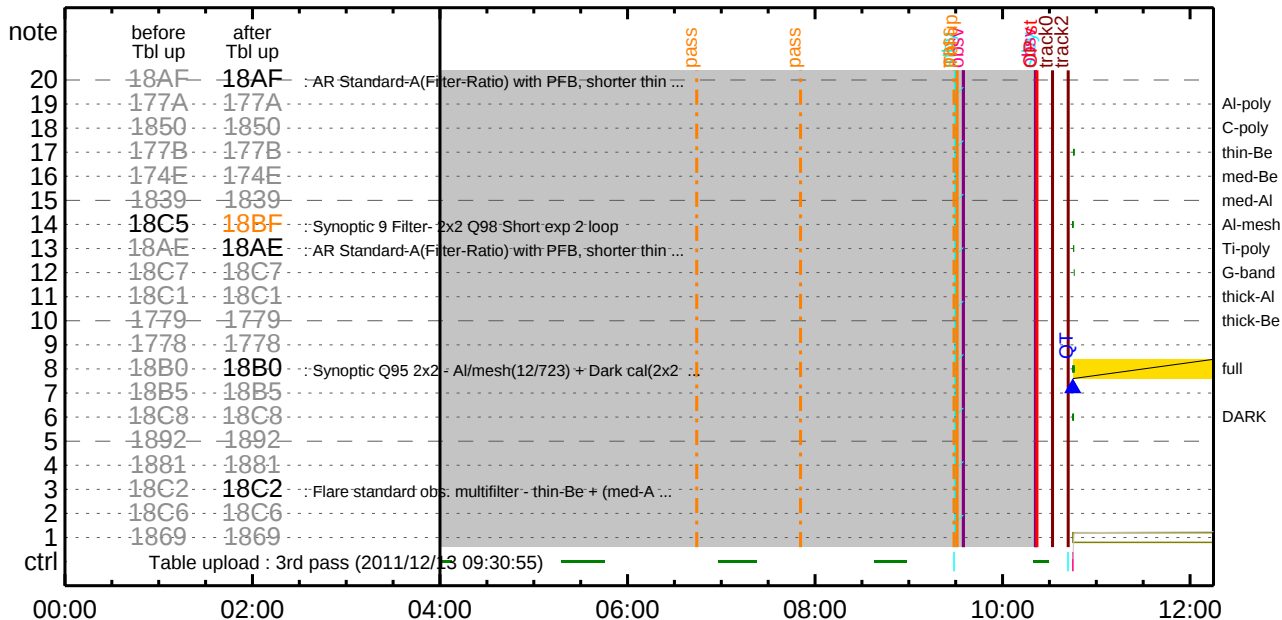
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FLD Patrol		
Term	Pointing (x, y)	Comment

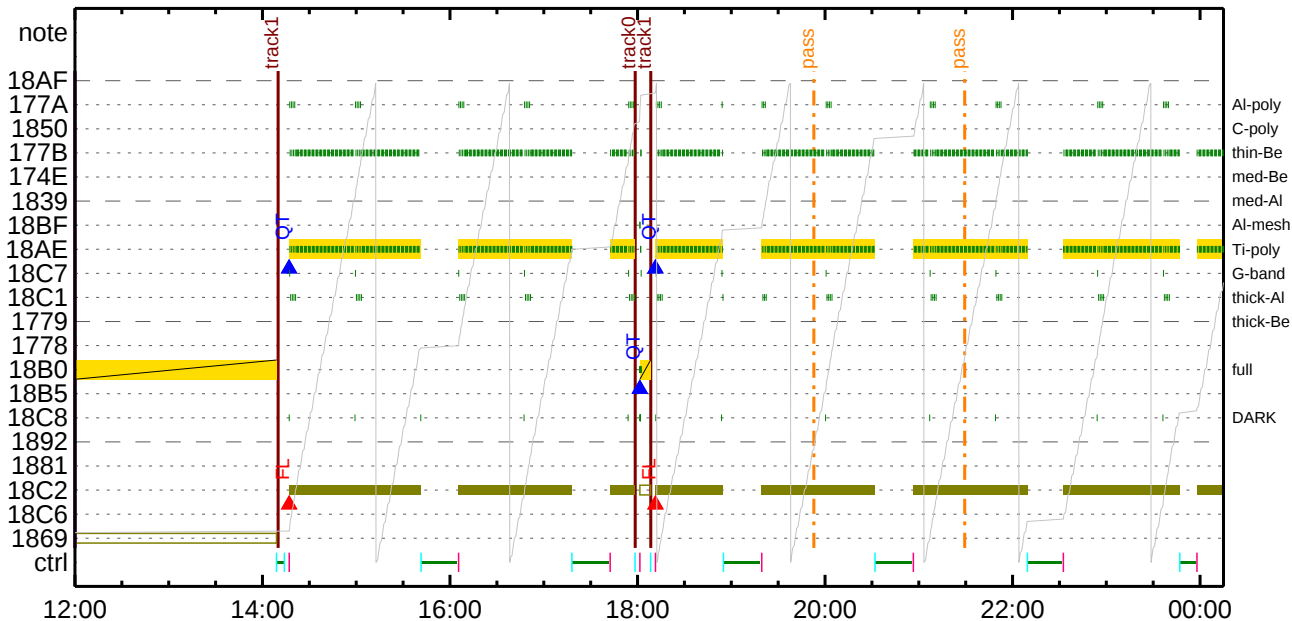
12/13 14:16:52 - 12/13 17:58:46 Track (-165.9, -303.1) @ 12/13 14:10:00 # Observe AR 11374
 12/13 18:11:16 - 12/14 06:31:46 Track (-131.3, -302.7) @ 12/13 18:08:30 # AR 11374.
 12/14 06:44:16 - 12/14 10:12:16 Track (-20.9, -301.5) @ 12/14 06:41:30 # AR 11374.
 12/14 13:22:46 - 12/14 18:18:16 Track (37.8, -301.0) @ 12/14 13:20:00 # AR 11374.
 12/14 18:30:46 - 12/15 06:03:46 Track (83.0, -300.6) @ 12/14 18:28:00 # AR 11374.
 12/15 06:16:16 - 12/17 09:21:00 Track (185.7, -299.8) @ 12/15 06:13:30 # AR 13374.

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

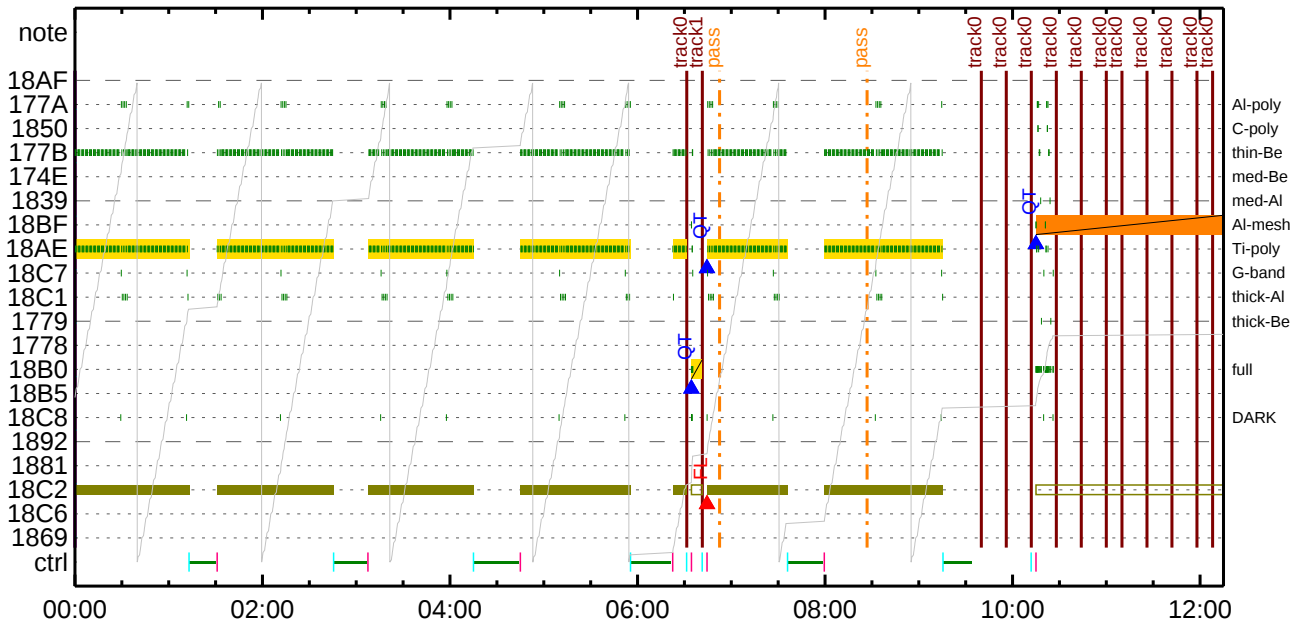
CMDI #0319 2011/12/13



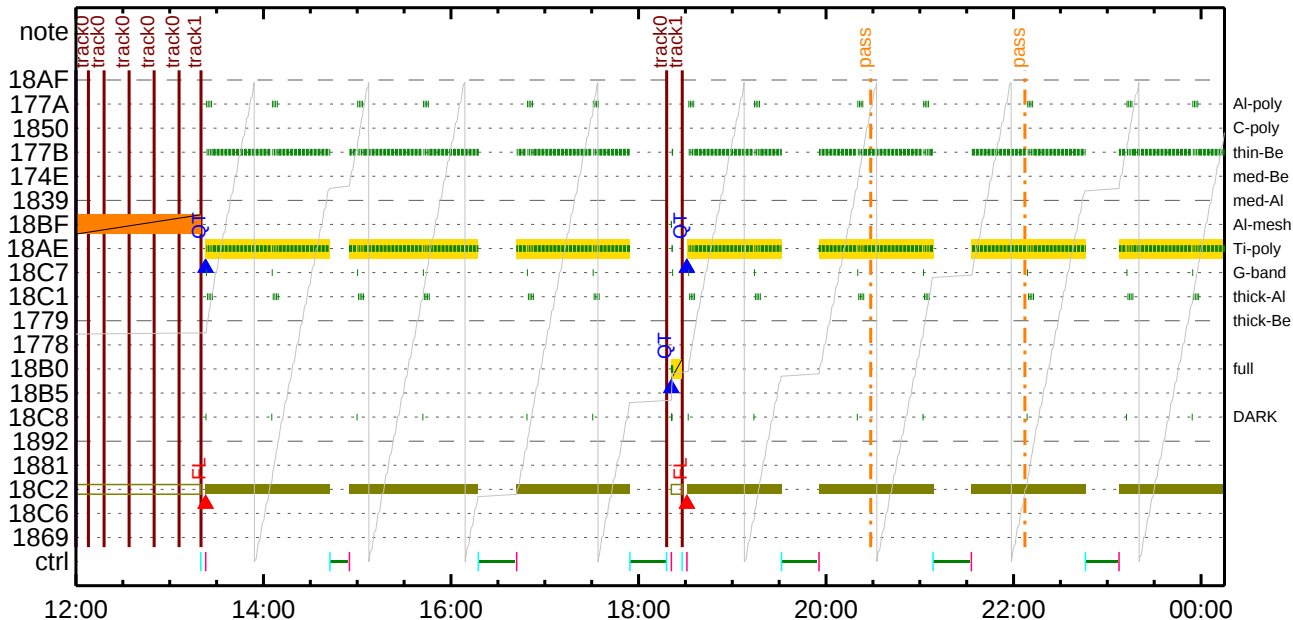
CMDI #0319 2011/12/13



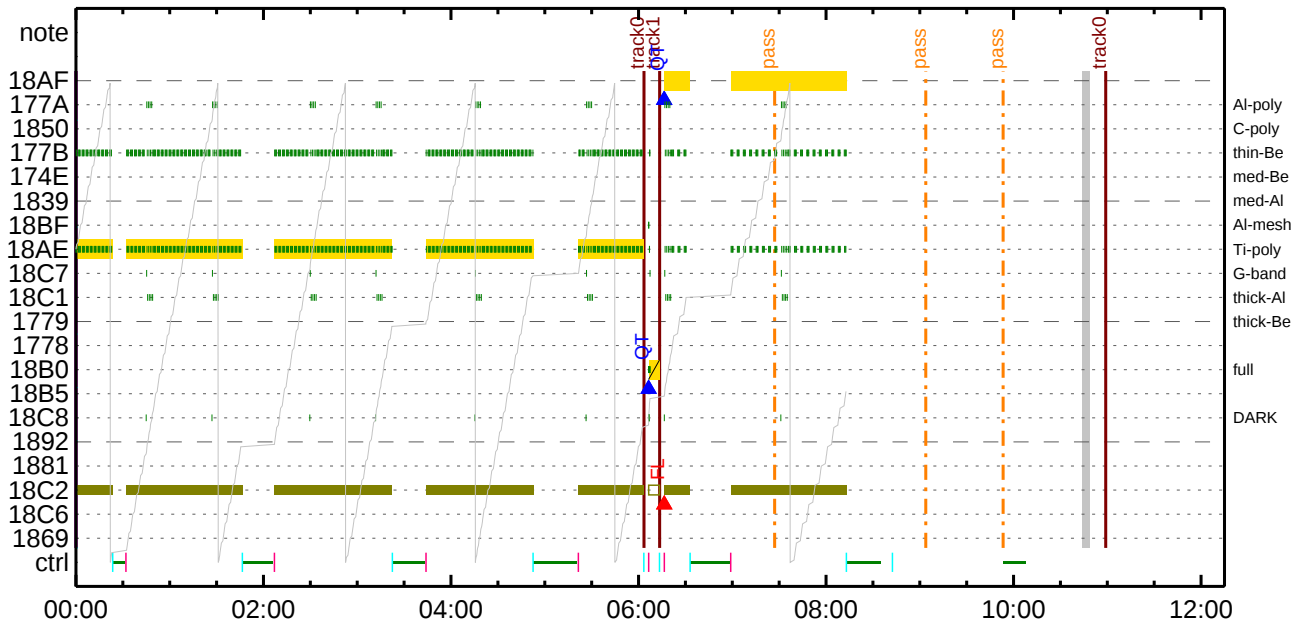
CMDI #0319 2011/12/14



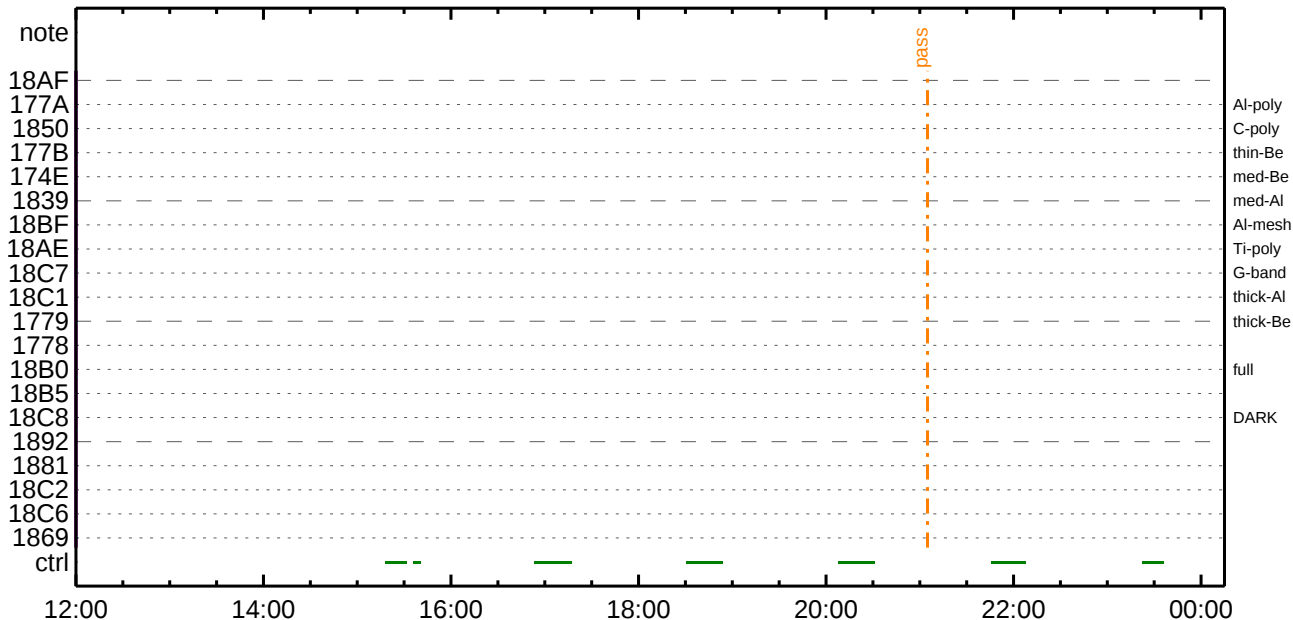
CMDI #0319 2011/12/14



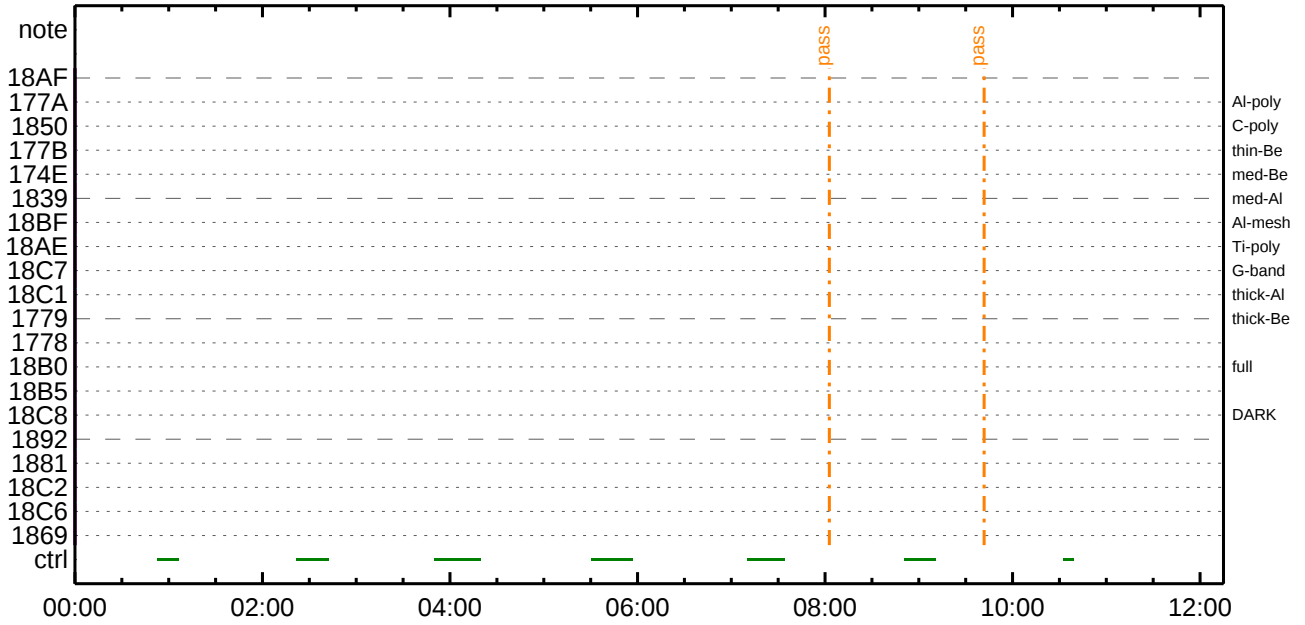
CMDI #0319 2011/12/15



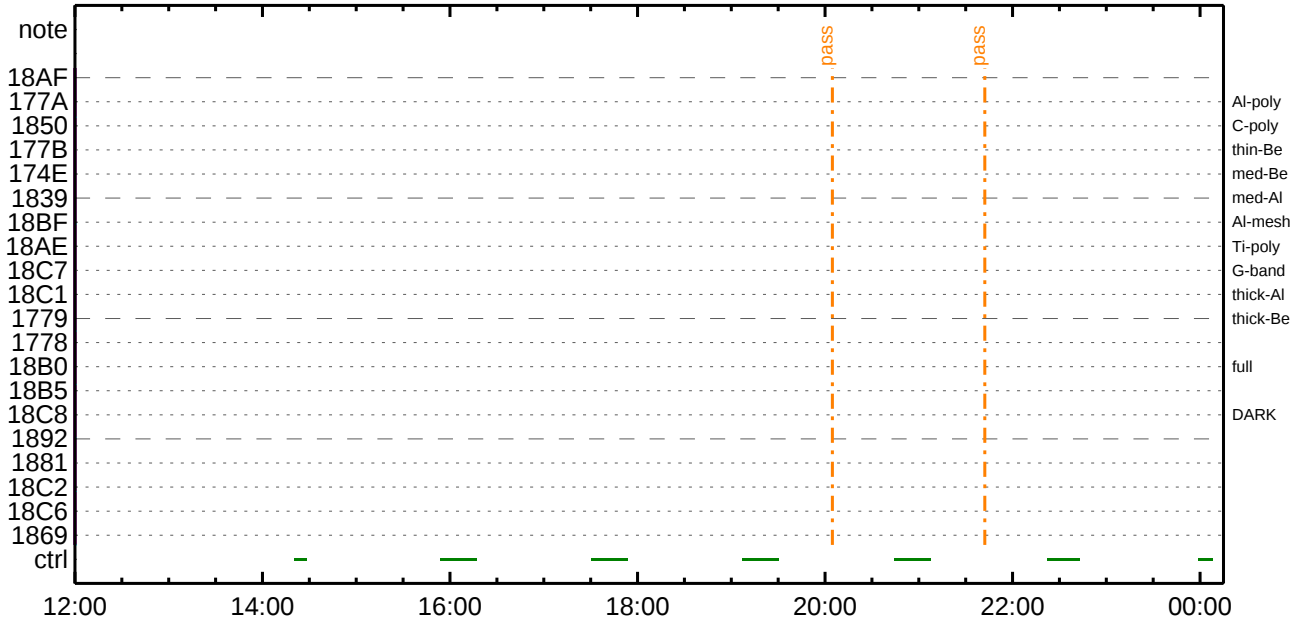
CMDI #0319 2011/12/15



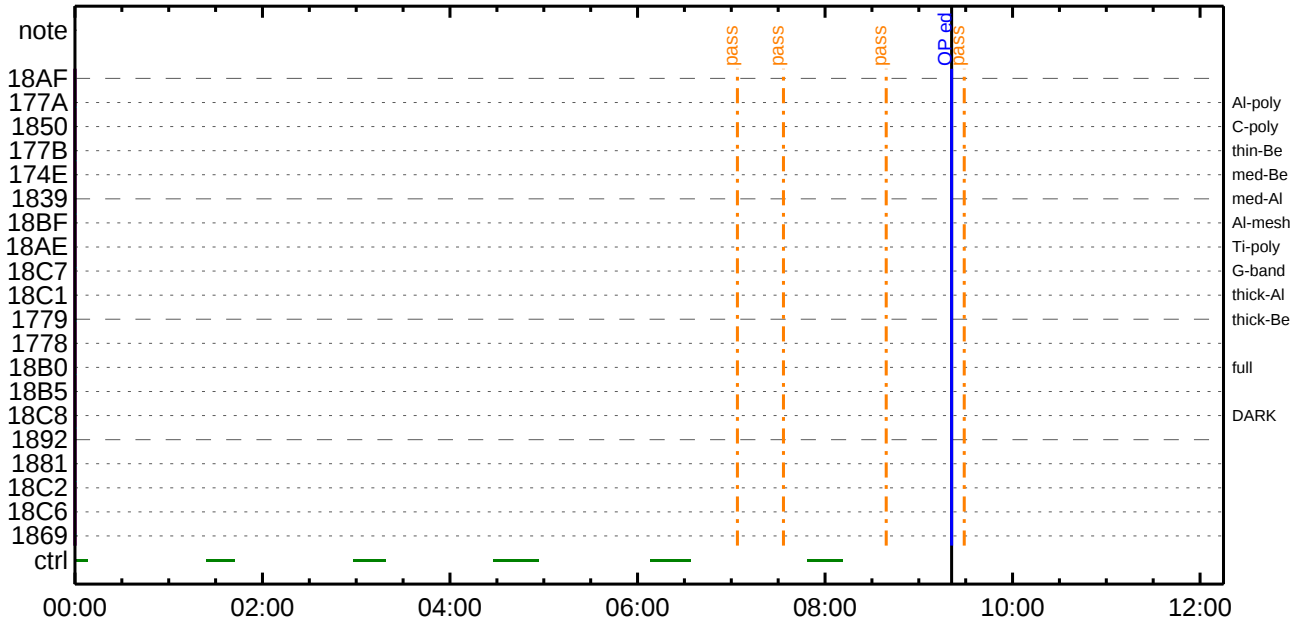
CMDI #0319 2011/12/16



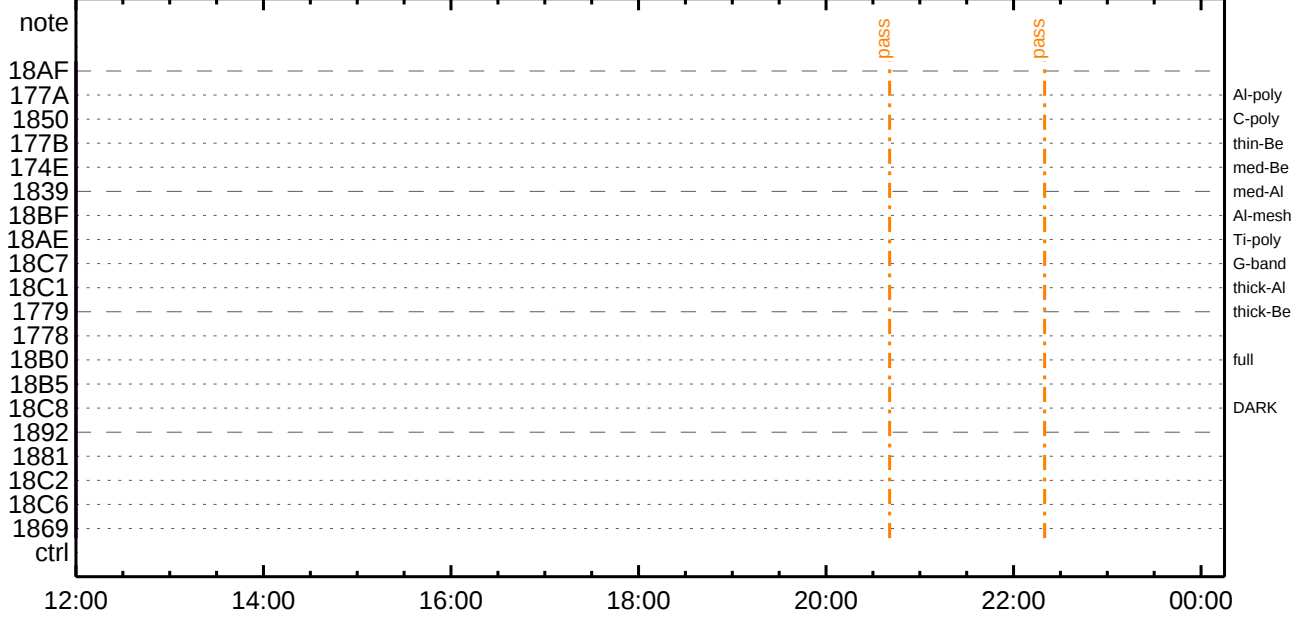
CMDI #0319 2011/12/16



CMDI #0319 2011/12/17



CMDI #0319 2011/12/17



Line	Command	Parameter	Condition	Count
0096	C.	0B0CjCSET0EDUMP0IÆ±°i¥Ñ¥10Ç100 030EjE		
0097	C.			
0098	C.	TI¥3¥D¥0¥É00ÀDİÇ(UT)		
0099	+	TI 2011-12-13 10:17:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	00[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.			
0103	+	TI 2011-12-13 10:17:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	00[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.			
0107	+	TI 2011-12-13 10:17:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	00[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.			
0111	+	TI 2011-12-13 10:21:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	00[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.			
0115	C.	°Ê²¼0IÄê%IİÑ0I¥Å\$¥Ä¥~¹àİÜ		
0116	C.	00[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	00[HK1_TI_CMD_NUM]	EQ	4
0118	C.	00[HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	00[HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.			
0121	C.	*****		
0122	C.	TIİİ°è¥Ä¥0¥x		
0123	C.	*****		
0124	C.			
0125	C.	TI_TBL(0x03AB00-0x03AEFFj\$ 1024byte)		
0126	+	DC 01-23 DHU_DMA_DUMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	00[HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	00[HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	00[HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	00[HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	00[HK1_DMA_DUMP_PIM]	EQ	DHU
0133	+	DC 01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	00[HK1_PKT_FORM_NO]	EQ	7
0136	C.	00[HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	00[HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	00[HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	00[HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.			
0141	C.	¥Ä¥0¥x½aİ»00³İÇ\$		
0142	C.	00[HK1_DMP_CHK_FLG]	EQ	NON
0143	C.			
0144	C.	RAM ID=TI_TBL0I%Ê¹Ç•è²İOK00³İÇ\$		
0145	C.			
0146	C.	DHU¥âj¼¥ÉjÊ¼ý½ ¥İj¼¥ÉjÊ00İâ0¹		
0147	+	DC 01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	00[HK1_PKT_FORM_NO]	EQ	2
0150	C.	00[HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	00[HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	00[HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.			
0154	C.	*****		
0155	C.	SOT TI command set		
0156	C.	*****		
0157	C.	Execute, after the success of OP upload.		
0158	+	TI 2011-12-13 10:21:16.0		
0159	DC	07-F0 MDP_SOT_MODE_STBY		
0160	BC	(41)		
0161	C.	-----		
0162	C.	HK1_TI_CMD_NUM = 1 CNTUP []		
0163	C.	-----		
0164	C.	***** SOT END *****		
0165	C.	Stop EIS observation and temporarily disable EIS mode changes		
0166	C.			
0167	C.			
0168	C.	***** Start EIS operation (TI set) *****		
0169	C.	Execute, after the success of OP upload.		
0170	C.	Set EIS TI-commands		
0171	+	TI 2011-12-13 10:21:30.0		
0172	DC	07-FC EIS_MODE_MANU		
0173	BC	(21 02)		
0174	+	TI 2011-12-13 10:21:40.0		
0175	DC	07-FC EIS_MODE_CHG_DIS		
0176	BC	(22)		
01				

0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥Ð¥¹•İ Daily±¿İÑ¤Ė´0¤¹¤ĖDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ĩăLOS¥Á¥S¥Ā¥¼Ā»Üĭă
0203 C.
0204 . C. ***** LOS *****
0205 C.


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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 5001483.6 +- 1.0 (s) [ ]
0136 C.
0137 C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_MANU
0140 BC (21 02)
0141 . C. Verify EIS in MANUAL mode
0142 . C. Estimated OBSTBL upload time is 36s
0143 C. *****
0144 C. EIS START OBSTBL LOAD
0145 C. *****
0146 . S. RAM ram-820:EIS_OBSTBL
0147 ( )
0148 +. DC 07-FC EIS_DUMP_OBSTBL
0149 BC (07 07 07 00 00 70 00)
0150 C.
0151 C. Execute, after the success of OBSTBL upload.
0152 C. Set EIS TI-commands
0153 +. TI 2011-12-13 10:21:50.0
0154 DC 07-FC EIS_MODE_CHG_ENA
0155 BC (20)
0156 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0157 C. *****
0158 C. EIS END OBSTBL LOAD
0159 C. *****
0160 C.
0161 . C. ***** MDP '0ÃÎÎ»0%ŸpĒĀĐ□1□ēDCBC•x²è *****
0162 C. (%ã°İ¥0¥Ā¥Ē¥p¥Ē¥Ā¥Ĉ¥ē□Ē½%□□Ā»Ů□1□ē)
0163 . S. DC-BC dcbc-402:DCBC
0164 (MDP_known_event)
0165 C.
0166 C.
0167 . C. ***** ¥Đ¥1•İ Daily±¿İÑ□Ē´Đ□1□ēDCBC•x²è *****
0168 . S. DC-BC dcbc-153:DCBC
0169 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0170 C.
0171 C.
0172 . C. ¡ãLOS¥Ā¥$¥Ā¥¬¼Ā»Ů¡ã
0173 C.
0174 . C. ***** LOS *****
0175 C.

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main-527 2011-12-13 13:27:23 234 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. jãAOS¥Á¥S¥Ã¥¬¼Å»Üjã
0005 C.
0006 C. ¥Ä¥ßj¼¥³¥Ð¥ó¥ÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ÄijË□¿□Ä□•µ°Æ»Î×ÅÇ□İ¥¢¥Ä¥×¥İj¼¥ÊjËÈ½µ•İÊjËÈ¼°Ç□□•□¿¼¹Ç□İj¢À®, ù□¹□È□□□ÇÄ÷¿®□•□È□□□³□Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷¿®µjON
0016 C. *****
0017 C. ¨ °ÆÄ, İ×ÊÝ□äLÖS□□Ç□İ»p´Ô□ò¹İİ, □•j¢ÉÔİ×□ÈXÂÓON□İ¹Ô□È□İ□È□□□³□Èj£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ¢¢[HK1_XPA_ON/OFF] EQ ON
0025 C. ¢¢[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ¢¢[HK1_XMOD_ON/OFF] EQ ON
0027 C. ¢¢[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Ð¥ó¥É¥İ¥Ã¥¬¼ÔÄÖ□¬°ÄÄÈ□•□¿□éj¢°Ê²¼□İ°ÆÄ, ¼ê½Ç□ò¼Ä¹Ô□¹□èj£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äİ¼İ°ÆÄ
0033 C. *****
0034 C. ¨ RESTARTjËPT1jË□•□¿□□¼¹Ç□İj¢°Ê²¼□İ¼Ä¹Ô□»□°j¢DCBC-150□Ø¿Ê□àj£
0035 C.
0036 . C. jãPT1°ÆÄ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ij¼Ú)
0043 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ij¼Ú)
0044 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ij¼Ú)
0045 C.
0046 . C. jã¥¢¥ó¥Æ¥ÉÄÜÄØjÊÄ•Ä°²óÈòjË, ä□İ°ÆÄ, °Æ³«ijã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ij¼Ú)
0050 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ij¼Ú)
0051 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ij¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, □¬¼«Æ°Ää»ß□•□¿, äj¢°Ê²¼□ò¼Ä¹Ô□¹□èj£
0055 C. ¥¢¥ó¥Æ¥ÉÄÜÄØ□äÄ•Ä°²óÈò¬¼á□□¼¹Ç□İ' °İ»□¹□È□□□ÇÄÔ□Äj£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äİ¼İ°ÆÄ
0059 C. *****
0060 C. ¨ RESTARTjËPT2jË□•□¿□□¼¹Ç□İj¢°Ê²¼□İ¼Ä¹Ô□»□°j¢DCBC-151□Ø¿Ê□àj£
0061 C.
0062 . C. jãPT2°ÆÄ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ij¼Ú)
0069 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ij¼Ú)
0070 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ij¼Ú)
0071 C.
0072 . C. jã¥¢¥ó¥Æ¥ÉÄÜÄØjÊÄ•Ä°²óÈòjË, ä□İ°ÆÄ, °Æ³«ijã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ij¼Ú)
0076 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ij¼Ú)
0077 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ij¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Ää»ßj¢XÄ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ÆÄ, Ää»ßjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ¢¢[HK1_REP_STA/STP] EQ STOP
0087 C. ¢¢[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ¢¢[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÄ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ¢¢[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ¢¢[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C. *****
0099 C. SOT table upload
0100 C. *****
0101 C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 C. <Upload FG Observation Table>
0109 S. RAM ram-266:MDP_OBS_F
0110 ( )
0111 C.
0112 C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. < Upload DPL table >
0120 C.
0121 C. ¥¢¥Ã¥×¥í¼¥É¤ÎÁ°¤ĖSTS_CHK¤òOFF¤Ė¤¹¤Ė
0122 C.
0123 S. RAM ram-271:MDP_DPL
0124 ( )
0125 C.
0126 C. < Dump RAMID=MDP_DPL >
0127 +. DC 07-F0 MDP_DUMP_FGTBL
0128 BC (82 07 00 38 b8 00 40)
0129 C. -----
0130 C. MDP_DPL verify = OK [ ]
0131 C. -----
0132 C.
0133 C. STS_CHK¤òON¤Ė¤¹¤Ė
0134 C.
0135 C. < Update MDP DSC PAR1 >
0136 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0137 BC (4c)
0138 C. MDP_CMD_CODE = F04C0700 [ ]
0139 C. MDP_CMD_CNT (count-up 1) [ ]
0140 C. -----
0141 C.
0142 C.
0143 C. *****
0144 C. SOT TI command set
0145 C. *****
0146 C. Execute, after the success of TBL upload.
0147 +. TI 2011-12-13 10:21:18.0
0148 DC 07-F0 MDP_SOT_MODE_OBSV
0149 BC (40)
0150 C. -----
0151 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0152 C. -----
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C.
0157 +. DC 07-F0 MDP_XRT_CTRL_MANU
0158 BC (c1)
0159 +. DC 07-F0 MDP_XRT_MODE_STBY
0160 BC (c3)
0161 C. ----- Success Verify ? OK / NG____
0162 C.
0163 C. XRT Obs. Table Upload
0164 S. RAM ram-291:MDP_OBS_X
0165 ( )
0166 C.
0167 +. DC 07-F0 MDP_DUMP_XRTTBL
0168 BC (84 07 00 00 00 3a d4)
0169 C. ----- Comparison Check ? OK / ERR ____
0170 C.
0171 C.
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 01 b1 b1 04 04)
0174 +. DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 02 b1 b1 08 08)
0176 +. DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 03 b1 b1 08 08)
0178 +. DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 04 b1 b1 06 06)
0180 +. DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 05 85 83 06 06)
0182 +. DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 06 80 80 20 20)
0184 +. DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 07 80 80 20 08)
0186 +. DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 08 80 80 08 20)
0188 +. DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 09 85 83 06 06)
0190 +. DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 0a 85 83 08 08)
0192 +. DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 0f 80 80 06 06)

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0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 10 80 80 08 08)
0196 + DC 07-F0 MDP_XRT_FLD_ENA
0197 BC (d8)
0198 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0199 BC (c8)
0200 + DC 07-F0 MDP_XRT_AEC_RESET
0201 BC (d0)
0202 + DC 07-F0 MDP_XRT_ARS_DIS
0203 BC (d5)
0204 + DC 07-F0 MDP_XRT_FLD_RESET
0205 BC (da)
0206 . C. ----- Success Verify ? OK / NG ____
0207 C.
0208 C.
0209 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0210 C.
0211 +. DC 07-F0 MDP_XRT_MODE_OBSV
0212 BC (c2)
0213 +. TI 2011-12-13 10:21:02.0
0214 DC 07-F0 MDP_XRT_MODE_OBSV
0215 BC (c2)
0216 . C. ----- Success Verify ? OK / NG ____
0217 C.
0218 C. ***** XRT END *****
0219 C.
0220 . C. ***** MDP 'ûÃîï»ö¼ýðĒð¹æðCBC•x²è *****
0221 C. (%ã°î¥0¥Å¥Ē¥ð¥Ē¥ã¥Ĉ¥ĕðĒ¼¼¼¼Å»Û¹æ)
0222 . S. DC-BC dcbc-402:DCBC
0223 (MDP_known_event)
0224 C.
0225 C.
0226 . C. ***** ¥ð¥¹•İ Daily±ĈİŇðĒ'ð¹æðCBC•x²è *****
0227 . S. DC-BC dcbc-153:DCBC
0228 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0229 C.
0230 C.
0231 . C. ¡ãLOS¥Å¥S¥Ā¥¼Å»Û¡ä
0232 C.
0233 . C. ***** LOS *****
0234 C.
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*** OP Sequence for XRT ***

2011/12/13	10:32:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76 00 dc 73 4f 1a	
2011/12/13	10:41:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2011/12/13	10:41:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2011/12/13	10:42:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76 02 00 00 00 00	
2011/12/13	10:42:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0 d9	
2011/12/13	10:42:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9	
2011/12/13	10:42:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2011/12/13	10:44:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 08	
2011/12/13	10:45:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2011/12/13	14:09:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2011/12/13	14:09:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2011/12/13	14:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2011/12/13	14:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76 01 00 00 00 00	
2011/12/13	14:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2011/12/13	14:14:00.5	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2011/12/13	14:16:32.5	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00	
2011/12/13	14:16:52.5	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2011/12/13	14:16:54.5	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2011/12/13	14:16:56.5	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2011/12/13	14:16:58.5	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2011/12/13	14:17:00.5	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2011/12/13	14:17:02.5	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0d	
2011/12/13	14:17:04.5	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 03	
2011/12/13	14:17:06.5	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2011/12/13	15:41:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2011/12/13	15:41:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2011/12/13	15:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2011/12/13	15:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2011/12/13	16:04:30.0	XRT_Custom_418_OG [0x1a2]			
2011/12/13	16:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2011/12/13	17:18:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2011/12/13	17:18:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2011/12/13	17:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2011/12/13	17:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2011/12/13	17:41:30.0	XRT_Custom_418_OG [0x1a2]			
2011/12/13	17:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2011/12/13	17:58:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2011/12/13	17:58:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2011/12/13	17:58:30.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76 00 00 00 00 00	
2011/12/13	17:58:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0 d9	
2011/12/13	17:58:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9	
2011/12/13	17:58:50.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2011/12/13	18:01:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 08	
2011/12/13	18:01:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2011/12/13	18:08:24.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2011/12/13	18:08:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76 01 00 00 00 00	
2011/12/13	18:10:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			

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2011/12/13	18:11:16.0	XRT_FLD_ENA_411_0G [0x19b]	4	07-F8	22	fe	97	00
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/12/13	18:11:18.0	XRT_FLRCTRL_ENA_413_0G [0x19d]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/12/13	18:11:20.0	XRT_AEC_RESET_443_0G [0x1bb]	1	07-F0	d0			
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/12/13	18:11:22.0	XRT_ARS_DIS_431_0G [0x1af]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/12/13	18:11:24.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/13	18:11:26.0	XRT_QT_PROG_SET_440_0G [0x1b8]	2	07-F0	c4	0d		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2011/12/13	18:11:28.0	XRT_FL_PROG_SET_414_0G [0x19e]	2	07-F0	c5	03		
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2011/12/13	18:11:30.0	XRT_CTRL_AUTO_406_0G [0x196]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/13	18:55:00.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/13	18:55:02.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/13	18:55:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/13	18:58:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/13	19:18:30.0	XRT_Custom_418_0G [0x1a2]						
2011/12/13	19:19:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/13	20:32:00.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/13	20:32:02.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/13	20:32:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/13	20:35:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/13	20:55:30.0	XRT_Custom_418_0G [0x1a2]						
2011/12/13	20:56:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/13	22:09:30.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/13	22:09:32.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/13	22:09:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/13	22:12:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/13	22:31:30.0	XRT_Custom_418_0G [0x1a2]						
2011/12/13	22:32:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/13	23:47:00.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/13	23:47:02.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/13	23:47:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/13	23:50:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/13	23:57:00.0	XRT_Custom_418_0G [0x1a2]						
2011/12/13	23:58:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/14	01:13:00.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/14	01:13:02.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/14	01:13:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/14	01:16:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/14	01:30:00.0	XRT_Custom_418_0G [0x1a2]						
2011/12/14	01:31:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/14	02:45:30.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/14	02:45:32.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/14	02:45:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/14	02:48:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/14	03:06:30.0	XRT_Custom_418_0G [0x1a2]						
2011/12/14	03:07:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/14	04:15:00.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/14	04:15:02.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/14	04:15:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/14	04:18:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/14	04:44:00.0	XRT_Custom_418_0G [0x1a2]						
2011/12/14	04:45:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]						

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2011/12/14	05:55:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/14	05:55:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/14	05:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/14	05:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/14	06:21:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/14	06:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/14	06:31:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/14	06:31:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/14	06:31:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00
2011/12/14	06:31:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/14	06:31:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/12/14	06:31:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/14	06:34:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/12/14	06:34:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/14	06:41:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/14	06:41:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00
2011/12/14	06:43:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/14	06:44:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/14	06:44:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/14	06:44:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/14	06:44:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/14	06:44:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/14	06:44:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2011/12/14	06:44:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/14	06:44:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/14	07:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/14	07:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/14	07:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/14	07:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/14	07:58:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/14	07:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/14	09:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/14	09:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/14	09:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/14	09:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/14	09:40:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 00 00 ac cd
2011/12/14	09:56:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 00 00 d6 67
2011/12/14	10:11:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/14	10:11:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/14	10:12:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00
2011/12/14	10:12:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/14	10:12:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/12/14	10:12:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/14	10:14:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2011/12/14	10:15:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/14	10:28:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00 29 99
2011/12/14	10:44:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 00 00 53 33

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2011/12/14	11:00:00.0	AOCS_ORe-point_Start_9_OG [0x09f] AOCU_NM	5 02-76 00 d6 36 b7 8e
2011/12/14	11:10:00.0	AOCS_ORe-point_Start_10_OG [0x0a0] AOCU_NM	5 02-76 00 b4 b5 db 75
2011/12/14	11:26:00.0	AOCS_ORe-point_Start_11_OG [0x0a1] AOCU_NM	5 02-76 00 ac 5b 00 00
2011/12/14	11:42:00.0	AOCS_ORe-point_Start_12_OG [0x0a2] AOCU_NM	5 02-76 00 b4 b5 24 8b
2011/12/14	11:58:00.0	AOCS_ORe-point_Start_13_OG [0x0a3] AOCU_NM	5 02-76 00 d6 36 48 72
2011/12/14	12:08:00.0	AOCS_ORe-point_Start_14_OG [0x0a4] AOCU_NM	5 02-76 00 29 ca b7 8e
2011/12/14	12:18:00.0	AOCS_ORe-point_Start_15_OG [0x0a5] AOCU_NM	5 02-76 00 4b 4b db 75
2011/12/14	12:34:00.0	AOCS_ORe-point_Start_16_OG [0x0a6] AOCU_NM	5 02-76 00 53 a5 00 00
2011/12/14	12:50:00.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5 02-76 00 4b 4b 24 8b
2011/12/14	13:06:00.0	AOCS_ORe-point_Start_18_OG [0x0a8] AOCU_NM	5 02-76 00 29 db 48 72
2011/12/14	13:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1 07-F0 c1
2011/12/14	13:20:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5 02-76 01 00 00 00 00
2011/12/14	13:22:26.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4 07-F8 22 fe 97 00
2011/12/14	13:22:46.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1 07-F0 d8
2011/12/14	13:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1 07-F0 c8
2011/12/14	13:22:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1 07-F0 d0
2011/12/14	13:22:52.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1 07-F0 d5
2011/12/14	13:22:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1 07-F0 da
2011/12/14	13:22:56.0	XRT_QT_PROG_SET_440_OG [0x1b8] MDP_XRT_QT_PROG_SET	2 07-F0 c4 0d
2011/12/14	13:22:58.0	XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET	2 07-F0 c5 03
2011/12/14	13:23:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2011/12/14	14:42:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1 07-F0 c1
2011/12/14	14:42:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1 07-F0 da
2011/12/14	14:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1 07-F0 e8
2011/12/14	14:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1 07-F0 e9
2011/12/14	14:54:00.0	XRT_Custom_418_OG [0x1a2]	
2011/12/14	14:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2011/12/14	16:17:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1 07-F0 c1
2011/12/14	16:17:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1 07-F0 da
2011/12/14	16:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1 07-F0 e8
2011/12/14	16:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1 07-F0 e9
2011/12/14	16:41:00.0	XRT_Custom_418_OG [0x1a2]	
2011/12/14	16:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2011/12/14	17:54:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1 07-F0 c1
2011/12/14	17:54:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1 07-F0 da
2011/12/14	17:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1 07-F0 e8
2011/12/14	17:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1 07-F0 e9
2011/12/14	18:17:54.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1 07-F0 c1
2011/12/14	18:17:56.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4 07-F8 22 ff aa 00
2011/12/14	18:18:00.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5 02-76 00 00 00 00 00
2011/12/14	18:18:16.0	XRT_FLD_DIS_402_OG [0x192] MDP_XRT_FLD_DIS	1 07-F0 d9
2011/12/14	18:18:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1] MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2011/12/14	18:18:20.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1 07-F0 d5
2011/12/14	18:20:58.0	XRT_QT_PROG_SET_417_OG [0x1a1] MDP_XRT_QT_PROG_SET	2 07-F0 c4 08
2011/12/14	18:21:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2011/12/14	18:27:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1 07-F0 c1
2011/12/14	18:28:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5 02-76 01 00 00 00 00
2011/12/14	18:30:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	

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2011/12/14	18:30:46.0	XRT_FLD_ENA_411_0G [0x19b]	4	07-F8	22	fe	97	00
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/12/14	18:30:48.0	XRT_FLRCTRL_ENA_413_0G [0x19d]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/12/14	18:30:50.0	XRT_AEC_RESET_443_0G [0x1bb]	1	07-F0	d0			
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/12/14	18:30:52.0	XRT_ARS_DIS_431_0G [0x1af]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/12/14	18:30:54.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/14	18:30:56.0	XRT_QT_PROG_SET_440_0G [0x1b8]	2	07-F0	c4	0d		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2011/12/14	18:30:58.0	XRT_FL_PROG_SET_414_0G [0x19e]	2	07-F0	c5	03		
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2011/12/14	18:31:00.0	XRT_CTRL_AUTO_406_0G [0x196]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/14	19:31:30.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/14	19:31:32.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/14	19:31:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/14	19:34:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/14	19:54:30.0	XRT_Custom_418_0G [0x1a2]						
2011/12/14	19:55:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/14	21:08:30.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/14	21:08:32.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/14	21:08:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/14	21:11:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/14	21:32:00.0	XRT_Custom_418_0G [0x1a2]						
2011/12/14	21:33:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/14	22:46:00.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/14	22:46:02.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/14	22:46:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/14	22:49:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/14	23:06:30.0	XRT_Custom_418_0G [0x1a2]						
2011/12/14	23:07:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/15	00:23:30.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/15	00:23:32.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/15	00:23:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/15	00:26:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/15	00:31:00.0	XRT_Custom_418_0G [0x1a2]						
2011/12/15	00:32:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/15	01:46:30.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/15	01:46:32.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/15	01:46:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/15	01:49:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/15	02:06:00.0	XRT_Custom_418_0G [0x1a2]						
2011/12/15	02:07:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/15	03:22:30.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/15	03:22:32.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/15	03:22:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/15	03:25:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/15	03:43:00.0	XRT_Custom_418_0G [0x1a2]						
2011/12/15	03:44:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/12/15	04:52:30.0	XRT_CTRL_MANU_408_0G [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/12/15	04:52:32.0	XRT_FLD_RESET_412_0G [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2011/12/15	04:52:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2011/12/15	04:55:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/12/15	05:20:30.0	XRT_Custom_418_0G [0x1a2]						
2011/12/15	05:21:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]						

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2011/12/15	06:03:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/15	06:03:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/15	06:03:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/15	06:03:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00
2011/12/15	06:03:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/15	06:03:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/12/15	06:06:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/15	06:06:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/12/15	06:13:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/15	06:13:30.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/15	06:15:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	01 00 00 00 00
2011/12/15	06:16:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/15	06:16:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/15	06:16:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/15	06:16:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/15	06:16:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/15	06:16:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/15	06:16:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2011/12/15	06:16:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/15	06:33:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/15	06:33:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/15	06:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/15	06:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/15	06:58:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/15	06:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2011/12/15	08:13:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/15	08:13:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/15	08:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/15	08:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/15	08:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/15	10:59:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00