

XRT Timeline to be uploaded on 2011/12/15

Period: 2011/12/15 10:49:00 - 2011/12/20 09:33:00

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Normal mode

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XOB #18AF: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 4min cad

Term	Pointing (x, y)	Comment
12/15 11:02:00 - 12/15 17:59:54	Track (252.6, -299.6) @ 12/15 10:59:00	# OP start + 10min, AR11374
12/15 18:13:00 - 12/15 23:22:30	Track (313.0, -299.3) @ 12/15 18:10:00	# Cont.
12/16 02:42:30 - 12/16 05:30:30	Track (381.3, -299.1) @ 12/16 02:31:00	AR 11374
12/16 06:20:00 - 12/16 17:30:00	Track (411.4, -299.0) @ 12/16 06:17:00	# Cont.
12/16 18:08:30 - 12/17 04:27:30	Track (501.9, -299.0) @ 12/16 18:05:30	# Cont.
12/17 05:52:30 - 12/17 06:08:00	Track (585.6, -299.3) @ 12/17 05:50:30	# Cont.

PROG= 20 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 19	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 96	4-time(s)	2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Subr= 2	1-time(s)	2.0sec
Seqn= 62	11-time(s)	240.0sec
thin-Be/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
thin-Be/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
thin-Be/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
thin-Be/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #18B0: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(24/1443) + Thin-Be(88/2)

Term	Pointing (x, y)	Comment
12/15 18:03:00 - 12/15 18:09:54	Fixed (0.0, 0.0)	synoptic
12/16 06:10:00 - 12/16 06:16:54	Fixed (0.0, 0.0)	synoptic, shifted 7.0 min
12/16 17:58:30 - 12/16 18:05:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min
12/17 05:43:30 - 12/17 05:49:24	Fixed (0.0, 0.0)	synoptic, shifted -19.5 min

PROG= 08 1-time(s)

Subr= 1	1-time(s)	12.0sec
Seqn= 46	1-time(s)	4.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 5	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 69	1-time(s)	4.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 50	1-time(s)	2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 4	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #18C9: Comet Lovejoy-Al/Mesh 2x2 2s-with Med-Be Med-Al and Dark in context-1x1-512x512 16s-East Limb offset (928,1024)

Term	Pointing (x, y)	Comment
12/15 23:57:58 - 12/16 00:30:54	Fixed (-903.5, 375.0)	Comet obs. at NE limb.

PROG= 02 1-time(s)

Subr= 2	1-time(s)	2.0sec
Seqn= 4	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 1	20-time(s)	2.0sec
Seqn= 11	10-time(s)	2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.00s Obs 2x2 1024x1024 (928, 1024) DPCM 0 0 2.0sec
Seqn= 35	1-time(s)	2.0sec
med-Be/Open	Open/thick-Be close	Safe Norm 16.0s Obs 1x1 512x512 (928, 1024) DPCM 0 0 2.0sec
med-Al/Open	med-Al/Open close	Safe Norm 16.0s Obs 1x1 512x512 (928, 1024) DPCM 0 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Dark 16.0s Obs 1x1 512x512 (928, 1024) DPCM 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #18CA: Comet Lovejoy-Al/Mesh 2x2 4s-with Med-Be Med-Al and Dark in context-1x1-512x512 16s-NW offset												
Term		Pointing (x, y)					Comment					
12/16 00:33:58 - 12/16 02:30:54		Fixed (465.0, 835.0)					Comet obs. at NW limb with EIS.					
PROG= 09 1-time(s)												
Subr= 2 1-time(s) 2.0sec												
Seqn= 4 1-time(s) 2.0sec												
Open/G-band Open/G-band open		Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 1 20-time(s) 2.0sec												
Seqn= 26 10-time(s) 2.0sec												
Open/Al-mesh Open/Al-mesh close		Safe	Norm	4.00s	Obs	2x2	1024x1024 (1120, 1048)	DPCM	0	0	2.0sec	
Seqn= 44 1-time(s) 2.0sec												
med-Be/Open Open/thick-Be close		Safe	Norm	16.0s	Obs	1x1	512x512 (1120, 1048)	DPCM	0	0	2.0sec	
med-Al/Open med-Al/Open close		Safe	Norm	16.0s	Obs	1x1	512x512 (1120, 1048)	DPCM	0	0	2.0sec	
med-Be/Open med-Be/Open close		Safe	Dark	16.0s	Obs	1x1	512x512 (1120, 1048)	DPCM	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops												
Term		Pointing (x, y)					Comment					
12/15 11:02:00 - 12/15 17:59:54		Track (252.6, -299.6) @ 12/15 10:59:00					# OP start + 10min, AR11374					
12/15 18:13:00 - 12/15 23:22:30		Track (313.0, -299.3) @ 12/15 18:10:00					# Cont.					
12/16 02:42:30 - 12/16 05:30:30		Track (381.3, -299.1) @ 12/16 02:31:00					AR 11374					
12/16 06:20:00 - 12/16 17:30:00		Track (411.4, -299.0) @ 12/16 06:17:00					# Cont.					
12/16 18:08:30 - 12/17 04:27:30		Track (501.9, -299.0) @ 12/16 18:05:30					# Cont.					
12/17 05:52:30 - 12/17 06:08:00		Track (585.6, -299.3) @ 12/17 05:50:30					# Cont.					
PROG= 03 12-time(s)												
Subr= 1 45-time(s) 10.0sec												
Seqn= 20 1-time(s) 2.0sec												
thin-Be/Open med-Be/Open close		Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 63 1-time(s) 2.0sec												
med-Al/Open med-Al/thick-Al close		Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Be Open/thick-Be close		Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 77 1-time(s) 2.0sec												
Al-poly/Open Al-poly/thick-Al close		Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Subr= 2 1-time(s) 10.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band Open/G-band open		Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al Open/thick-Al close		Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al Open/thick-Al close		Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

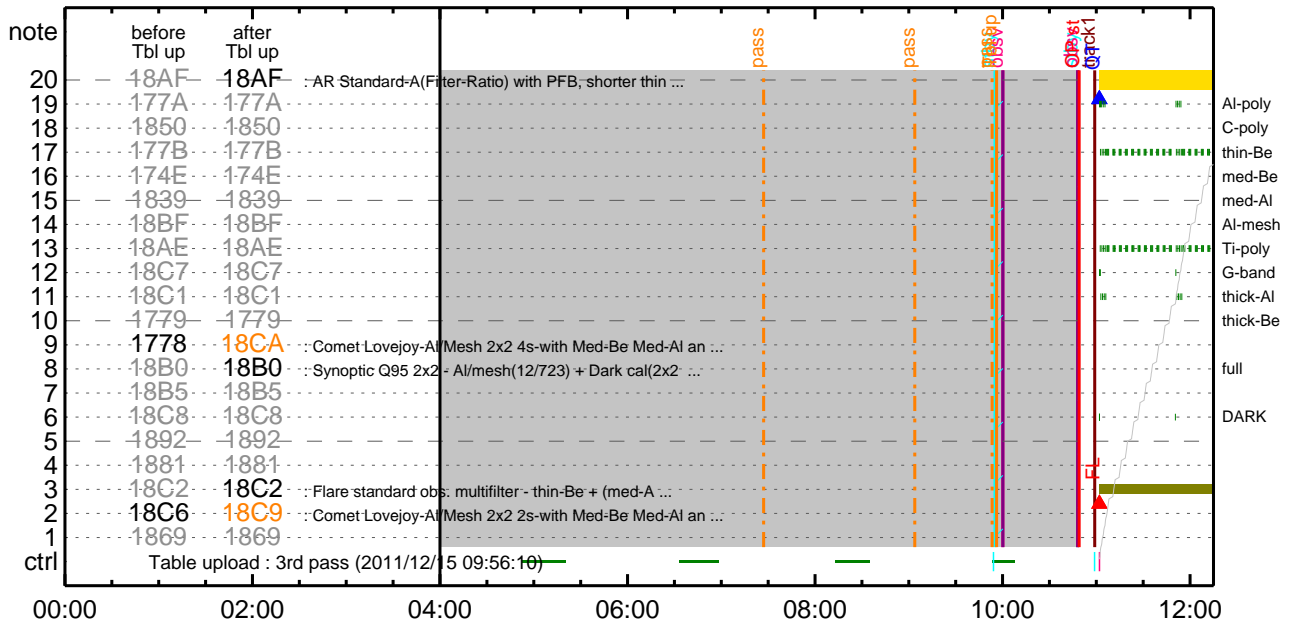
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Flare Detection

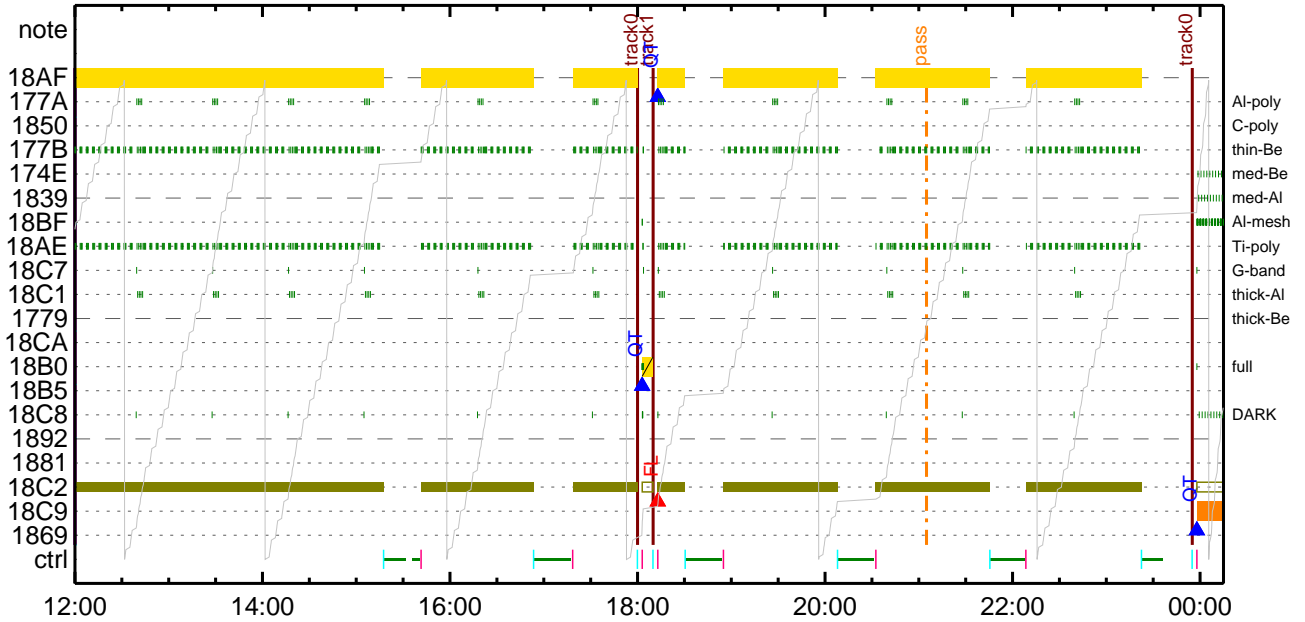
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FLD Patrol												
Term		Pointing (x, y)					Comment					
12/15 18:12:46 - 12/15 23:57:46		Track (313.0, -299.3) @ 12/15 18:10:00					# Cont.					
12/16 02:33:46 - 12/16 06:07:16		Track (381.3, -299.1) @ 12/16 02:31:00					AR 11374					
12/16 06:19:46 - 12/16 17:55:46		Track (411.4, -299.0) @ 12/16 06:17:00					# Cont.					
12/16 18:08:16 - 12/17 05:40:46		Track (501.9, -299.0) @ 12/16 18:05:30					# Cont.					
12/17 05:52:16 - 12/20 09:33:00		Track (585.6, -299.3) @ 12/17 05:50:30					# Cont.					
Open/Ti-poly Open/thick-Al close		Safe	Norm	8ms	Obs	8x8		Q=50			30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

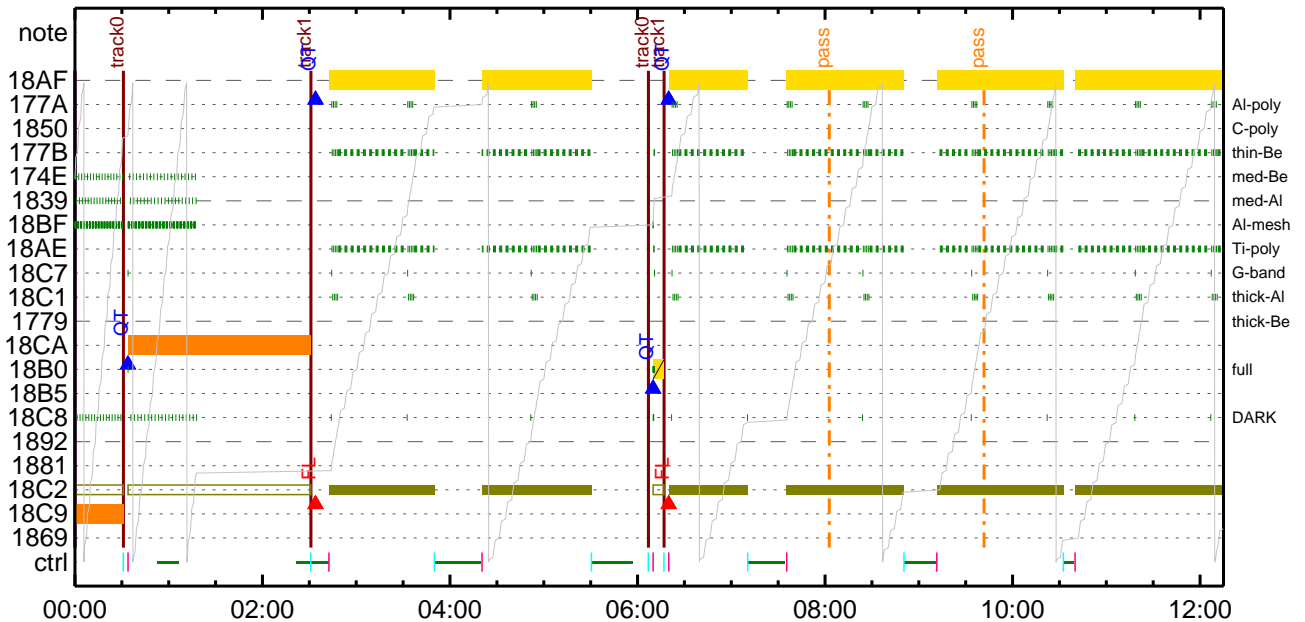
CMDI #0323 2011/12/15



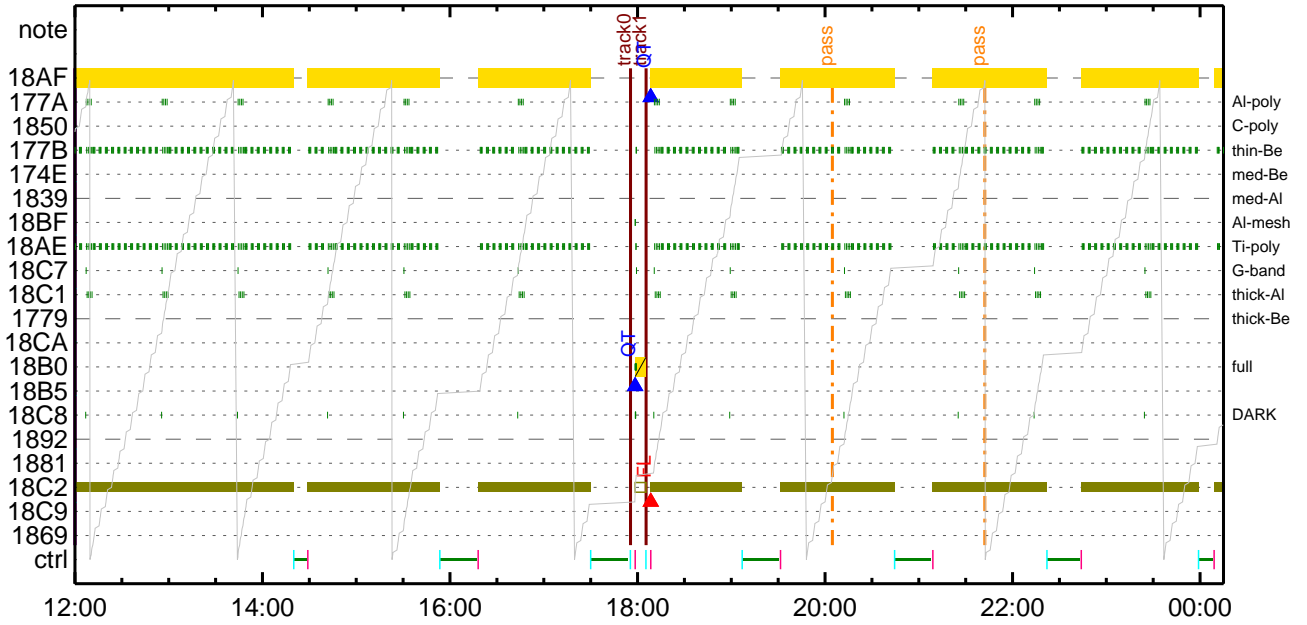
CMDI #0323 2011/12/15



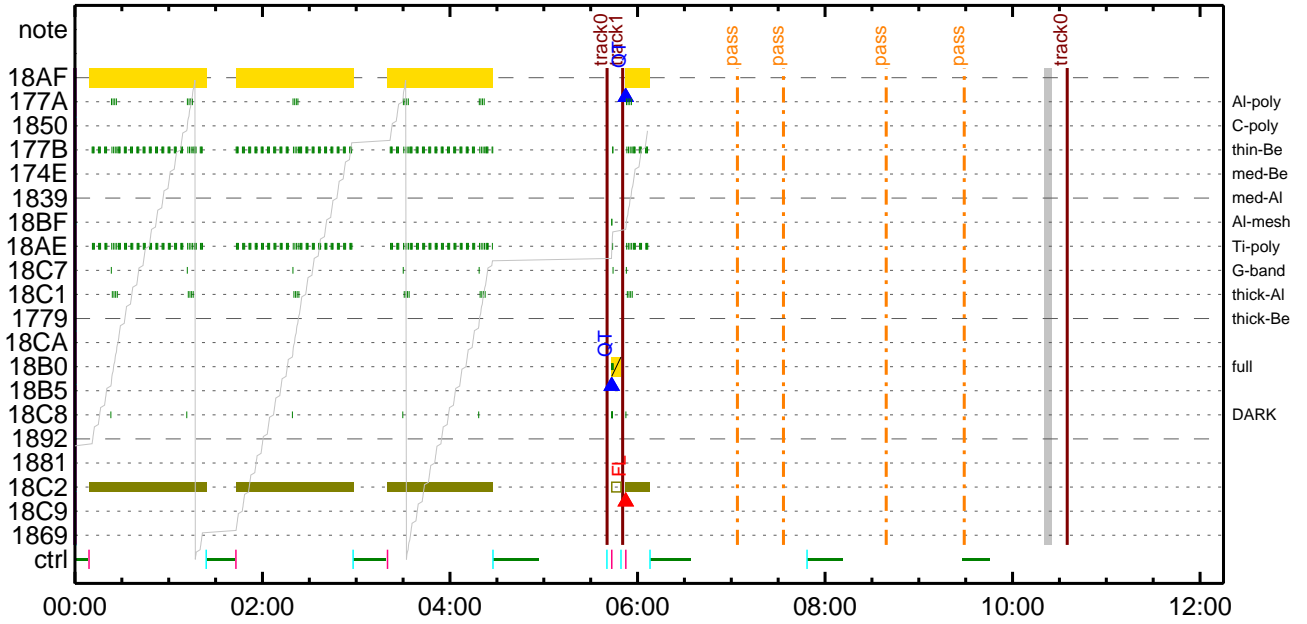
CMDI #0323 2011/12/16



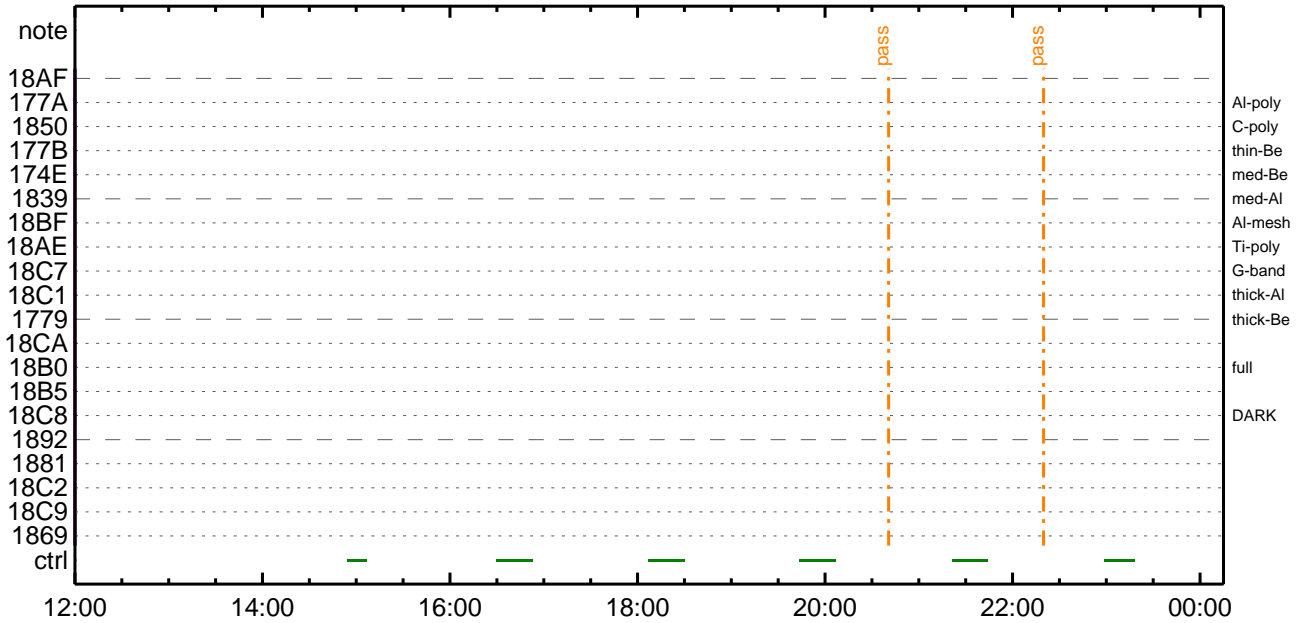
CMDI #0323 2011/12/16



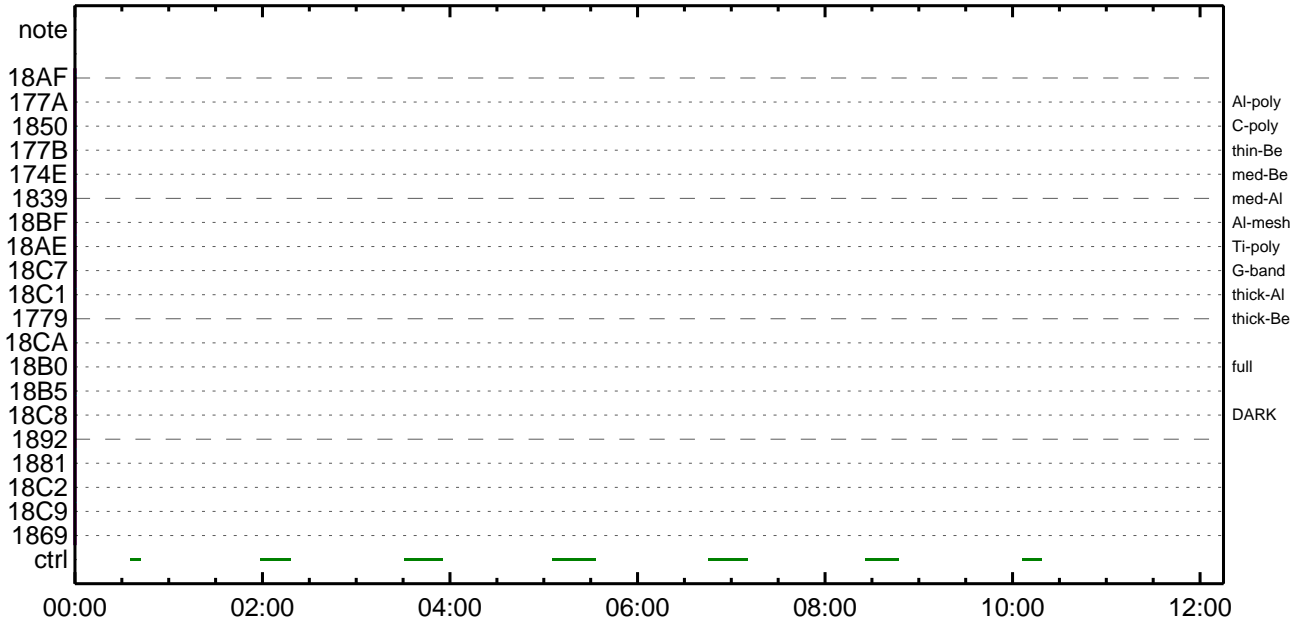
CMDI #0323 2011/12/17



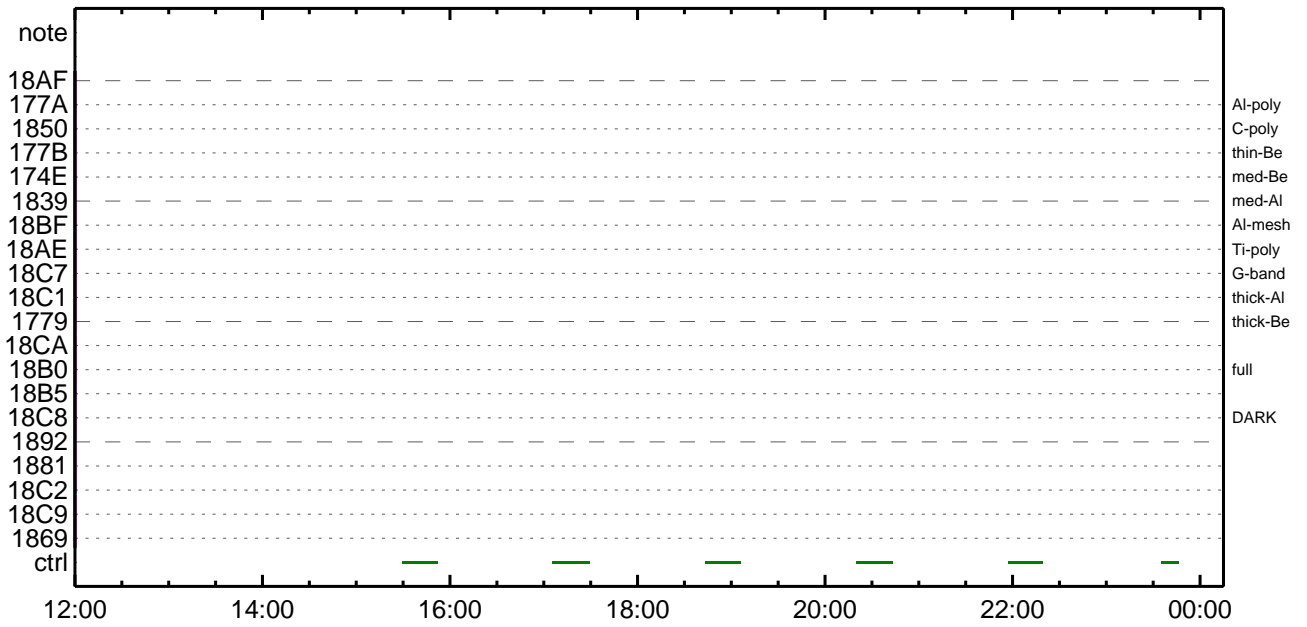
CMDI #0323 2011/12/17



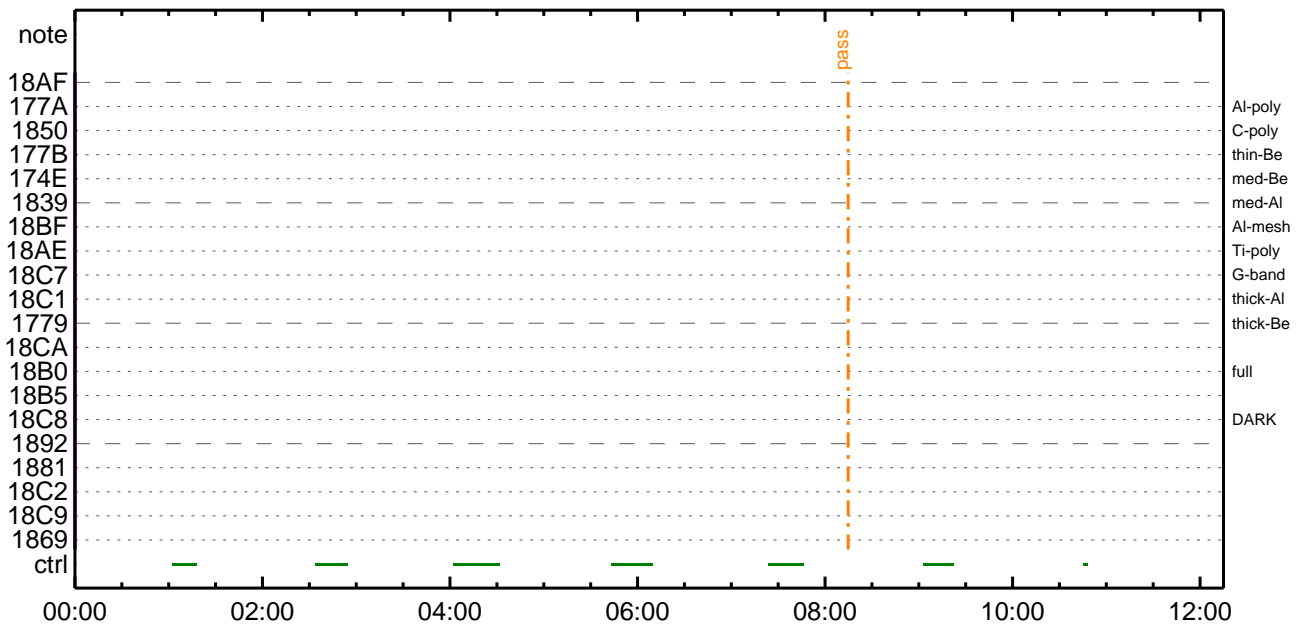
CMDI #0323 2011/12/18



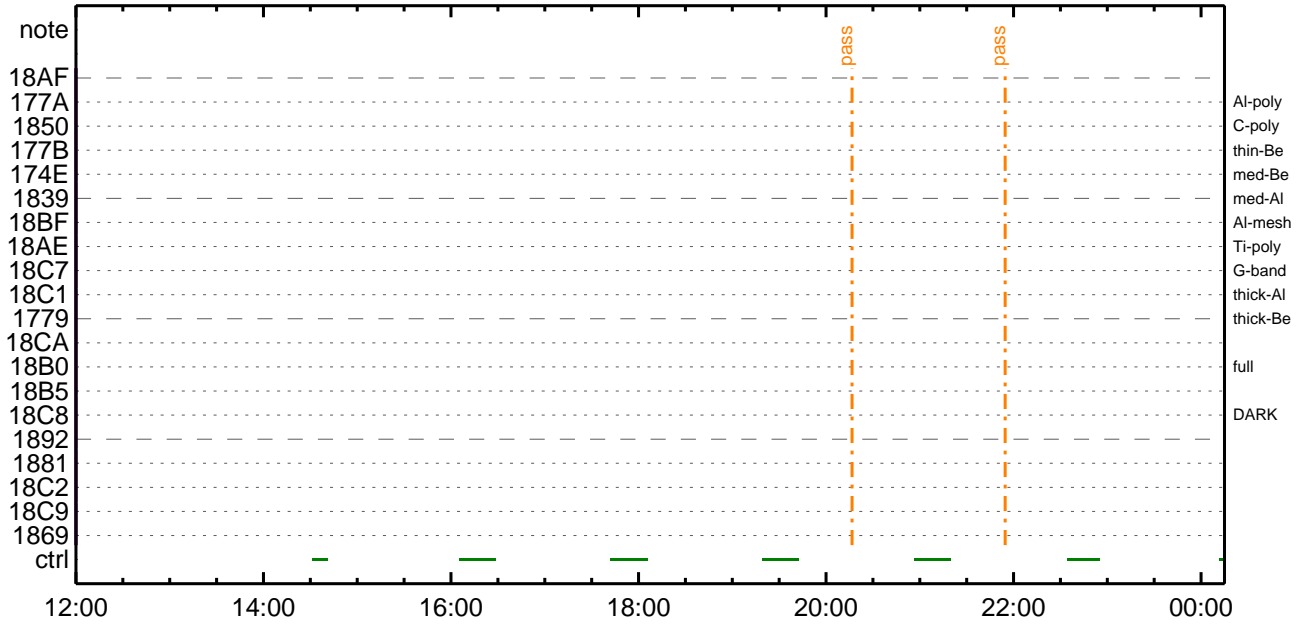
CMDI #0323 2011/12/18



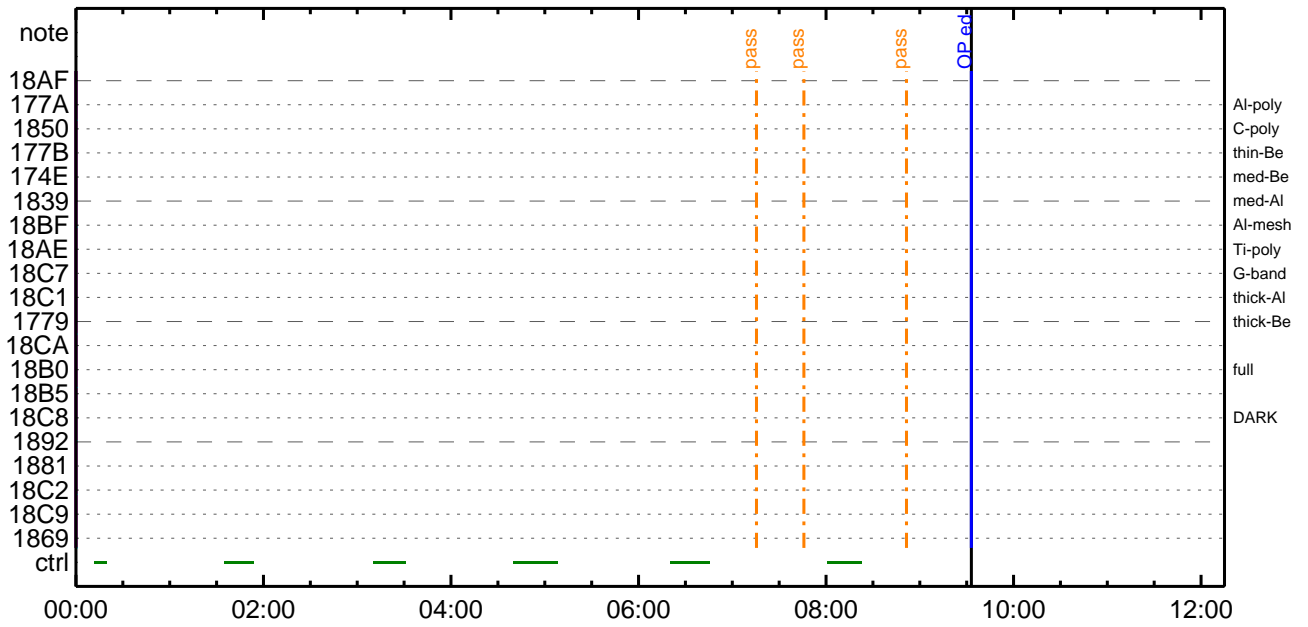
CMDI #0323 2011/12/19



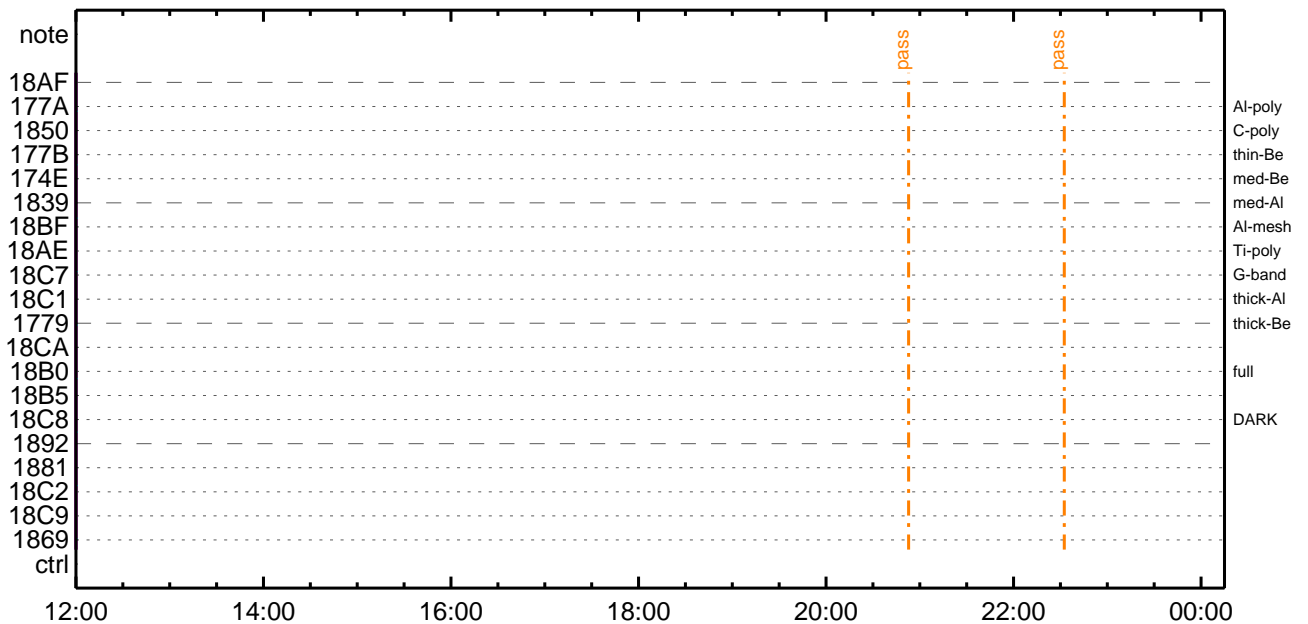
CMDI #0323 2011/12/19



CMDI #0323 2011/12/20



CMDI #0323 2011/12/20



(a) Spacecraft Operation Procedure (real-commands)

```
main-529 2011-12-15 13:29:33 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÃÝÏÝÃÝ-¼Ã»Û;ã
0005 C.
0006 C. ÝÃÝË;¼Ý³ÝÏÝÓÝÉÃ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C.  Åí;Ëç¿ãã•µ°Ë»í×ÁçãíÝçÝÃÝ×Ýí;¼ÝÉ;ËËÈµ•íÉ;ËÈÈ¼°çã•ãç¼í¹çãí;çÃ®, ùã¹ãÈãããçÃ÷ç®ã•ãÈããã³ãÈ;í
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÃ÷ç®µ;ON
0016 C. *****
0017 C. ç” °ÆÃ, í×ËÝãããããããããããã»p´ Ñãò¹íí, ã•; çÉÔí×ãËXÃÓããí¹ãÈãíãÈããã³ãÈ;í
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XÝDÝÓÝÉÝíÝÃÝ-¼ÔÃãã•ãçãé; ç°Ë²¼ãí°ÆÃ, ¼È¼çãò¼Ã¹ã¹ãé;í
0030 C.
0031 . C. *****
0032 C. DR PT1 Åí¼í°ÆÃ,
0033 C. *****
0034 C. ç” RESTART;ËPT1;Ëã•ãçãã¼í¹çãí; ç°Ë²¼ãí¼Ã¹ã¹ãã°; çDCBC-150ãØçÈãã;í
0035 C.
0036 . C. ;ãPT1°ÆÃ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ã¹ã¹, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ã¹ã¹, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ã¹ã¹, ;¼Ú)
0045 C.
0046 . C. ;ãÝçÝÓÝËÝËÈÀÙÀØ;ËÃ•Ã°²óÈð;Ë, àãí°ÆÃ, °Ë³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ã¹ã¹, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ã¹ã¹, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ã¹ã¹, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÃ, ã-¼«Æ°Ãã»ãã•ãçã, á; ç°Ë²¼ãò¼Ã¹ã¹ãé;í
0055 C. ÝçÝÓÝËÝËÈÀÙÀØããÃ•Ã°²óÈðãã¼ãã¼í¹çãí°í»ã¹ãÈãããçÃãã;í
0056 C.
0057 . C. *****
0058 C. DR PT2 Åí¼í°ÆÃ,
0059 C. *****
0060 C. ç” RESTART;ËPT2;Ëã•ãçãã¼í¹çãí; ç°Ë²¼ãí¼Ã¹ã¹ãã°; çDCBC-151ãØçÈãã;í
0061 C.
0062 . C. ;ãPT2°ÆÃ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ã¹ã¹, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ã¹ã¹, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ã¹ã¹, ;¼Ú)
0071 C.
0072 . C. ;ãÝçÝÓÝËÝËÈÀÙÀØ;ËÃ•Ã°²óÈð;Ë, àãí°ÆÃ, °Ë³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ã¹ã¹, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ã¹ã¹, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ã¹ã¹, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÃ, Åã»Ë; çXÃ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÃ, Åã»Ë;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÃ÷ç®µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-529:OP
0104 ( )
0105 S. OG og-529:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶A°EÉ¬òA÷¿@ (¼âµ-YAYOYx½ê½çòðÃÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²¬Á÷¿@NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½î¹ç°²E²¼î¹ç²E;f
0180 C. °²E²¼î¹ç²E;f
0181 C.
0182 C. TIY³Y²Y½E²òðÃî¿¿(UT)
0183 +. TI 2011-12-15 10:44:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-12-15 10:44:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-12-15 10:44:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2011-12-15 10:48:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄë%îíñαîŷÄŷ§ŷÄŷ-¹àîŰ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]    EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]    EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ          EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLαîŷÈ¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2011-12-15 10:48:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC          (21 02)
0247 +. TI 2011-12-15 10:48:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC          (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2011-12-15 10:48:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC          (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÄîαî»ò¼ŷαÈÄα¹αèDCBC•×²è *****
0265 C. (¼ª°îŷÖŷÄŷÈŷŷŷÈŷâŷçŷèαÈ¼αα¼Ä»Űα¹αè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.İ Daily±;îñαÈ´Øα¹αèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. îãLOSŷÄŷŷŷÄŷ-¼Ä»Ű;ã
0276 C.
0277 C. ***** LOS *****
0278 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-530 2011-12-15 13:29:33 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YÞYÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëð¿ðÁð•µ°Æ»Í×ÁÇðÍYÇYÁY×YÍ;¼YÉ;ËÈè¼µ•íÍË;ËðÈ¼°Çð••µ¿¼í¹Çðí;ÇÀ®, ù¼¹ðÈððÇÁ+¿®••ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. Ç” °ÆÀ, Í×ËYðáLÒSððÇðí»p`Öðð¹íí, ð•; ÇÉÖÍ×ðÈXÁÓONðí¹ÒðÈð¹ðÈððð³ðÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY-¾ÔÁÖð-°ÁÁÈð•µ¿;µÉ; Ç°È²¼ðí°ÆÀ, ¼È¼Çðð¼Á¹Òð¹µÉ;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. Ç” RESTART;ËPT1;Ëð•µ¿¼µ¼í¹Çðí; Ç°È²¼ðí¼Á¹Òð»°; ÇDCBC-150ð¿¿Èðà;f
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ò, ;¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYËÁÚÁØ;ËÁ•Á°²óÈð;Ë, áµí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ò, ;¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ð-¼«Æ°Áá»ßð•µ¿; á; Ç°È²¼ð¼Á¹Òð¹µÉ;f
0055 C. YÇYÓYÉYËÁÚÁØðáÁ•Á°²óÈðð-¾áµ¼í¹Çðí`°í»ð¹ðÈððÇÁÓðÁ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. Ç” RESTART;ËPT2;Ëð•µ¿¼µ¼í¹Çðí; Ç°È²¼ðí¼Á¹Òð»°; ÇDCBC-151ð¿¿Èðà;f
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ò, ;¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYËÁÚÁØ;ËÁ•Á°²óÈð;Ë, áµí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ò, ;¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áá»ß; ÇXÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 7s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2011-12-15 10:48:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'úÃîîî»ö¼ÝðËÄð¹ñëDCBC•x²è *****
0153 C. (%ã°îÿÓÿÄÿËÿÏÿËÿÄÿçÿèðË¼¼¼¼»Û¹ñë)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ÿÐÿ¹•Ï Daily±çÍÑñË´Ø¹ñëDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSÿÄÿËÿÏÿËÿÄÿçÿè¼¼¼¼¼»Û¹ñë;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

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0096 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0097 C.
0098 C.
0099 . C. ;äLOSŸÁŸSŸÄŸ-¼Ä»Û;ä
0100 C.
0101 . C. ***** LOS *****
0102 C.
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XRT_OGLIST_0323.chk

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*** OP Sequence for XRT ***

2011/12/15	10:58:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/15	10:59:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/12/15	11:01:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/12/15	11:01:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/12/15	11:01:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/12/15	11:01:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/15	11:01:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/15	11:01:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/15	11:01:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/12/15	11:01:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/12/15	11:02:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/15	15:17:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/15	15:17:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/15	15:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/15	15:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/15	15:40:30.0	XRT_Custom_418_OG [0x1a2]							
2011/12/15	15:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/15	16:53:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/15	16:53:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/15	16:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/15	16:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/15	17:17:30.0	XRT_Custom_418_OG [0x1a2]							
2011/12/15	17:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/15	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/15	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/12/15	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/12/15	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/12/15	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/12/15	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/15	18:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2011/12/15	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/15	18:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/15	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/12/15	18:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/12/15	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/12/15	18:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/12/15	18:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/15	18:12:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/15	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/15	18:12:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/12/15	18:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/12/15	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/15	18:30:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/15	18:30:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/15	18:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/15	18:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/15	18:54:00.0	XRT_Custom_418_OG [0x1a2]							

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2011/12/15	18:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/15	20:08:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/15	20:08:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/15	20:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/15	20:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/15	20:31:30.0	XRT_Custom_418_OG [0x1a2]							
2011/12/15	20:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/15	21:45:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/15	21:45:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/15	21:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/15	21:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/15	22:07:30.0	XRT_Custom_418_OG [0x1a2]							
2011/12/15	22:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/15	23:22:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/15	23:22:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/15	23:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/15	23:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/15	23:54:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/15	23:55:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 de a8 50 51				
2011/12/15	23:57:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/12/15	23:57:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/12/15	23:57:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/12/15	23:57:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/15	23:57:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/15	23:57:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/15	23:57:56.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2011/12/15	23:57:58.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/16	00:30:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/16	00:31:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 b5 cb d6 a8				
2011/12/16	00:33:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/12/16	00:33:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/12/16	00:33:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/12/16	00:33:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/16	00:33:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/16	00:33:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/16	00:33:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2011/12/16	00:33:58.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/16	02:30:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/16	02:31:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/12/16	02:33:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/12/16	02:33:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/12/16	02:33:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/12/16	02:33:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/16	02:33:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/16	02:33:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/16	02:33:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/12/16	02:33:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/12/16	02:42:30.0	XRT_CTRL_AUTO_406_OG [0x196]							

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2011/12/16	03:50:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/16	03:50:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/16	03:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/16	03:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/16	04:19:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/16	04:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/16	05:30:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/16	05:30:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/16	05:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/16	05:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/16	06:06:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/16	06:06:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/12/16	06:07:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2011/12/16	06:07:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/12/16	06:07:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/12/16	06:07:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/16	06:09:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2011/12/16	06:10:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/16	06:16:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/16	06:17:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00	
2011/12/16	06:19:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/12/16	06:19:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/16	06:19:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/16	06:19:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/16	06:19:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/16	06:19:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/16	06:19:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2011/12/16	06:19:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2011/12/16	06:20:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/16	07:10:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/16	07:10:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/16	07:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/16	07:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/16	07:34:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/16	07:35:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/16	08:50:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/16	08:50:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/16	08:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/16	08:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/16	09:10:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/16	09:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/16	10:32:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/16	10:32:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/16	10:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/16	10:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/16	10:39:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/16	10:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/16	14:20:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2011/12/16	14:20:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/16	14:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/16	14:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/16	14:28:00.0	XRT_Custom_418_OG [0x1a2]							
2011/12/16	14:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/16	15:53:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/16	15:53:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/16	15:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/16	15:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/16	16:17:00.0	XRT_Custom_418_OG [0x1a2]							
2011/12/16	16:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/16	17:30:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/16	17:30:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/16	17:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/16	17:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/16	17:55:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/16	17:55:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/12/16	17:55:30.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/12/16	17:55:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/12/16	17:55:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/12/16	17:55:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/16	17:58:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2011/12/16	17:58:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/16	18:05:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/16	18:05:30.0	AOCS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2011/12/16	18:07:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/12/16	18:08:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/12/16	18:08:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/12/16	18:08:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/12/16	18:08:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/12/16	18:08:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/16	18:08:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/12/16	18:08:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2011/12/16	18:08:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/16	19:07:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/16	19:07:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/16	19:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/16	19:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/16	19:30:30.0	XRT_Custom_418_OG [0x1a2]							
2011/12/16	19:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/16	20:44:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/16	20:44:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/16	20:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/12/16	20:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/12/16	21:08:00.0	XRT_Custom_418_OG [0x1a2]							
2011/12/16	21:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/12/16	22:22:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/12/16	22:22:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/12/16	22:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							

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2011/12/16	22:25:14.0	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
		XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/16	22:43:00.0	XRT_Custom_418_OG [0x1a2]				
2011/12/16	22:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/16	23:59:00.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/16	23:59:02.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/16	23:59:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/17	00:02:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/17	00:08:00.0	XRT_Custom_418_OG [0x1a2]				
2011/12/17	00:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/17	01:24:00.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/17	01:24:02.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/17	01:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/17	01:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/17	01:42:00.0	XRT_Custom_418_OG [0x1a2]				
2011/12/17	01:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/17	02:58:00.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/17	02:58:02.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/17	02:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/17	03:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/17	03:19:00.0	XRT_Custom_418_OG [0x1a2]				
2011/12/17	03:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/17	04:27:30.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/17	04:27:32.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/17	04:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/17	04:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/17	05:40:24.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/17	05:40:26.0	XRT_FOCUS_POSITION_401_OG [0x191]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/12/17	05:40:30.0	AOCS_Ore-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2011/12/17	05:40:46.0	XRT_FLD_DIS_402_OG [0x192]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/12/17	05:40:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/12/17	05:40:50.0	XRT_ARS_DIS_438_OG [0x1b6]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/17	05:43:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2011/12/17	05:43:30.0	XRT_CTRL_AUTO_406_OG [0x196]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/17	05:49:24.0	XRT_CTRL_MANU_439_OG [0x1b7]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/17	05:50:30.0	AOCS_Ore-point_Start_1_OG [0x097]				
		AOCU_NM	5	02-76	01 00 00 00 00	
2011/12/17	05:51:56.0	XRT_FOCUS_POSITION_409_OG [0x199]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/12/17	05:52:16.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/17	05:52:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/17	05:52:20.0	XRT_AEC_RESET_443_OG [0x1bb]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/17	05:52:22.0	XRT_ARS_DIS_431_OG [0x1af]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/17	05:52:24.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/17	05:52:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2011/12/17	05:52:28.0	XRT_FL_PROG_SET_414_OG [0x19e]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2011/12/17	05:52:30.0	XRT_CTRL_AUTO_406_OG [0x196]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/17	06:08:00.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/17	06:08:02.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/17	06:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/17	06:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	

2011/12/17	07:48:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/12/17	10:35:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00