

XRT Timeline to be uploaded on 2011/12/17

Period: 2011/12/17 10:25:00 - 2011/12/22 10:13:00

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Normal mode

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XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad														
Term		Pointing (x, y)				Comment								
12/17 10:38:00 - 12/17 17:42:54		Track (-181.1, 200.8) @ 12/17 10:35:00				# OP start + 10min AR 11377								
12/17 17:56:00 - 12/18 05:59:54		Track (-114.2, 201.6) @ 12/17 17:53:00				AR 11377 (Cont.)								
12/18 07:11:00 - 12/18 18:00:24		Track (7.3, 202.9) @ 12/18 07:00:00				AR 11377 (cont.)								
12/18 18:13:30 - 12/19 03:49:54		Track (110.8, 203.8) @ 12/18 18:10:30				AR11377 (Cont.)								
12/19 04:33:00 - 12/19 17:42:00		Track (200.7, 204.3) @ 12/19 04:00:00				AR11377 (Cont.)								
12/19 18:18:30 - 12/20 03:59:54		Track (327.8, 204.5) @ 12/19 18:15:30				AR11377 (Cont.)								
PROG= 13 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 19		1-time(s)		2.0sec										
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Seqn= 96		4-time(s)		2.0sec										
Al-poly/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec	
thin-Be/Open		med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec	
Open/thick-Al		Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Subr= 2		1-time(s)		2.0sec										
Seqn= 62		15-time(s)		150.0sec										
thin-Be/Open		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec	
thin-Be/Open		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec	
thin-Be/Open		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec	
thin-Be/Open		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec	
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #18B0: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(24/1443) + Thin-Be(88/24)													
Term		Pointing (x, y)		Comment									
12/17 17:46:00 - 12/17 17:52:54		Fixed (0.0, 0.0)		synoptic, shifted -17.0 min									
12/18 06:03:00 - 12/18 06:15:00		Fixed (0.0, 0.0)		synoptic & SOT Flat Field									
12/18 18:03:30 - 12/18 18:10:24		Fixed (0.0, 0.0)		synoptic, shifted 0.5 min									
12/19 03:53:00 - 12/19 03:59:54		Fixed (0.0, 0.0)		synoptic for HOP173									
12/19 18:08:30 - 12/19 18:15:24		Fixed (0.0, 0.0)		synoptic, shifted 5.5 min									
12/20 04:03:00 - 12/20 04:09:54		Fixed (0.0, 0.0)		synoptic for HOP173									

PROG= 08															1-time(s)				
Subr= 1		1-time(s)			12.0sec														
Seqn= 46		1-time(s)			4.0sec														
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec			
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec			
Seqn= 5		1-time(s)			2.0sec														
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec			
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec			
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec			
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)			DPCM	0	0	2.0sec			
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)			DPCM	0	0	2.0sec			
Seqn= 69		1-time(s)			4.0sec														
Open/Ti-poly		Open/Ti-poly		close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec			
Open/Ti-poly		Open/Ti-poly		close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec			
Seqn= 50		1-time(s)			2.0sec														
thin-Be/Open		thin-Be/Open		close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec			
thin-Be/Open		thin-Be/Open		close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec			
Seqn= 4		1-time(s)			2.0sec														
Open/G-band		Open/G-band		open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec			
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval				

XOB #18BF: Synoptic 9 Filter- 2x2 Q98 Short exp 2 loop														
Term			Pointing (x, y)					Comment						
12/18 06:18:06 - 12/18 06:45:30			Fixed (0.0, 0.0)					synoptic & SOT Flat Field						
PROG= 14 2-time(s)														
└ Subr= 1 1-time(s) 180.0sec														
└ Seqn= 82 1-time(s) 25.0sec														
└┐ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└┐ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└ Seqn= 98 1-time(s) 25.0sec														
└┐ Open/Ti-poly Open/Ti-poly close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└┐ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└ Seqn= 74 1-time(s) 25.0sec														
└┐ Al-poly/Open Al-poly/Open close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														

Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 18 1-time(s) 25.0sec												
C-poly/Open	C-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
C-poly/Open	C-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 24 1-time(s) 25.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 9 1-time(s) 4.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 180.0sec												
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 71 1-time(s) 4.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 67 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 4 1-time(s) 4.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18AF: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 4min cad

Term		Pointing (x, y)								Comment				
12/20 04:13:00 - 12/20 07:15:00		Track (412.5, 204.4) @ 12/20 04:10:00								AR11377 (Cont.)				
PROG= 20 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 19		1-time(s)		2.0sec										
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
└ Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Seqn= 96		4-time(s)		2.0sec										
└ Al-poly/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
└ Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec
└ thin-Be/Open		med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec
└ Open/thick-Al		Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Subr= 2		1-time(s)		2.0sec										
Seqn= 62		11-time(s)		240.0sec										
└ thin-Be/Open		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
└ Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	15.0sec
└ thin-Be/Open		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec
└ Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	15.0sec
└ thin-Be/Open		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec
└ Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	15.0sec
└ thin-Be/Open		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	2.0sec
└ Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	15.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops

Term	Pointing (x, y)	Comment										
12/17 10:38:00 - 12/17 17:42:54	Track (-181.1, 200.8) @ 12/17 10:35:00	# OP start + 10min AR 11377										
12/17 17:56:00 - 12/18 05:59:54	Track (-114.2, 201.6) @ 12/17 17:53:00	AR 11377 (Cont.)										
12/18 07:11:00 - 12/18 18:00:24	Track (7.3, 202.9) @ 12/18 07:00:00	AR 11377 (cont.)										
12/18 18:13:30 - 12/19 03:49:54	Track (110.8, 203.8) @ 12/18 18:10:30	AR11377 (Cont.)										
12/19 04:33:00 - 12/19 17:42:00	Track (200.7, 204.3) @ 12/19 04:00:00	AR11377 (Cont.)										
12/19 18:18:30 - 12/20 03:59:54	Track (327.8, 204.5) @ 12/19 18:15:30	AR11377 (Cont.)										
12/20 04:13:00 - 12/20 07:15:00	Track (412.5, 204.4) @ 12/20 04:10:00	AR11377 (Cont.)										
PROG= 03 12-time(s)												
Subr= 1 45-time(s) 10.0sec												
Seqn= 20 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 63 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 77 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

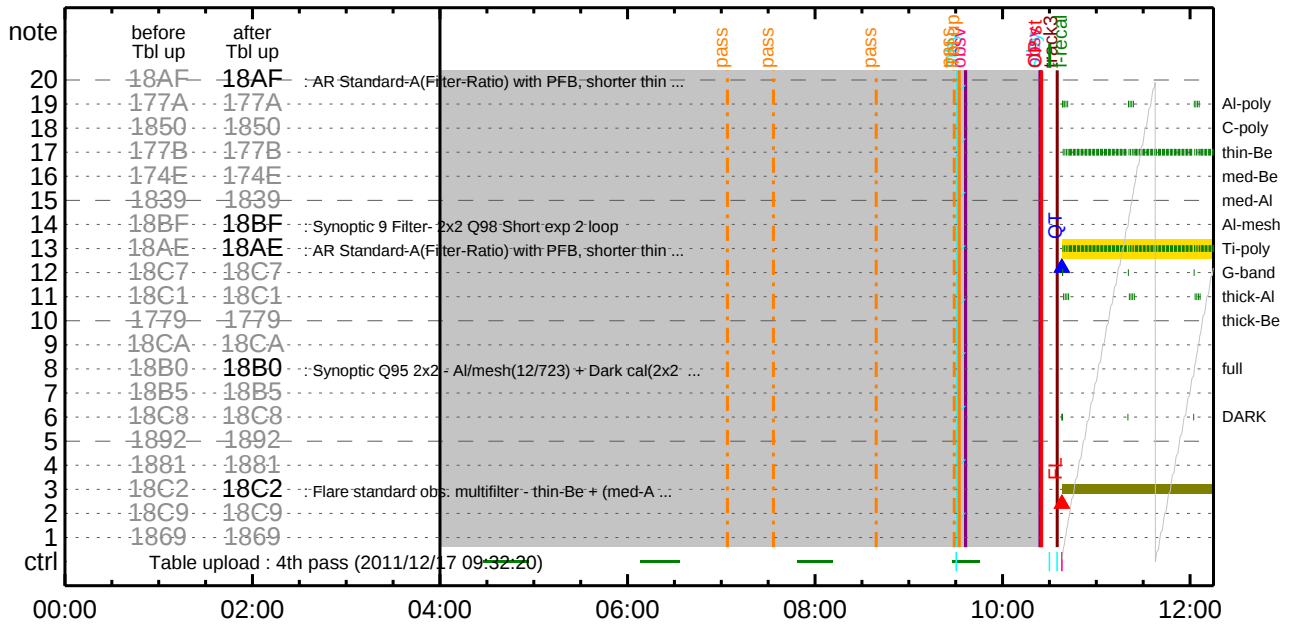
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Flare Detection

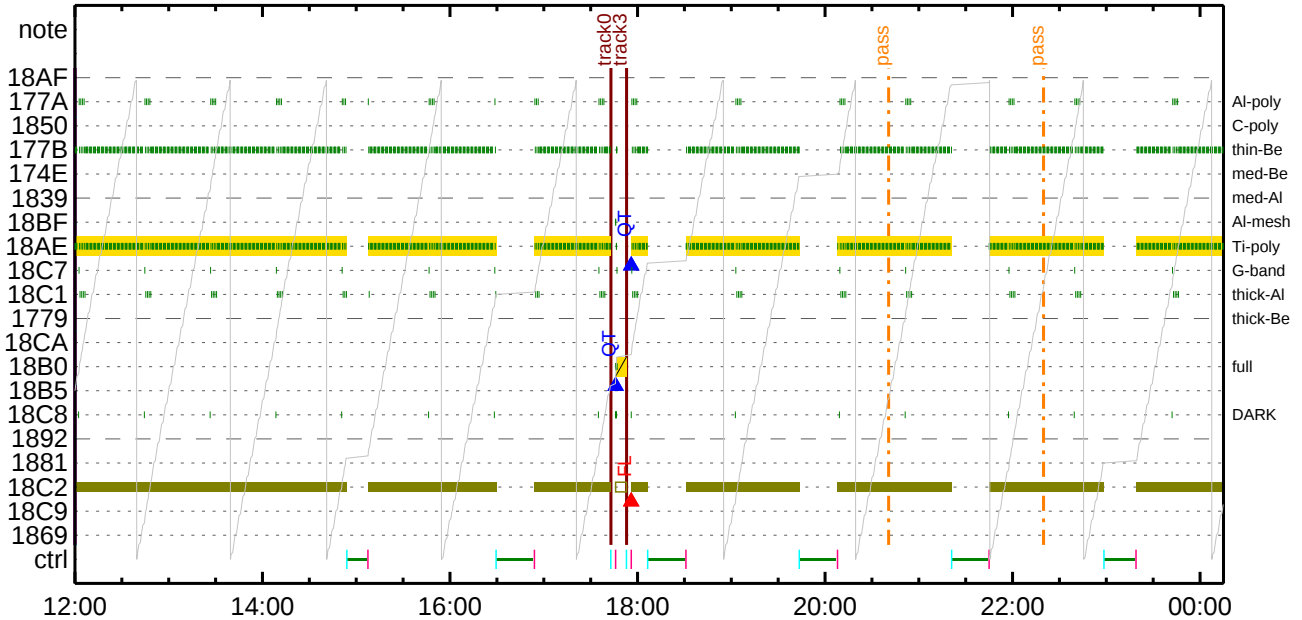
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FLD Patrol												
Term			Pointing (x, y)						Comment			
12/17 17:55:46 - 12/18 06:00:16	Track (	-114.2, 201.6)	@ 12/17 17:53:00	AR 11377 (Cont.)								
12/18 07:02:46 - 12/18 18:00:46	Track (	7.3, 202.9)	@ 12/18 07:00:00	AR 11377 (cont.)								
12/18 18:13:16 - 12/19 03:50:16	Track (	110.8, 203.8)	@ 12/18 18:10:30	AR11377 (Cont.)								
12/19 04:02:46 - 12/19 18:05:46	Track (	200.7, 204.3)	@ 12/19 04:00:00	AR11377 (Cont.)								
12/19 18:18:16 - 12/20 04:00:16	Track (	327.8, 204.5)	@ 12/19 18:15:30	AR11377 (Cont.)								
12/20 04:12:46 - 12/22 10:13:00	Track (	412.5, 204.4)	@ 12/20 04:10:00	AR11377 (Cont.)								
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

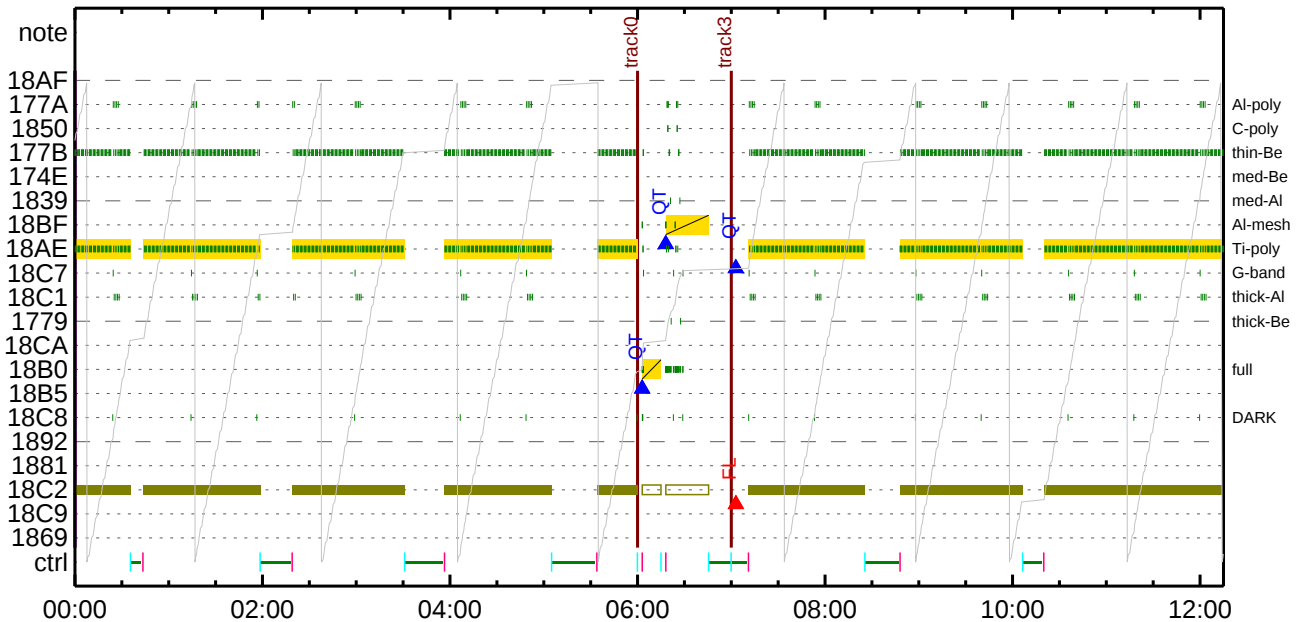
CMDI #0328 2011/12/17



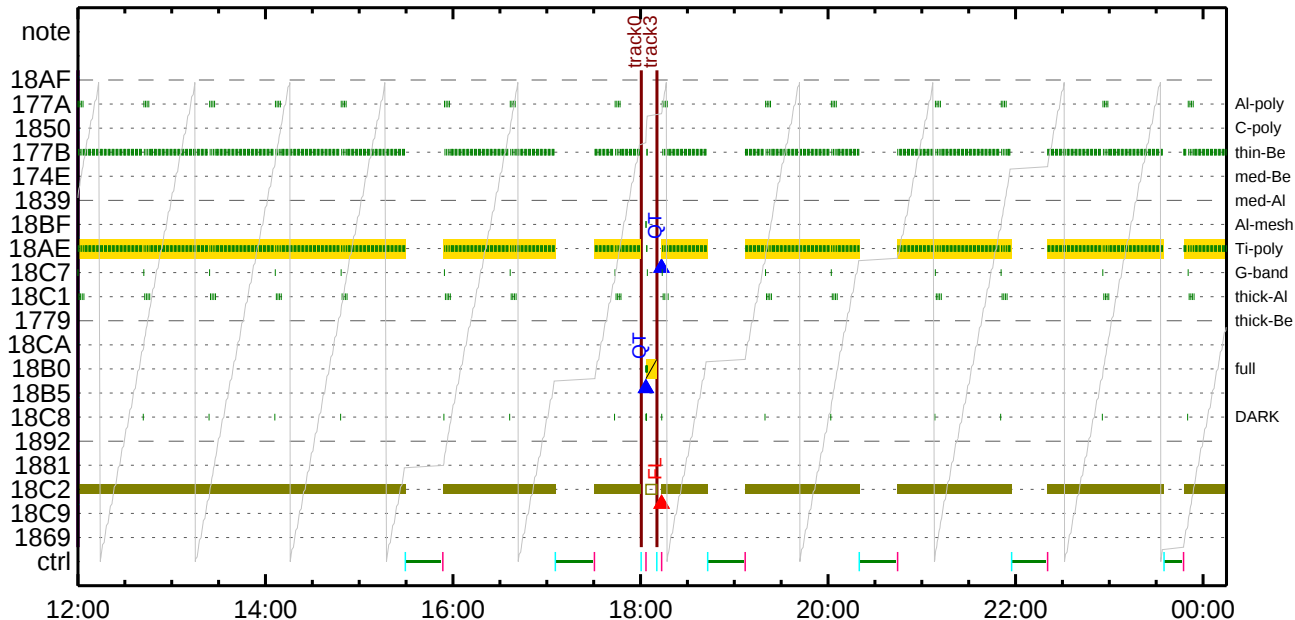
CMDI #0328 2011/12/17



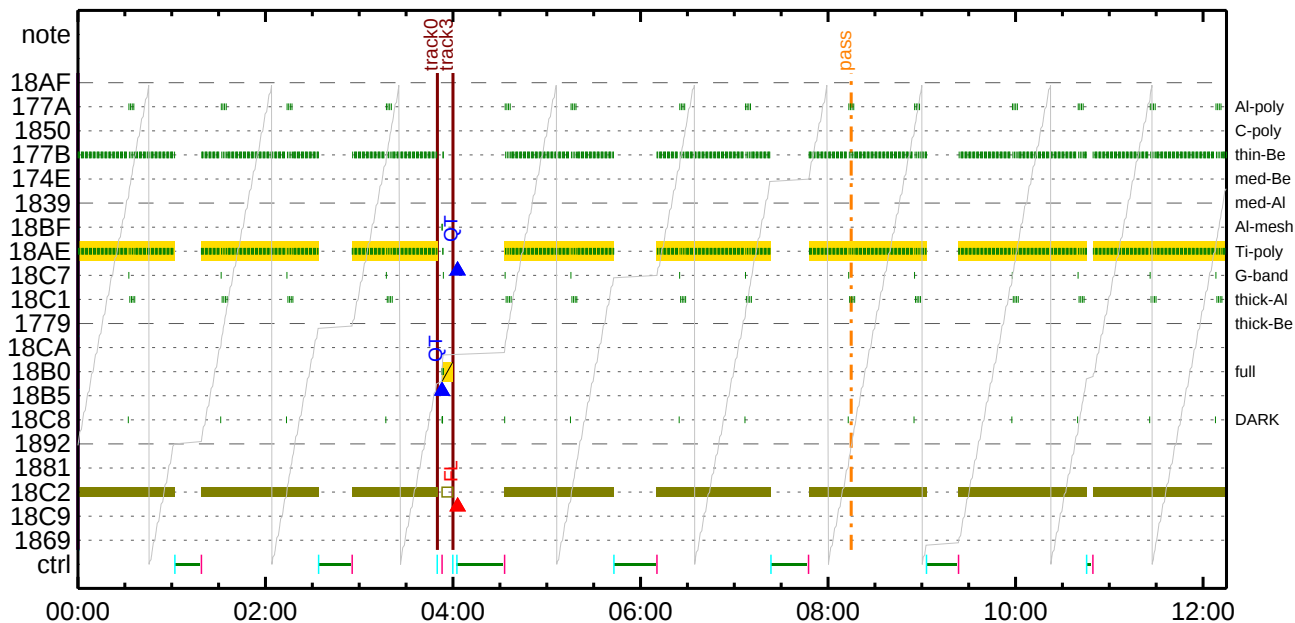
CMDI #0328 2011/12/18



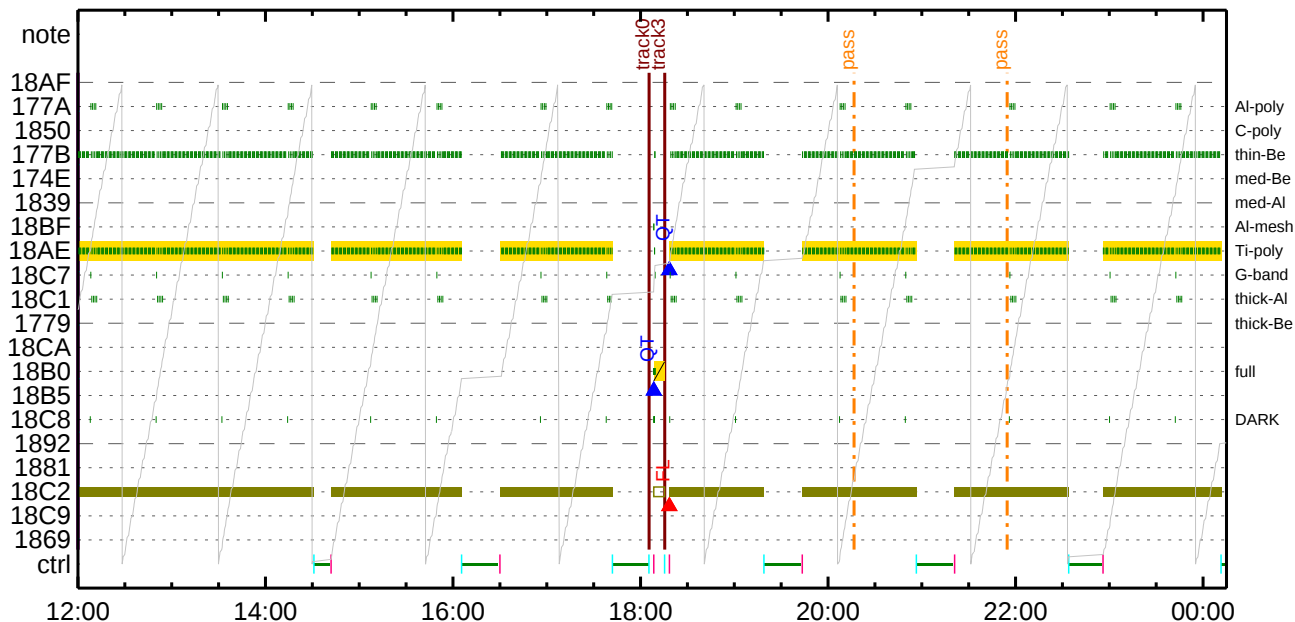
CMDI #0328 2011/12/18



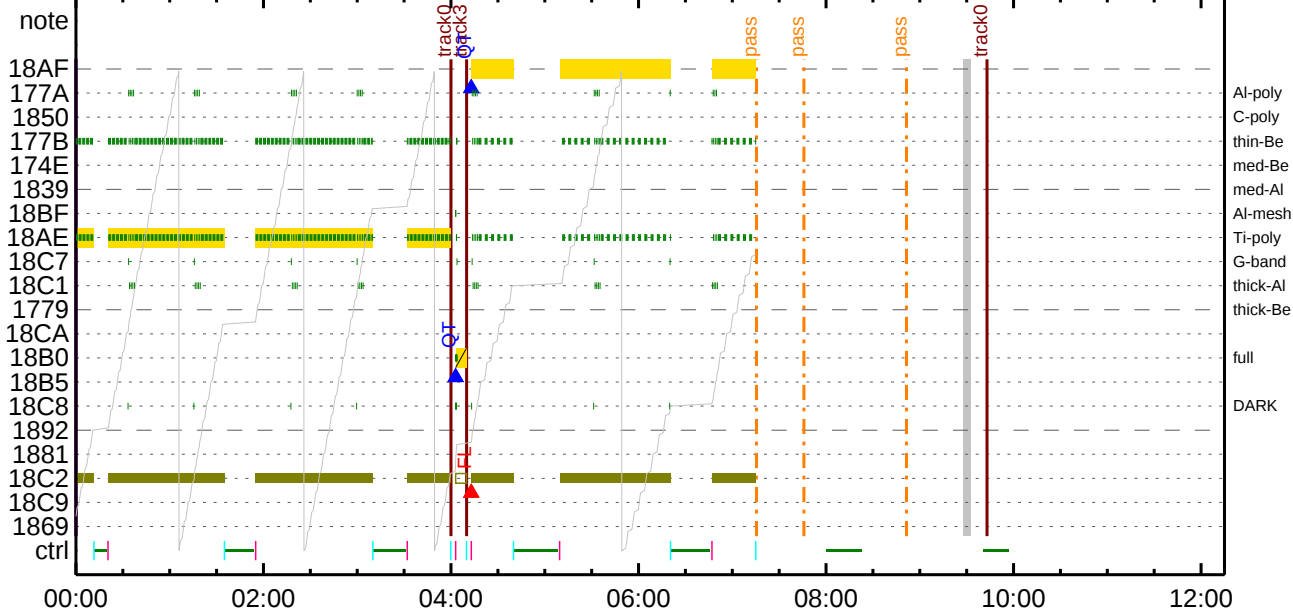
CMDI #0328 2011/12/19



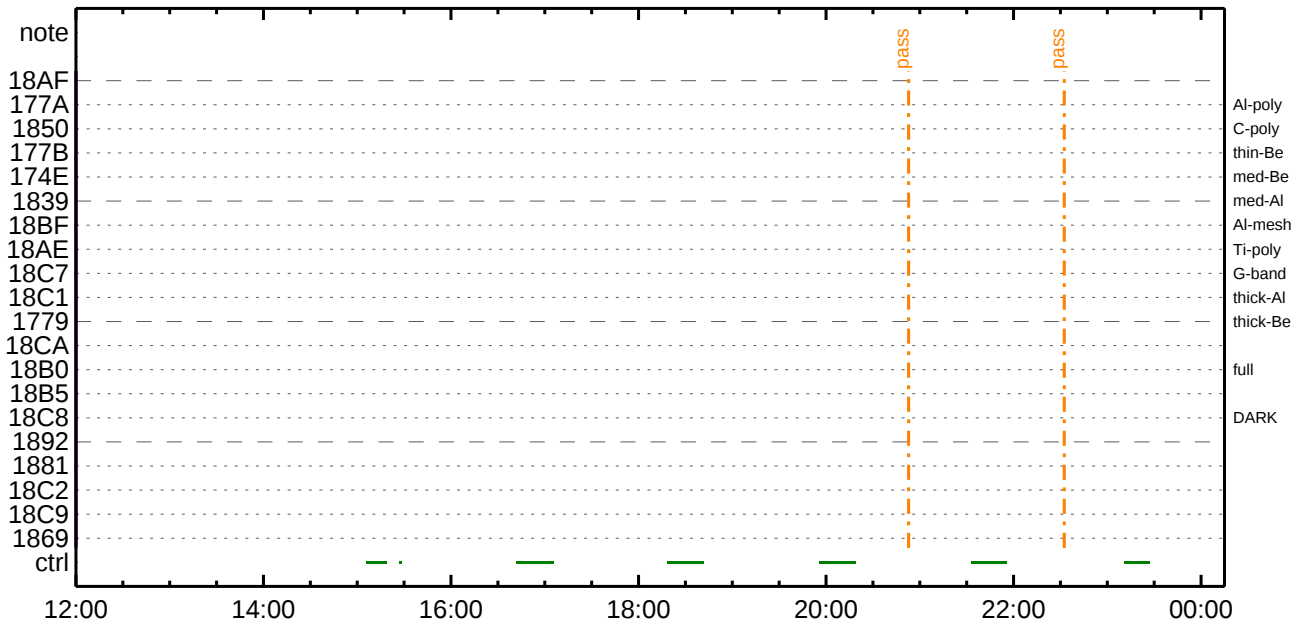
CMDI #0328 2011/12/19



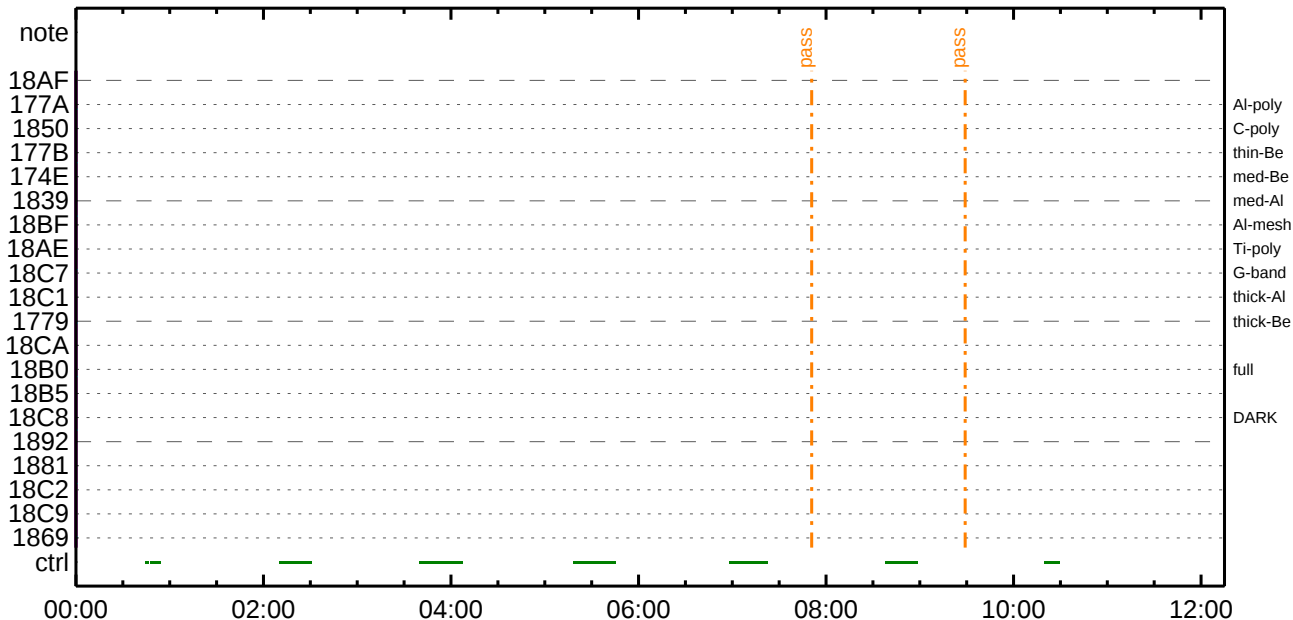
CMDI #0328 2011/12/20



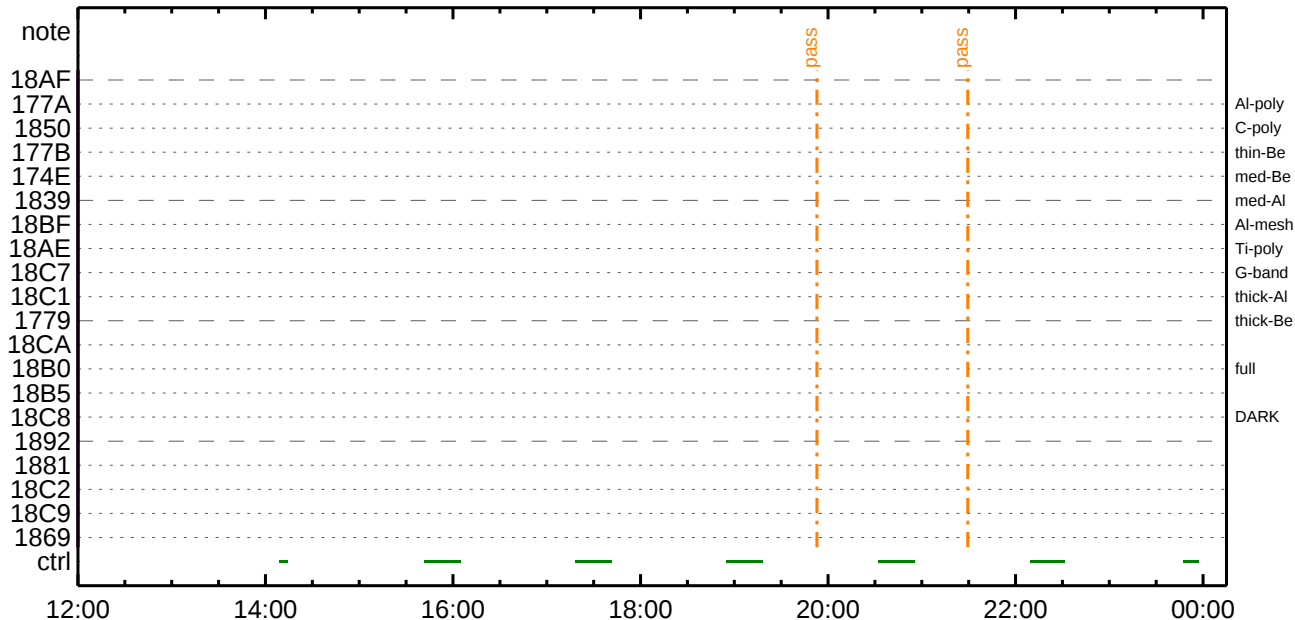
CMDI #0328 2011/12/20



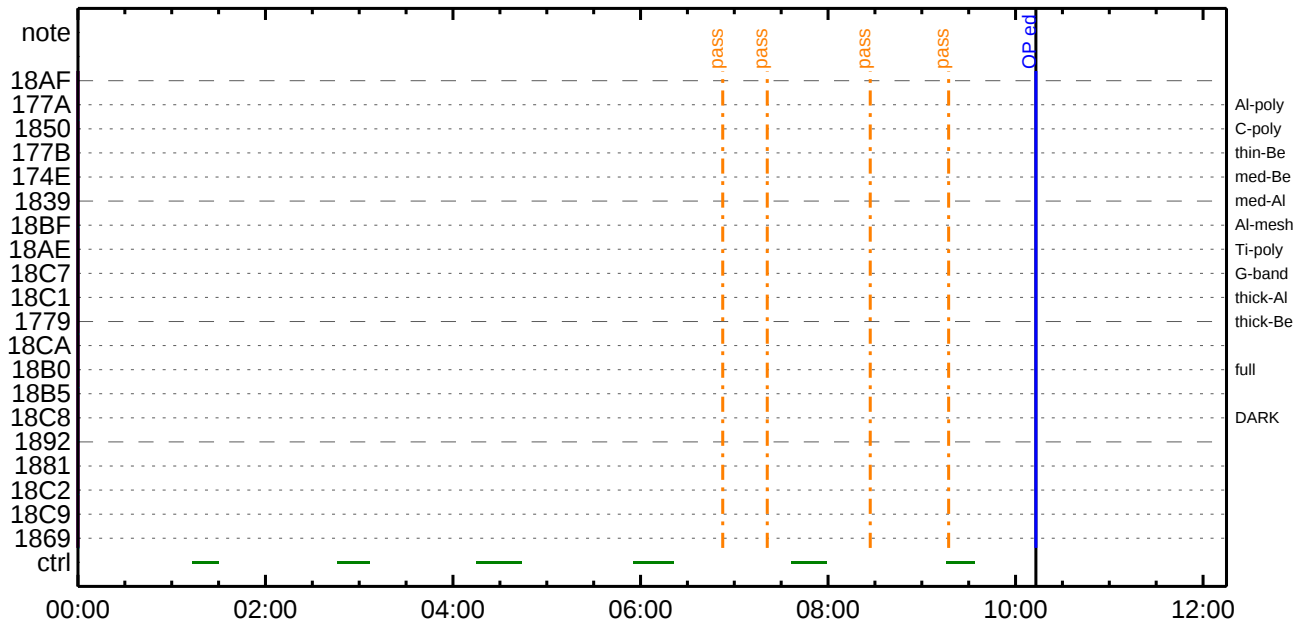
CMDI #0328 2011/12/21



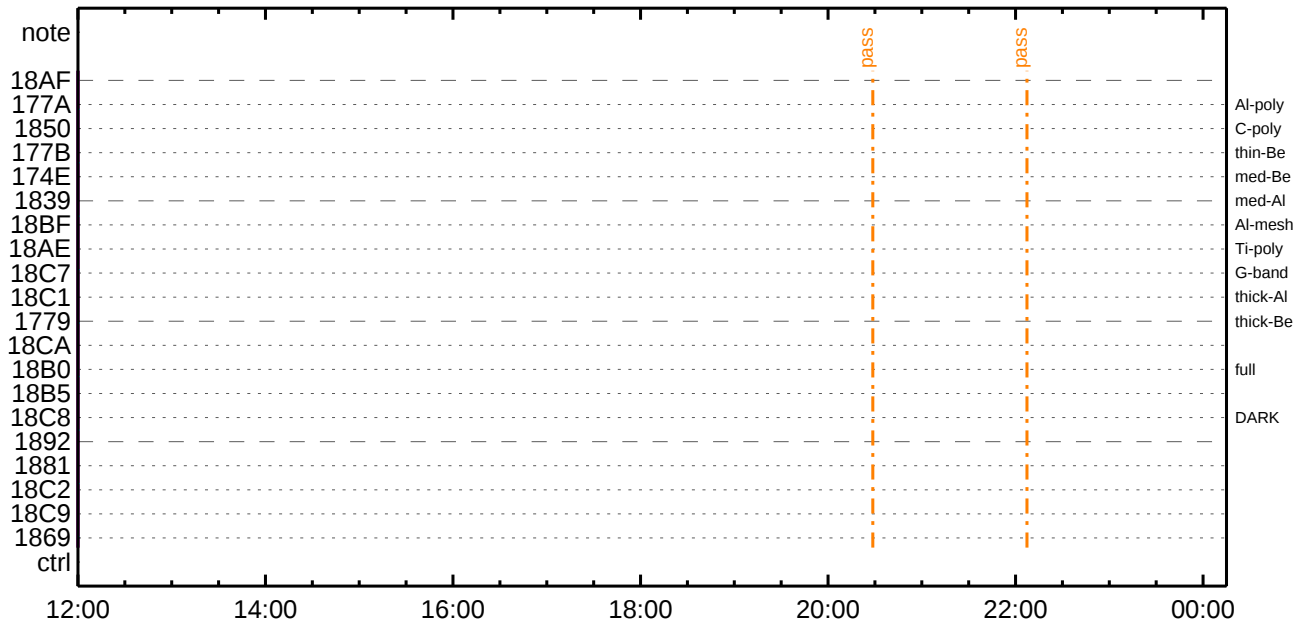
CMDI #0328 2011/12/21



CMDI #0328 2011/12/22



CMDI #0328 2011/12/22



(a) Spacecraft Operation Procedure (real-commands)

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main-535 2011-12-17 11:43:13 113 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥P¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¤•µ°Æ»İ×AÇ¤İ¥Ç¥Á¥×¥İj¼¥ÉjĒĒē½µ•İĒĒjĒē½°Ç¿¤•¤¿¼¹Ç¤İjÇÀ®, Ü¤¹¤ē¤¤¤ÇÁ÷¿®¤•¤Ē¤¤¤¤ējĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ÆĀ, İ×ĒY¤¤ŁOS¤¤¤Ç¤İ»p`Ŏ¤Ŏ¹İĒ, ¤•jÇĒŎİ×¤ĒXĀŎON¤İ¹Ŏ¤Ēİ¤¤¤¤¤¤ējĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Ó¥É¥İ¥Ã¥¼ŎĀŎ¤-°ĀĀē¤•¤¿¤ējÇ°Ē²¼¤İ°ÆĀ, ¼ē½Ç¤Ŏ¼Ā¹Ŏ¤¹¤ējĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼İ°ÆĀ
0033 C. *****
0034 C. ¨ RESTARTjĒPT1jĒ¤•¤¿¤¤¼¹Ç¤İjÇ°Ē²¼¤İ¼Ā¹Ŏ¤»¤°jÇDCBC-150¤Ŏ¿Ēē¤¤jĒ
0035 C.
0036 . C. jãPT1°ÆĀ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Ŏ, j¼Ŭ)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0045 C.
0046 . C. jã¥Ç¥Ó¥Æ¥ĒĀŬĀŎjĒĀ•Ā²ŎēŎjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Ŏ, j¼Ŭ)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0052 C.
0053 C.
0054 . C. PT1°ÆĀ, ¤-¼«Æ°ĀĀ»B¤•¤¿, ĀjÇ°Ē²¼¤Ŏ¼Ā¹Ŏ¤¹¤ējĒ
0055 C. ¥Ç¥Ó¥Æ¥ĒĀŬĀŎ¤ĀĀ•Ā²ŎēŎ¤-ŋĀ¤¤¼¹Ç¤İ °İ»¤¹¤ē¤¤¤ÇĀŎ¤ĀjĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼İ°ÆĀ
0059 C. *****
0060 C. ¨ RESTARTjĒPT2jĒ¤•¤¿¤¤¼¹Ç¤İjÇ°Ē²¼¤İ¼Ā¹Ŏ¤»¤°jÇDCBC-151¤Ŏ¿Ēē¤¤jĒ
0061 C.
0062 . C. jãPT2°ÆĀ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Ŏ, j¼Ŭ)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0071 C.
0072 . C. jã¥Ç¥Ó¥Æ¥ĒĀŬĀŎjĒĀ•Ā²ŎēŎjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Ŏ, j¼Ŭ)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0078 C.
0079 . C. *****
0080 C. DR°ÆĀ, Āã»BjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ÆĀ, Āã»Bjã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

0096 C.
0097 C.
0098 C.
0099 . C. ***** MDP ´ûÃîï»ö%ýðĒð¹ðĒDCBC•x²è *****
0100 C. (¾ă°î¥ŒŒ¥Ē¥ð¥Ē¥ă¥ċ¥ĒðĒĒ¼ð¼Œ»Ū¹ðĒ)
0101 . S. DC-BC dcbc-402:DCBC
0102 (MDP_known_event)
0103 C.
0104 C.
0105 . C. ***** ¥ð¥¹•İ Daily±ċİŃðĒĒ´Œ¹ðĒDCBC•x²è *****
0106 . S. DC-BC dcbc-153:DCBC
0107 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0108 C.
0109 C.
0110 . C. įăLOS¥Œ¥S¥Œ¥¼Œ»Ūįă
0111 C.
0112 . C. ***** LOS *****
0113 C.

Line	Command	Parameter	Condition	Count
0096	C.	0B0CjCSET0EDUMP0IÆ±°i¥Ñ¹0Ç¹00 0³0EjE		
0097	C.			
0098	C.	TI¥³¥D¥Ó¥É00ÀDİ¿(UT)		
0099	+	TI 2011-12-17 10:20:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	00[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.			
0103	+	TI 2011-12-17 10:20:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	00[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.			
0107	+	TI 2011-12-17 10:20:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	00[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.			
0111	+	TI 2011-12-17 10:24:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	00[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.			
0115	C.	°Ê²¼0İÄê%İİÑ0İ¥Ä\$¥Ä¥~¹àİÜ		
0116	C.	00[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	00[HK1_TI_CMD_NUM]	EQ	4
0118	C.	00[HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	00[HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.			
0121	C.	*****		
0122	C.	TIİİ°è¥Ä¥Ó¥×		
0123	C.	*****		
0124	C.			
0125	C.	TI_TBL(0x03AB00-0x03AEFFj\$ 1024byte)		
0126	+	DC 01-23 DHU_DMA_DUMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	00[HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	00[HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	00[HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	00[HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	00[HK1_DMA_DMP_PIM]	EQ	DHU
0133	+	DC 01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	00[HK1_PKT_FORM_NO]	EQ	7
0136	C.	00[HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	00[HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	00[HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	00[HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.			
0141	C.	¥Ä¥Ó¥×½ªİ»00³İÇ\$		
0142	C.	00[HK1_DMP_CHK_FLG]	EQ	NON
0143	C.			
0144	C.	RAM ID=TI_TBL0İ%Ê¹Ç•è²İOK00³İÇ\$		
0145	C.			
0146	C.	DHU¥âj¼¥ÉjÊ¼ý½, ¥İj¼¥ÊjË00İâ0¹		
0147	+	DC 01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	00[HK1_PKT_FORM_NO]	EQ	2
0150	C.	00[HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	00[HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	00[HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.			
0154	C.	Stop EIS observation and temporarily disable EIS mode changes		
0155	C.			
0156	C.			
0157	C.	***** Start EIS operation (TI set) *****		
0158	C.	Execute, after the success of OP upload.		
0159	C.	Set EIS TI-commands		
0160	+	TI 2011-12-17 10:24:30.0		
0161	DC	07-FC EIS_MODE_MANU		
0162	BC	(21 02)		
0163	+	TI 2011-12-17 10:24:40.0		
0164	DC	07-FC EIS_MODE_CHG_DIS		
0165	BC	(22)		
0166	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0167	C.	***** End EIS operation (TI set) *****		
0168	C.			
0169	C.			
0170	C.	*****		
0171	C.	SOT TI command set		
0172	C.	*****		
0173	C.	Execute, after the success of OP upload.		
0174	+	TI 2011-12-17 10:24:16.0		
0175	DC	07-F0 MDP_SOT_MODE_STBY		
0176	BC	(41)		
0177	C.	-----		
0178	C.	HK1_TI_CMD_NUM = 1 CNTUP []		
0179	C.	-----		
0180	C.	***** SOT END *****		
0181	C.			
0182	C.	***** XRT START *****		
0183	C.	Execute, after the success of OP upload.		
0184	+	TI 2011-12-17 10:24:00.0		
0185	DC	07-F0 MDP_XRT_MODE_STBY		
0186	BC	(c3)		
0187	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0188	C.			
0189	C.			

0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥Ð¥¹•İ Daily±¿İÑ¤Ė´Ø¤¹¤ĖDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ĩăLOS¥Á¥S¥Ā¥¼Ā»Üĭă
0203 C.
0204 . C. ***** LOS *****
0205 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-537 2011-12-17 11:43:13 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¤•µ°Æ»İ×AÇ¤İ¥Ç¥Á¥×¥İj¼¥ÉjĒĒ½µ•İĒĒjĒĒ½°Ç¿¤•¤¿¼¹Ç¤İjÇÀ®, Ü¤¹¤Ē¤¤¤ÇÁ÷¿®¤•¤Ē¤¤¤¤ĒjĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ÆĀ, İ×ĒY¤¤ŁOS¤¤¤Ç¤İ»p´Ŏ¤Ŏ¹İĒ, ¤•jÇĒŎİ×¤ĒXĀŎON¤İ¹Ŏ¤Ēİ¤¤¤¤¤¤¤ĒjĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Ŏ¥É¥İ¥Ã¥¼ŎĀŎ¤¬°ĀĀĒ¤•¤¿¤ĒjÇ°Ē²¼¤İ°ÆĀ, ¼Ē½Ç¤Ŏ¼Ā¹Ŏ¤¹¤ĒjĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼İ°ÆĀ
0033 C. *****
0034 C. ¨ ¨ RESTARTjĒPT1jĒ¤¤•¤¿¤¤¼¹Ç¤İjÇ°Ē²¼¤İ¼Ā¹Ŏ¤»¤°jÇDCBC-150¤Ŏ¿ĒĒ¤¤jĒ
0035 C.
0036 . C. jãPT1°ÆĀ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Ŏ, j¼Ŭ)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0045 C.
0046 . C. jã¥Ç¥Ŏ¥Æ¥ĒĀŬĀŎjĒĀ•Ā²ŎĒŎjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Ŏ, j¼Ŭ)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0052 C.
0053 C.
0054 . C. PT1°ÆĀ, ¤¬¼«Æ°ĀĀ»B¤•¤¿, ĀjÇ°Ē²¼¤Ŏ¼Ā¹Ŏ¤¹¤ĒjĒ
0055 C. ¥Ç¥Ŏ¥Æ¥ĒĀŬĀŎ¤ĀĀ•Ā²ŎĒĒ¤¬ŎĀ¤¼¹Ç¤İ °İ»¤¹¤Ē¤¤¤ÇĀŎ¤ĀjĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼İ°ÆĀ
0059 C. *****
0060 C. ¨ ¨ RESTARTjĒPT2jĒ¤¤•¤¿¤¤¼¹Ç¤İjÇ°Ē²¼¤İ¼Ā¹Ŏ¤»¤°jÇDCBC-151¤Ŏ¿ĒĒ¤¤¤jĒ
0061 C.
0062 . C. jãPT2°ÆĀ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Ŏ, j¼Ŭ)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0071 C.
0072 . C. jã¥Ç¥Ŏ¥Æ¥ĒĀŬĀŎjĒĀ•Ā²ŎĒŎjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Ŏ, j¼Ŭ)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0078 C.
0079 . C. *****
0080 C. DR°ÆĀ, Āã»BjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ÆĀ, Āã»Bjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 13s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2011-12-17 10:24:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'ûÃÎ»ô%ÝðÊÄð¹ðÐCBC•x²è *****
0153 C. (%ã°Î¥Ô¥Ã¥Ê¥Ð¥Ê¥Ä¥Ç¥ÊðÊ½¼ð¼Ã»Û¹ðé)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ¥Ð¥¹•Î Daily±¿ÎÑðÊ´ð¹ðÐCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ¡ãLOS¥Á¥$¥Ã¥¬¼Ã»Û¡ä
0164 C.
0165 . C. ***** LOS *****
0166 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-538 2011-12-17 11:43:13 126 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ĀiĒ¿¿Aa•µ°£»İ×AÇ¿İ¥C¥A¥×¥İj¼¥ÉjĒĒ¿µ•İİĒjĒĒ¿°Ç¿¿•¿¿İ¹¿¿İjÇÀ®, ũ¿¹¿Ē¿¿¿¿ÇÁ÷¿®¿•¿Ē¿¿¿¿¿ĒĒj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-267:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2011-12-17 10:24:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR ____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 08)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 08 20)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0f 80 80 06 06)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 10 80 80 08 08)
0088 +. DC 07-F0 MDP_XRT_FLD_ENA
0089 BC (d8)
0090 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091 BC (c8)
0092 +. DC 07-F0 MDP_XRT_AEC_RESET
0093 BC (d0)
0094 +. DC 07-F0 MDP_XRT_ARS_DIS
0095 BC (d5)
```

```
0096 + DC 07-F0 MDP_XRT_FLD_RESET
0097 BC (da)
0098 . C. ----- Success Verify ? OK / NG ____
0099 C.
0100 C.
0101 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0102 C.
0103 +. DC 07-F0 MDP_XRT_MODE_OBSV
0104 BC (c2)
0105 +. TI 2011-12-17 10:24:02.0
0106 DC 07-F0 MDP_XRT_MODE_OBSV
0107 BC (c2)
0108 . C. ----- Success Verify ? OK / NG ____
0109 C.
0110 C. ***** XRT END *****
0111 C.
0112 . C. ***** MDP `0ÃÎ«0%ÿ«ÊÃð«¹«èDCBC•x²è *****
0113 C. (%ã°Ï¥0¥Ã¥Ë¥ð¥Ë¥ã¥¢¥è«Ë½¼«¼Ã»Û«¹«è)
0114 . S. DC-BC dcbc-402:DCBC
0115 (MDP_known_event)
0116 C.
0117 C.
0118 . C. ***** ¥Ð¥¹•Ï Daily±¿ÎÑ«Ë´0«¹«èDCBC•x²è *****
0119 . S. DC-BC dcbc-153:DCBC
0120 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0121 C.
0122 C.
0123 . C. ¡ãLOS¥Á¥$¥Ã¥¬¼Ã»Û¡ã
0124 C.
0125 . C. ***** LOS *****
0126 C.
```

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*** OP Sequence for XRT ***

2011/12/17	10:30:00.0	XRT_CTRL_MANU_447_OG [0x1bf]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/12/17	10:30:10.0	XRT_FOCUS_RECALIBRATE_428_OG [0x1ac]		
		XRT_FOCUS_RECAL	2	07-F8 78 00
2011/12/17	10:34:10.0	XRT_FOCUS_POSITION_409_OG [0x199]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2011/12/17	10:34:54.0	XRT_CTRL_MANU_439_OG [0x1b7]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/12/17	10:35:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 03 00 00 00 00
2011/12/17	10:37:26.0	XRT_FOCUS_POSITION_409_OG [0x199]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2011/12/17	10:37:46.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2011/12/17	10:37:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2011/12/17	10:37:50.0	XRT_AEC_RESET_443_OG [0x1bb]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2011/12/17	10:37:52.0	XRT_ARS_DIS_431_OG [0x1af]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2011/12/17	10:37:54.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/12/17	10:37:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0d
2011/12/17	10:37:58.0	XRT_FL_PROG_SET_414_OG [0x19e]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 03
2011/12/17	10:38:00.0	XRT_CTRL_AUTO_406_OG [0x196]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/12/17	14:54:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/12/17	14:54:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/12/17	14:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/12/17	14:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/12/17	15:06:30.0	XRT_Custom_418_OG [0x1a2]		
2011/12/17	15:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/12/17	16:29:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/12/17	16:29:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/12/17	16:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2011/12/17	16:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2011/12/17	16:53:00.0	XRT_Custom_418_OG [0x1a2]		
2011/12/17	16:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/12/17	17:42:54.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/12/17	17:42:56.0	XRT_FOCUS_POSITION_401_OG [0x191]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2011/12/17	17:43:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00
2011/12/17	17:43:16.0	XRT_FLD_DIS_402_OG [0x192]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2011/12/17	17:43:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2011/12/17	17:43:20.0	XRT_ARS_DIS_438_OG [0x1b6]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2011/12/17	17:45:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 08
2011/12/17	17:46:00.0	XRT_CTRL_AUTO_406_OG [0x196]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/12/17	17:52:54.0	XRT_CTRL_MANU_439_OG [0x1b7]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/12/17	17:53:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 03 00 00 00 00
2011/12/17	17:55:26.0	XRT_FOCUS_POSITION_409_OG [0x199]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2011/12/17	17:55:46.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2011/12/17	17:55:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2011/12/17	17:55:50.0	XRT_AEC_RESET_443_OG [0x1bb]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2011/12/17	17:55:52.0	XRT_ARS_DIS_431_OG [0x1af]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2011/12/17	17:55:54.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2011/12/17	17:55:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0d
2011/12/17	17:55:58.0	XRT_FL_PROG_SET_414_OG [0x19e]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 03
2011/12/17	17:56:00.0	XRT_CTRL_AUTO_406_OG [0x196]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2011/12/17	18:06:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2011/12/17	18:06:32.0	XRT_FLD_RESET_412_OG [0x19c]		

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2011/12/17	18:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/17	18:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/17	18:30:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/17	18:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/17	19:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/17	19:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/17	19:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/17	19:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/17	20:07:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/17	20:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/17	21:21:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/17	21:21:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/17	21:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/17	21:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/17	21:44:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/17	21:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/17	22:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/17	22:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/17	22:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/17	23:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/17	23:18:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/17	23:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	00:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	00:35:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	00:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	00:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	00:42:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	00:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	01:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	01:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	01:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	02:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	02:18:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	02:19:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	03:31:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	03:31:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	03:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	03:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	03:55:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	03:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	05:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	05:05:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	05:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	05:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	05:33:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	05:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/18	06:00:00.0	AOCs_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2011/12/18	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9

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2011/12/18	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/12/18	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/18	06:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/12/18	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	06:15:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	06:15:02.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/18	06:15:22.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/18	06:15:24.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/12/18	06:15:26.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/18	06:18:04.0	XRT_QT_PROG_SET_446_OG [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2011/12/18	06:18:06.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	06:45:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	06:45:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	06:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	06:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	06:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	07:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/12/18	07:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/18	07:02:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/18	07:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/18	07:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/18	07:02:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/18	07:02:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	07:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2011/12/18	07:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/18	07:10:00.0	XRT_Custom_418_OG [0x1a2]			
2011/12/18	07:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	08:25:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	08:25:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	08:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	08:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	08:47:00.0	XRT_Custom_418_OG [0x1a2]			
2011/12/18	08:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	10:06:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	10:06:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	10:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	10:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	10:19:00.0	XRT_Custom_418_OG [0x1a2]			
2011/12/18	10:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	15:29:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	15:29:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	15:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	15:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	15:52:30.0	XRT_Custom_418_OG [0x1a2]			
2011/12/18	15:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	17:05:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	17:05:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	17:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	17:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			

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2011/12/18	17:29:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	17:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
2011/12/18	18:00:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	18:00:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	18:00:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/18	18:00:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00
2011/12/18	18:00:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/18	18:00:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/12/18	18:03:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/18	18:03:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/12/18	18:10:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	18:10:30.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	18:12:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	03 00 00 00 00
2011/12/18	18:13:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/18	18:13:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/18	18:13:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/18	18:13:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/18	18:13:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/18	18:13:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	18:13:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2011/12/18	18:13:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/18	18:43:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	18:43:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	18:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	18:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	19:06:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	19:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
2011/12/18	20:20:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	20:20:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	20:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	20:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	20:43:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	20:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
2011/12/18	21:57:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	21:57:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	21:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	22:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	22:19:30.5	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	22:20:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]				
2011/12/18	23:35:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/18	23:35:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/18	23:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/18	23:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/18	23:46:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/18	23:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
2011/12/19	01:02:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/19	01:02:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/19	01:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/19	01:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2011/12/19	01:18:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/19	01:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/19	02:34:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/19	02:34:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/19	02:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/19	02:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/19	02:54:30.0	XRT_Custom_418_OG [0x1a2]					
2011/12/19	02:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/19	03:49:54.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/19	03:49:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/12/19	03:50:00.0	AOCs_Or-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2011/12/19	03:50:16.0	XRT_FLD_DIS_402_OG [0x192]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/12/19	03:50:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/12/19	03:50:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/19	03:52:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2011/12/19	03:53:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/19	03:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/19	04:00:00.0	AOCs_Or-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	03 00 00 00 00	
2011/12/19	04:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/12/19	04:02:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/19	04:02:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/19	04:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/19	04:02:46.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/19	04:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/19	04:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/19	04:02:52.0	XRT_ARS_DIS_431_OG [0x1af]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/19	04:02:54.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/19	04:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2011/12/19	04:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2011/12/19	04:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/19	04:32:00.0	XRT_Custom_418_OG [0x1a2]					
2011/12/19	04:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/19	05:43:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/19	05:43:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/19	05:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/19	05:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/19	06:09:30.0	XRT_Custom_418_OG [0x1a2]					
2011/12/19	06:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/19	07:23:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/19	07:23:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/19	07:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/19	07:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/19	07:46:30.0	XRT_Custom_418_OG [0x1a2]					
2011/12/19	07:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/19	09:03:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/19	09:03:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/19	09:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/19	09:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/19	09:22:30.0	XRT_Custom_418_OG [0x1a2]					

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2011/12/19	09:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/19	10:45:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/19	10:45:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/19	10:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/19	10:48:30.0	XRT_Custom_418_OG [0x1a2]			
2011/12/19	10:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/19	10:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/19	14:31:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/19	14:31:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/19	14:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/19	14:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/19	14:41:00.0	XRT_Custom_418_OG [0x1a2]			
2011/12/19	14:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/19	16:05:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/19	16:05:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/19	16:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/19	16:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/19	16:29:00.0	XRT_Custom_418_OG [0x1a2]			
2011/12/19	16:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/19	17:42:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/19	17:42:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/19	17:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/19	17:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/19	18:05:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/19	18:05:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/19	18:05:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/12/19	18:05:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/19	18:05:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/12/19	18:05:50.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/19	18:08:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/12/19	18:08:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/19	18:15:24.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/19	18:15:30.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2011/12/19	18:17:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/19	18:18:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/19	18:18:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/19	18:18:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/19	18:18:22.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/19	18:18:24.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/19	18:18:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2011/12/19	18:18:28.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/19	18:18:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/19	19:19:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/19	19:19:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/19	19:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/19	19:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/19	19:42:30.0	XRT_Custom_418_OG [0x1a2]			
2011/12/19	19:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/19	20:56:30.0	XRT_CTRL_MANU_408_OG [0x198]			

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2011/12/19	20:56:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/19	20:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/19	20:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/19	21:20:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/19	21:21:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/19	22:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/19	22:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/19	22:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/19	22:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/19	22:55:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/19	22:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/20	00:11:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/20	00:11:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/20	00:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/20	00:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/20	00:19:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/20	00:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/20	01:35:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/20	01:35:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/20	01:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/20	01:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/20	01:54:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/20	01:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/20	03:10:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/20	03:10:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/20	03:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/20	03:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/20	03:31:00.5	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/20	03:32:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/20	03:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/20	03:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/20	04:00:00.0	AOCs_OrRe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2011/12/20	04:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/20	04:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/12/20	04:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/20	04:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/12/20	04:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/20	04:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/20	04:10:00.0	AOCs_OrRe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00
2011/12/20	04:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/20	04:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/20	04:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/20	04:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/20	04:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/20	04:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/20	04:12:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2011/12/20	04:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/20	04:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/20	04:40:00.0	XRT_CTRL_MANU_408_OG [0x198]				

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2011/12/20	04:40:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/20	04:40:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/20	04:43:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/20	05:08:30.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/20	05:09:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/20	06:20:30.5	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/20	06:20:32.5	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/20	06:20:34.5	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/20	06:23:44.5	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/20	06:46:00.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/20	06:47:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/20	07:15:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/20	09:43:00.0	AOCS_ORe-point_Start_2_0G [0x098]	AOCU_NM	5	02-76	00 00 00 00 00