

Period: 2011/12/29 10:42:00 - 2012/01/04 09:29:00

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Normal mode

XOB #18B0: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(															
Term			Pointing (x, y)					Comment							
12/29 15:33:00 - 12/29 15:41:30			Fixed (0.0, 0.0)					synoptic, shifted -1.0 min							
12/30 06:34:30 - 12/30 06:41:24			Fixed (0.0, 0.0)					synoptic, shifted 31.5 min							
PROG= 08 1-time(s)															
└ Subr= 1 1-time(s) 12.0sec															
└ Seqn= 46 1-time(s) 4.0sec															
└ Open/Al-mesh			Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Open/Al-mesh			Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Seqn= 5 1-time(s) 2.0sec															
└ Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)		DPCM	0	0	2.0sec
└ Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)		DPCM	0	0	2.0sec
└ Seqn= 69 1-time(s) 4.0sec															
└ Open/Ti-poly			Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Open/Ti-poly			Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Seqn= 50 1-time(s) 2.0sec															
└ thin-Be/Open			thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ thin-Be/Open			thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Seqn= 4 1-time(s) 2.0sec															
└ Open/G-band			Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Default Filter			Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly			
Term	Pointing (x, y)	Comment	
12/29 16:09:00 - 12/29 16:15:54	Fixed (-918.8, -15.0)	HOP79, E-W scan 1/21	
12/29 16:19:00 - 12/29 16:25:54	Fixed (-719.7, -15.0)	HOP79, E-W scan 2/21	
12/29 16:29:00 - 12/29 16:35:54	Fixed (-520.7, -15.0)	HOP79, E-W scan 3/21	
12/29 16:39:00 - 12/29 16:45:54	Fixed (-321.6, -15.0)	HOP79, E-W scan 4/21	
12/29 16:49:00 - 12/29 16:55:54	Fixed (-122.5, -15.0)	HOP79, E-W scan 5/21	
12/29 16:59:00 - 12/29 17:05:54	Fixed (76.6, -15.0)	HOP79, E-W scan 6/21	
12/29 17:09:00 - 12/29 17:18:00	Fixed (275.6, -15.0)	HOP79, E-W scan 7/21	
12/29 17:45:30 - 12/29 17:52:24	Fixed (474.7, -15.0)	HOP79, E-W scan 8/21	
12/29 17:55:30 - 12/29 18:02:24	Fixed (673.7, -15.0)	HOP79, E-W scan 9/21	
12/29 18:05:30 - 12/29 18:12:24	Fixed (872.8, -15.0)	HOP79, E-W scan 10/21	
12/29 18:15:30 - 12/29 18:22:24	Fixed (-917.0, -227.1)	HOP79, E-W scan 11/21	
12/29 18:25:30 - 12/29 18:32:24	Fixed (-749.6, -241.3)	HOP79, E-W scan 12/21	
12/29 18:35:30 - 12/29 18:42:24	Fixed (-568.7, -233.6)	HOP79, E-W scan 13/21	
12/29 18:45:30 - 12/29 18:55:00	Fixed (-382.9, -228.9)	HOP79, E-W scan 14/21	

12/29 19:22:30 - 12/29 19:29:24	Fixed (-192.6, -226.4)	HOP79, E-W scan 15/21
12/29 19:32:30 - 12/29 19:39:24	Fixed (2.2, -225.6)	HOP79, E-W scan 16/21
12/29 19:42:30 - 12/29 19:49:24	Fixed (172.6, -226.4)	HOP79, E-W scan 17/21
12/29 19:52:30 - 12/29 19:59:24	Fixed (362.9, -228.9)	HOP79, E-W scan 18/21
12/29 20:02:30 - 12/29 20:09:24	Fixed (548.7, -233.6)	HOP79, E-W scan 19/21
12/29 20:12:30 - 12/29 20:19:24	Fixed (729.6, -241.3)	HOP79, E-W scan 20/21
12/29 20:22:30 - 12/29 20:29:54	Fixed (907.0, -242.1)	HOP79, E-W scan 21/21

PROG= 04 Inf.-time(s)

└ Subr= 1 1-time(s) 120.0sec

└ Seqn= 57 1-time(s) 4.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad

Term	Pointing (x, y)	Comment
12/29 20:57:00 - 12/30 06:31:24	Track (-106.2, -297.0) @ 12/29 20:30:00	plage obs.

PROG= 13 Inf.-time(s)

└ Subr= 1 1-time(s) 2.0sec

└ Seqn= 19 1-time(s) 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

└ Seqn= 96 4-time(s) 2.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

└ Subr= 2 1-time(s) 2.0sec

└ Seqn= 62 15-time(s) 150.0sec

thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
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Flare mode

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XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops

Term	Pointing (x, y)	Comment
12/29 10:55:00 - 12/29 15:29:54	Track (-189.1, -298.3) @ 12/29 10:52:00	# OP start + 10min, plage obs.

12/29 16:09:00 - 12/29 16:15:54	Fixed (-918.8, -15.0)	HOP79, E-W scan 1/21
12/29 16:19:00 - 12/29 16:25:54	Fixed (-719.7, -15.0)	HOP79, E-W scan 2/21
12/29 16:29:00 - 12/29 16:35:54	Fixed (-520.7, -15.0)	HOP79, E-W scan 3/21
12/29 16:39:00 - 12/29 16:45:54	Fixed (-321.6, -15.0)	HOP79, E-W scan 4/21
12/29 16:49:00 - 12/29 16:55:54	Fixed (-122.5, -15.0)	HOP79, E-W scan 5/21
12/29 16:59:00 - 12/29 17:05:54	Fixed (76.6, -15.0)	HOP79, E-W scan 6/21
12/29 17:09:00 - 12/29 17:18:00	Fixed (275.6, -15.0)	HOP79, E-W scan 7/21
12/29 17:45:30 - 12/29 17:52:24	Fixed (474.7, -15.0)	HOP79, E-W scan 8/21
12/29 17:55:30 - 12/29 18:02:24	Fixed (673.7, -15.0)	HOP79, E-W scan 9/21
12/29 18:05:30 - 12/29 18:12:24	Fixed (872.8, -15.0)	HOP79, E-W scan 10/21
12/29 18:15:30 - 12/29 18:22:24	Fixed (-917.0, -227.1)	HOP79, E-W scan 11/21
12/29 18:25:30 - 12/29 18:32:24	Fixed (-749.6, -241.3)	HOP79, E-W scan 12/21
12/29 18:35:30 - 12/29 18:42:24	Fixed (-568.7, -233.6)	HOP79, E-W scan 13/21
12/29 18:45:30 - 12/29 18:55:00	Fixed (-382.9, -228.9)	HOP79, E-W scan 14/21
12/29 19:22:30 - 12/29 19:29:24	Fixed (-192.6, -226.4)	HOP79, E-W scan 15/21
12/29 19:32:30 - 12/29 19:39:24	Fixed (2.2, -225.6)	HOP79, E-W scan 16/21
12/29 19:42:30 - 12/29 19:49:24	Fixed (172.6, -226.4)	HOP79, E-W scan 17/21
12/29 19:52:30 - 12/29 19:59:24	Fixed (362.9, -228.9)	HOP79, E-W scan 18/21
12/29 20:02:30 - 12/29 20:09:24	Fixed (548.7, -233.6)	HOP79, E-W scan 19/21
12/29 20:12:30 - 12/29 20:19:24	Fixed (729.6, -241.3)	HOP79, E-W scan 20/21
12/29 20:22:30 - 12/29 20:29:54	Fixed (907.0, -242.1)	HOP79, E-W scan 21/21

12/29 20:57:00 - 12/30 06:31:24 Track (-106.2, -297.0) @ 12/29 20:30:00

plage obs.

12/30 06:44:30 - 12/30 09:50:54 Track (-17.6, -295.9) @ 12/30 06:41:30

plage obs.

PROG= 03 12-time(s)

└ Subr= 1 45-time(s) 10.0sec

└ Seqn= 20 1-time(s) 2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
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└ Seqn= 63 1-time(s) 2.0sec

med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

└ Seqn= 77 1-time(s) 2.0sec

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
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└ Subr= 2 1-time(s) 10.0sec

└ Seqn= 90 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
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Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

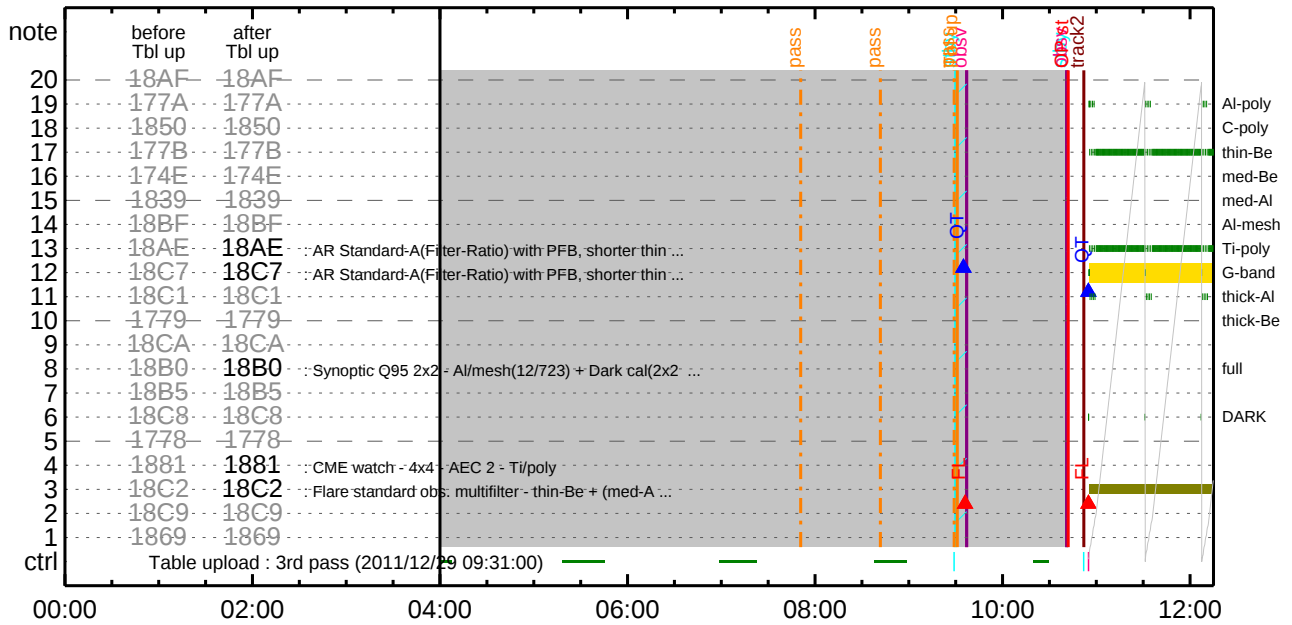
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Flare Detection

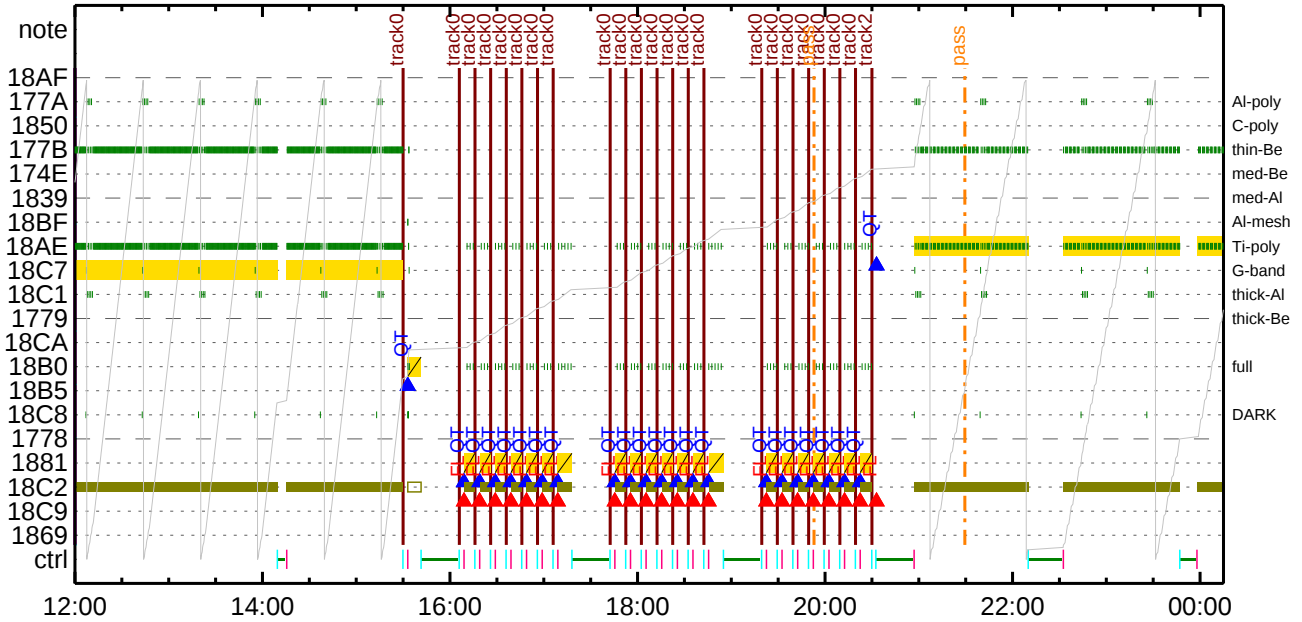
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FLD Patrol												
Term			Pointing (x, y)					Comment				
12/29 16:08:46 - 12/30 06:31:46			Fixed (-918.8, -15.0)					HOP79, E-W scan 1/21				
12/30 06:44:16 - 01/04 09:29:00			Track (-17.6, -295.9) @ 12/30 06:41:30					plage obs.				
Open/Ti-poly	Open/thick-AI	close	Safe	Norm	8ms	Obs	8x8	Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

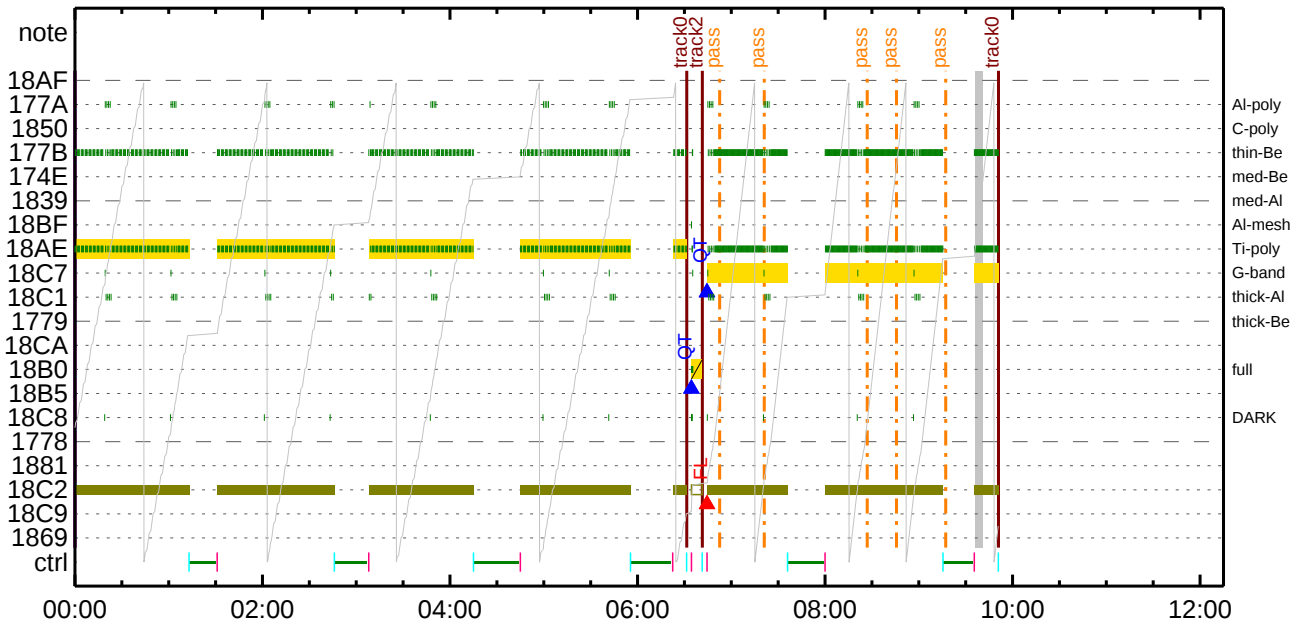
CMDI #0350 2011/12/29



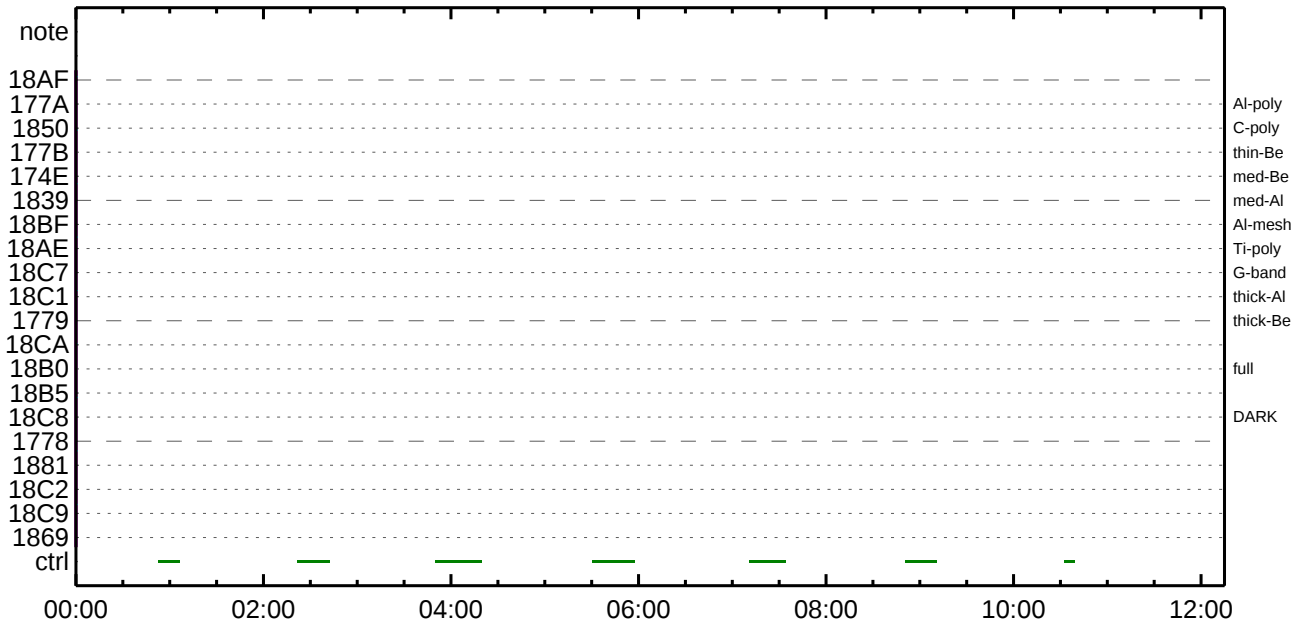
CMDI #0350 2011/12/29



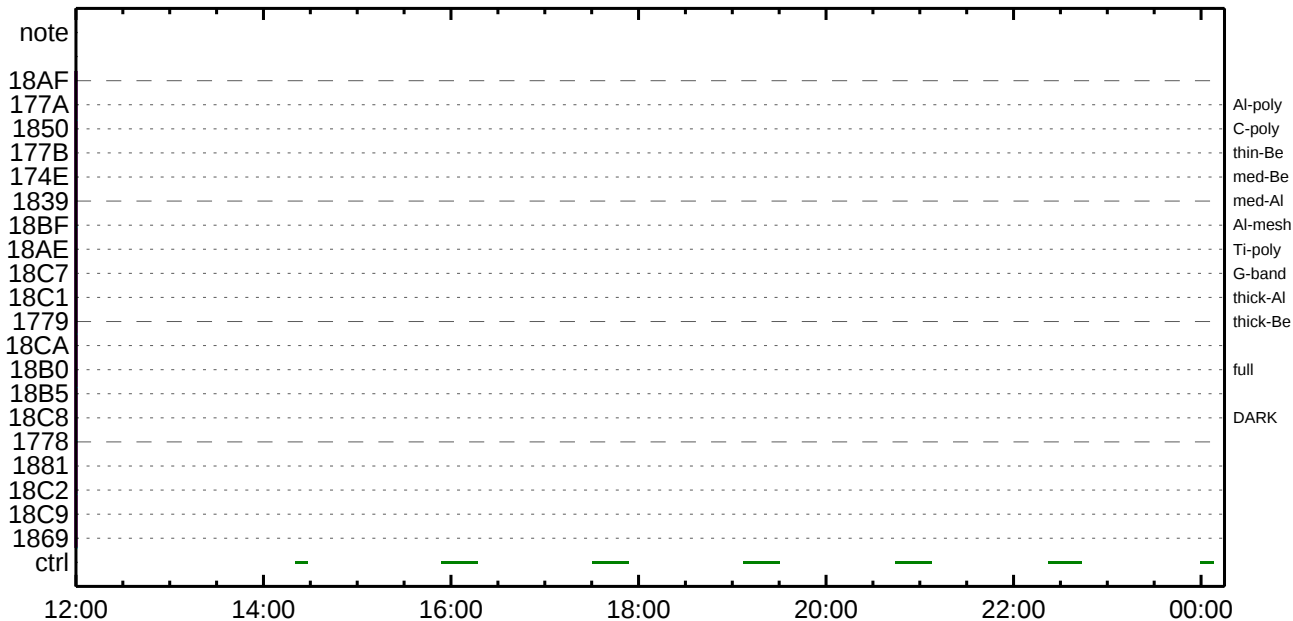
CMDI #0350 2011/12/30



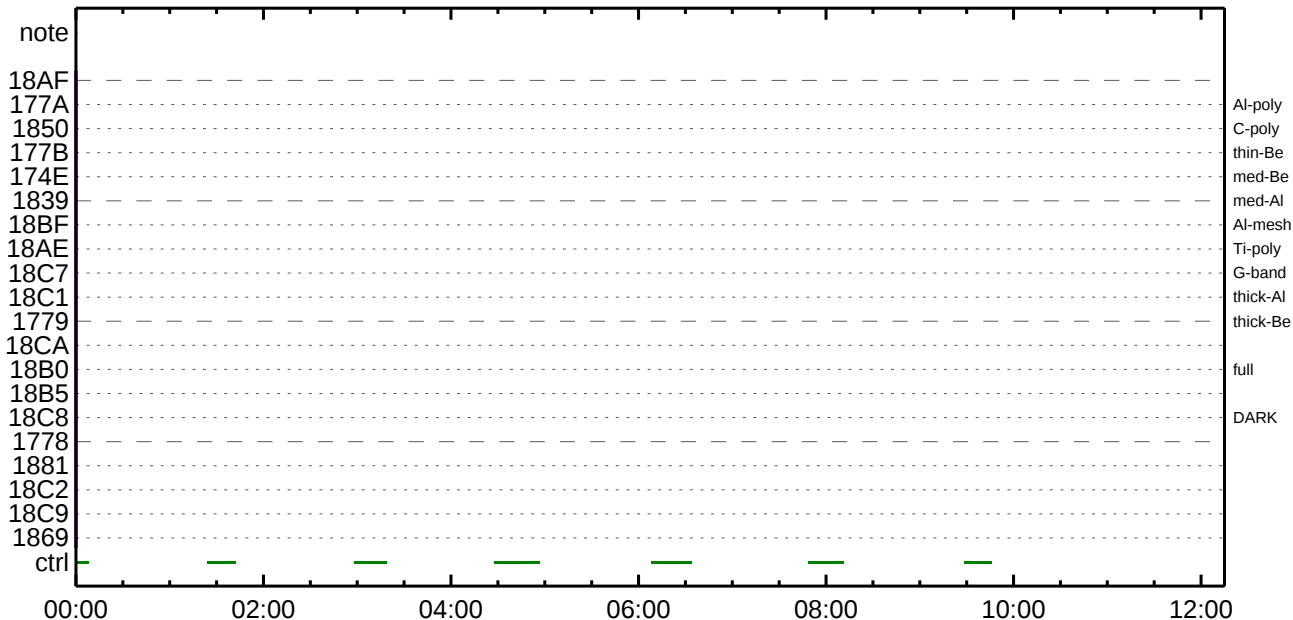
CMDI #0350 2012/01/01



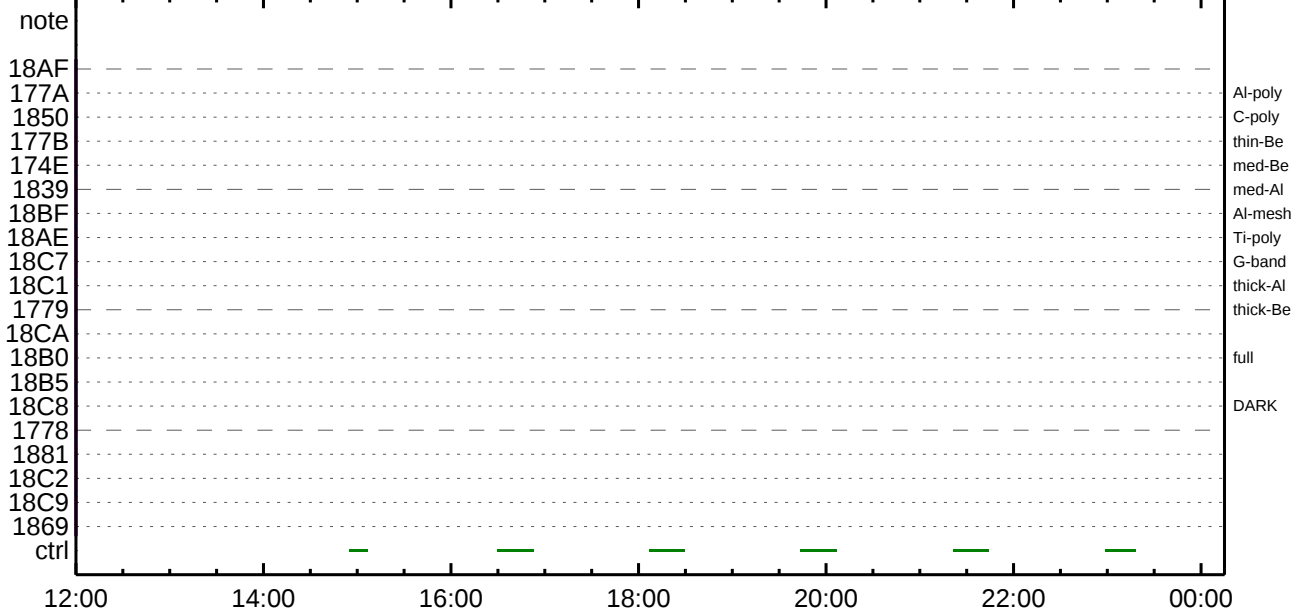
CMDI #0350 2012/01/01



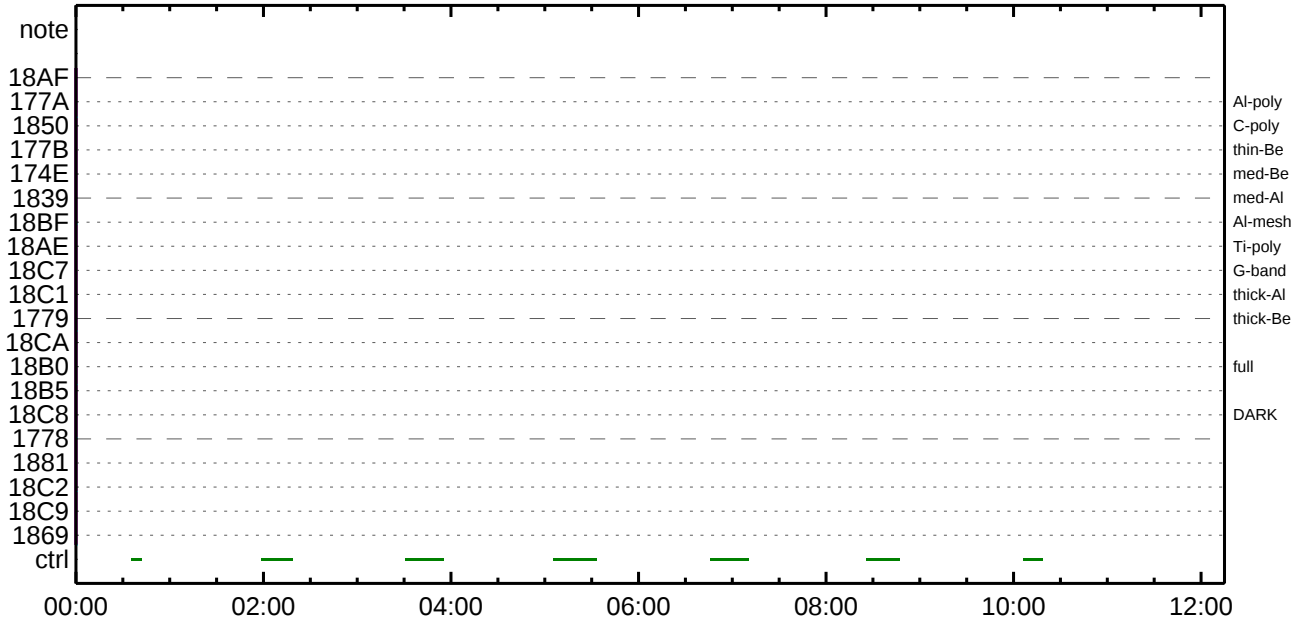
CMDI #0350 2012/01/02



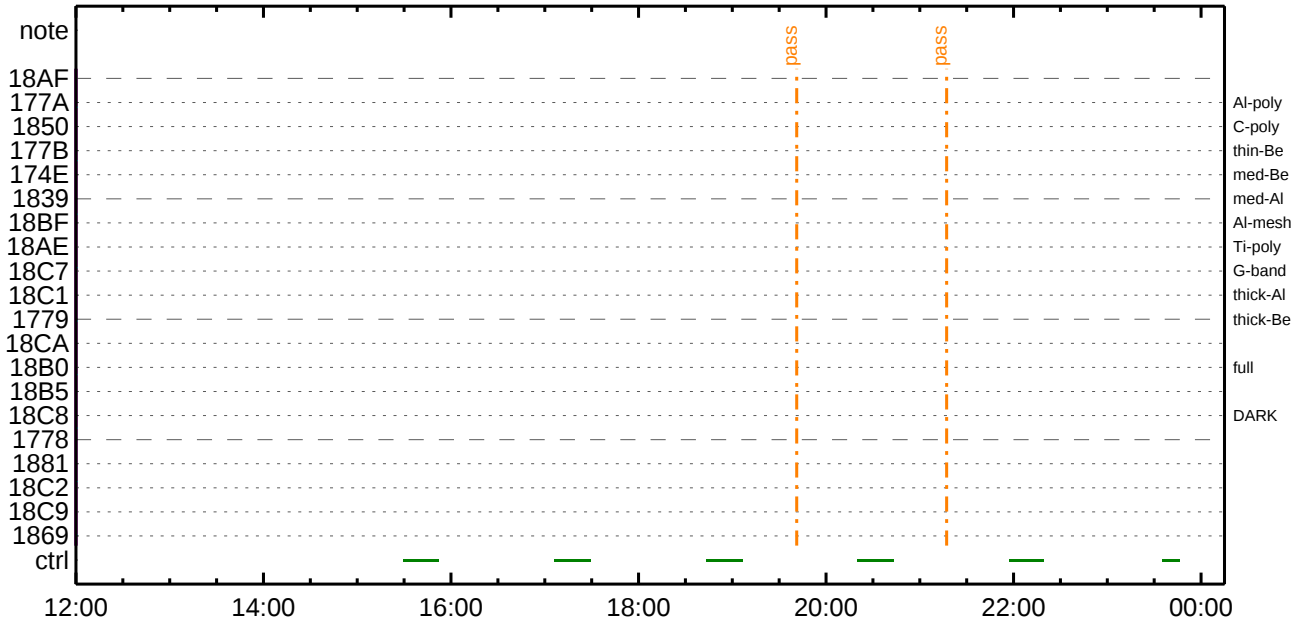
CMDI #0350 2012/01/02



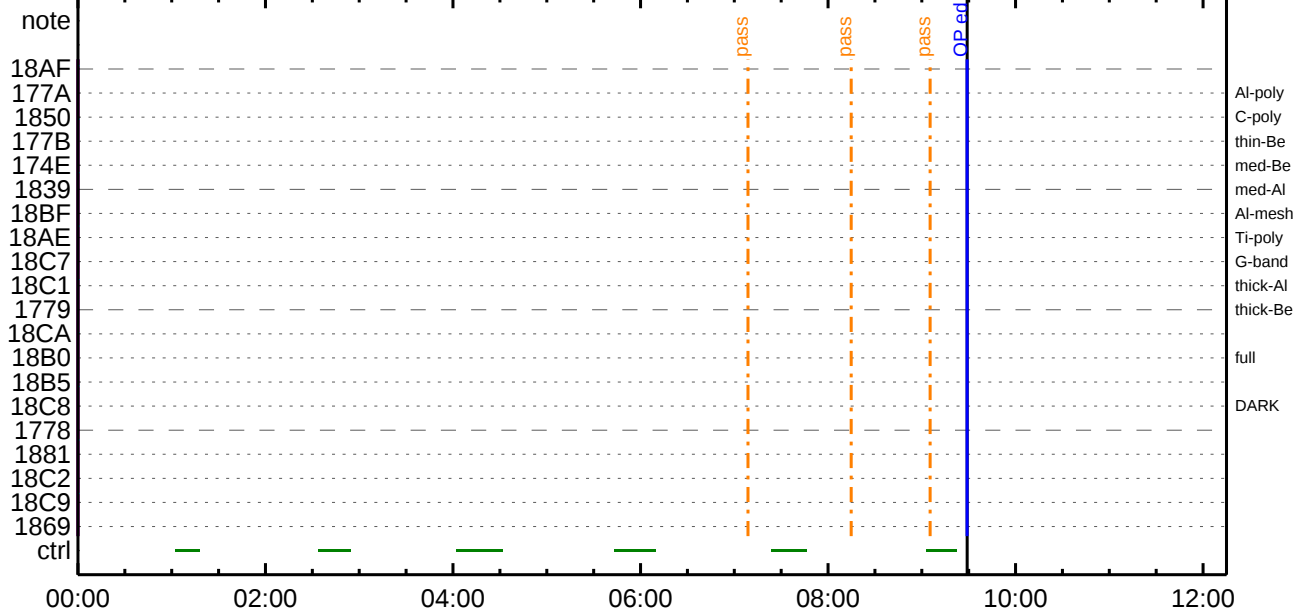
CMDI #0350 2012/01/03



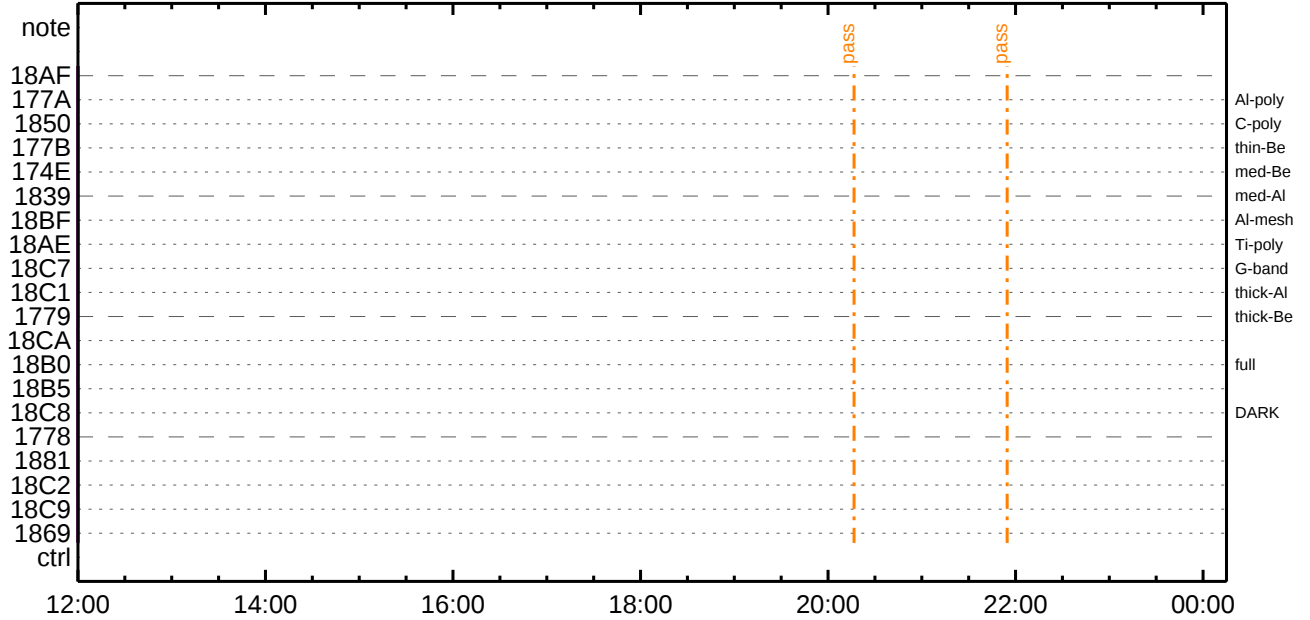
CMDI #0350 2012/01/03



CMDI #0350 2012/01/04



CMDI #0350 2012/01/04




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OG%1j¼¥Éj¼¥Á%0¥x
0100 C. *****
0101 C.
0102 C. jãOP/OG%1j¼¥Éjã
0103 S. OP op-561:OP
0104 ( )
0105 S. OG og-561:OG
0106 ( )
0107 C.
0108 C. jãNMOG&OPîî°è¥Á%0¥xjã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ¥Á%0¥x½ªî»ð³îÇ§
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG¤î%É¹Ç•è²îOK¤ð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ¥Á%0¥x½ªî»ð³îÇ§
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG¤î%É¹Ç•è²îOK¤ð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ¥Á%0¥x½ªî»ð³îÇ§
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP¤î%É¹Ç•è²îOK¤ð³îÇ§
0165 C.
0166 C. ***** °É²¼¤î¼Ã·¶Á°¤ÉÉ-¤°Á÷¿® (%ãµ-¥Á%0¥x½ªè½Ç¤ðÁÖ¤Ç½¤°·¤è¼î¹Ç¤Ç¤â) *****
0167 C. DHU¥ã¼¥É;É½¥½, ¥½¼¥É;É¤ðîã¤¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE j§ OPOG UPLOAD¤-Á÷¿®NG¤î¼¹Ç;¢°É²¼¤îîTI-CMDÁ÷¿®¤î¼¹Ô¤°¤É¤¤¤³¤Éj£
0180 C. ¢¤¤¿;¢SET¤ÉDUMP¤î¤±°î¥½¥½¤¹Ô¤°¤³¤Éj£
0181 C.
0182 C. TI¥³¥¤¥ó¥É¤ðÁÖî¿(UT)
0183 +. TI 2011-12-29 10:37:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-12-29 10:37:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-12-29 10:37:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```
0194 C.  
0195 +. TI 2011-12-29 10:41:59.5  
0196 DC 01-B2 DHU_OP_START EQ 1COUNTUP  
0197 C. C#[HK1_TI_CMD_NUM]  
0198 C.  
0199 C. °Ê²¼□İÄê%ıİÎñİ¥Á¥$¥Ã¥¹àİÜ  
0200 C. C#[HK1_TI_CMD_ENA/DIS] EQ ENA  
0201 C. C#[HK1_TI_CMD_NUM] EQ 4  
0202 C. C#[HK1_NEXT_EXEC_PIM] EQ DHU  
0203 C. C#[HK1_NEXT_EXEC_DC] EQ 0xB3  
0204 C.  
0205 C. *****  
0206 C. TIIî°è¥À¥Ó¥×  
0207 C. *****  
0208 C.  
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)  
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET  
0211 BC (03 ab 03 01 02)  
0212 C. C#[HK1_DMP_TOP_ADRS_1] EQ 07  
0213 C. C#[HK1_DMP_TOP_ADRS_0] EQ 2B  
0214 C. C#[HK1_DMP_BLOCK_NUM] EQ 3  
0215 C. C#[HK1_DMP_REPEAT_NUM] EQ 0  
0216 C. C#[HK1_DMA_DMP_PIM] EQ DHU  
0217 +. DC 01-22 DHU_MODE_CHNG  
0218 BC (07 0b f8)  
0219 C. C#[HK1_PKT_FORM_NO] EQ 7  
0220 C. C#[HK1_PKT_GEN_TIME] EQ 0.25 s  
0221 C. C#[HK1_S_TLM_BIT_RATE] EQ 32k  
0222 C. C#[HK1_X_TLM_BIT_RATE] EQ 4M  
0223 C. C#[HK1_DMP_CHK_FLG] EQ EXEC  
0224 C.  
0225 C. ¥À¥Ó¥×½ªİ»ð³İÇ$  
0226 C. C#[HK1_DMP_CHK_FLG] EQ NON  
0227 C.  
0228 C. RAM ID=TI_TBL□İ%È¹Ç•è²İOKð³İÇ$  
0229 C.  
0230 C. DHU¥âı¼¥É;Ê¼Ý½, ¥ìı¼¥Ë;Ëðòİä□¹  
0231 +. DC 01-22 DHU_MODE_CHNG  
0232 BC (02 0a f8)  
0233 C. C#[HK1_PKT_FORM_NO] EQ 2  
0234 C. C#[HK1_PKT_GEN_TIME] EQ 0.5S  
0235 C. C#[HK1_S_TLM_BIT_RATE] EQ 32K  
0236 C. C#[HK1_X_TLM_BIT_RATE] EQ 4M  
0237 C.  
0238 C. *****  
0239 C. SOT TI command set  
0240 C. *****  
0241 C. Execute, after the success of OP upload.  
0242 +. TI 2011-12-29 10:41:16.0  
0243 DC 07-F0 MDP_SOT_MODE_STBY  
0244 BC (41)  
0245 C. -----  
0246 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]  
0247 C. -----  
0248 C. ***** SOT END *****  
0249 C. Stop EIS observation and temporarily disable EIS mode changes  
0250 C.  
0251 C.  
0252 C. ***** Start EIS operation (TI set) *****  
0253 C. Execute, after the success of OP upload.  
0254 C. Set EIS TI-commands  
0255 +. TI 2011-12-29 10:41:30.0  
0256 DC 07-FC EIS_MODE_MANU  
0257 BC (21 02)  
0258 +. TI 2011-12-29 10:41:40.0  
0259 DC 07-FC EIS_MODE_CHG_DIS  
0260 BC (22)  
0261 C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP  
0262 C. ***** End EIS operation (TI set) *****  
0263 C.  
0264 C.  
0265 C.  
0266 C. ***** XRT START *****  
0267 C. Execute, after the success of OP upload.  
0268 +. TI 2011-12-29 10:41:00.0  
0269 DC 07-F0 MDP_XRT_MODE_STBY  
0270 BC (c3)  
0271 C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP  
0272 C.  
0273 C. ***** XRT END *****  
0274 C.  
0275 C. ***** MDP `ŮĀİ□İ»ō¾Ý□ĒĀĐ□¹ēDCBC•×²è *****  
0276 C. (%ā°İ¥Ō¥Ā¥Ĕ¥Đ¥Ė¥ā¥Ç¥ē□Ē¾¼□¼A»Ŭ□¹ē)  
0277 S. DC-BC dcbc-402:DCBC  
0278 (MDP_known_event)  
0279 C.  
0280 C.  
0281 C. ***** ¥Đ¥¹•İ Daily±çİÎñ□Ē`Ø□¹ēDCBC•×²è *****  
0282 S. DC-BC dcbc-153:DCBC  
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0284 C.  
0285 C.  
0286 C. įāLOS¥Ā¥$¥Ā¥¼Å»Ŭįä  
0287 C.  
0288 C. ***** LOS *****  
0289 C.
```

```

main-562 2011-12-29 13:02:13 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥$¥Ã¥¬¼Á»Üjã
0005 C.
0006 C. ¥Ã¥ßj¼¥³¥p¥ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ĀījĒ□¿Á□•μ°Æ»İ×Á□İ¥¢¥Á¥×¥īj¼¥ĒjĒĒ½μ•īĒjĒ□Ē¼°Ç□□•□¿¼ī¹Ç□īj¢À®, ù□¹□Ē□□□ÇÁ÷¿®□•□Ē□□□³□Ēj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 C.
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 C.
0045 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 C. Verify EIS in MANUAL mode
0049 C. Estimated OBSTBL upload time is 37s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 C.
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2011-12-29 10:41:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ŪĀī□ī»Ō¼Ÿ□ĒĀ□¹□ĒDCBC•×²Ē *****
0069 C. (¼Ā°īŸŌ¼Ÿ¥Ē¥p¥Ē¥Ē¥Ē¥Ē¥Ē½¼□□¼Ā»Ū□¹□Ē)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** ¥Đ¥¹•ī Daily±¿īŃ□Ē´Ō□¹□ĒDCBC•×²Ē *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. iãLOS¥Á¥$¥Ã¥¬¼Á»Üjã
0080 C.
0081 . C. ***** LOS *****
0082 C.

```

```
main-563      2011-12-29   13:03:13    214     33 SOLAR-B MAIN //
0001          C.
0002          . C. ***** AOS *****
0003          C.
0004          . C. jãAOS¥Á¥$¥Ã¥¬¼Å»Üjã
0005          C.
0006          C. ¥Ä¥ßj¼¥³¥Ð¥Ó¥ÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008          C.
0009          . C. ***** AOCS : Reload orbital element (send every contact) *****
0010          C. ÄijÊ¤¿¤À¤•µ°Æ»İ×AC¤I¥¢¥Ä¥×¥ıj¼¥ÉE;ĒĒē½µ•īİĒ;ĒōĒ¼°ÇŎ¤•¤¿%î¹ç¤İjÇÄ®, ù¤¹¤ē¤¤¤ÇÄ÷¿®¤•¤Ē¤¤¤³¤Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012          C.
0013          C.
0014          . C. *****
0015          C. XÄ÷¿@µjON
0016          C. *****
0017          C. ¨ °ÆÄ, İxËÝ¤äLOS¤¤¤Ç¤İ»þ´Ö¤ò¹İİ, ¤•jÇĖÔİ×¤EXÄŌŎ¤İ¹Ō¤Ē¤ı¤Ē¤¤¤³¤Èj£
0018          C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020          M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022          M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024          C.               ÇÇ[HK1_XPA_ON/OFF]                     EQ       ON
0025          C.               ÇÇ[HK1_XPA_PWR_HI/LŐ]                EQ       HI
0026          C.               ÇÇ[HK1_XMOD_ON/OFF]                   EQ       ON
0027          C.               ÇÇ[HK1_XMOD_QPSK/PM]                  EQ       QPSK
0028          C.
0029          . C. X¥Đ¥Ŏ¥É¥İ¥Ä¥¬¾ŏÄŐ¤¬°ÄÄê¤•¤¿¤¤èjÇ°Ĕ²¼¤İ°ÆÄ, ¼ê½Ç¤ð¼Ä¹Ō¤¹¤ēj£
0030          C.
0031          . C. *****
0032          C. DR PT1 Äİ¼ı°ÆÄ
0033          C. *****
0034          C. ¨ RESTARTjĚPT1jĒ¤•¤¿¤¤%î¹ç¤İjÇ°Ĕ²¼¤İ¼Ä¹Ō¤»¤ºjÇDCBC-150¤Ø¿Ē¤àj£
0035          C.
0036          . C. jãPT1°ÆÄ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039          BC             (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042          C.               ÇÇ[HK1_REP_PT_1/2]                    EQ       PT1        (¼Ä¹Ō, j¼Ú)
0043          C.               ÇÇ[HK1_REP_STA/STP]                  EQ       START    (¼Ä¹Ō, j¼Ú)
0044          C.               ÇÇ[HK1_X_VC4_ON/OFF]                 EQ       ON         (¼Ä¹Ō, j¼Ú)
0045          C.
0046          . C. jã¥¢¥ó¥Æ¥ĒÄÙÄŦjĒÄ•Ä°²ôĒēđjĒ, ä¤İ°ÆÄ, °Æ³«ıã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049          C.               ÇÇ[HK1_REP_PT_1/2]                    EQ       PT1        (¼Ä¹Ō, j¼Ú)
0050          C.               ÇÇ[HK1_REP_STA/STP]                  EQ       START    (¼Ä¹Ō, j¼Ú)
0051          C.               ÇÇ[HK1_X_VC4_ON/OFF]                 EQ       ON         (¼Ä¹Ō, j¼Ú)
0052          C.
0053          C.
0054          . C. PT1°ÆÄ, ¤¬¼«Æ°Ää»ß¤•¤¿, äjÇ°Ĕ²¼¤ð¼Ä¹Ō¤¹¤ēj£
0055          C. ¥¢¥ō¥Æ¥ĒÄÙÄŦ¤ÄÄ•Ä°²ôĒēđ¤¬¶á¤¤%î¹ç¤İ'İ»¤¹¤ē¤¤¤ÇÄŐ¤Äj£
0056          C.
0057          . C. *****
0058          C. DR PT2 Äİ¼ı°ÆÄ
0059          C. *****
0060          C. ¨ RESTARTjĚPT2jĒ¤•¤¿¤¤%î¹ç¤İjÇ°Ĕ²¼¤İ¼Ä¹Ō¤»¤ºjÇDCBC-151¤Ø¿Ē¤àj£
0061          C.
0062          . C. jãPT2°ÆÄ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065          BC             (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068          C.               ÇÇ[HK1_REP_PT_1/2]                    EQ       PT2        (¼Ä¹Ō, j¼Ú)
0069          C.               ÇÇ[HK1_REP_STA/STP]                  EQ       START    (¼Ä¹Ō, j¼Ú)
0070          C.               ÇÇ[HK1_X_VC4_ON/OFF]                 EQ       ON         (¼Ä¹Ō, j¼Ú)
0071          C.
0072          . C. jã¥¢¥ó¥Æ¥ĒÄÙÄŦjĒÄ•Ä°²ôĒēđjĒ, ä¤İ°ÆÄ, °Æ³«ıã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075          C.               ÇÇ[HK1_REP_PT_1/2]                    EQ       PT2        (¼Ä¹Ō, j¼Ú)
0076          C.               ÇÇ[HK1_REP_STA/STP]                  EQ       START    (¼Ä¹Ō, j¼Ú)
0077          C.               ÇÇ[HK1_X_VC4_ON/OFF]                 EQ       ON         (¼Ä¹Ō, j¼Ú)
0078          C.
0079          . C. *****
0080          C. DR°ÆÄ, Ää»ßjÇXÄ÷¿@µjOFF
0081          C. *****
0082          C.
0083          . C. jãDR°ÆÄ, Ää»ßjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086          C.               ÇÇ[HK1_REP_STA/STP]                    EQ       STOP
0087          C.               ÇÇ[HK1_S_VC4_ON/OFF]                  EQ       OFF
0088          C.               ÇÇ[HK1_X_VC4_ON/OFF]                  EQ       OFF
0089          C.
0090          . C. jãXÄ÷¿@µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092          M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094          C.               ÇÇ[HK1_XMOD_ON/OFF]                      EQ       OFF
0095          C.               ÇÇ[HK1_XPA_ON/OFF]                       EŲ       OFF
```

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 +. DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 +. DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 +. DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 +. DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 +. DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 +. DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 +. DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 +. DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 +. DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 +. DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 +. DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 +. DC 07-F0 MDP_XRT_AEC_RESET
0145 BC (d0)
0146 +. DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 +. DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 0d)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 03)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2011-12-29 10:41:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 . C. *****
0168 C. SOT table upload
0169 C. *****
0170 . C. < Stop FG table >
0171 +. DC 07-F0 MDP_FG_CTRL_MANU
0172 BC (51)
0173 . C. -----
0174 C. MDP_FG_CTRL_MODE = MANU [ ]
0175 C. -----
0176 C.
0177 . C. <Upload FG Observation Table>
0178 . S. RAM ram-263:MDP_OBS_F
0179 ( )
0180 C.
0181 . C. < Dump RAMID=MDP_OBS_F >
0182 +. DC 07-F0 MDP_DUMP_FGTBL
0183 BC (82 07 00 00 00 38 b8)
0184 C. -----
0185 C. MDP_OBS_F verify = OK/NG [ ]
0186 C. -----
0187 C.
0188 C. *****
0189 C. SOT TI command set
0190 C. *****
0191 C. Execute, after the success of TBL upload.
0192 +. TI 2011-12-29 10:41:18.0
0193 DC 07-F0 MDP_SOT_MODE_OBSV

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0194 BC (40)
0195 . C. -----
0196 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0197 C. -----
0198 C.
0199 C.
0200 . C. ***** MDP ´ûÃîï»ö%ýðëâð¹ðëDCBC•x²è *****
0201 C. (¾ã°î¥0¥Ã¥Ê¥ð¥Ê¥ã¥¢¥èð½¼ð¼Ã»Û¹ðè)
0202 . S. DC-BC dcbc-402:DCBC
0203 (MDP_known_event)
0204 C.
0205 C.
0206 . C. ***** ¥ð¥¹•î Daily±¿îñðë´ð¹ðëDCBC•x²è *****
0207 . S. DC-BC dcbc-153:DCBC
0208 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0209 C.
0210 C.
0211 . C. ¡ãLOS¥Á¥S¥Ã¥¬¼Ã»Û¡ä
0212 C.
0213 . C. ***** LOS *****
0214 C.
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Dec 29, 11 13:03

XRT_OGLIST_0350.chk

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*** OP Sequence for XRT ***

2011/12/29	10:51:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/29	10:52:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2011/12/29	10:54:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/29	10:54:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/29	10:54:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/29	10:54:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/29	10:54:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/29	10:54:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/29	10:54:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2011/12/29	10:54:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/29	10:55:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/29	14:09:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/29	14:09:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/29	14:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/29	14:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/29	14:14:30.0	XRT_Custom_418_OG [0x1a2]			
2011/12/29	14:15:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/29	15:29:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/29	15:29:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/29	15:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/12/29	15:30:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/29	15:30:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/12/29	15:30:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/29	15:32:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/12/29	15:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/29	15:41:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/29	15:41:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/29	15:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/29	15:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/29	16:05:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/29	16:06:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 01 58 51 a9
2011/12/29	16:08:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/29	16:08:46.0	XRT_FLD_ENA_435_OG [0x1b3]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/29	16:08:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/29	16:08:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/29	16:08:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/29	16:08:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/29	16:08:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2011/12/29	16:08:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/29	16:09:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/29	16:15:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/29	16:16:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 01 58 3f f7
2011/12/29	16:18:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/29	16:18:46.0	XRT_FLD_ENA_435_OG [0x1b3]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/29	16:18:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/29	16:18:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0

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2011/12/29	16:18:52.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/29	16:18:54.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/29	16:18:56.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2011/12/29	16:18:58.0	XRT_FL_PROG_SET_414_0G [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/29	16:19:00.0	XRT_CTRL_AUTO_406_0G [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/29	16:25:54.0	XRT_CTRL_MANU_439_0G [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/29	16:26:00.0	AOCS_ORe-point_Start_5_0G [0x09b]	AOCU_NM	5	02-76	00 01 58 2e 45
2011/12/29	16:28:26.0	XRT_FOCUS_POSITION_401_0G [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/29	16:28:46.0	XRT_FLD_ENA_435_0G [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/29	16:28:48.0	XRT_FLRCTRL_ENA_403_0G [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/29	16:28:50.0	XRT_AEC_RESET_443_0G [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/29	16:28:52.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/29	16:28:54.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/29	16:28:56.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2011/12/29	16:28:58.0	XRT_FL_PROG_SET_414_0G [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/29	16:29:00.0	XRT_CTRL_AUTO_406_0G [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/29	16:35:54.0	XRT_CTRL_MANU_439_0G [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/29	16:36:00.0	AOCS_ORe-point_Start_6_0G [0x09c]	AOCU_NM	5	02-76	00 01 58 1c 93
2011/12/29	16:38:26.0	XRT_FOCUS_POSITION_401_0G [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/29	16:38:46.0	XRT_FLD_ENA_435_0G [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/29	16:38:48.0	XRT_FLRCTRL_ENA_403_0G [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/29	16:38:50.0	XRT_AEC_RESET_443_0G [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/29	16:38:52.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/29	16:38:54.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/29	16:38:56.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2011/12/29	16:38:58.0	XRT_FL_PROG_SET_414_0G [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/29	16:39:00.0	XRT_CTRL_AUTO_406_0G [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/29	16:45:54.0	XRT_CTRL_MANU_439_0G [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/29	16:46:00.0	AOCS_ORe-point_Start_7_0G [0x09d]	AOCU_NM	5	02-76	00 01 58 0a e1
2011/12/29	16:48:26.0	XRT_FOCUS_POSITION_401_0G [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/29	16:48:46.0	XRT_FLD_ENA_435_0G [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/29	16:48:48.0	XRT_FLRCTRL_ENA_403_0G [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/29	16:48:50.0	XRT_AEC_RESET_443_0G [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/29	16:48:52.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/29	16:48:54.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/29	16:48:56.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2011/12/29	16:48:58.0	XRT_FL_PROG_SET_414_0G [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/29	16:49:00.0	XRT_CTRL_AUTO_406_0G [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/29	16:55:54.0	XRT_CTRL_MANU_439_0G [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/29	16:56:00.0	AOCS_ORe-point_Start_8_0G [0x09e]	AOCU_NM	5	02-76	00 01 58 f9 30
2011/12/29	16:58:26.0	XRT_FOCUS_POSITION_401_0G [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/29	16:58:46.0	XRT_FLD_ENA_435_0G [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/29	16:58:48.0	XRT_FLRCTRL_ENA_403_0G [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/29	16:58:50.0	XRT_AEC_RESET_443_0G [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/29	16:58:52.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/29	16:58:54.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/29	16:58:56.0	XRT_QT_PROG_SET_416_0G [0x1a0]				

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2011/12/29	16:58:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2011/12/29	16:59:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/29	17:05:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/29	17:06:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	17:08:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00	01 58 e7 7d
2011/12/29	17:08:46.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2011/12/29	17:08:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/29	17:08:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/29	17:08:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/29	17:08:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/29	17:08:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/29	17:08:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2011/12/29	17:09:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/29	17:18:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/29	17:18:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	17:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/29	17:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/29	17:42:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/29	17:42:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	17:44:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00	01 58 d5 cb
2011/12/29	17:45:16.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2011/12/29	17:45:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/29	17:45:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/29	17:45:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/29	17:45:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/29	17:45:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/29	17:45:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2011/12/29	17:45:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/29	17:52:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/29	17:52:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	17:54:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00	01 58 c4 19
2011/12/29	17:55:16.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2011/12/29	17:55:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/29	17:55:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/29	17:55:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/29	17:55:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/29	17:55:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/29	17:55:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2011/12/29	17:55:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/29	18:02:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/29	18:02:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	18:04:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00	01 58 b2 6f
2011/12/29	18:05:16.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2011/12/29	18:05:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/29	18:05:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/29	18:05:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
			MDP_XRT_ARS_DIS	1	07-F0	d5	

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2011/12/29	18:05:24.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/29	18:05:26.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2011/12/29	18:05:28.0	XRT_FL_PROG_SET_414_0G [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2011/12/29	18:05:30.0	XRT_CTRL_AUTO_406_0G [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/29	18:12:24.0	XRT_CTRL_MANU_439_0G [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	18:12:30.0	AOCS_ORe-point_Start_13_0G [0x0a3]	AOCU_NM	5	02-76	00 14 31 51 81	
2011/12/29	18:14:56.0	XRT_FOCUS_POSITION_401_0G [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/12/29	18:15:16.0	XRT_FLD_ENA_435_0G [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/29	18:15:18.0	XRT_FLRCTRL_ENA_403_0G [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/29	18:15:20.0	XRT_AEC_RESET_443_0G [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/29	18:15:22.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/29	18:15:24.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/29	18:15:26.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2011/12/29	18:15:28.0	XRT_FL_PROG_SET_414_0G [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2011/12/29	18:15:30.0	XRT_CTRL_AUTO_406_0G [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/29	18:22:24.0	XRT_CTRL_MANU_439_0G [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	18:22:30.0	AOCS_ORe-point_Start_14_0G [0x0a4]	AOCU_NM	5	02-76	00 15 70 42 9f	
2011/12/29	18:24:56.0	XRT_FOCUS_POSITION_401_0G [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/12/29	18:25:16.0	XRT_FLD_ENA_435_0G [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/29	18:25:18.0	XRT_FLRCTRL_ENA_403_0G [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/29	18:25:20.0	XRT_AEC_RESET_443_0G [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/29	18:25:22.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/29	18:25:24.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/29	18:25:26.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2011/12/29	18:25:28.0	XRT_FL_PROG_SET_414_0G [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2011/12/29	18:25:30.0	XRT_CTRL_AUTO_406_0G [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/29	18:32:24.0	XRT_CTRL_MANU_439_0G [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	18:32:30.0	AOCS_ORe-point_Start_15_0G [0x0a5]	AOCU_NM	5	02-76	00 14 c4 32 8f	
2011/12/29	18:34:56.0	XRT_FOCUS_POSITION_401_0G [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/12/29	18:35:16.0	XRT_FLD_ENA_435_0G [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/29	18:35:18.0	XRT_FLRCTRL_ENA_403_0G [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/29	18:35:20.0	XRT_AEC_RESET_443_0G [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/29	18:35:22.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/29	18:35:24.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/29	18:35:26.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2011/12/29	18:35:28.0	XRT_FL_PROG_SET_414_0G [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2011/12/29	18:35:30.0	XRT_CTRL_AUTO_406_0G [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/29	18:42:24.0	XRT_CTRL_MANU_439_0G [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	18:42:30.0	AOCS_ORe-point_Start_16_0G [0x0a6]	AOCU_NM	5	02-76	00 14 5a 22 0c	
2011/12/29	18:44:56.0	XRT_FOCUS_POSITION_401_0G [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2011/12/29	18:45:16.0	XRT_FLD_ENA_435_0G [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/29	18:45:18.0	XRT_FLRCTRL_ENA_403_0G [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/29	18:45:20.0	XRT_AEC_RESET_443_0G [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/29	18:45:22.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/29	18:45:24.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/29	18:45:26.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2011/12/29	18:45:28.0	XRT_FL_PROG_SET_414_0G [0x19e]					

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2011/12/29	18:45:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/29	18:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/29	18:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	18:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/29	18:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/29	19:19:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/29	19:19:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	19:21:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00	14 20 11 1e
2011/12/29	19:22:16.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2011/12/29	19:22:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/29	19:22:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/29	19:22:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/29	19:22:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/29	19:22:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/29	19:22:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2011/12/29	19:22:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/29	19:29:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/29	19:29:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	19:31:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00	14 10 ff cf
2011/12/29	19:32:16.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2011/12/29	19:32:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/29	19:32:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/29	19:32:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/29	19:32:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/29	19:32:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/29	19:32:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2011/12/29	19:32:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/29	19:39:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/29	19:39:30.0	AOCS_ORe-point_Start_19_OG [0x0a9]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	19:41:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00	14 20 f0 ad
2011/12/29	19:42:16.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2011/12/29	19:42:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/29	19:42:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/29	19:42:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/29	19:42:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/29	19:42:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/29	19:42:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2011/12/29	19:42:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/29	19:49:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/29	19:49:30.0	AOCS_ORe-point_Start_20_OG [0x0aa]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/29	19:51:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00	14 5a df bf
2011/12/29	19:52:16.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2011/12/29	19:52:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/29	19:52:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/29	19:52:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/29	19:52:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
			MDP_XRT_FLD_RESET	1	07-F0	da	

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2011/12/29	19:52:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 04	
2011/12/29	19:52:28.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 03	
2011/12/29	19:52:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2011/12/29	19:59:24.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2011/12/29	19:59:30.0	AOCS_ORe-point_Start_21_OG [0x0ab]			
		AOCU_NM	5	02-76 00 14 c4 cf 3c	
2011/12/29	20:01:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2011/12/29	20:02:16.0	XRT_FLD_ENA_435_OG [0x1b3]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2011/12/29	20:02:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2011/12/29	20:02:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2011/12/29	20:02:22.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2011/12/29	20:02:24.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2011/12/29	20:02:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 04	
2011/12/29	20:02:28.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 03	
2011/12/29	20:02:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2011/12/29	20:09:24.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2011/12/29	20:09:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]			
		AOCU_NM	5	02-76 00 15 70 bf 23	
2011/12/29	20:11:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2011/12/29	20:12:16.0	XRT_FLD_ENA_435_OG [0x1b3]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2011/12/29	20:12:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2011/12/29	20:12:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2011/12/29	20:12:22.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2011/12/29	20:12:24.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2011/12/29	20:12:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 04	
2011/12/29	20:12:28.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 03	
2011/12/29	20:12:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2011/12/29	20:19:24.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2011/12/29	20:19:30.0	AOCS_ORe-point_Start_23_OG [0x0ad]			
		AOCU_NM	5	02-76 00 15 89 af 65	
2011/12/29	20:21:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2011/12/29	20:22:16.0	XRT_FLD_ENA_435_OG [0x1b3]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2011/12/29	20:22:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2011/12/29	20:22:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2011/12/29	20:22:22.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2011/12/29	20:22:24.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2011/12/29	20:22:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 04	
2011/12/29	20:22:28.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 03	
2011/12/29	20:22:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2011/12/29	20:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2011/12/29	20:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76 02 00 00 00 00	
2011/12/29	20:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00	
2011/12/29	20:32:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2011/12/29	20:32:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2011/12/29	20:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2011/12/29	20:32:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2011/12/29	20:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2011/12/29	20:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2011/12/29	20:32:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2011/12/29	20:32:54.0	XRT_FLD_RESET_412_OG [0x19c]			

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2011/12/29	20:32:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/29	20:32:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2011/12/29	20:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/29	20:56:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/29	20:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/29	22:10:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/29	22:10:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/29	22:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/29	22:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/29	22:31:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/29	22:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/29	23:47:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/29	23:47:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/29	23:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/29	23:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/29	23:57:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/29	23:58:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	01:13:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/30	01:13:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	01:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/30	01:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/30	01:30:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	01:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	02:46:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/30	02:46:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	02:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/30	02:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/30	03:07:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	03:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	04:15:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/30	04:15:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	04:15:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/30	04:18:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/30	04:44:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	04:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	05:55:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/30	05:55:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	05:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/30	05:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/30	06:21:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	06:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	06:31:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/30	06:31:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	06:31:30.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/30	06:31:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00
2011/12/30	06:31:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/30	06:31:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/12/30	06:34:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/30	06:34:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2011/12/30	06:41:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2011/12/30	06:41:30.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	02 00 00 00 00
2011/12/30	06:43:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/30	06:44:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/30	06:44:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/30	06:44:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/30	06:44:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/30	06:44:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/30	06:44:26.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2011/12/30	06:44:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/30	06:44:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/30	07:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	07:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/30	07:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/30	07:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	07:59:00.0	XRT_Custom_418_OG [0x1a2]				
2011/12/30	08:00:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/30	09:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	09:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/30	09:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/30	09:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	09:34:30.0	XRT_Custom_418_OG [0x1a2]				
2011/12/30	09:35:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/30	09:50:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	09:51:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00