

XRT Timeline to be uploaded on 2011/12/30

Period: 2011/12/30 09:41:00 - 2012/01/05 09:32:00

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Normal mode

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XOB #183E: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad													
Term	Pointing (x, y)		Comment										
12/30 09:54:00 - 12/30 17:54:30	Track (-780.2, -398.5) @ 12/30 09:51:00		# OP start + 10min/ track AR11389										
12/30 18:31:00 - 12/31 06:03:24	Track (-744.6, -395.3) @ 12/30 18:28:00		#track AR11389										
12/31 06:16:30 - 12/31 17:59:54	Track (-688.1, -391.0) @ 12/31 06:13:30		#cont.										
12/31 18:13:00 - 01/01 06:06:54	Track (-622.1, -386.8) @ 12/31 18:10:00		#cont.										
01/01 06:20:00 - 01/01 17:30:00	Track (-547.1, -382.8) @ 01/01 06:17:00		#cont.										
01/01 18:08:30 - 01/02 05:40:24	Track (-467.1, -379.2) @ 01/01 18:05:30		#cont.										
01/02 05:53:30 - 01/02 17:42:54	Track (-381.8, -376.0) @ 01/02 05:50:30		#cont.										
01/02 17:56:00 - 01/03 05:59:54	Track (-289.6, -373.2) @ 01/02 17:53:00		#cont.										
01/03 06:13:00 - 01/03 18:00:54	Track (-191.6, -370.8) @ 01/03 06:10:00		#cont.										
01/04 06:32:30 - 01/04 09:03:00	Track (8.6, -367.8) @ 01/04 06:29:30		#cont.										
PROG= 15 Inf.-time(s)													
└ Subr= 2 1-time(s) 2.0sec													
└ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└ Seqn= 41 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└ Subr= 1 1-time(s) 2.0sec													
└ Seqn= 91 30-time(s) 80.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	20.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	20.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	20.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18AD: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be(12)													
Term	Pointing (x, y)		Comment										
12/30 18:21:00 - 12/30 18:27:54	Fixed (0.0, 0.0)		synoptic, shifted 18.0 min										
12/31 06:06:30 - 12/31 06:13:24	Fixed (0.0, 0.0)		synoptic, shifted 3.5 min										
12/31 18:03:00 - 12/31 18:09:54	Fixed (0.0, 0.0)		synoptic										
01/01 06:10:00 - 01/01 06:16:54	Fixed (0.0, 0.0)		synoptic, shifted 7.0 min										
01/01 17:58:30 - 01/01 18:05:24	Fixed (0.0, 0.0)		synoptic, shifted -4.5 min										
01/02 05:43:30 - 01/02 05:50:24	Fixed (0.0, 0.0)		synoptic, shifted -19.5 min										
01/02 17:46:00 - 01/02 17:52:54	Fixed (0.0, 0.0)		synoptic, shifted -17.0 min										
01/03 06:03:00 - 01/03 06:09:54	Fixed (0.0, 0.0)		synoptic										
01/03 18:04:00 - 01/03 18:10:54	Fixed (0.0, 0.0)		synoptic, shifted 1.0 min										
01/04 06:22:30 - 01/04 06:29:24	Fixed (0.0, 0.0)		synoptic, shifted 19.5 min										
PROG= 18 1-time(s)													
└ Subr= 1 1-time(s) 12.0sec													
└ Seqn= 7 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└ Seqn= 8 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 75 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 4 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18BB: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 512x512 at 1064 1048 (all), 60s cad													
Term	Pointing (x, y)		Comment										
01/03 18:14:00 - 01/04 06:19:24	Track (-93.3, -369.0) @ 01/03 18:11:00		#cont.										
PROG= 01 Inf.-time(s)													
└ Subr= 1 1-time(s) 2.0sec													
└ Seqn= 17 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└ Seqn= 53 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 2	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec	
	1-time(s)		2.0sec											
	Seqn= 81	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	15.0sec
		thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
		thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec	
thin-Be/Open		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec	
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval		

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Flare mode

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XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops

Term	Pointing (x, y)	Comment
12/30 09:54:00 - 12/30 17:54:30	Track (-780.2, -398.5) @ 12/30 09:51:00	# OP start + 10min/ track AR11389
12/30 18:31:00 - 12/31 06:03:24	Track (-744.6, -395.3) @ 12/30 18:28:00	#track AR11389
12/31 06:16:30 - 12/31 17:59:54	Track (-688.1, -391.0) @ 12/31 06:13:30	#cont.
12/31 18:13:00 - 01/01 06:06:54	Track (-622.1, -386.8) @ 12/31 18:10:00	#cont.
01/01 06:20:00 - 01/01 17:30:00	Track (-547.1, -382.8) @ 01/01 06:17:00	#cont.
01/01 18:08:30 - 01/02 05:40:24	Track (-467.1, -379.2) @ 01/01 18:05:30	#cont.
01/02 05:53:30 - 01/02 17:42:54	Track (-381.8, -376.0) @ 01/02 05:50:30	#cont.
01/02 17:56:00 - 01/03 05:59:54	Track (-289.6, -373.2) @ 01/02 17:53:00	#cont.
01/03 06:13:00 - 01/03 18:00:54	Track (-191.6, -370.8) @ 01/03 06:10:00	#cont.
01/03 18:14:00 - 01/04 06:19:24	Track (-93.3, -369.0) @ 01/03 18:11:00	#cont.
01/04 06:32:30 - 01/04 09:03:00	Track (8.6, -367.8) @ 01/04 06:29:30	#cont.

PROG= 03 12-time(s)

Subr= 1	45-time(s)		10.0sec												
	Seqn= 20	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
		Seqn= 63	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
			Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 77	1-time(s)		2.0sec											
		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
	Subr= 2	1-time(s)		10.0sec											
		Seqn= 90	1-time(s)		2.0sec										
			Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al			Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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Active Region Search

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NOT USED

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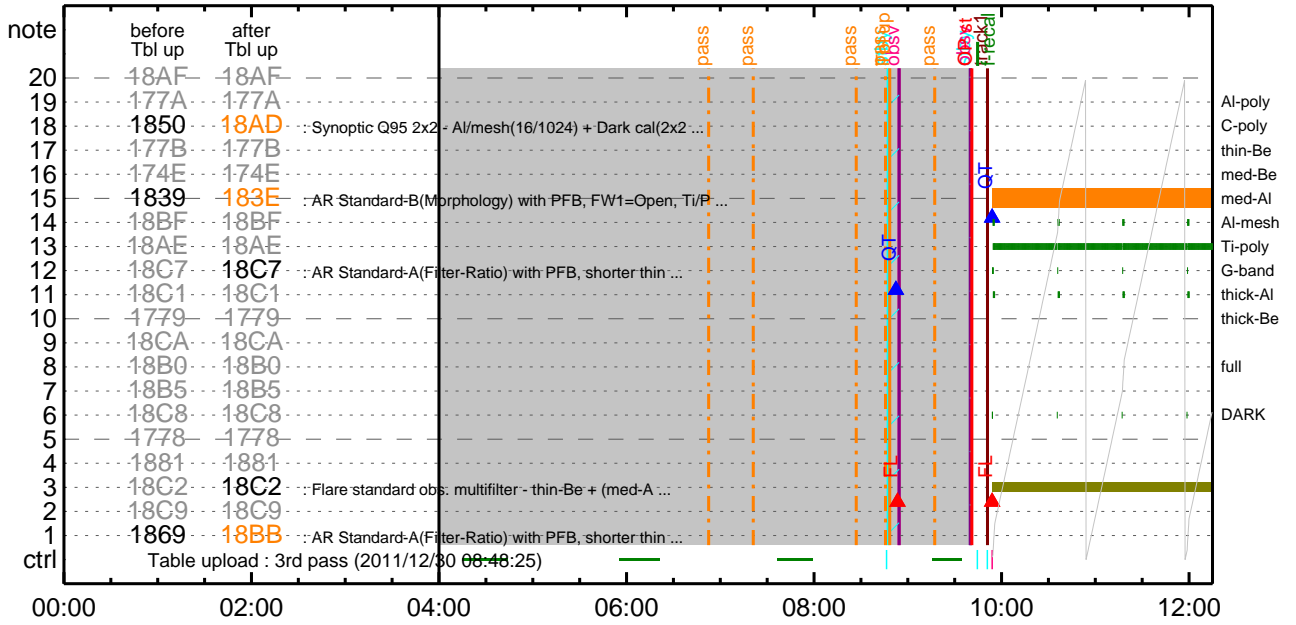
Flare Detection

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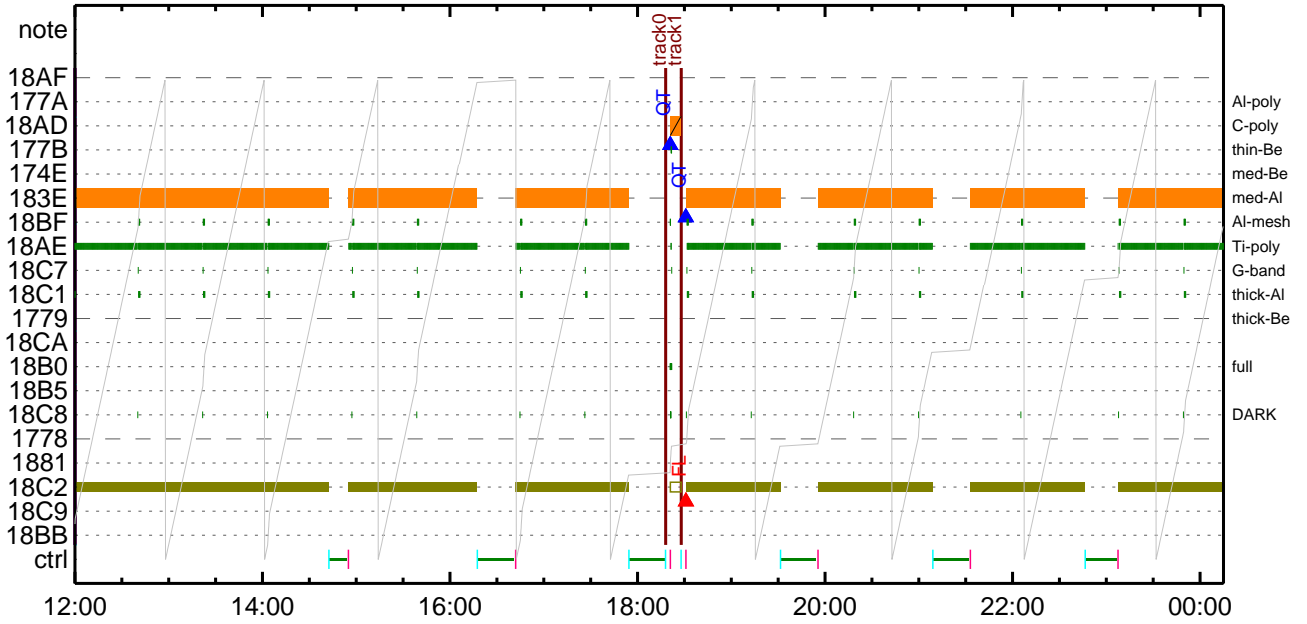
FLD Patrol

Term	Pointing (x, y)	Comment										
12/30 18:30:46 - 12/31 06:03:46	Track (-744.6, -395.3) @ 12/30 18:28:00	#track AR11389										
12/31 06:16:16 - 12/31 18:00:16	Track (-688.1, -391.0) @ 12/31 06:13:30	#cont.										
12/31 18:12:46 - 01/01 06:07:16	Track (-622.1, -386.8) @ 12/31 18:10:00	#cont.										
01/01 06:19:46 - 01/01 17:55:46	Track (-547.1, -382.8) @ 01/01 06:17:00	#cont.										
01/01 18:08:16 - 01/02 05:40:46	Track (-467.1, -379.2) @ 01/01 18:05:30	#cont.										
01/02 05:53:16 - 01/02 17:43:16	Track (-381.8, -376.0) @ 01/02 05:50:30	#cont.										
01/02 17:55:46 - 01/03 06:00:16	Track (-289.6, -373.2) @ 01/02 17:53:00	#cont.										
01/03 06:12:46 - 01/03 18:01:16	Track (-191.6, -370.8) @ 01/03 06:10:00	#cont.										
01/03 18:13:46 - 01/04 06:19:46	Track (-93.3, -369.0) @ 01/03 18:11:00	#cont.										
01/04 06:32:16 - 01/05 09:32:00	Track (8.6, -367.8) @ 01/04 06:29:30	#cont.										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

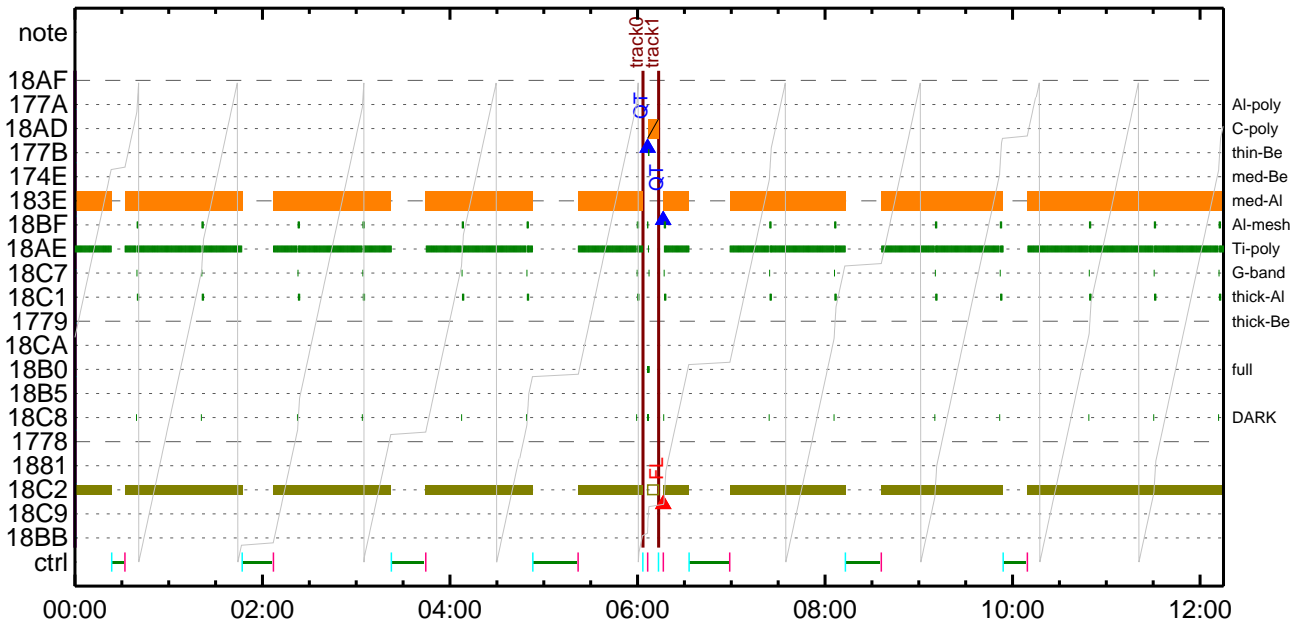
CMDI #0353 2011/12/30



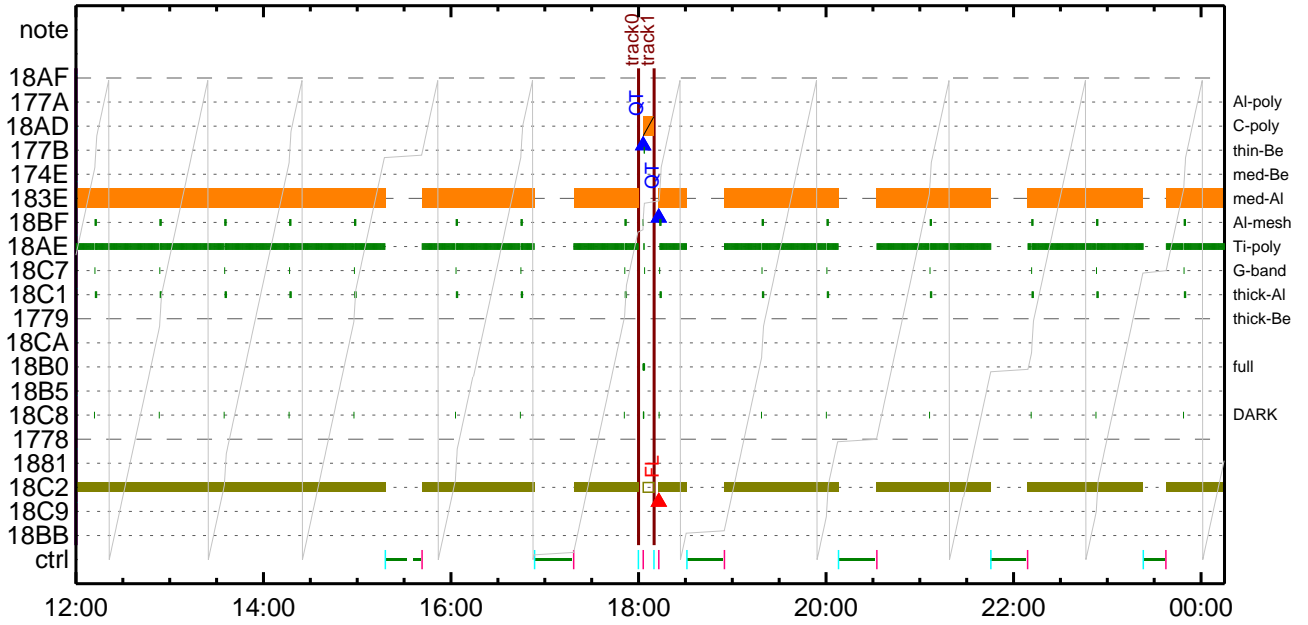
CMDI #0353 2011/12/30



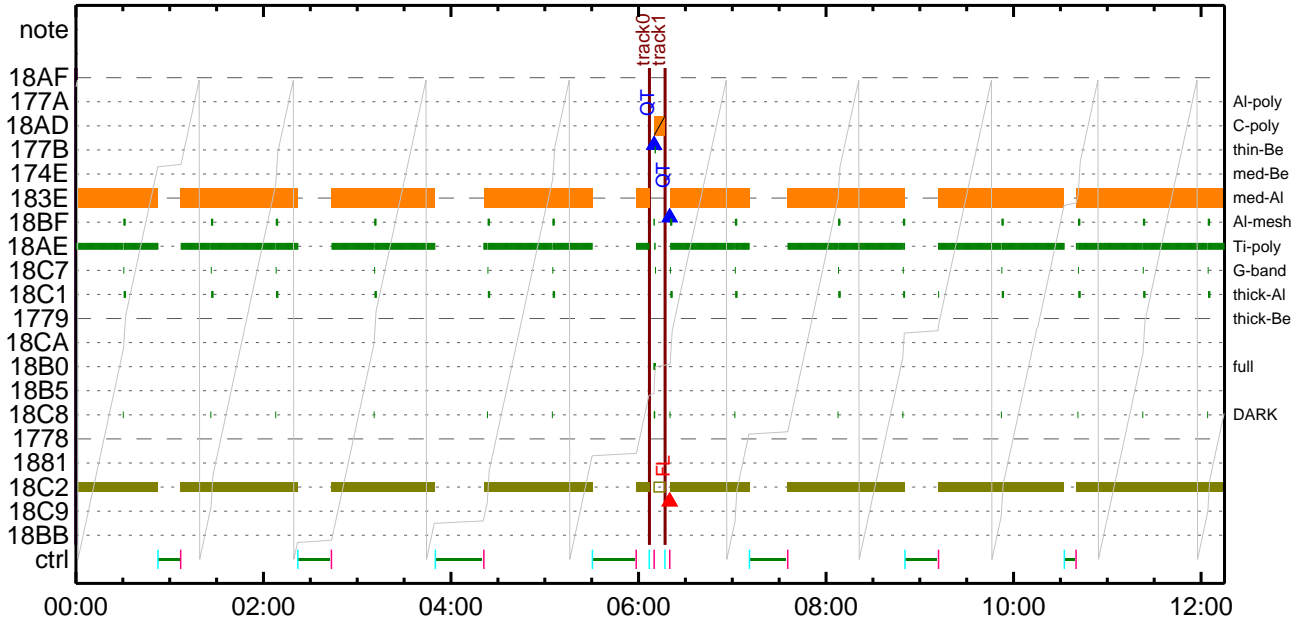
CMDI #0353 2011/12/31



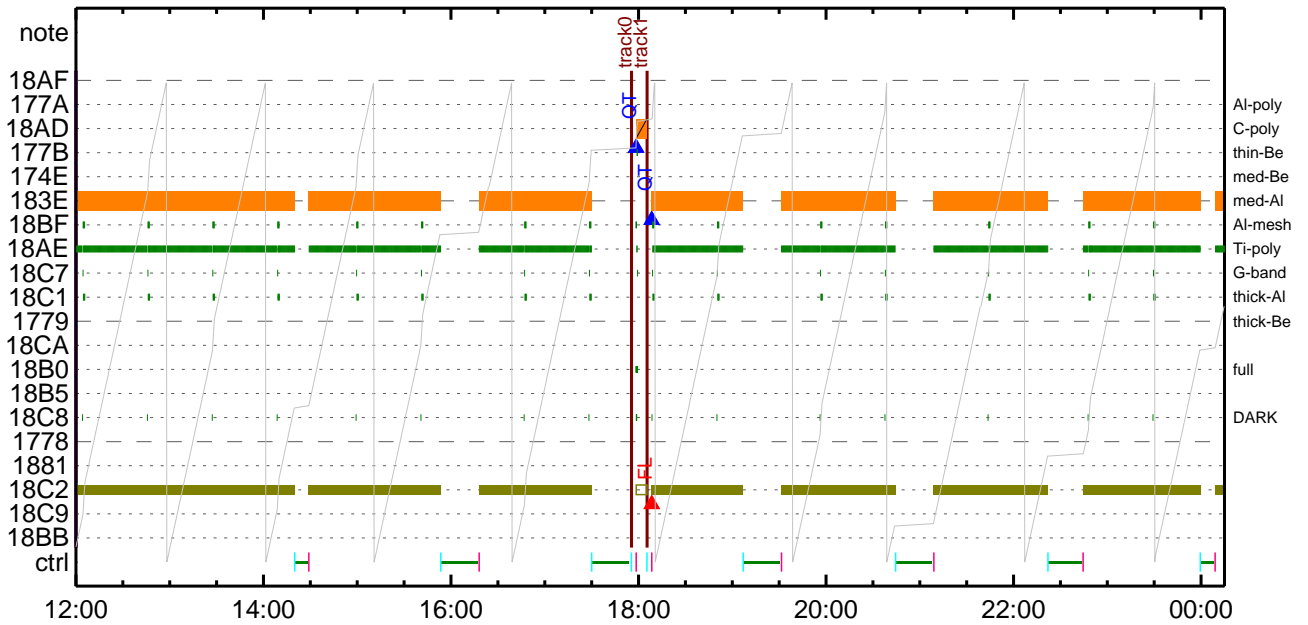
CMDI #0353 2011/12/31



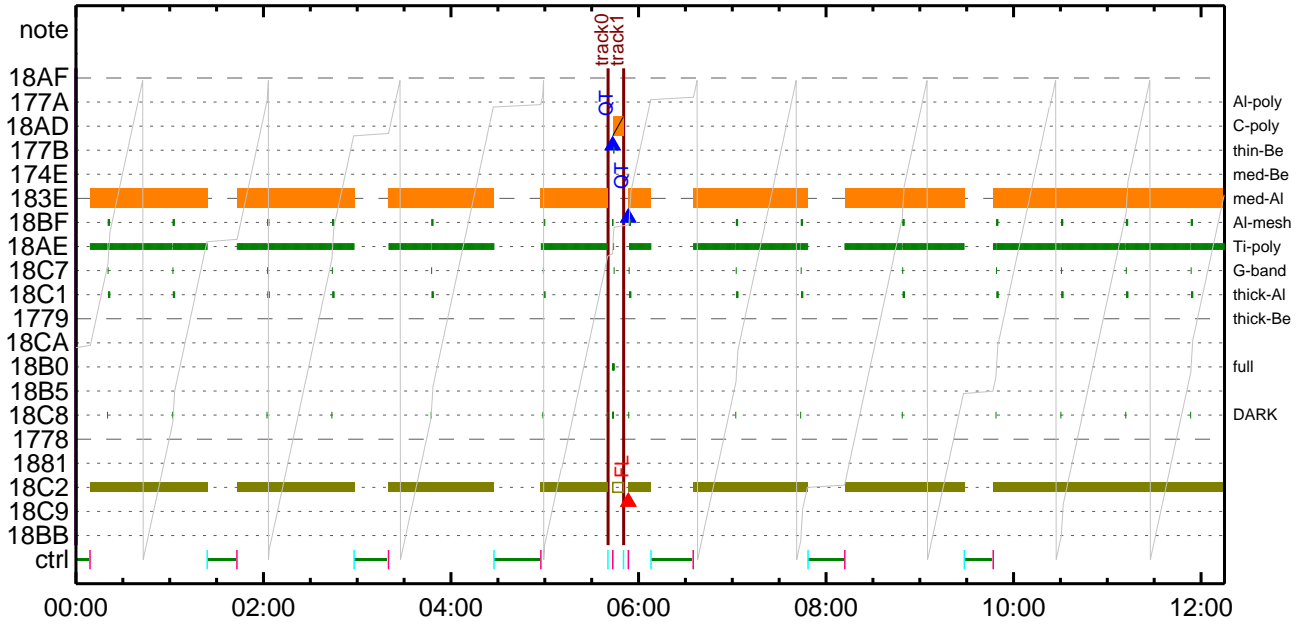
CMDI #0353 2012/01/01



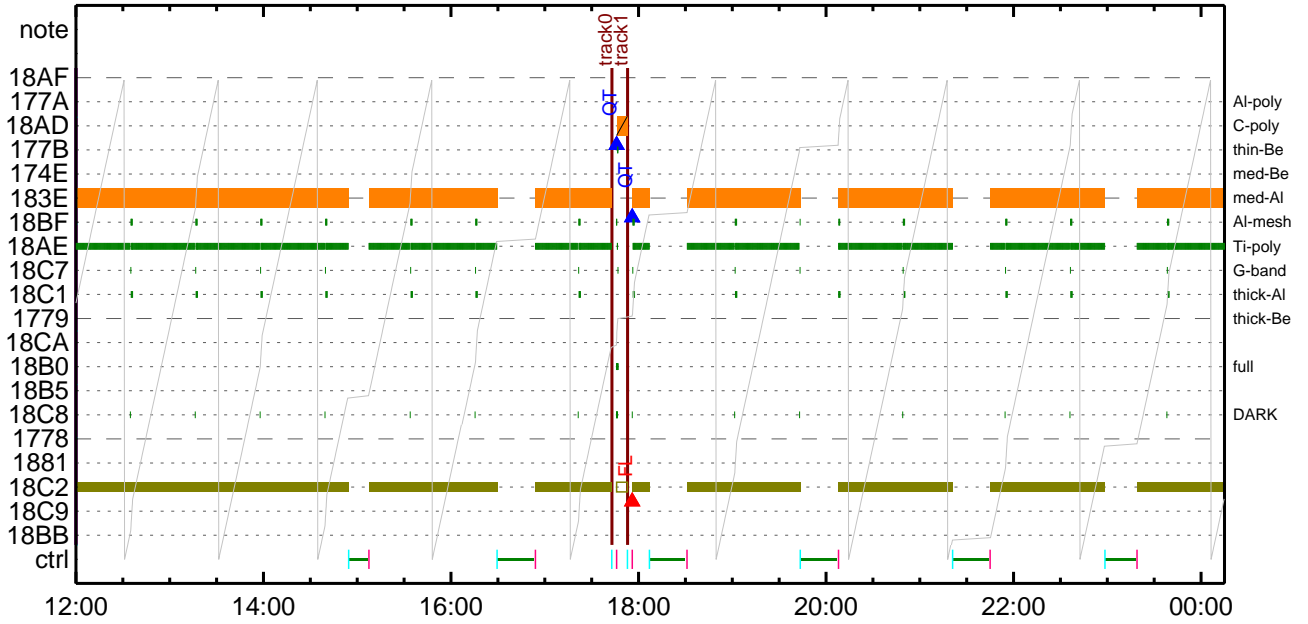
CMDI #0353 2012/01/01



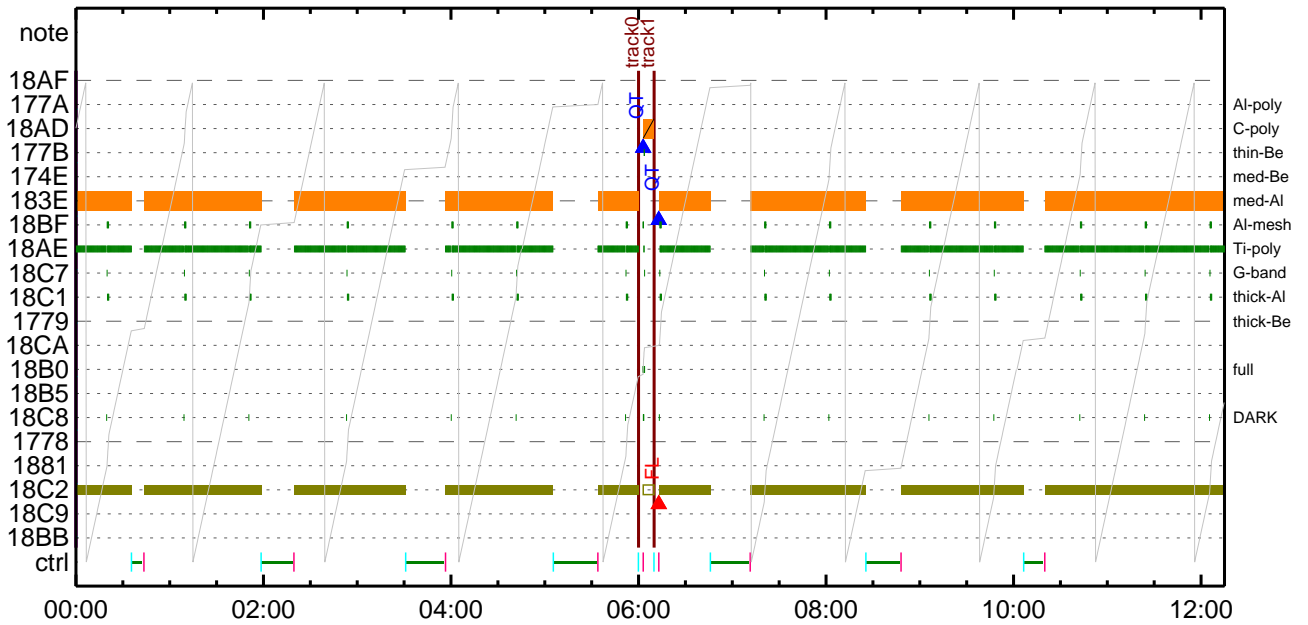
CMDI #0353 2012/01/02



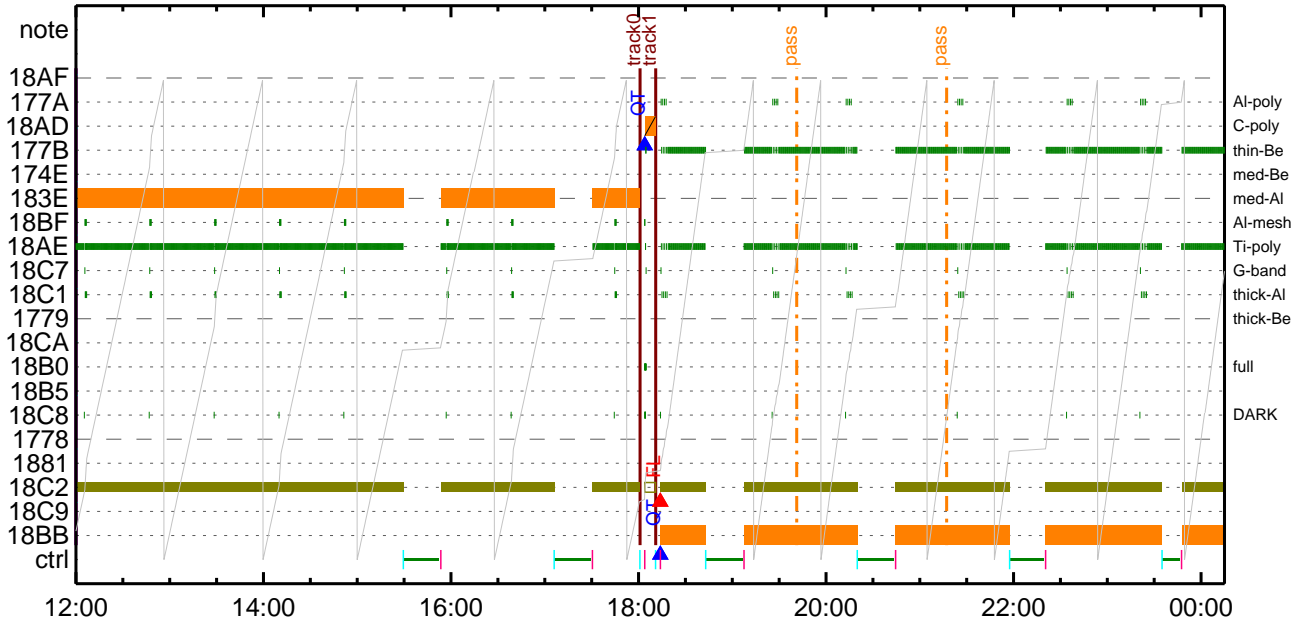
CMDI #0353 2012/01/02



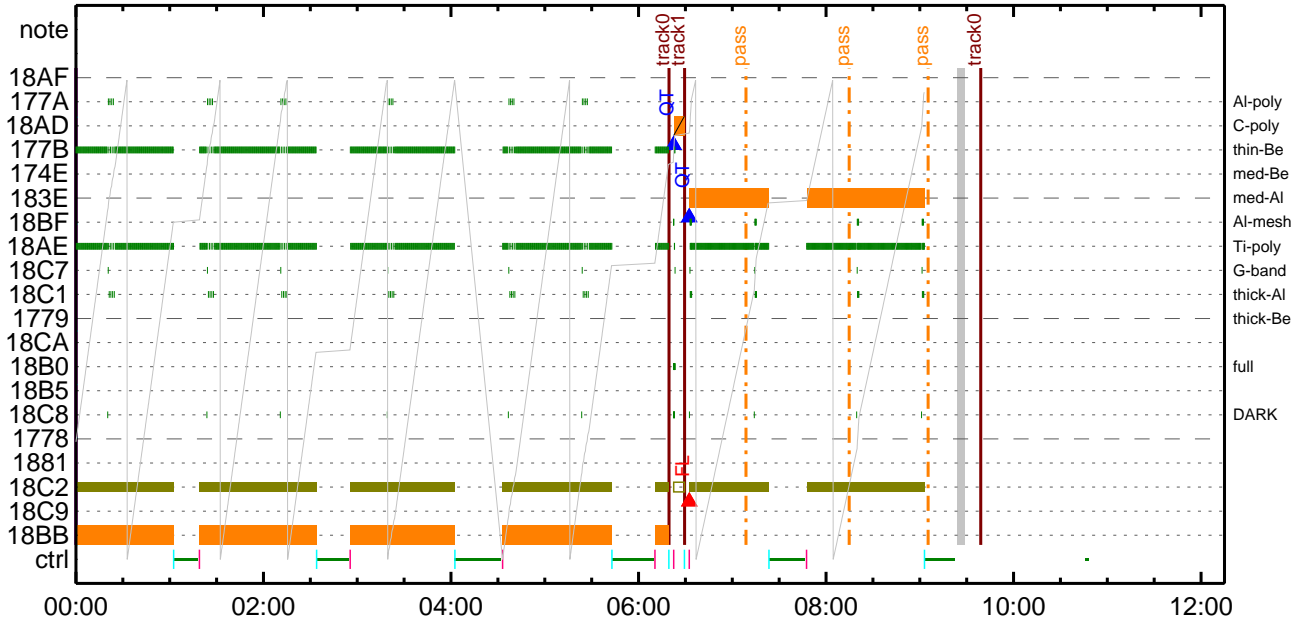
CMDI #0353 2012/01/03



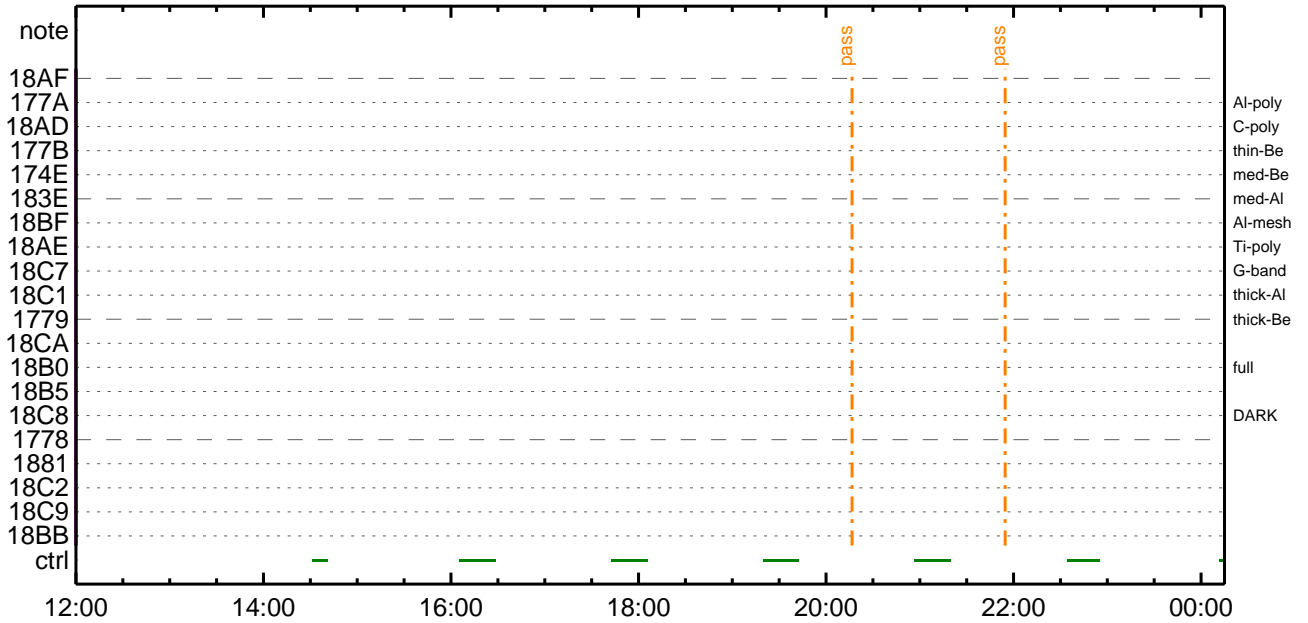
CMDI #0353 2012/01/03



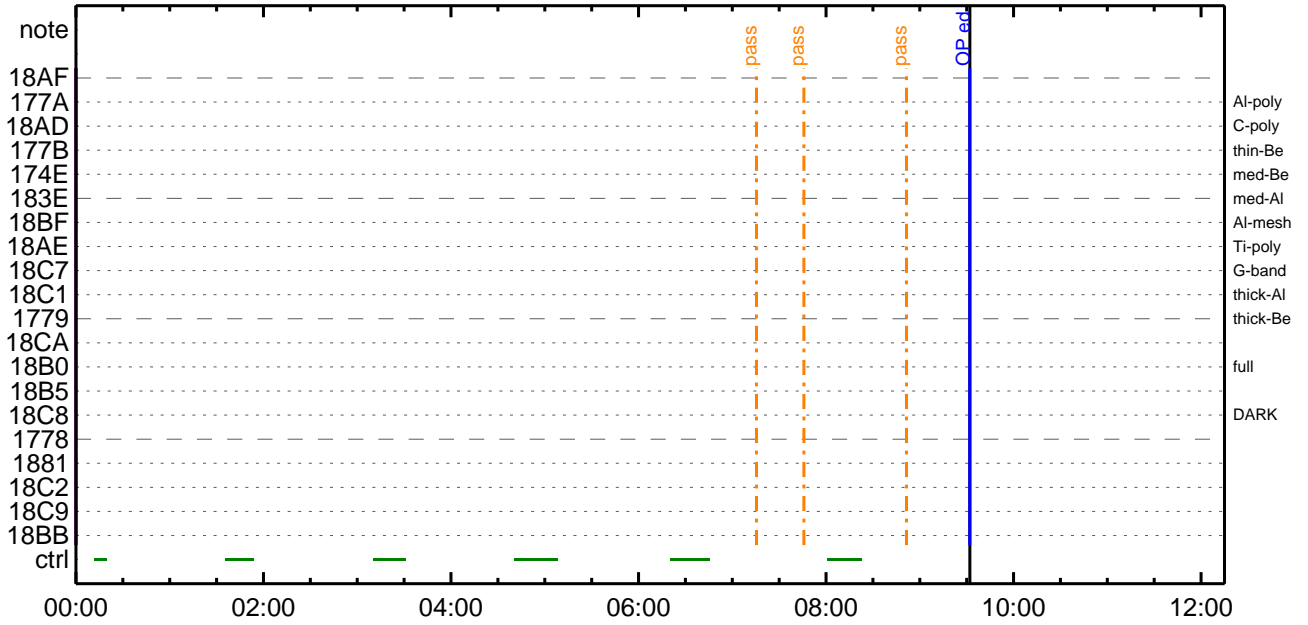
CMDI #0353 2012/01/04



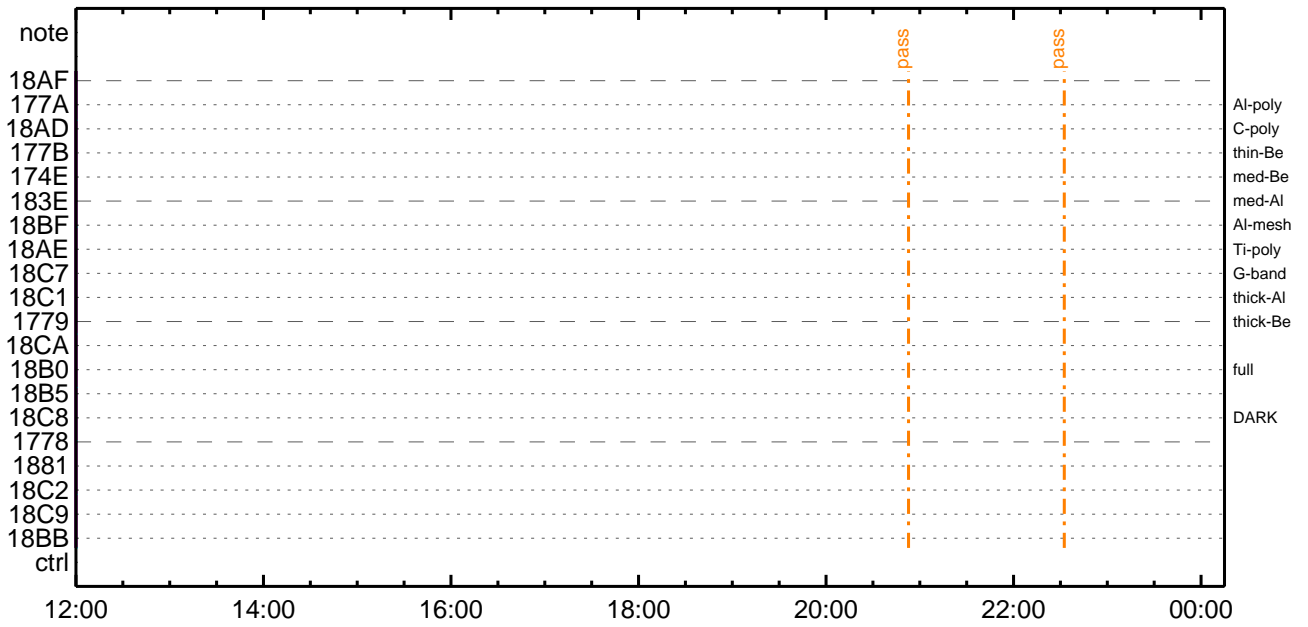
CMDI #0353 2012/01/04



CMDI #0353 2012/01/05



CMDI #0353 2012/01/05




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0096 C.                0p0z;çSET0EDUMP0i±0iYÑY¹0ç¹00|0³0E;f
0097 C.
0098 C. TIY³YFÝÖÝE00dÅDî¿(UT)
0099 +. TI 2011-12-30 09:36:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]           EQ       1COUNTUP
0102 C.
0103 +. TI 2011-12-30 09:36:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]           EQ       1COUNTUP
0106 C.
0107 +. TI 2011-12-30 09:36:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]           EQ       1COUNTUP
0110 C.
0111 +. TI 2011-12-30 09:40:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]           EQ       1COUNTUP
0114 C.
0115 C. °E²¼0iÄè%îíÑ0iYÁY§YÁY-¹àîÛ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]       EQ       ENA
0117 C.                çç[HK1_TI_CMD_NUM]         EQ       4
0118 C.                çç[HK1_NEXT_EXEC_PIM]       EQ       DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]       EQ       0xB3
0120 C.
0121 C. *****
0122 C. TIîî°èYÁYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC                (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]       EQ       07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]       EQ       2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]       EQ       3
0131 C.                çç[HK1_DMP_REPEAT_NUM]      EQ       0
0132 C.                çç[HK1_DMA_DMP_PIM]         EQ       DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC                (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]         EQ       7
0136 C.                çç[HK1_PKT_GEN_TIME]        EQ       0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]      EQ       32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]     EQ       4M
0139 C.                çç[HK1_DMP_CHK_FLG]        EQ       EXEC
0140 C.
0141 C. YÁYÖY×½ªî»0ð³îç§
0142 C.                çç[HK1_DMP_CHK_FLG]        EQ       NON
0143 C.
0144 C. RAM ID=TI_TBL0iE¹ç•è²iOK0ð³îç§
0145 C.
0146 C. DHUYâ;¼YÉ;Ê¼Y½;Yî;¼YÈ;Ë0ðîá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC                (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]         EQ       2
0150 C.                çç[HK1_PKT_GEN_TIME]        EQ       0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]      EQ       32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]     EQ       4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2011-12-30 09:40:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC                (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2011-12-30 09:40:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC                (21 02)
0174 +. TI 2011-12-30 09:40:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC                (22)
0177 C.                [ ] [HK1_TI_CMD_NUM] EQ     2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2011-12-30 09:40:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC                (c3)
0187 C.                [ ] [HK1_TI_CMD_NUM] EQ     1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP `úÄî0î»ö¼Y0ËÄ0¹0èDCBC•×²è *****
0192 C. (%â0iYÖYÁYÈYBÝEYáYçYè0E¼00¼Ä»Û0¹0é)
0193 C. DC-BC dcbc-402:DCBC
```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```

main-566 2011-12-30 13:23:31 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Ü;ã
0005 C.
0006 C. YÀYŞ;¼Y³YŞYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ÁÍ;ÈçðÅñ·µ°Æ»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ÈÈÈµ·ÍÉ;ÈÈÈ¼°ÇÓñ·µç¼¹çñÍ;çÁ®. ùñ¹ñðñßñÇÁ+ç®ñ·µÈñññ³ñÈ;ç
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+çµ;ON
0016 C. *****
0017 C. °ÆÀ. Í×ÈYñÄLOSñßñÇñÍ»p´Ïñð¹ÍÍ. ñ·;çÉÏÍ×ñÈXÁÓONñÍ¹ÏñÈñ¹ñÈñññ³ñÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY~¼ÖÁÏñ-°ÁÀÈñ·µç;ç°È²¼ñÍ°ÆÀ. ¼È¼çñð¼Á¹Ïñ¹ñÈ;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÍ¼í°ÆÀ.
0033 C. *****
0034 C. ° RESTART;ÈPT1;Èñ·µçñ¼¹¹çñÍ;ç°È²¼ñÍ¼Á¹Ïññ°;çDCBC-150ñØçÈñà;ç
0035 C.
0036 . C. ;ãPT1°ÆÀ. ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ï. ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ï. ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ï. ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÉÁÛÁØ;ÈÁ·Á°²óÈð;È. ãñÍ°ÆÀ. °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ï. ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ï. ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ï. ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ. ñ-¼«Æ°Áá»ßñ·µç;á;ç°È²¼ñð¼Á¹Ïñ¹ñÈ;ç
0055 C. YçYÓYÉYÉÁÛÁØñÁ·Á°²óÈðñ-¼áñ¼¹¹çñÍ°í»ñ¹ñÈñßñÇÁÏñÁ;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÍ¼í°ÆÀ.
0059 C. *****
0060 C. ° RESTART;ÈPT2;Èñ·µçñ¼¹¹çñÍ;ç°È²¼ñÍ¼Á¹Ïññ°;çDCBC-151ñØçÈñà;ç
0061 C.
0062 . C. ;ãPT2°ÆÀ. ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ï. ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ï. ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ï. ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÉÁÛÁØ;ÈÁ·Á°²óÈð;È. ãñÍ°ÆÀ. °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ï. ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ï. ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ï. ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ. Áá»ß;çXÁ+çµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ. Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+çµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
  
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-567 2011-12-30 13:23:31 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŖYÄY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFYÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āí;Èα¿αΑ•μ°Ε»Í×ΑÇαÍYÇYÁY×YÍ;¼YÉ;ÈÈ%μ·fÍÈ;ÈBÈ¼°ÇÓα•α¿¼l¹ÇαÍ;çÀ®, ùα¹αÈαBαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-264:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2011-12-30 09:40:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 08)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 08 20)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0f 80 80 06 06)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 10 80 80 08 08)
0088 + DC 07-F0 MDP_XRT_FLD_ENA
0089 BC (d8)
0090 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091 BC (c8)
0092 + DC 07-F0 MDP_XRT_AEC_RESET
0093 BC (d0)
0094 + DC 07-F0 MDP_XRT_ARS_DIS
0095 BC (d5)
```

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0096 + DC 07-F0 MDP_XRT_FLD_RESET
0097 BC (da)
0098 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0099 BC (c4 0c)
0100 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0101 BC (c5 03)
0102 . C. ----- Success Verify ? OK / NG ____
0103 C.
0104 C.
0105 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0106 C.
0107 +. DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 +. TI 2011-12-30 09:40:02.0
0110 DC 07-F0 MDP_XRT_MODE_OBSV
0111 BC (c2)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C. ***** XRT END *****
0115 C.
0116 . C. ***** MDP 'úÃîâî»ö¼ÝðËÂð¹ñèDCBC•x²è *****
0117 C. (%ã°îÿÓÿÄÿËÿÏÿËÿáÿçÿèñ¼ññ¼Ä»Û¹ñè)
0118 . S. DC-BC dcbc-402:DCBC
0119 (MDP_known_event)
0120 C.
0121 C.
0122 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑñË´Ø¹ñèDCBC•x²è *****
0123 . S. DC-BC dcbc-153:DCBC
0124 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125 C.
0126 C.
0127 . C. ;ãLOSÿÁÿ§ÿÄÿ-¼Ä»Û;ä
0128 C.
0129 . C. ***** LOS *****
0130 C.
```


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*** OP Sequence for XRT ***

2011/12/30	09:44:30.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	09:44:40.0	XRT_FOCUS_RECALIBRATE_428_OG [0x1ac]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2011/12/30	09:48:40.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/30	09:50:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	09:51:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2011/12/30	09:53:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/30	09:53:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/30	09:53:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/30	09:53:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/30	09:53:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/30	09:53:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/30	09:53:56.0	XRT_QT_PROG_SET_404_OG [0x194]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2011/12/30	09:53:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/30	09:54:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/30	14:42:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	14:42:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/30	14:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/30	14:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	14:54:00.0	XRT_Custom_418_OG [0x1a2]			
2011/12/30	14:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/30	16:17:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	16:17:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/30	16:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/30	16:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	16:41:00.0	XRT_Custom_418_OG [0x1a2]			
2011/12/30	16:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/30	17:54:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	17:54:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/30	17:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/30	17:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/30	18:17:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	18:17:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/30	18:18:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2011/12/30	18:18:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/30	18:18:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/12/30	18:18:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/30	18:20:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2011/12/30	18:21:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/30	18:27:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/30	18:28:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2011/12/30	18:30:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/30	18:30:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/30	18:30:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/30	18:30:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/30	18:30:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/30	18:30:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/30	18:30:56.0	XRT_QT_PROG_SET_404_OG [0x194]			

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2011/12/30	18:30:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/30	18:31:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/30	19:31:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/30	19:31:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/30	19:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/30	19:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/30	19:54:30.0	XRT_Custom_418_OG [0x1a2]					
2011/12/30	19:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/30	21:09:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/30	21:09:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/30	21:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/30	21:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/30	21:32:00.0	XRT_Custom_418_OG [0x1a2]					
2011/12/30	21:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/30	22:46:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/30	22:46:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/30	22:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/30	22:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/30	23:06:30.0	XRT_Custom_418_OG [0x1a2]					
2011/12/30	23:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/31	00:23:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/31	00:23:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/31	00:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/31	00:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/31	00:31:00.0	XRT_Custom_418_OG [0x1a2]					
2011/12/31	00:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/31	01:47:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/31	01:47:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/31	01:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/31	01:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/31	02:06:00.0	XRT_Custom_418_OG [0x1a2]					
2011/12/31	02:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/31	03:22:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/31	03:22:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/31	03:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/31	03:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/31	03:43:30.0	XRT_Custom_418_OG [0x1a2]					
2011/12/31	03:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/31	04:53:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/31	04:53:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/31	04:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/31	04:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/31	05:21:00.0	XRT_Custom_418_OG [0x1a2]					
2011/12/31	05:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/31	06:03:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/31	06:03:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2011/12/31	06:03:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00
2011/12/31	06:03:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/12/31	06:03:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/12/31	06:03:50.0	XRT_ARS_DIS_438_OG [0x1b6]					

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2011/12/31	06:06:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2011/12/31	06:06:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/31	06:13:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/31	06:13:30.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			AOCU_NM	5	02-76	01 00 00 00 00
2011/12/31	06:15:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2011/12/31	06:16:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2011/12/31	06:16:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2011/12/31	06:16:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2011/12/31	06:16:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2011/12/31	06:16:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/31	06:16:26.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2011/12/31	06:16:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2011/12/31	06:16:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/31	06:33:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/31	06:33:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/31	06:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/31	06:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/31	06:58:00.0	XRT_Custom_418_OG [0x1a2]				
2011/12/31	06:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/31	08:13:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/31	08:13:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/31	08:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/31	08:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/31	08:35:00.0	XRT_Custom_418_OG [0x1a2]				
2011/12/31	08:36:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/31	09:54:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/31	09:54:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/31	09:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/31	09:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/31	10:08:30.0	XRT_Custom_418_OG [0x1a2]				
2011/12/31	10:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/31	15:18:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/31	15:18:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/31	15:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/31	15:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/31	15:40:30.0	XRT_Custom_418_OG [0x1a2]				
2011/12/31	15:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/31	16:53:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/31	16:53:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2011/12/31	16:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2011/12/31	16:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2011/12/31	17:17:30.0	XRT_Custom_418_OG [0x1a2]				
2011/12/31	17:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2011/12/31	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2011/12/31	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2011/12/31	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
			AOCU_NM	5	02-76	00 00 00 00 00
2011/12/31	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2011/12/31	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2011/12/31	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]				

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2011/12/31	18:02:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2011/12/31	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/31	18:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/31	18:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00	
2011/12/31	18:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2011/12/31	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/12/31	18:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/12/31	18:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/12/31	18:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/12/31	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/31	18:12:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2011/12/31	18:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2011/12/31	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/31	18:31:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/31	18:31:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/31	18:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/31	18:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/31	18:54:00.0	XRT_Custom_418_OG [0x1a2]					
2011/12/31	18:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/31	20:08:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/31	20:08:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/31	20:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/31	20:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/31	20:31:30.0	XRT_Custom_418_OG [0x1a2]					
2011/12/31	20:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/31	21:45:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/31	21:45:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/31	21:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/31	21:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/31	22:08:00.0	XRT_Custom_418_OG [0x1a2]					
2011/12/31	22:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/12/31	23:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/12/31	23:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/12/31	23:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/12/31	23:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/12/31	23:36:30.0	XRT_Custom_418_OG [0x1a2]					
2011/12/31	23:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/01/01	00:52:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/01/01	00:52:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/01/01	00:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/01/01	00:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/01/01	01:06:00.0	XRT_Custom_418_OG [0x1a2]					
2012/01/01	01:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/01/01	02:22:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/01/01	02:22:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/01/01	02:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/01/01	02:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/01/01	02:42:30.0	XRT_Custom_418_OG [0x1a2]					
2012/01/01	02:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2012/01/01	03:50:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	03:50:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/01	03:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/01	03:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/01	04:20:00.0	XRT_Custom_418_OG [0x1a2]							
2012/01/01	04:21:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/01	05:30:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	05:30:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/01	05:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/01	05:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/01	05:57:30.0	XRT_Custom_418_OG [0x1a2]							
2012/01/01	05:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/01	06:06:54.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	06:06:56.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/01/01	06:07:00.0	AOCS_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00				
2012/01/01	06:07:16.0	XRT_FLD_DIS_402_OG [0x192] MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/01/01	06:07:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/01/01	06:07:20.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/01/01	06:09:58.0	XRT_QT_PROG_SET_423_OG [0x1a7] MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2012/01/01	06:10:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/01	06:16:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	06:17:00.0	AOCS_OrE-point_Start_1_OG [0x097] AOCU_NM	5	02-76	01 00 00 00 00				
2012/01/01	06:19:26.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/01/01	06:19:46.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/01/01	06:19:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/01/01	06:19:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/01/01	06:19:52.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/01/01	06:19:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/01	06:19:56.0	XRT_QT_PROG_SET_404_OG [0x194] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2012/01/01	06:19:58.0	XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/01/01	06:20:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/01	07:11:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	07:11:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/01	07:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/01	07:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/01	07:34:30.0	XRT_Custom_418_OG [0x1a2]							
2012/01/01	07:35:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/01	08:50:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	08:50:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/01	08:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/01	08:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/01	09:11:00.0	XRT_Custom_418_OG [0x1a2]							
2012/01/01	09:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/01	10:32:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	10:32:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/01	10:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/01	10:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/01	10:39:00.0	XRT_Custom_418_OG [0x1a2]							
2012/01/01	10:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2012/01/01	14:20:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	14:20:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/01	14:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/01	14:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/01	14:28:00.0	XRT_Custom_418_OG [0x1a2]							
2012/01/01	14:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/01	15:53:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	15:53:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/01	15:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/01	15:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/01	16:17:00.0	XRT_Custom_418_OG [0x1a2]							
2012/01/01	16:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/01	17:30:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	17:30:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/01	17:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/01	17:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/01	17:55:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	17:55:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/01/01	17:55:30.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/01/01	17:55:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/01/01	17:55:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/01/01	17:55:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/01/01	17:58:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2012/01/01	17:58:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/01	18:05:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	18:05:30.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/01/01	18:07:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/01/01	18:08:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/01/01	18:08:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/01/01	18:08:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/01/01	18:08:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/01/01	18:08:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/01	18:08:26.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2012/01/01	18:08:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/01/01	18:08:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/01	19:07:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	19:07:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/01	19:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/01	19:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/01	19:30:30.0	XRT_Custom_418_OG [0x1a2]							
2012/01/01	19:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/01	20:44:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	20:44:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/01	20:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/01	20:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/01	21:08:00.0	XRT_Custom_418_OG [0x1a2]							
2012/01/01	21:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/01	22:22:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/01	22:22:02.0	XRT_FLD_RESET_412_OG [0x19c]							

2012/01/01	22:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/01/01	22:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/01/01	22:43:30.0	XRT_Custom_418_OG [0x1a2]							
2012/01/01	22:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/01/01	23:59:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/01	23:59:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/01/01	23:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/01/02	00:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/01/02	00:08:00.0	XRT_Custom_418_OG [0x1a2]							
2012/01/02	00:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/01/02	01:24:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/02	01:24:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/01/02	01:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/01/02	01:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/01/02	01:42:00.0	XRT_Custom_418_OG [0x1a2]							
2012/01/02	01:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/01/02	02:58:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/02	02:58:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/01/02	02:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/01/02	03:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/01/02	03:19:00.0	XRT_Custom_418_OG [0x1a2]							
2012/01/02	03:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/01/02	04:27:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/02	04:27:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/01/02	04:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/01/02	04:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/01/02	04:56:30.0	XRT_Custom_418_OG [0x1a2]							
2012/01/02	04:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/01/02	05:40:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/02	05:40:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/01/02	05:40:30.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2012/01/02	05:40:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/01/02	05:40:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/01/02	05:40:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/01/02	05:43:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2012/01/02	05:43:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/01/02	05:50:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/02	05:50:30.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00			
2012/01/02	05:52:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/01/02	05:53:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/01/02	05:53:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/01/02	05:53:20.5	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/01/02	05:53:22.5	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/01/02	05:53:24.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/01/02	05:53:26.5	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f			
2012/01/02	05:53:28.5	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2012/01/02	05:53:30.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/01/02	06:08:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/02	06:08:02.0	XRT_FLD_RESET_412_OG [0x19c]							

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2012/01/02	06:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/02	06:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/02	06:34:00.0	XRT_Custom_418_OG [0x1a2]								
2012/01/02	06:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/02	07:48:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/02	07:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/02	07:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/02	07:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/02	08:11:00.0	XRT_Custom_418_OG [0x1a2]								
2012/01/02	08:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/02	09:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/02	09:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/02	09:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/02	09:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/02	09:46:00.0	XRT_Custom_418_OG [0x1a2]								
2012/01/02	09:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/02	14:54:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/02	14:54:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/02	14:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/02	14:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/02	15:06:30.0	XRT_Custom_418_OG [0x1a2]								
2012/01/02	15:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/02	16:29:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/02	16:29:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/02	16:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/02	16:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/02	16:53:00.0	XRT_Custom_418_OG [0x1a2]								
2012/01/02	16:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/02	17:42:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/02	17:42:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/01/02	17:43:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2012/01/02	17:43:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/01/02	17:43:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/01/02	17:43:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/01/02	17:45:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2012/01/02	17:46:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/02	17:52:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/02	17:53:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00				
2012/01/02	17:55:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/01/02	17:55:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/01/02	17:55:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/01/02	17:55:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/01/02	17:55:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/01/02	17:55:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/02	17:55:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2012/01/02	17:55:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/01/02	17:56:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/02	18:07:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/02	18:07:02.0	XRT_FLD_RESET_412_OG [0x19c]								

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2012/01/02	18:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/02	18:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/02	18:30:01.0	XRT_Custom_418_OG [0x1a2]				
2012/01/02	18:31:01.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/02	19:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/02	19:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/02	19:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/02	19:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/02	20:07:00.0	XRT_Custom_418_OG [0x1a2]				
2012/01/02	20:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/02	21:21:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/02	21:21:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/02	21:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/02	21:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/02	21:44:00.0	XRT_Custom_418_OG [0x1a2]				
2012/01/02	21:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/02	22:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/02	22:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/02	22:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/02	23:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/02	23:18:00.0	XRT_Custom_418_OG [0x1a2]				
2012/01/02	23:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/03	00:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/03	00:35:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/03	00:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/03	00:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/03	00:42:30.0	XRT_Custom_418_OG [0x1a2]				
2012/01/03	00:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/03	01:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/03	01:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/03	01:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/03	02:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/03	02:18:30.0	XRT_Custom_418_OG [0x1a2]				
2012/01/03	02:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/03	03:31:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/03	03:31:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/03	03:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/03	03:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/03	03:55:30.0	XRT_Custom_418_OG [0x1a2]				
2012/01/03	03:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/03	05:05:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/03	05:05:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/03	05:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/03	05:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/03	05:33:00.0	XRT_Custom_418_OG [0x1a2]				
2012/01/03	05:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/03	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/03	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/01/03	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2012/01/03	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9

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2012/01/03	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2012/01/03	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/01/03	06:02:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12				
2012/01/03	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/01/03	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/01/03	06:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]								
		AOCU_NM	5	02-76	01	00	00	00	00	00
2012/01/03	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]								
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2012/01/03	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2012/01/03	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2012/01/03	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2012/01/03	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/01/03	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/01/03	06:12:56.0	XRT_QT_PROG_SET_404_OG [0x194]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f				
2012/01/03	06:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03				
2012/01/03	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/01/03	06:46:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/01/03	06:46:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/01/03	06:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/01/03	06:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/01/03	07:10:30.0	XRT_Custom_418_OG [0x1a2]								
2012/01/03	07:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/01/03	08:25:30.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/01/03	08:25:32.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/01/03	08:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/01/03	08:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/01/03	08:47:00.0	XRT_Custom_418_OG [0x1a2]								
2012/01/03	08:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/01/03	10:06:30.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/01/03	10:06:32.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/01/03	10:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/01/03	10:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/01/03	10:19:00.0	XRT_Custom_418_OG [0x1a2]								
2012/01/03	10:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/01/03	15:29:30.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/01/03	15:29:32.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/01/03	15:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/01/03	15:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/01/03	15:52:30.0	XRT_Custom_418_OG [0x1a2]								
2012/01/03	15:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/01/03	17:06:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/01/03	17:06:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/01/03	17:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/01/03	17:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/01/03	17:29:30.0	XRT_Custom_418_OG [0x1a2]								
2012/01/03	17:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/01/03	18:00:54.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/01/03	18:00:56.0	XRT_FOCUS_POSITION_401_OG [0x191]								
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2012/01/03	18:01:00.0	AOCS_OrE-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	00	00	00	00	00	00
2012/01/03	18:01:16.0	XRT_FLD_DIS_402_OG [0x192]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					

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2012/01/03	18:01:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/01/03	18:01:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/03	18:03:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2012/01/03	18:04:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/03	18:10:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/03	18:11:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00
2012/01/03	18:13:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/01/03	18:13:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/01/03	18:13:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/01/03	18:13:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/01/03	18:13:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/03	18:13:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/03	18:13:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2012/01/03	18:13:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/01/03	18:14:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/03	18:43:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/03	18:43:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/03	18:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/03	18:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/03	19:06:30.0	XRT_Custom_418_OG [0x1a2]				
2012/01/03	19:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/03	20:20:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/03	20:20:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/03	20:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/03	20:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/03	20:43:30.0	XRT_Custom_418_OG [0x1a2]				
2012/01/03	20:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/03	21:57:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/03	21:57:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/03	21:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/03	22:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/03	22:19:30.5	XRT_Custom_418_OG [0x1a2]				
2012/01/03	22:20:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/03	23:35:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/03	23:35:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/03	23:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/03	23:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/03	23:46:30.0	XRT_Custom_418_OG [0x1a2]				
2012/01/03	23:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/04	01:02:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/04	01:02:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/04	01:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/04	01:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/04	01:18:00.0	XRT_Custom_418_OG [0x1a2]				
2012/01/04	01:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/04	02:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/04	02:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/04	02:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/04	02:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2012/01/04	02:54:30.0	XRT_Custom_418_OG [0x1a2]							
2012/01/04	02:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/04	04:02:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/04	04:02:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/04	04:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/04	04:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/04	04:32:00.0	XRT_Custom_418_OG [0x1a2]							
2012/01/04	04:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/04	05:43:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/04	05:43:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/04	05:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/04	05:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/04	06:09:30.0	XRT_Custom_418_OG [0x1a2]							
2012/01/04	06:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/04	06:19:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/04	06:19:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/01/04	06:19:30.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/01/04	06:19:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/01/04	06:19:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/01/04	06:19:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/01/04	06:22:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2012/01/04	06:22:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/04	06:29:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/04	06:29:30.0	AOCS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/01/04	06:31:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/01/04	06:32:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/01/04	06:32:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/01/04	06:32:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/01/04	06:32:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/01/04	06:32:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/04	06:32:26.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2012/01/04	06:32:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/01/04	06:32:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/04	07:23:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/04	07:23:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/04	07:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/04	07:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/04	07:46:30.0	XRT_Custom_418_OG [0x1a2]							
2012/01/04	07:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/01/04	09:03:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/01/04	09:03:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/01/04	09:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/01/04	09:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/01/04	09:39:00.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				