

# XRT Timeline to be uploaded on 2012/01/04

Period: 2012/01/04 09:29:00 - 2012/01/07 10:13:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

## XOB #18BB: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 512x512 at 1064 1048 (all), 60s cad

Term	Pointing (x, y)	Comment
01/04 09:42:00 - 01/04 17:42:30	Track ( -13.2, -314.6) @ 01/04 09:39:00	# OP start + 10min/track AR11389
01/04 18:38:30 - 01/05 05:52:24	Track ( 63.6, -314.0) @ 01/04 18:35:30	#track AR11389
01/05 06:49:36 - 01/05 09:25:30	Track ( 163.9, -313.9) @ 01/05 06:22:30	#cont.

**PROG= 01 1-time(s)**

Subr= 1	1-time(s)	2.0sec										
Seqn= 17 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 53 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 81 25-time(s) 60.0sec												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

## XOB #18AD: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be(12)

Term	Pointing (x, y)	Comment
01/04 18:08:30 - 01/04 18:15:30	Fixed ( 0.0, 0.0)	synoptic, shifted 5.5 min
01/05 05:55:30 - 01/05 06:00:00	Fixed ( 0.0, 0.0)	synoptic, shifted -7.5 min

**PROG= 18 1-time(s)**

Subr= 1	1-time(s)	12.0sec										
Seqn= 7 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 8 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

## XOB #18CB: Synoptic 9 Filter- 1x1 Q98 Shorter exp 2 loop

Term	Pointing (x, y)	Comment
01/04 18:18:36 - 01/04 18:35:24	Fixed ( 0.0, 0.0)	synoptic, shifted 5.5 min
01/05 06:03:06 - 01/05 06:20:30	Fixed ( 0.0, 0.0)	synoptic, shifted -7.5 min

**PROG= 06 2-time(s)**

Subr= 1	1-time(s)	180.0sec										
Seqn= 47 1-time(s) 25.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	125ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 25 1-time(s) 25.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 72 1-time(s) 25.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 15 1-time(s) 25.0sec												
C-poly/Open	C-poly/Open	close	Safe	Norm	707ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
C-poly/Open	C-poly/Open	close	Safe	Norm	8.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 13 1-time(s) 25.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 32</b>		<b>1-time(s)</b>		<b>4.0sec</b>								
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b>		<b>360.0sec</b>								
<b>Seqn= 86</b>		<b>1-time(s)</b>		<b>4.0sec</b>								
med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 37</b>		<b>1-time(s)</b>		<b>4.0sec</b>								
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 23</b>		<b>1-time(s)</b>		<b>4.0sec</b>								
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 4</b>		<b>1-time(s)</b>		<b>4.0sec</b>								
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Flare mode

\* \* \* \* \*

#### XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops

Term	Pointing (x, y)	Comment
01/04 09:42:00 - 01/04 17:42:30	Track ( -13.2, -314.6) <sup>Ⓜ 01/04 09:39:00</sup>	# OP start + 10min/track AR11389
01/04 18:38:30 - 01/05 05:52:24	Track ( 63.6, -314.0) <sup>Ⓜ 01/04 18:35:30</sup>	#track AR11389
01/05 06:49:36 - 01/05 09:25:30	Track ( 163.9, -313.9) <sup>Ⓜ 01/05 06:22:30</sup>	#cont.

#### PROG= 03 12-time(s)

<b>Subr= 1</b>		<b>45-time(s)</b>		<b>10.0sec</b>								
<b>Seqn= 20</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 63</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 77</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b>		<b>10.0sec</b>								
<b>Seqn= 90</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Active Region Search

\* \* \* \* \*

NOT USED

\* \* \* \* \*

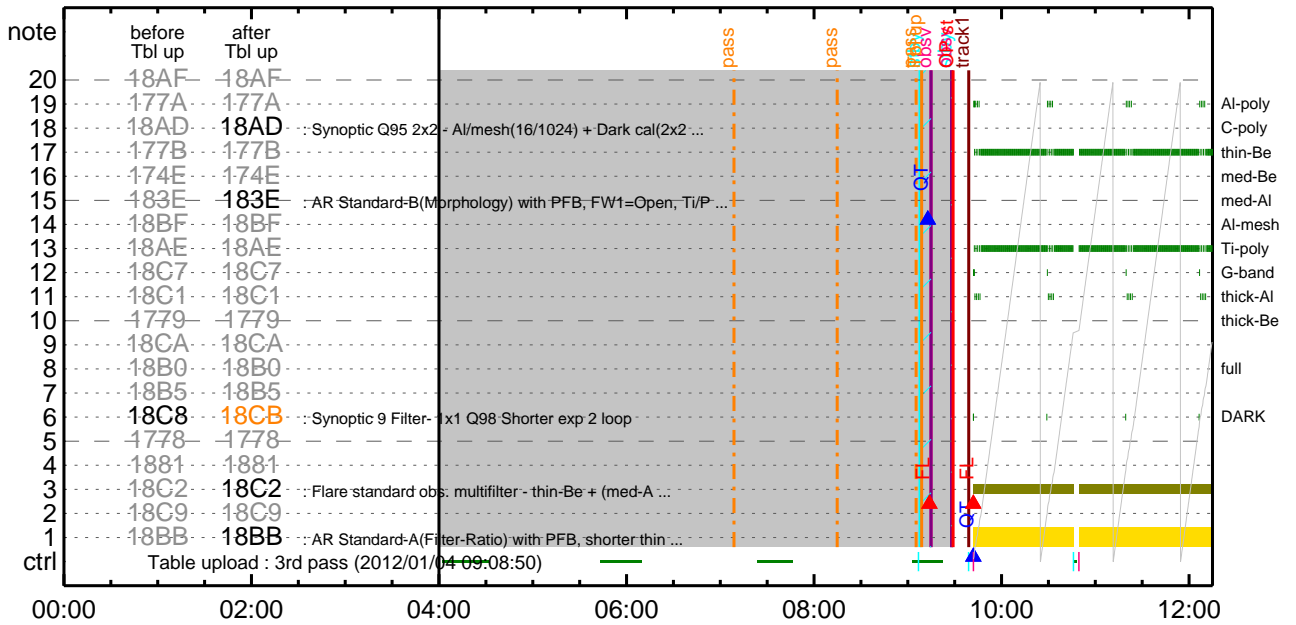
### Flare Detection

\* \* \* \* \*

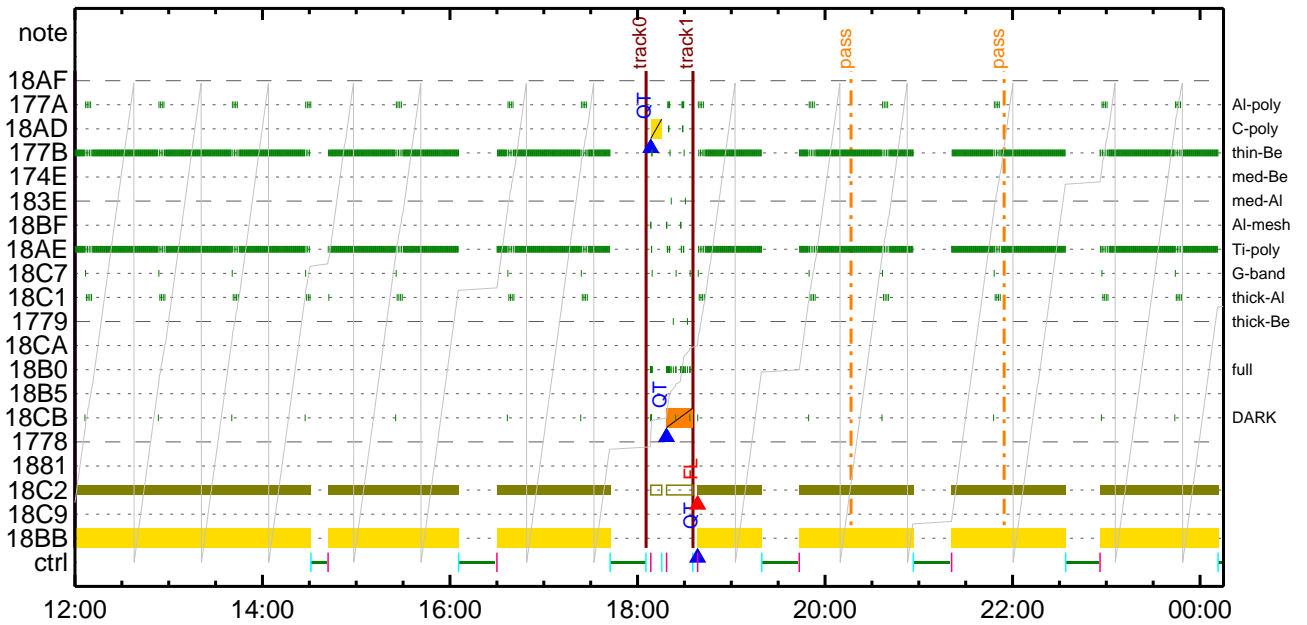
#### FLD Patrol

Term	Pointing (x, y)	Comment
01/04 18:38:16 - 01/05 05:52:46	Track ( 63.6, -314.0) <sup>Ⓜ 01/04 18:35:30</sup>	#track AR11389
01/05 06:49:22 - 01/07 10:13:00	Track ( 163.9, -313.9) <sup>Ⓜ 01/05 06:22:30</sup>	#cont.
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

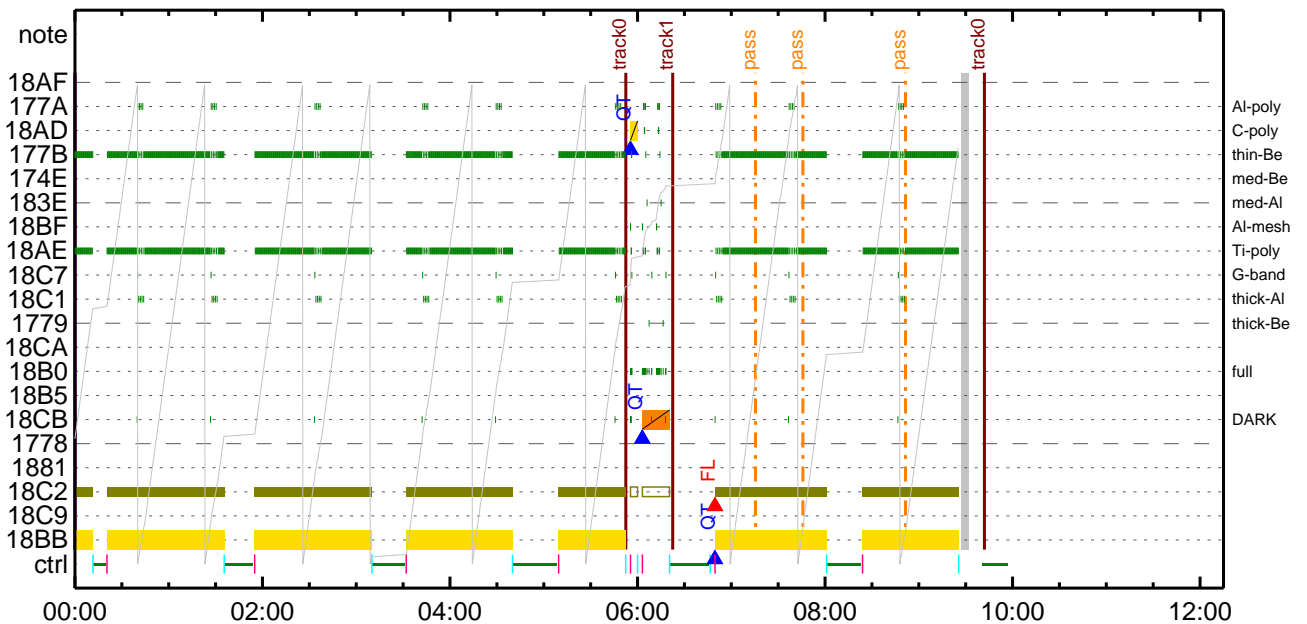
### CMDI #0355 2012/01/04



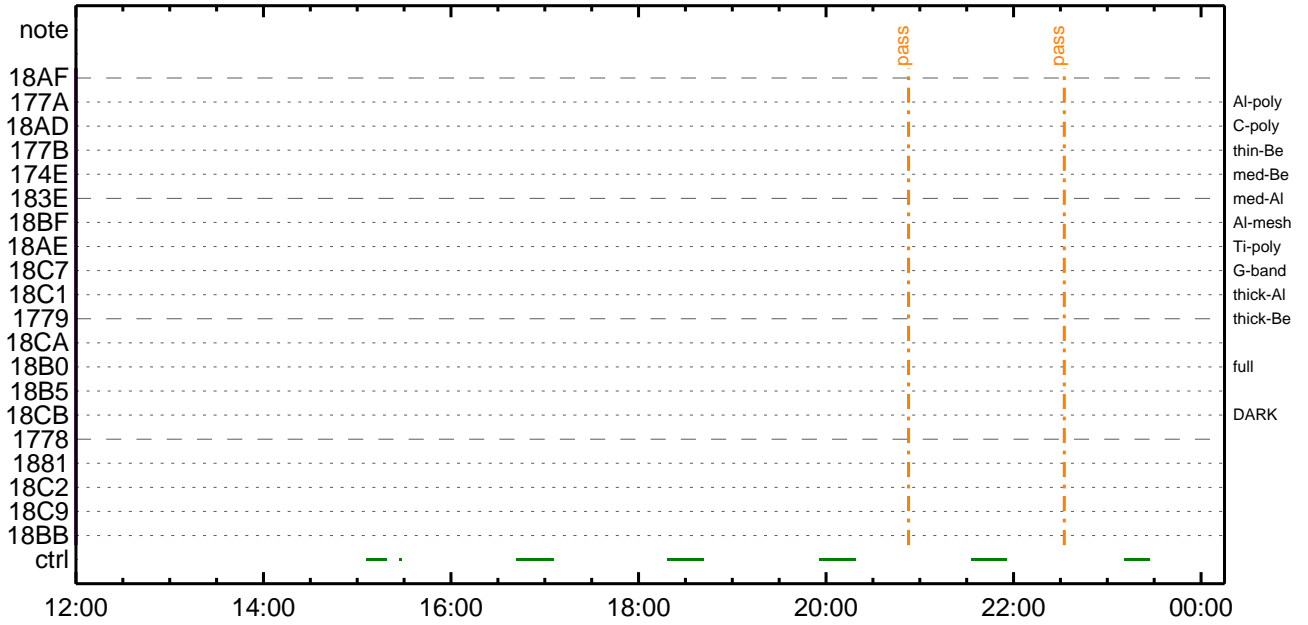
### CMDI #0355 2012/01/04



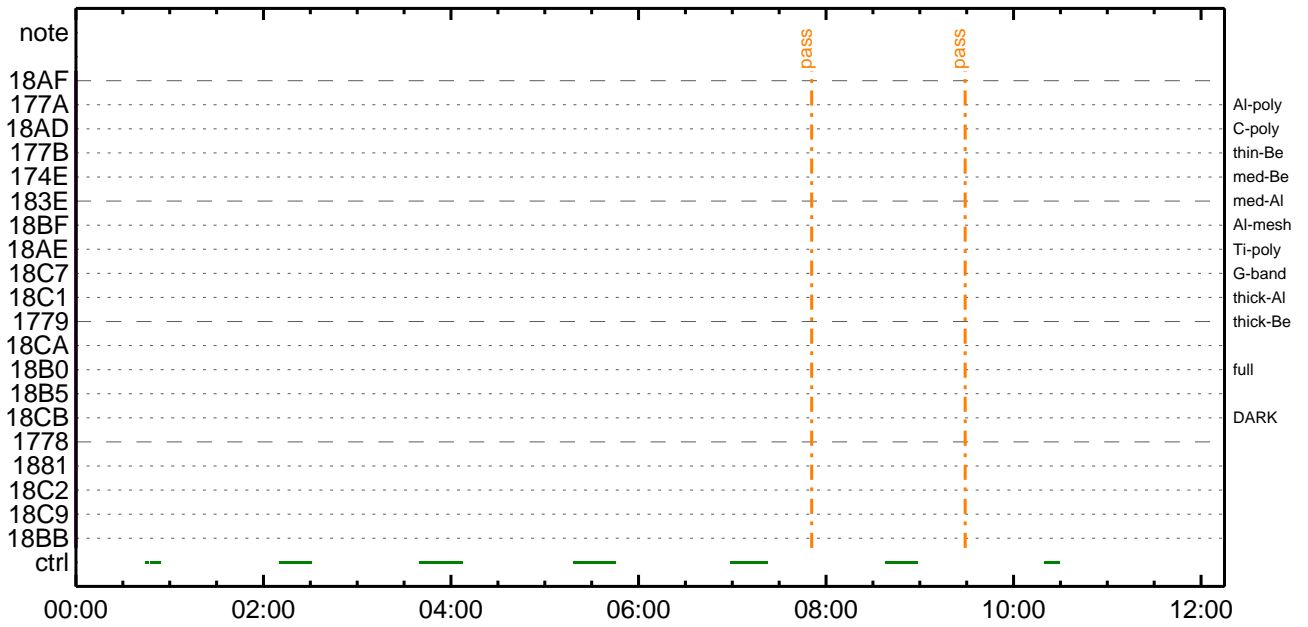
### CMDI #0355 2012/01/05



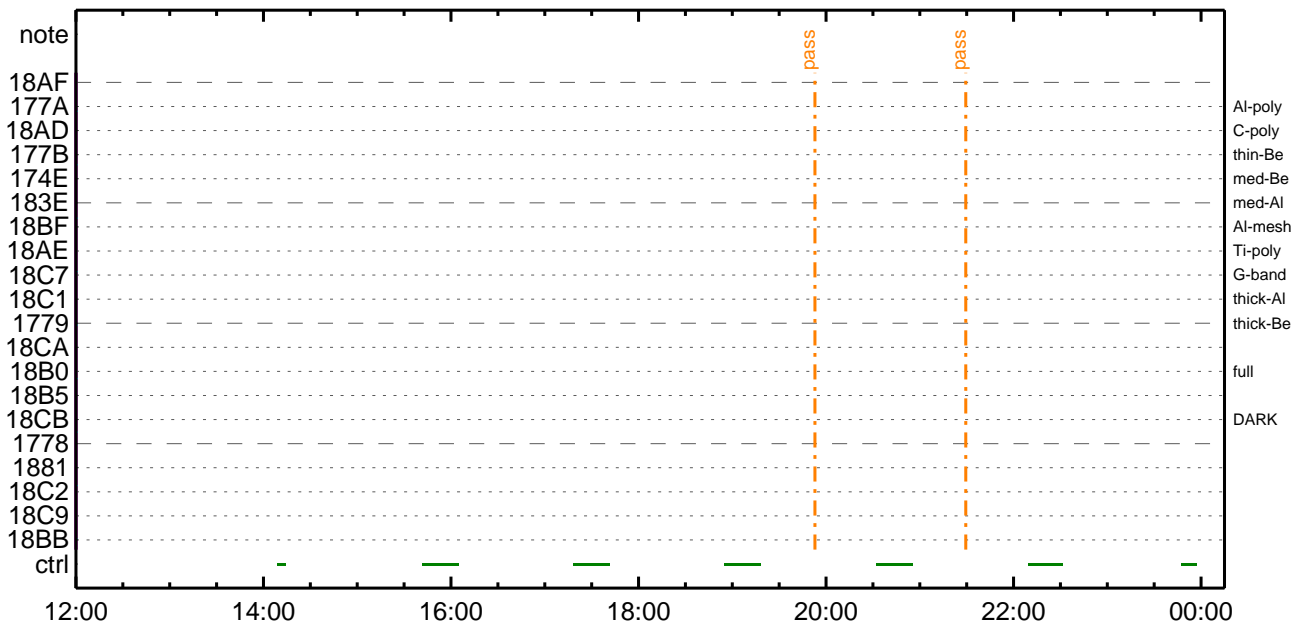
CMDI #0355 2012/01/05



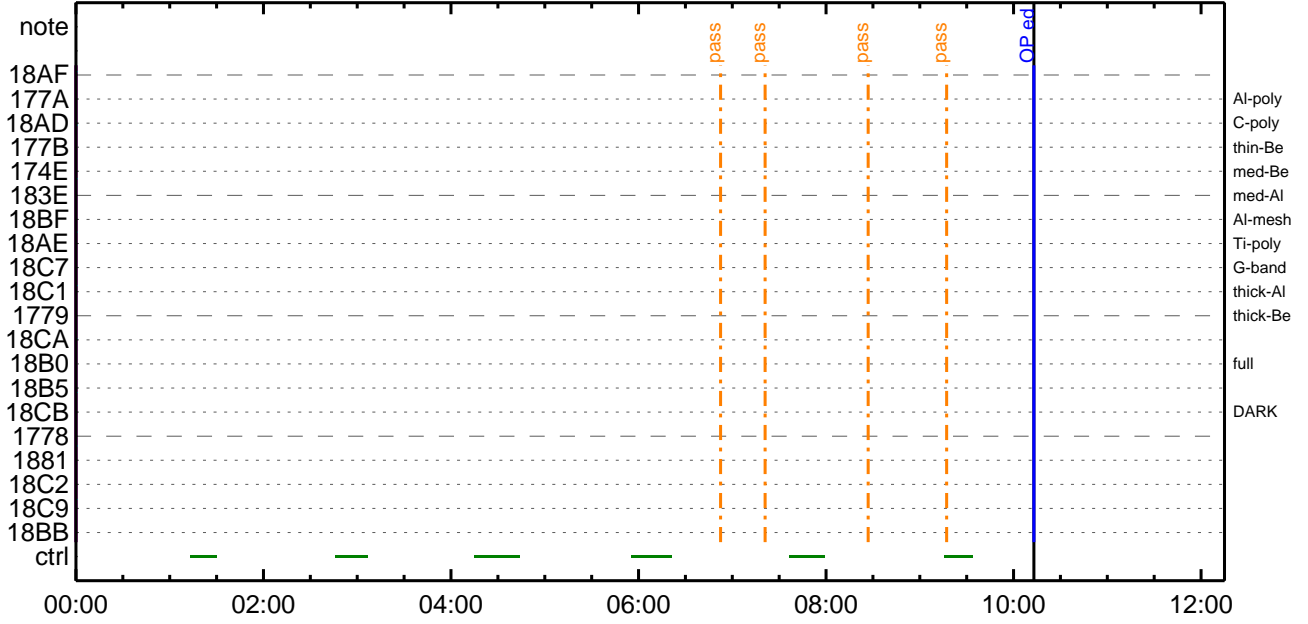
CMDI #0355 2012/01/06



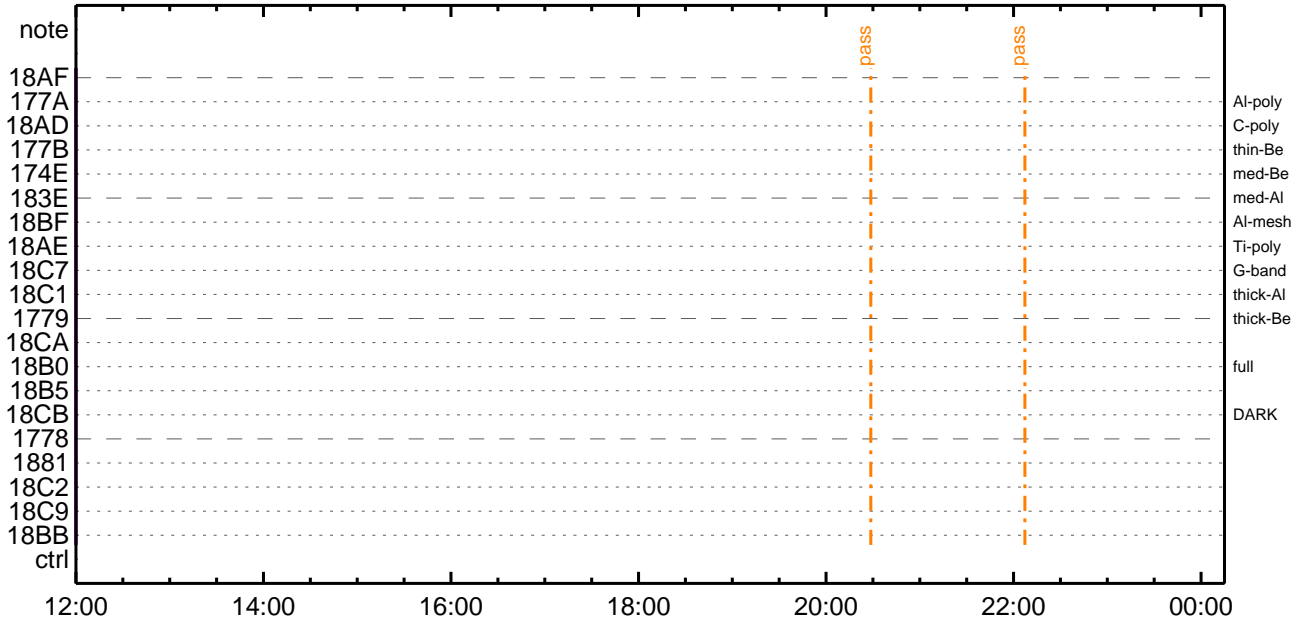
CMDI #0355 2012/01/06



CMDI #0355 2012/01/07



CMDI #0355 2012/01/07





```

0096 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0097 C.
0098 C. TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0099 +. TI 2012-01-04 09:24:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0102 C.
0103 +. TI 2012-01-04 09:24:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0106 C.
0107 +. TI 2012-01-04 09:24:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0110 C.
0111 +. TI 2012-01-04 09:28:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0114 C.
0115 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0116 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0117 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0118 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0119 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0120 C.
0121 C. *****
0122 C. TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC      (03 ab 03 01 02)
0128 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0129 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0130 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0131 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0132 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC      (07 0b f8)
0135 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0136 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0137 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0138 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0139 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0140 C.
0141 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0142 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0143 C.
0144 C. RAM ID=TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0145 C.
0146 C. DHU_DMA_DMP_PRM_SET(0x03AB00-0x03AEFF; § 1024byte)
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC      (02 0a f8)
0149 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0150 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0151 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0152 C.          0x00000000; 0x00000000; 0x00000000; 0x00000000;
0153 C.
0154 C. *****
0155 C. OTA CLU OP HTR ENA
0156 C. *****
0157 C.
0158 C. < Check initial status & temperatures >
0159 C.
0160 C. CTM_CLU_HTR_OP1_E/D = DIS      [ ]
0161 C. CTM_CLU_HTR_OP2_E/D = DIS      [ ]
0162 C. CTM_M2_HTR_OP1_E/D = ENA      [ ]
0163 C. CTM_M2_HTR_OP2_E/D = DIS      [ ]
0164 C.
0165 C. HK1_OTA_CLU_HTR_TMP (BChtr)= [ ]
0166 C. HK1_OTA-29_TEMP (CLU) = [ ]
0167 C. HK1_FPP_PMU_TEMP = [ ]
0168 C.
0169 C.
0170 +. DC 07-52 CTM_CLU_HTR_OP1_ENA
0171 C.
0172 C. CTM_CLU_HTR_OP1_E/D = ENA      [ ]
0173 C. CTM_OTA_HTR_VOLT = NORM      [ ]
0174 C. HK1_OTA_CLU_HTR_TMP increase [ ]
0175 C.
0176 C.
0177 C.
0178 C. 0x00000000; 0x00000000; 0x00000000; 0x00000000;
0179 C. < Enable Back-up Heaters >
0180 +. DC 07-53 CTM_CLU_HTR_OP2_ENA
0181 C.
0182 C. CTM_CLU_HTR_OP2_E/D = ENA      [ ]
0183 C. CTM_OTA_HTR_VOLT = NORM      [ ]
0184 C.
0185 C.
0186 +. DC 07-50 CTM_M2_HTR_OP1_ENA
0187 C.
0188 C. CTM_M2_HTR_OP1_E/D = ENA      [ ]
0189 C. CTM_OTA_HTR_VOLT = NORM      [ ]
0190 C.
0191 C.
0192 +. DC 07-51 CTM_M2_HTR_OP2_ENA
0193 C.

```

```

0194 C. CTM_M2_HTR_OP2_E/D = ENA [ ]
0195 C. CTM_OTA_HTR_VOLT = NORM [ ]
0196 C. -----
0197 C.
0198 C. < Disable Heaters >
0199 +. DC 07-56 CTM_OTA_HTR_ALL_DIS
0200 . C. -----
0201 C. CTM_CLU_HTR_OP1_E/D = DIS [ ]
0202 C. CTM_CLU_HTR_OP2_E/D = DIS [ ]
0203 C. CTM_M2_HTR_OP1_E/D = DIS [ ]
0204 C. CTM_M2_HTR_OP2_E/D = DIS [ ]
0205 C. -----
0206 C.
0207 C. ***** SOT END *****
0208 . C. Stop EIS observation and temporarily disable EIS mode changes
0209 C.
0210 C.
0211 C. ***** Start EIS operation (TI set) *****
0212 C. Execute, after the success of OP upload.
0213 C. Set EIS TI-commands
0214 +. TI 2012-01-04 09:28:30.0
0215 DC 07-FC EIS_MODE_MANU
0216 BC (21 02)
0217 +. TI 2012-01-04 09:28:40.0
0218 DC 07-FC EIS_MODE_CHG_DIS
0219 BC (22)
0220 . C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0221 C. ***** End EIS operation (TI set) *****
0222 C.
0223 C.
0224 C.
0225 C. ***** XRT START *****
0226 C. Execute, after the success of OP upload.
0227 +. TI 2012-01-04 09:28:00.0
0228 DC 07-F0 MDP_XRT_MODE_STBY
0229 BC (c3)
0230 . C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0231 C.
0232 C. ***** XRT END *****
0233 C.
0234 . C. ***** MDP `uAÎaÎ»ô%YnEÂDn¹nèDCBC•x²è *****
0235 C. (%â°iYÔYÃYÈYÏYËYâYçYènE%¼n¼A»Ûn¹nè)
0236 . S. DC-BC dcbc-402:DCBC
0237 (MDP_known_event)
0238 C.
0239 C.
0240 . C. ***** YDY¹.Ï Daily±;iÎnE´Øn¹nèDCBC•x²è *****
0241 . S. DC-BC dcbc-153:DCBC
0242 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0243 C.
0244 C.
0245 . C. ;ãLOSŸÁYŠYÃY¬¼A»Û;ã
0246 C.
0247 . C. ***** LOS *****
0248 C.

```



(a) Spacecraft Operation Procedure (real-commands)

```
main-570 2012-01-04 12:03:56 175 33 SOLAR-B MAIN //
0001  C.
0002  . C. ***** AOS *****
0003  C.
0004  . C. ;ãAOSYÅYÏYÅY-¼Å»Û;ä
0005  C.
0006  C. YÀYË;¼Y³YÏYÖYÉÅ+¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008  C.
0009  . C. ***** AOCS : Reload orbital element (send every contact) *****
0010  C.  Åí;Èø¿ðÅð•µ°Æ»Í×ÁÇøÍYÇYÅY×Yí;¼YËÉ;ÈÈÈ%µ•íÉ;ÈøÈ%ÇÖð•ø¿¼í¹ÇøÍ;çÅ®, ùø¹øðøðøÇÅ+¿®ð•øÈøøøøøÈ;f
0011  +. DC 02-8E AOCU_ORB_UPD
0012  C.
0013  C.
0014  . C. *****
0015  C. XÅ+¿øµ;ON
0016  C. *****
0017  C. ç" °ÅÀ, Í×ÈYøÀLOSøðÇøÍ»p´Öøð¹ÍÍ, ø.; çÈÖÍ×øÈXÅÖONøÍ¹ÖøÈøÈøøøøøÈ;f
0018  C.
0019  +. DC 03-B4 TCIA_XPA_ON/HI
0020  M. WAIT_SEC 1
0021  +. DC 03-84 TCIA_XMOD_ON
0022  M. WAIT_SEC 1
0023  +. DC 03-95 TCIA_XMOD_QPSK
0024  C.          çç[HK1_XPA_ON/OFF]            EQ      ON
0025  C.          çç[HK1_XPA_PWR_HI/LO]         EQ      HI
0026  C.          çç[HK1_XMOD_ON/OFF]           EQ      ON
0027  C.          çç[HK1_XMOD_QPSK/PM]          EQ      QPSK
0028  C.
0029  . C. XYDYÖYÉYÍYÅY-¾ÖÅÖø-øÅÀøø•ø¿øé; ç°È²¼øÍ°ÈÀ, ¼È%çøðø¼Å¹Öø¹øé;f
0030  C.
0031  . C. *****
0032  C. DR PT1 Åí%í°ÈÀ,
0033  C. *****
0034  C. ç" RESTART;ÈPT1;Èø•ø¿øø¼í¹ÇøÍ; ç°È²¼øÍ°ÈÀ¹Öø»ø°; çDCBC-150øø¿Èøà;f
0035  C.
0036  . C. ;ãPT1°ÈÀ, ³«»Í;ä
0037  +. DC 01-29 DHU_S/X_VC4_OFF
0038  +. DC 06-C8 DR_PT1_REP_SEL
0039  BC          (01 00)
0040  +. DC 06-B3 DR_REP_START
0041  +. DC 01-32 DHU_X_VC4_ON
0042  C.          çç[HK1_REP_PT_1/2]            EQ      PT1   (¼Å¹Ö., ;¼Ú)
0043  C.          çç[HK1_REP_STA/STP]           EQ      START (¼Å¹Ö., ;¼Ú)
0044  C.          çç[HK1_X_VC4_ON/OFF]          EQ      ON    (¼Å¹Ö., ;¼Ú)
0045  C.
0046  . C. ;ãYçYÖYÆYÈÀÜÅø;ÈÅ•Å°²øÈø;È, åøÍ°ÈÀ, °Æ³«;ä
0047  +. DC 06-B3 DR_REP_START
0048  +. DC 01-32 DHU_X_VC4_ON
0049  C.          çç[HK1_REP_PT_1/2]            EQ      PT1   (¼Å¹Ö., ;¼Ú)
0050  C.          çç[HK1_REP_STA/STP]           EQ      START (¼Å¹Ö., ;¼Ú)
0051  C.          çç[HK1_X_VC4_ON/OFF]          EQ      ON    (¼Å¹Ö., ;¼Ú)
0052  C.
0053  C.
0054  . C. PT1°ÈÀ, ø-¼«Æ°Åà»ðø•ø¿, å; ç°È²¼øðø¼Å¹Öø¹øé;f
0055  C. YçYÖYÆYÈÀÜÅøøøÅ•Å°²øÈøø-¶åøø¼í¹ÇøÍ'°Í»ø¹øðøøøÇÅÖøÅ;f
0056  C.
0057  . C. *****
0058  C. DR PT2 Åí%í°ÈÀ,
0059  C. *****
0060  C. ç" RESTART;ÈPT2;Èø•ø¿øø¼í¹ÇøÍ; ç°È²¼øÍ°ÈÀ¹Öø»ø°; çDCBC-151øø¿Èøà;f
0061  C.
0062  . C. ;ãPT2°ÈÀ, ³«»Í;ä
0063  +. DC 01-29 DHU_S/X_VC4_OFF
0064  +. DC 06-C8 DR_PT2_REP_SEL
0065  BC          (02 00)
0066  +. DC 06-B3 DR_REP_START
0067  +. DC 01-32 DHU_X_VC4_ON
0068  C.          çç[HK1_REP_PT_1/2]            EQ      PT2   (¼Å¹Ö., ;¼Ú)
0069  C.          çç[HK1_REP_STA/STP]           EQ      START (¼Å¹Ö., ;¼Ú)
0070  C.          çç[HK1_X_VC4_ON/OFF]          EQ      ON    (¼Å¹Ö., ;¼Ú)
0071  C.
0072  . C. ;ãYçYÖYÆYÈÀÜÅø;ÈÅ•Å°²øÈø;È, åøÍ°ÈÀ, °Æ³«;ä
0073  +. DC 06-B3 DR_REP_START
0074  +. DC 01-32 DHU_X_VC4_ON
0075  C.          çç[HK1_REP_PT_1/2]            EQ      PT2   (¼Å¹Ö., ;¼Ú)
0076  C.          çç[HK1_REP_STA/STP]           EQ      START (¼Å¹Ö., ;¼Ú)
0077  C.          çç[HK1_X_VC4_ON/OFF]          EQ      ON    (¼Å¹Ö., ;¼Ú)
0078  C.
0079  . C. *****
0080  C. DR°ÈÀ, Åà»Ë;çXÅ+¿øµ;OFF
0081  C. *****
0082  C.
0083  . C. ;ãDR°ÈÀ, Åà»Ë;ä
0084  +. DC 06-B4 DR_REP_STOP
0085  +. DC 01-29 DHU_S/X_VC4_OFF
0086  C.          çç[HK1_REP_STA/STP]           EQ      STOP
0087  C.          çç[HK1_S_VC4_ON/OFF]          EQ      OFF
0088  C.          çç[HK1_X_VC4_ON/OFF]          EQ      OFF
0089  C.
0090  C. ;ãXÅ+¿øµ;OFF;ä
0091  +. DC 03-85 TCIA_XMOD_OFF
0092  M. WAIT_SEC 1
0093  +. DC 03-B5 TCIA_XPA_OFF
0094  C.          çç[HK1_XMOD_ON/OFF]           EQ      OFF
0095  C.          çç[HK1_XPA_ON/OFF]            EQ      OFF
```



(a) Spacecraft Operation Procedure (real-commands)

```

main-571 2012-01-04 12:03:56 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ë¿ðÁð•µ°È»ÍxÁÇçÁYçYÁY×Yí;¼YÉj;ËÈèµ•ííË;ËÈÈ¼°ÇÓñ•ñ¿¼l¹çñí;çÁ®. ùñ¹ñÈñBñÇÁ+¿®ñ•ñËñññ³ñÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Upload DPL table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 C. YçYÁY×Yí;¼YÉñîÁ°ñËSTS_CHKñðOFFñËñ¹ñè
0025 C.
0026 . S. RAM ram-271:MDP_DPL
0027 ( )
0028 C.
0029 . C. < Dump RAMID=MDP_DPL >
0030 +. DC 07-F0 MDP_DUMP_FGTBL
0031 BC (82 07 00 38 b8 00 40)
0032 C. -----
0033 C. MDP_DPL verify = OK [ ]
0034 C. -----
0035 C.
0036 C. STS_CHKñðONñËñ¹ñè
0037 C.
0038 . C. < Update MDP DSC PAR1 >
0039 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0040 BC (4c)
0041 C. MDP_CMD_CODE = F04C0700 [ ]
0042 C. MDP_CMD_CNT (count-up 1) [ ]
0043 C. -----
0044 C.
0045 . C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR ____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 08)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 08 20)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0f 80 80 06 06)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 10 80 80 08 08)
0088 + DC 07-F0 MDP_XRT_FLD_ENA
0089 BC (d8)
0090 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091 BC (c8)
0092 + DC 07-F0 MDP_XRT_AEC_RESET
0093 BC (d0)
0094 + DC 07-F0 MDP_XRT_ARS_DIS
0095 BC (d5)

```

```

0096 + DC 07-F0 MDP_XRT_FLD_RESET
0097 BC (da)
0098 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0099 BC (c4 0f)
0100 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0101 BC (c5 03)
0102 . C. ----- Success Verify ? OK / NG ____
0103 C.
0104 C.
0105 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0106 C.
0107 +. DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 +. TI 2012-01-04 09:28:02.0
0110 DC 07-F0 MDP_XRT_MODE_OBSV
0111 BC (c2)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C. ***** XRT END *****
0115 C.
0116 . C. ***** MDP 'úÃîâî»ö¼ÝðÊÂð¹¹æDCBC•x²è *****
0117 C. (¼â°îÿÓÿÄÿÈÿÞÿËÿÁÿçÿÈæ¹¼¹¼¹¼»Û¹¹æ)
0118 . S. DC-BC dcbc-402:DCBC
0119 (MDP_known_event)
0120 C.
0121 C.
0122 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑæÉ´Ø¹¹æDCBC•x²è *****
0123 . S. DC-BC dcbc-153:DCBC
0124 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125 C.
0126 C.
0127 . C. ;ãLOSÿÁÿ§ÿÄÿÿ-¼Å»Û;ä
0128 C.
0129 . C. ***** LOS *****
0130 C.

```

Jan 04, 12 12:04

XRT\_OGLIST\_0355.chk

Page 1/3

\*\*\* OP Sequence for XRT \*\*\*

2012/01/04	09:38:54.0	XRT_CTRL_MANU_439_OG [0x1b7]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/04	09:39:00.0	AOCS_OrE-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2012/01/04	09:41:26.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/01/04	09:41:46.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/01/04	09:41:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/01/04	09:41:50.0	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/01/04	09:41:52.0	XRT_ARS_DIS_431_OG [0x1af]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/01/04	09:41:54.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/01/04	09:41:56.0	XRT_QT_PROG_SET_405_OG [0x195]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01			
2012/01/04	09:41:58.0	XRT_FL_PROG_SET_414_OG [0x19e]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2012/01/04	09:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/01/04	10:46:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/04	10:46:02.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/01/04	10:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/01/04	10:48:30.0	XRT_Custom_418_OG [0x1a2]						
2012/01/04	10:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/01/04	10:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/01/04	14:31:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/04	14:31:02.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/01/04	14:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/01/04	14:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/01/04	14:41:00.0	XRT_Custom_418_OG [0x1a2]						
2012/01/04	14:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/01/04	16:05:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/04	16:05:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/01/04	16:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/01/04	16:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/01/04	16:29:00.0	XRT_Custom_418_OG [0x1a2]						
2012/01/04	16:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/01/04	17:42:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/04	17:42:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/01/04	17:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/01/04	17:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/01/04	18:05:24.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/04	18:05:26.0	XRT_FOCUS_POSITION_401_OG [0x191]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/01/04	18:05:30.0	AOCS_OrE-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2012/01/04	18:05:46.0	XRT_FLD_DIS_402_OG [0x192]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/01/04	18:05:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/01/04	18:05:50.0	XRT_ARS_DIS_438_OG [0x1b6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/01/04	18:08:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2012/01/04	18:08:30.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/01/04	18:15:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/01/04	18:15:32.0	XRT_FOCUS_POSITION_401_OG [0x191]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/01/04	18:15:52.0	XRT_FLD_DIS_402_OG [0x192]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/01/04	18:15:54.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/01/04	18:15:56.0	XRT_ARS_DIS_438_OG [0x1b6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/01/04	18:18:34.0	XRT_QT_PROG_SET_445_OG [0x1bd]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06			

Jan 04, 12 12:04

## XRT\_OGLIST\_0355.chk

Page 2/3

2012/01/04	18:18:36.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/04	18:35:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/04	18:35:30.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00
2012/01/04	18:37:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/01/04	18:38:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/01/04	18:38:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/01/04	18:38:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/01/04	18:38:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/04	18:38:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/04	18:38:26.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2012/01/04	18:38:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/01/04	18:38:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/04	19:19:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/04	19:19:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/04	19:19:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/04	19:22:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/04	19:42:30.0	XRT_Custom_418_OG [0x1a2]				
2012/01/04	19:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/04	20:56:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/04	20:56:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/04	20:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/04	20:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/04	21:20:00.0	XRT_Custom_418_OG [0x1a2]				
2012/01/04	21:21:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/04	22:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/04	22:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/04	22:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/04	22:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/04	22:55:00.5	XRT_Custom_418_OG [0x1a2]				
2012/01/04	22:56:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/05	00:11:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/05	00:11:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/05	00:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/05	00:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/05	00:19:30.0	XRT_Custom_418_OG [0x1a2]				
2012/01/05	00:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/05	01:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/05	01:35:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/05	01:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/05	01:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/05	01:54:00.0	XRT_Custom_418_OG [0x1a2]				
2012/01/05	01:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/05	03:10:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/05	03:10:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/05	03:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/05	03:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/05	03:31:00.0	XRT_Custom_418_OG [0x1a2]				
2012/01/05	03:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/05	04:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/05	04:40:02.0	XRT_FLD_RESET_412_OG [0x19c]				

Jan 04, 12 12:04

## XRT\_OGLIST\_0355.chk

Page 3/3

2012/01/05	04:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/05	04:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/05	05:08:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/05	05:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/05	05:52:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/05	05:52:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/01/05	05:52:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00
2012/01/05	05:52:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/01/05	05:52:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/01/05	05:52:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/05	05:55:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2012/01/05	05:55:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/05	06:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/05	06:00:02.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/01/05	06:00:22.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/01/05	06:00:24.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/01/05	06:00:26.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/05	06:03:04.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2012/01/05	06:03:06.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/05	06:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/05	06:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/05	06:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/05	06:22:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00
2012/01/05	06:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/05	06:46:30.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/05	06:49:02.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/01/05	06:49:22.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/01/05	06:49:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/01/05	06:49:26.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/01/05	06:49:28.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/05	06:49:30.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/05	06:49:32.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2012/01/05	06:49:34.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/01/05	06:49:36.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/05	08:01:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/05	08:01:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/05	08:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/05	08:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/05	08:23:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/05	08:24:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/05	09:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/05	09:42:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00