

Period: 2012/01/10 09:20:00 - 2012/01/14 10:42:00

Period: 2012/01/10 09:20:00 - 2012/01/14 10:42:00

XOB #18BB: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 512x512 at 1064 1048 (all), 60s cad															
Term			Pointing (x, y)					Comment							
01/10 15:33:00 - 01/10 17:42:54			Track (368.9, 245.5) @ 01/10 15:30:00					Track AR11391							
01/10 17:56:00 - 01/11 05:49:54			Track (389.2, 245.0) @ 01/10 17:53:00					Cont,							
01/12 07:12:00 - 01/12 09:03:00			Track (673.1, 233.1) @ 01/12 07:09:00					Track AR11391							
PROG= 01 Inf.-time(s)															
Subr= 1			1-time(s)		2.0sec										
Seqn= 17			1-time(s)		2.0sec										
└ Open/Ti-poly			Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)		Q=98	0	0	2.0sec
└ Open/G-band			Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)		Q=98	0	0	2.0sec
Seqn= 53			4-time(s)		2.0sec										
└ Al-poly/Open			thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec
└ Open/Ti-poly			Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec
└ thin-Be/Open			med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec
└ Open/thick-Al			Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec
Subr= 2			1-time(s)		2.0sec										
Seqn= 81			25-time(s)		60.0sec										
└ thin-Be/Open			Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec
└ Open/Ti-poly			Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	15.0sec
└ thin-Be/Open			Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec
└ Open/Ti-poly			Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	15.0sec
└ thin-Be/Open			Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec
└ Open/Ti-poly			Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	15.0sec
└ thin-Be/Open			Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	2.0sec
└ Open/Ti-poly			Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	15.0sec
Default Filter			Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - All/mesh, Ti/Poly-long		
Term	Pointing (x, y)	Comment
01/12 06:32:00 - 01/12 06:38:54	Fixed (-528.4, -528.4)	XRT post bakeout quadrant obs 1/4
PROG= 20	1-time(s)	
└ Subr= 1	1-time(s)	12.0sec

XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long																		
Term			Pointing (x, y)						Comment									
01/12 07:02:00 - 01/12 07:08:54			Fixed (-528.4, 528.4)						4/4									
PROG= 17			1-time(s)															
Subr= 1			1-time(s)				12.0sec											
Seqn= 40			1-time(s)				12.0sec											
			Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)				Q=90	0	0	2.0sec
			Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)				Q=90	0	0	2.0sec
			Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)				Q=98	0	0	2.0sec
			Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)				Q=98	0	0	2.0sec
Subr= 2			1-time(s)				2.0sec											
Seqn= 93			2-time(s)				2.0sec											
			Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
			Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)				Comp.	AEC Buffer	Interval	

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XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops														
Term		Pointing (x, y)				Comment								
01/10 09:46:30 - 01/10 15:29:54		Fixed (-10.0, 885.0)				# OP start + 10min HOP206 for 6hr								
01/10 15:33:00 - 01/10 17:42:54		Track (368.9, 245.5) @ 01/10 15:30:00				Track AR11391								
01/10 17:56:00 - 01/11 05:49:54		Track (389.2, 245.0) @ 01/10 17:53:00				Cont,								
01/12 07:12:00 - 01/12 09:03:00		Track (673.1, 233.1) @ 01/12 07:09:00				Track AR11391								
PROG= 03 12-time(s) └ Subr= 1 45-time(s) 10.0sec └ Seqn= 20 1-time(s) 2.0sec └ thin-Be/Open med-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec └ Seqn= 63 1-time(s) 2.0sec └ med-Al/Open med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec └ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec └ Seqn= 77 1-time(s) 2.0sec └ Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec														

└ Subr= 21-time(s)10.0sec

└ Seqn= 901-time(s)2.0sec

└ Open/G-bandOpen/G-bandopenSafeNorm63msObs1x1384x384 (1024, 1024)Q=98002.0sec

└ Open/thick-AlOpen/thick-AlcloseSafeDark1.00sObs1x1384x384 (1024, 1024)Q=98002.0sec

└ Open/thick-AlOpen/thick-AlcloseSafeDark1.00sObs2x2512x512 (1024, 1024)Q=98002.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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Active Region Search

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NOT USED

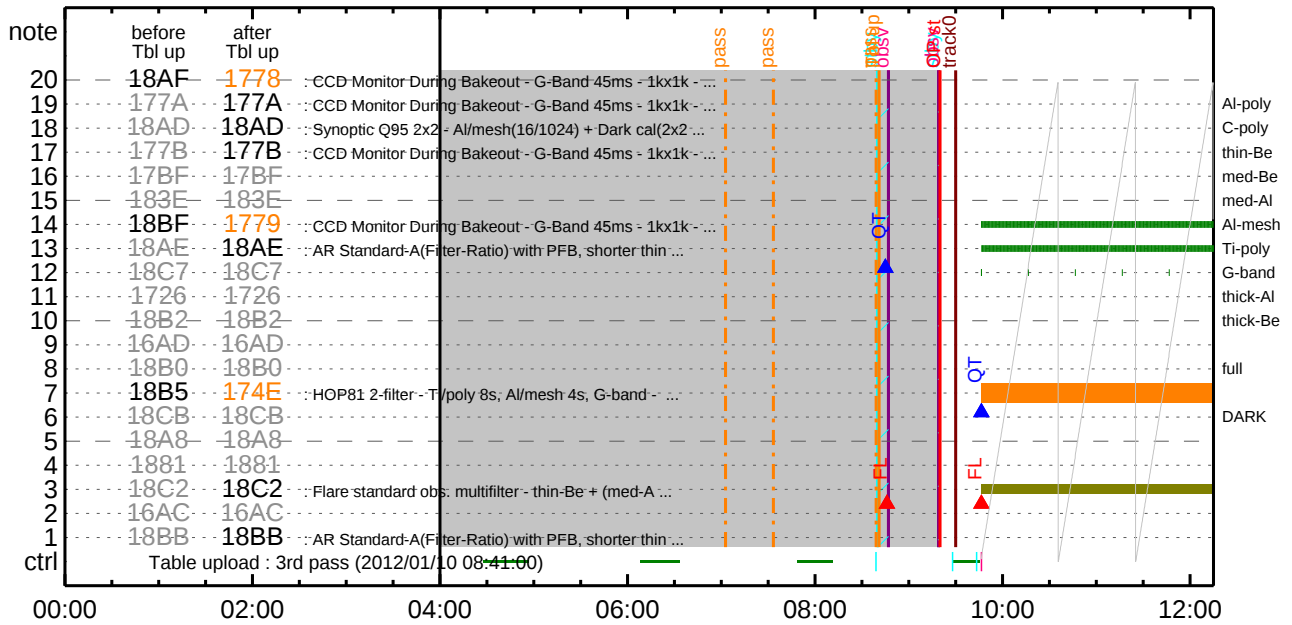
* * * * *

Flare Detection

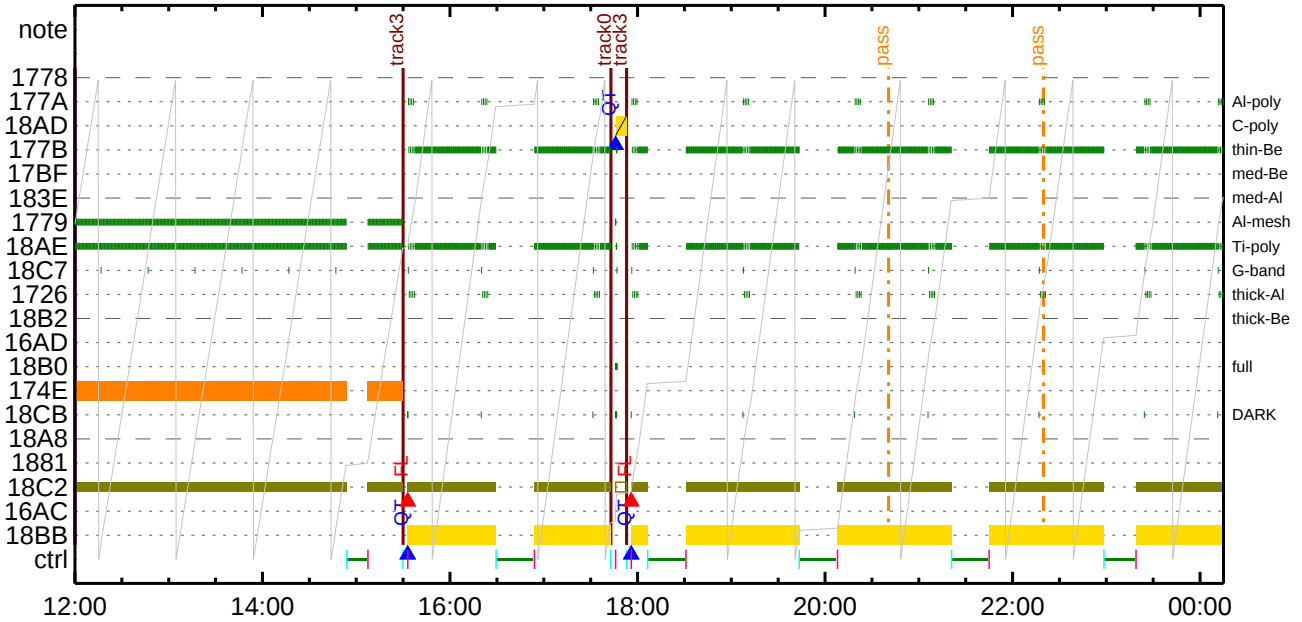
* * * * *

FLD Patrol											
Term			Pointing (x, y)					Comment			
01/10 17:55:46 - 01/11 05:50:16			Track (389.2, 245.0) @ 01/10 17:53:00					Cont,			
01/12 07:11:46 - 01/14 10:42:00			Track (673.1, 233.1) @ 01/12 07:09:00					Track AR11391			
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

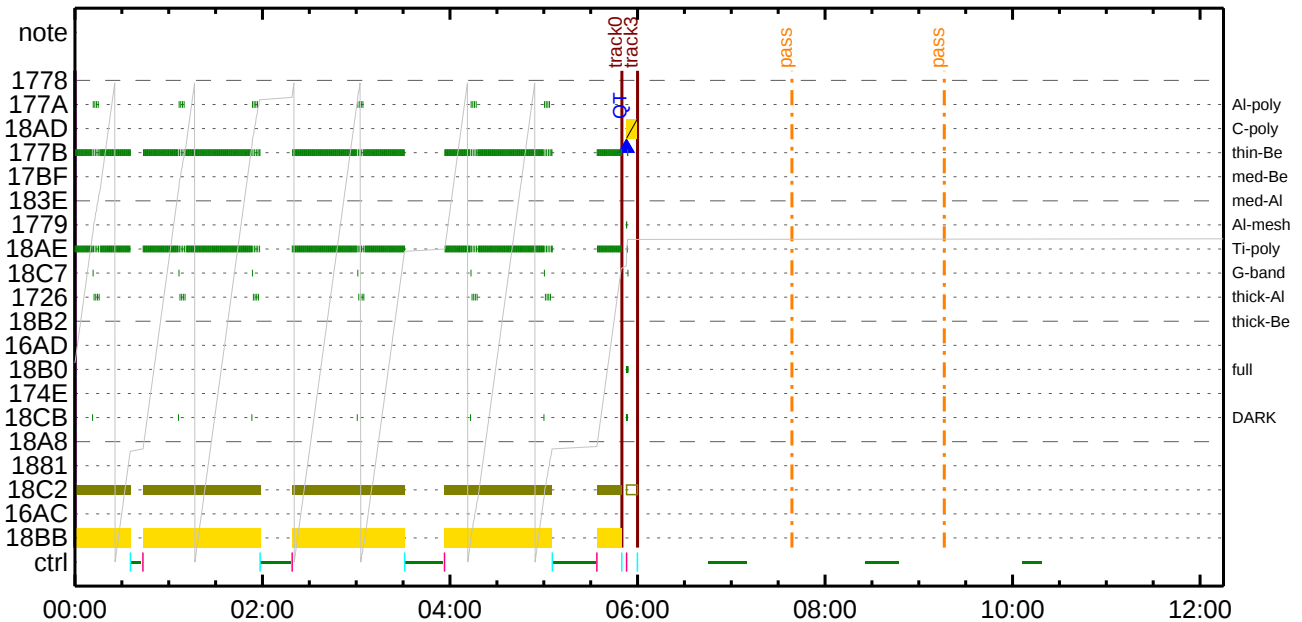
CMDI #0364 2012/01/10



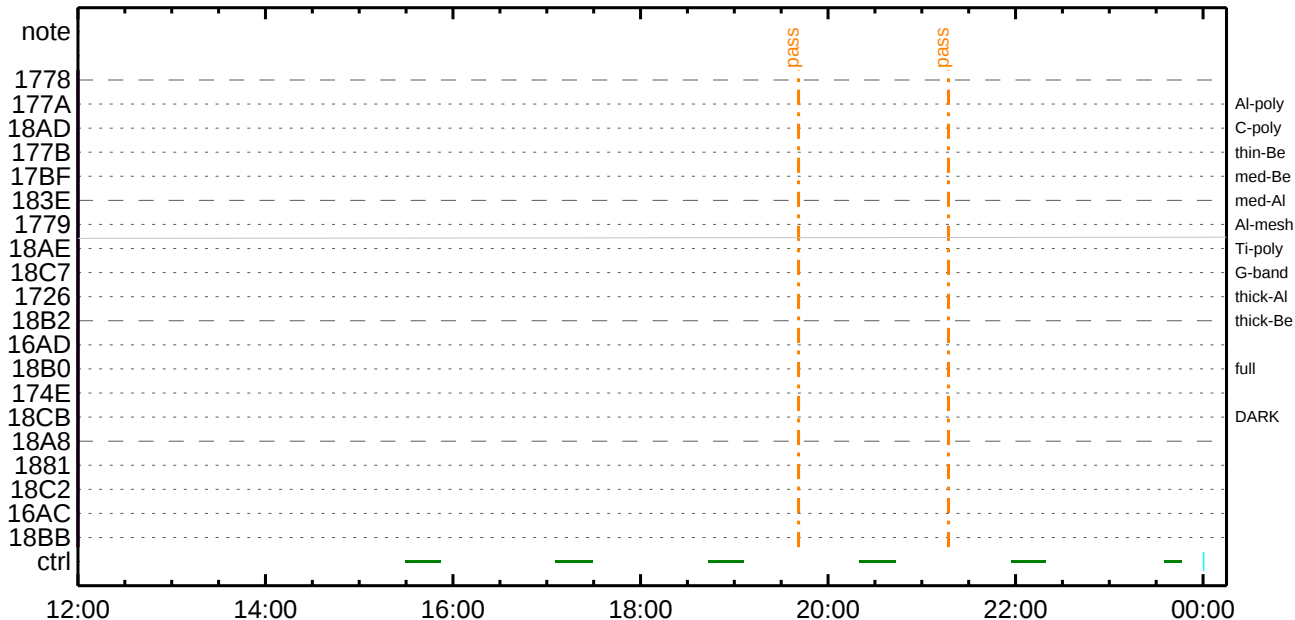
CMDI #0364 2012/01/10



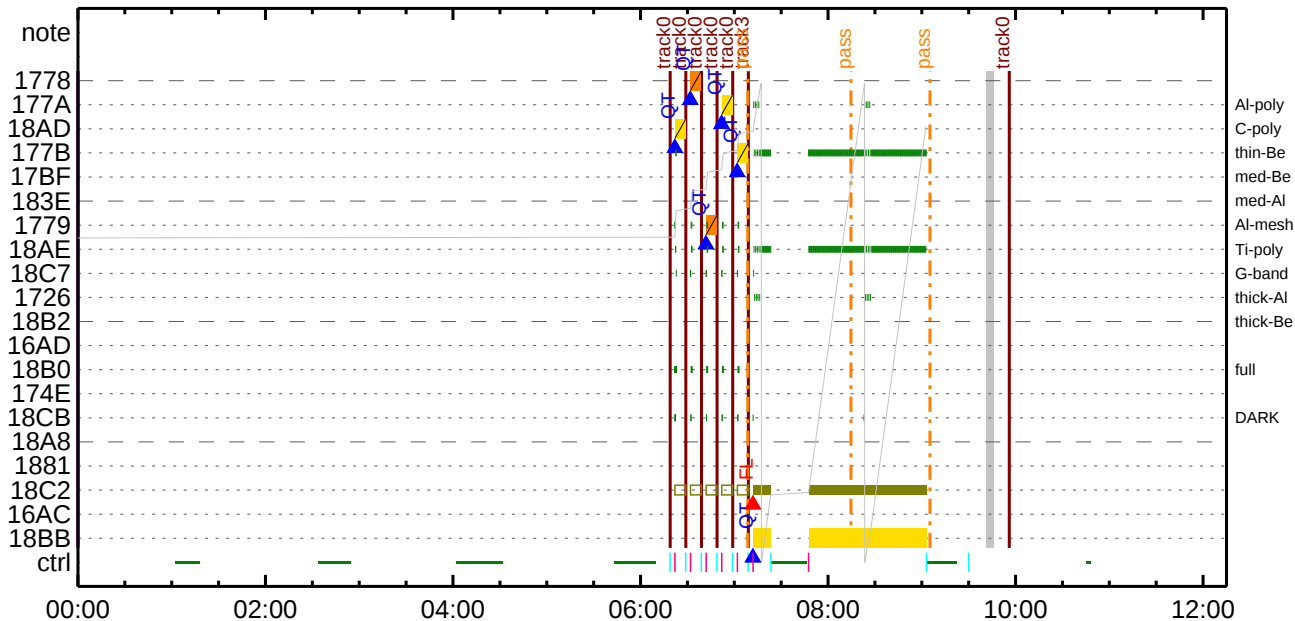
CMDI #0364 2012/01/11



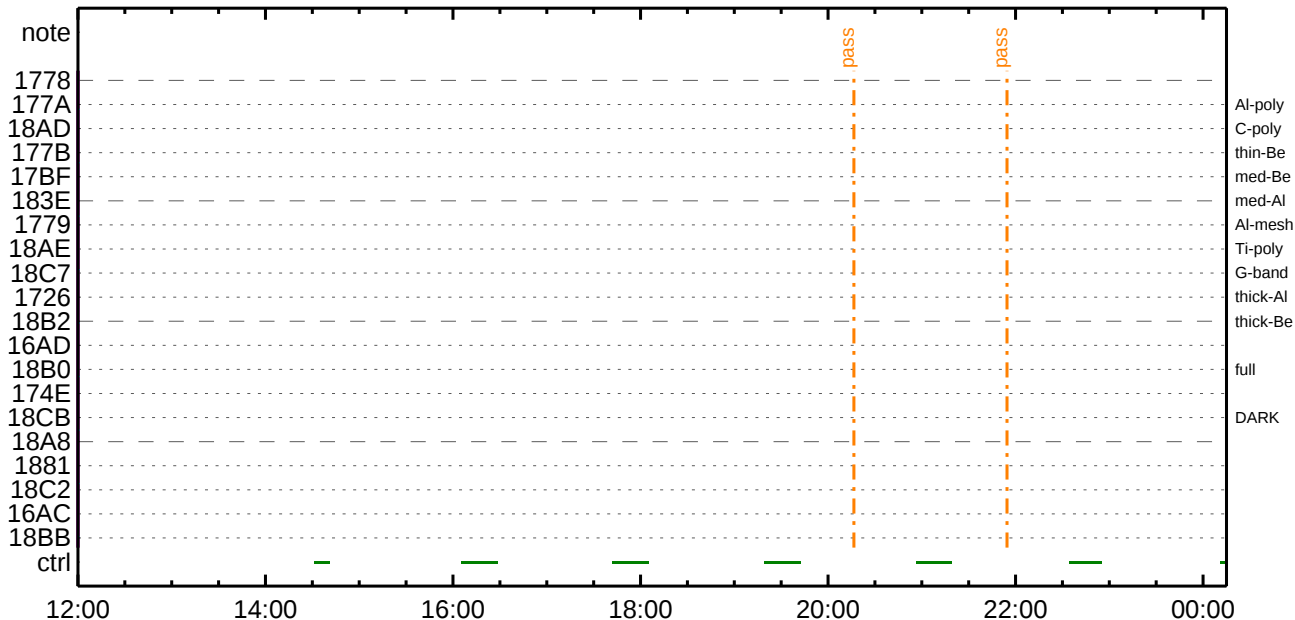
CMDI #0364 2012/01/11



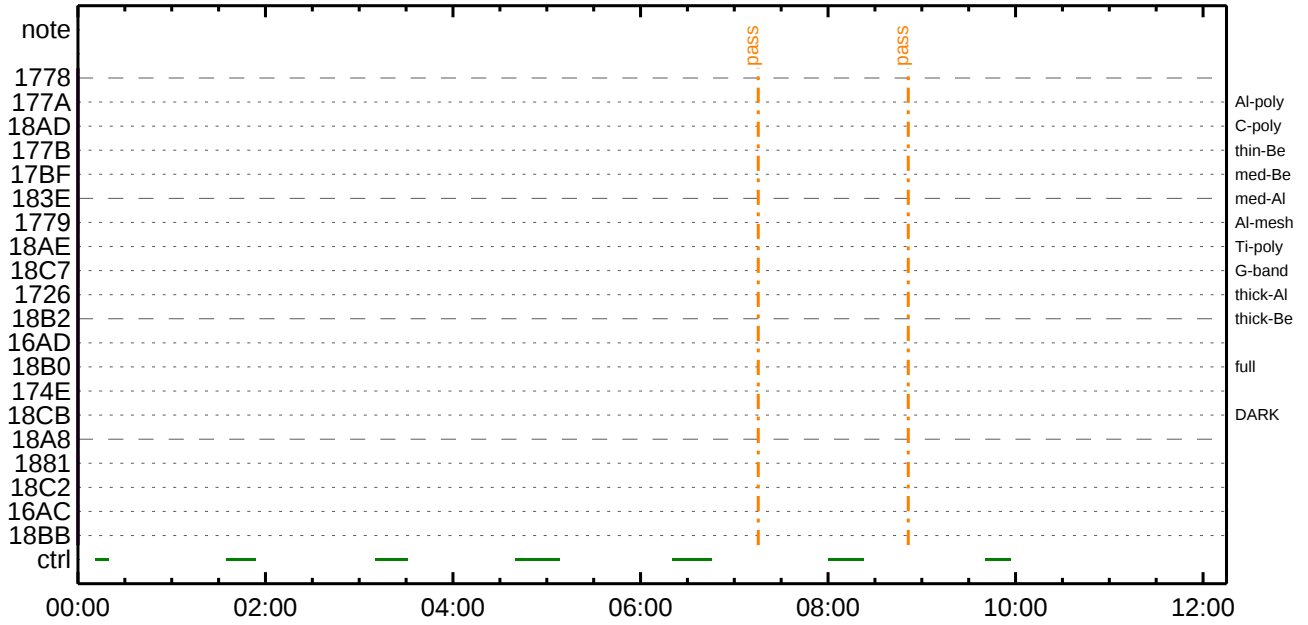
CMDI #0364 2012/01/12



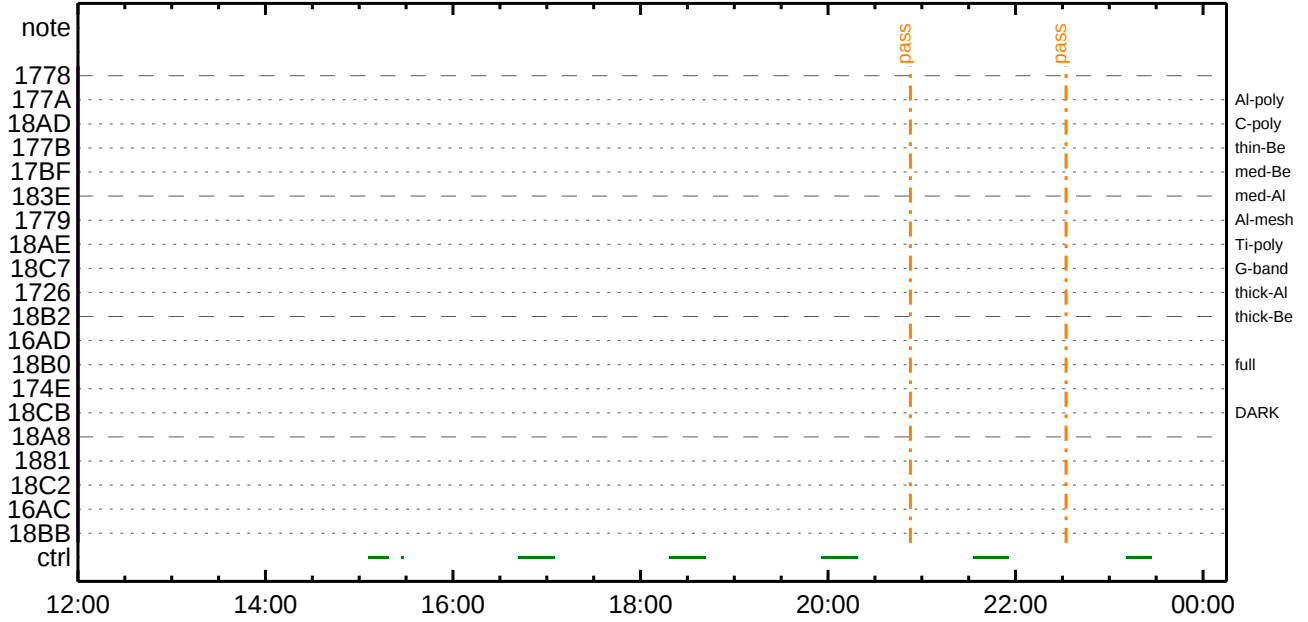
CMDI #0364 2012/01/12



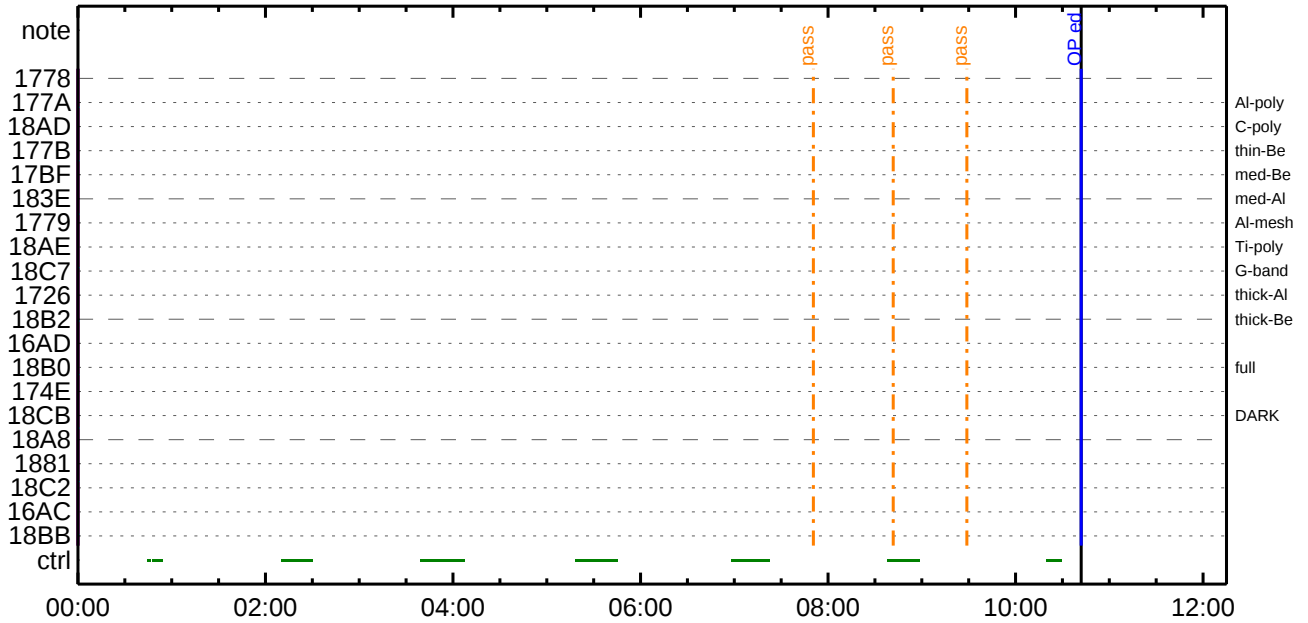
CMDI #0364 2012/01/13



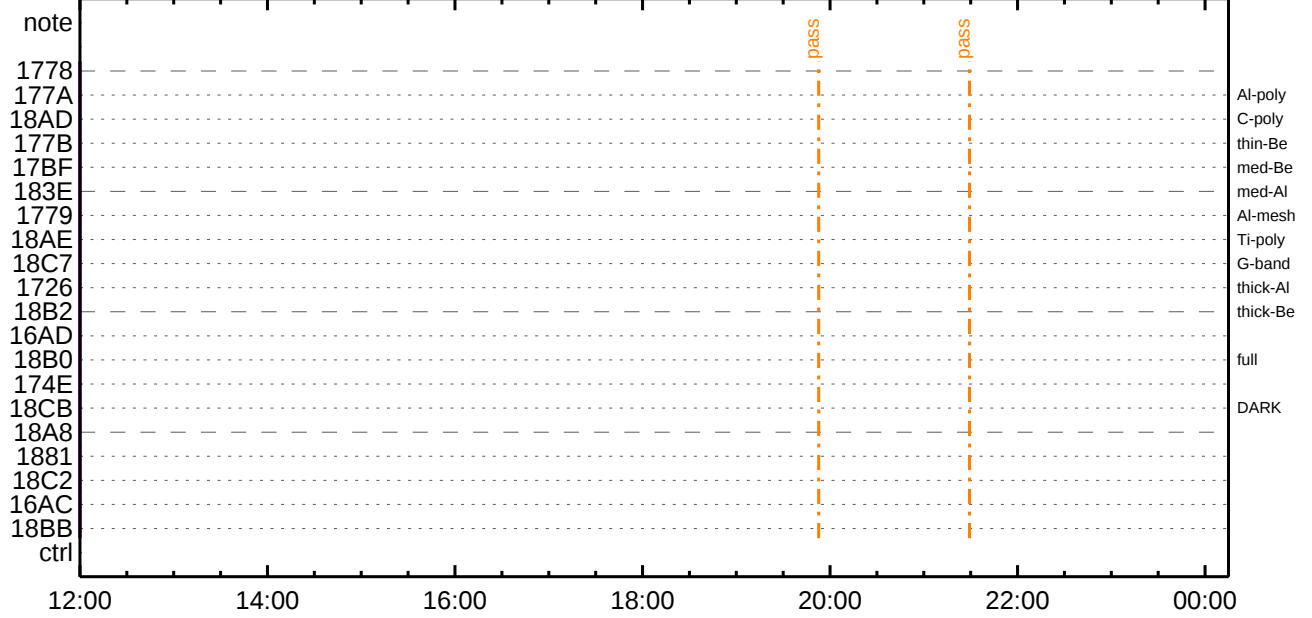
CMDI #0364 2012/01/13



CMDI #0364 2012/01/14



CMDI #0364 2012/01/14



Line	Command	Parameter	Condition	Count
0096	C.	0B0CjCSET0EDUMP0IÆ±°i¥Ñ¹0Ç¹00 0³0ÈjE		
0097	C.			
0098	C.	TI¥³¥p¥ó¥É00ÀDİç(UT)		
0099	+	TI 2012-01-10 09:15:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	00[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.			
0103	+	TI 2012-01-10 09:15:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	00[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.			
0107	+	TI 2012-01-10 09:15:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	00[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.			
0111	+	TI 2012-01-10 09:19:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	00[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.			
0115	C.	°Ê²¼0IÄê%IİÑ0I¥Å\$¥Ä¥~¹àİÜ		
0116	C.	00[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	00[HK1_TI_CMD_NUM]	EQ	4
0118	C.	00[HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	00[HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.			
0121	C.	*****		
0122	C.	TIİİ°è¥Ä¥ó¥x		
0123	C.	*****		
0124	C.			
0125	C.	TI_TBL(0x03AB00-0x03AEFFj\$ 1024byte)		
0126	+	DC 01-23 DHU_DMA_DUMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	00[HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	00[HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	00[HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	00[HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	00[HK1_DMA_DUMP_PIM]	EQ	DHU
0133	+	DC 01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	00[HK1_PKT_FORM_NO]	EQ	7
0136	C.	00[HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	00[HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	00[HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	00[HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.			
0141	C.	¥Ä¥ó¥x½¹aİ»00³İÇ\$		
0142	C.	00[HK1_DMP_CHK_FLG]	EQ	NON
0143	C.			
0144	C.	RAM ID=TI_TBL0I%Ê¹Ç•è²İOK00³İÇ\$		
0145	C.			
0146	C.	DHU¥âj¼¥ÉjÊ¼ý½, ¥İj¼¥ÉjÊ00İâ0¹		
0147	+	DC 01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	00[HK1_PKT_FORM_NO]	EQ	2
0150	C.	00[HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	00[HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	00[HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.			
0154	C.	*****		
0155	C.	SOT TI command set		
0156	C.	*****		
0157	C.	Execute, after the success of OP upload.		
0158	+	TI 2012-01-10 09:19:16.0		
0159	DC	07-F0 MDP_SOT_MODE_STBY		
0160	BC	(41)		
0161	C.	-----		
0162	C.	HK1_TI_CMD_NUM = 1 CNTUP []		
0163	C.	-----		
0164	C.	***** SOT END *****		
0165	C.	Stop EIS observation and temporarily disable EIS mode changes		
0166	C.			
0167	C.			
0168	C.	***** Start EIS operation (TI set) *****		
0169	C.	Execute, after the success of OP upload.		
0170	C.	Set EIS TI-commands		
0171	+	TI 2012-01-10 09:19:30.0		
0172	DC	07-FC EIS_MODE_MANU		
0173	BC	(21 02)		
0174	+	TI 2012-01-10 09:19:40.0		
0175	DC	07-FC EIS_MODE_CHG_DIS		
0176	BC	(22)		
0177	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0178	C.	***** End EIS operation (TI set) *****		
0179	C.			
0180	C.			
0181	C.			
0182	C.	***** XRT START *****		
0183	C.	Execute, after the success of OP upload.		
0184	+	TI 2012-01-10 09:19:00.0		
0185	DC	07-F0 MDP_XRT_MODE_STBY		
0186	BC	(c3)		
0187	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0188	C.			
0189	C.			

0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥Ð¥¹•İ Daily±¿İÑ¤Ė´0¤¹¤ĖDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ĩăLOS¥Á¥S¥Ā¥¼Ā»Üĭă
0203 C.
0204 . C. ***** LOS *****
0205 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-583 2012-01-10 11:05:43 91 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Ä»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äi¿¼¿¼Ä»µ°Ä»I×AÇ¼I¥C¥A¥×¥íj¼¥ÉjÉÉè¼µ•ííÉjÉ¼É¼°ÇÖ¼•¼¿¼i¹¿¼IjÇÀ®, Ü¼¹¼è¼¼¼ÇÁ÷¿®¼•¼É¼¼¼³¼Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 C.
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCS Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 7474701.1 +- 1.0 (s) [ ]
0052 C.
0053 C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_MANU
0056 BC (21 02)
0057 . C. Verify EIS in MANUAL mode
0058 . C. Estimated OBSTBL upload time is 17s
0059 C. *****
0060 C. EIS START OBSTBL LOAD
0061 C. *****
0062 . S. RAM ram-820:EIS_OBSTBL
0063 C.
0064 +. DC 07-FC EIS_DUMP_OBSTBL
0065 BC (07 07 07 00 00 70 00)
0066 C.
0067 C. Execute, after the success of OBSTBL upload.
0068 C. Set EIS TI-commands
0069 +. TI 2012-01-10 09:19:50.0
0070 DC 07-FC EIS_MODE_CHG_ENA
0071 BC (20)
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0073 C. *****
0074 C. EIS END OBSTBL LOAD
0075 C. *****
0076 C.
0077 . C. ***** MDP ´ÜÄI¼I»Ö¼Y¼ÉÄ¼¹¼èDCBC•x²è *****
0078 C. (¼Ä°I¥Ó¥A¥É¥P¥É¥Ä¥C¥è¼É¼¼¼¼Ä»Ü¼¹¼è)
0079 . S. DC-BC dcbc-402:DCBC
0080 (MDP_known_event)
0081 C.
0082 C.
0083 . C. ***** ¥Ð¥¹•I Daily±¿IÑ¼É´Ø¼¹¼èDCBC•x²è *****
0084 . S. DC-BC dcbc-153:DCBC
0085 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086 C.
0087 C.
0088 . C. iãLOS¥Á¥S¥Ã¥¼Ä»Üjã
0089 C.
0090 . C. ***** LOS *****
0091 C.
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0096 C.
0097 C.
0098 C. *****
0099 C. SOT table upload
0100 C. *****
0101 C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 C. <Upload FG Observation Table>
0109 S. RAM ram-266:MDP_OBS_F
0110 ( )
0111 C.
0112 C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. < Upload DPL table >
0120 C.
0121 C. ¥¢¥Ã¥×¥í¼¥É¤ÎÁ°¤ĖSTS_CHK¤òOFF¤Ė¤¹¤Ė
0122 C.
0123 S. RAM ram-271:MDP_DPL
0124 ( )
0125 C.
0126 C. < Dump RAMID=MDP_DPL >
0127 +. DC 07-F0 MDP_DUMP_FGTBL
0128 BC (82 07 00 38 b8 00 40)
0129 C. -----
0130 C. MDP_DPL verify = OK [ ]
0131 C. -----
0132 C.
0133 C. STS_CHK¤òON¤Ė¤¹¤Ė
0134 C.
0135 C. < Update MDP DSC PAR1 >
0136 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0137 BC (4c)
0138 C. MDP_CMD_CODE = F04C0700 [ ]
0139 C. MDP_CMD_CNT (count-up 1) [ ]
0140 C. -----
0141 C.
0142 C.
0143 C. *****
0144 C. SOT TI command set
0145 C. *****
0146 C. Execute, after the success of TBL upload.
0147 +. TI 2012-01-10 09:19:18.0
0148 DC 07-F0 MDP_SOT_MODE_OBSV
0149 BC (40)
0150 C. -----
0151 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0152 C. -----
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C.
0157 +. DC 07-F0 MDP_XRT_CTRL_MANU
0158 BC (c1)
0159 +. DC 07-F0 MDP_XRT_MODE_STBY
0160 BC (c3)
0161 C. ----- Success Verify ? OK / NG____
0162 C.
0163 C. XRT Obs. Table Upload
0164 S. RAM ram-291:MDP_OBS_X
0165 ( )
0166 C.
0167 +. DC 07-F0 MDP_DUMP_XRTTBL
0168 BC (84 07 00 00 00 3a d4)
0169 C. ----- Comparison Check ? OK / ERR ____
0170 C.
0171 C.
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 01 b1 b1 04 04)
0174 +. DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 02 b1 b1 08 08)
0176 +. DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 03 b1 b1 08 08)
0178 +. DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 04 b1 b1 06 06)
0180 +. DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 05 85 83 06 06)
0182 +. DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 06 85 83 06 06)
0184 +. DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 07 85 83 08 08)
0186 +. DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 08 80 80 20 20)
0188 +. DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 09 80 80 20 08)
0190 +. DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 0a 80 80 08 20)
0192 +. DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 0b c0 c0 10 10)

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0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 0c 40 c0 10 10)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 0d 40 40 10 10)
0198 + DC 07-F0 MDP_XRT_ROI_SET
0199 BC (cd 0e c0 40 10 10)
0200 + DC 07-F0 MDP_XRT_ROI_SET
0201 BC (cd 0f 80 80 06 06)
0202 + DC 07-F0 MDP_XRT_ROI_SET
0203 BC (cd 10 80 80 08 08)
0204 + DC 07-F0 MDP_XRT_FLD_ENA
0205 BC (d8)
0206 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0207 BC (c8)
0208 + DC 07-F0 MDP_XRT_AEC_RESET
0209 BC (d0)
0210 + DC 07-F0 MDP_XRT_ARS_DIS
0211 BC (d5)
0212 + DC 07-F0 MDP_XRT_FLD_RESET
0213 BC (da)
0214 + DC 07-F0 MDP_XRT_QT_PROG_SET
0215 BC (c4 0d)
0216 + DC 07-F0 MDP_XRT_FL_PROG_SET
0217 BC (c5 03)
0218 . C. ----- Success Verify ? OK / NG ____
0219 C.
0220 C.
0221 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0222 C.
0223 + DC 07-F0 MDP_XRT_MODE_OBSV
0224 BC (c2)
0225 + TI 2012-01-10 09:19:02.0
0226 DC 07-F0 MDP_XRT_MODE_OBSV
0227 BC (c2)
0228 . C. ----- Success Verify ? OK / NG ____
0229 C.
0230 C. ***** XRT END *****
0231 C.
0232 . C. ***** MDP '0ÃÎ±Î»0%Ý±ĒÂð±±ēDCBC•x²è *****
0233 C. (%ã°î¥0¥Ã¥Ē¥ð¥Ē¥ã¥Ĉ¥ē±Ē½¼±¼Ã»Ů±±ē)
0234 . S. DC-BC dcbc-402:DCBC
0235 (MDP_known_event)
0236 C.
0237 C.
0238 . C. ***** ¥Ð¥¹•İ Daily±¿İŇ±Ē'0±±ēDCBC•x²è *****
0239 . S. DC-BC dcbc-153:DCBC
0240 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0241 C.
0242 C.
0243 . C. ¡ãLOS¥Ã¥$¥Ã¥¬¼Ã»Ů¡ã
0244 C.
0245 . C. ***** LOS *****
0246 C.
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*** OP Sequence for XRT ***

2012/01/10	09:28:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/10	09:28:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/10	09:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/10	09:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 b1 59 00 e5
2012/01/10	09:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/10	09:43:24.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/10	09:45:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/01/10	09:46:16.0	XRT_FLD_ENA_435_OG [0x1b3]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/01/10	09:46:18.0	XRT_FLRCTRL_ENA_403_OG [0x193]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/01/10	09:46:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/01/10	09:46:22.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/10	09:46:24.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/10	09:46:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2012/01/10	09:46:28.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/01/10	09:46:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/10	14:54:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/10	14:54:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/10	14:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/10	14:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/10	15:06:30.0	XRT_Custom_418_OG [0x1a2]			
2012/01/10	15:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/10	15:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/10	15:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/01/10	15:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/01/10	15:32:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/01/10	15:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/01/10	15:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/01/10	15:32:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/10	15:32:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/10	15:32:56.0	XRT_QT_PROG_SET_405_OG [0x195]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2012/01/10	15:32:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/01/10	15:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/10	16:29:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/10	16:29:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/10	16:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/10	16:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/10	16:53:00.0	XRT_Custom_418_OG [0x1a2]			
2012/01/10	16:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/10	17:42:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/10	17:42:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/01/10	17:43:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/01/10	17:43:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/01/10	17:43:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/01/10	17:43:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/10	17:45:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2012/01/10	17:46:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/10	17:52:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			

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2012/01/10	17:53:00.0	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCs_Or-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/01/10	17:55:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/01/10	17:55:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/01/10	17:55:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/01/10	17:55:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/01/10	17:55:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/10	17:55:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/10	17:55:56.0	XRT_QT_PROG_SET_405_OG [0x195]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2012/01/10	17:55:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/01/10	17:56:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/10	18:06:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/10	18:06:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/10	18:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/10	18:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/10	18:30:00.0	XRT_Custom_418_OG [0x1a2]			
2012/01/10	18:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/10	19:43:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/10	19:43:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/10	19:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/10	19:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/10	20:07:00.0	XRT_Custom_418_OG [0x1a2]			
2012/01/10	20:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/10	21:21:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/10	21:21:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/10	21:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/10	21:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/10	21:44:00.0	XRT_Custom_418_OG [0x1a2]			
2012/01/10	21:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/10	22:58:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/10	22:58:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/10	22:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/10	23:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/10	23:18:00.0	XRT_Custom_418_OG [0x1a2]			
2012/01/10	23:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/11	00:35:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/11	00:35:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/11	00:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/11	00:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/11	00:42:30.0	XRT_Custom_418_OG [0x1a2]			
2012/01/11	00:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/11	01:58:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/11	01:58:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/11	01:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/11	02:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/11	02:18:00.0	XRT_Custom_418_OG [0x1a2]			
2012/01/11	02:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/11	03:31:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/11	03:31:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/11	03:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2012/01/11	03:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/11	03:55:30.0	XRT_Custom_418_OG [0x1a2]			
2012/01/11	03:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/11	05:05:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/11	05:05:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/11	05:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/11	05:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/11	05:33:00.0	XRT_Custom_418_OG [0x1a2]			
2012/01/11	05:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/11	05:49:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/11	05:49:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/01/11	05:50:00.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/01/11	05:50:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/01/11	05:50:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/01/11	05:50:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/11	05:52:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2012/01/11	05:53:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/11	05:59:54.0	XRT_CTRL_MANU_429_OG [0x1ad]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/11	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/01/11	06:00:24.0	XRT_TCIB_XRT_S_HTR_A_ENA_442_OG [0x1ba]			
		TCIB_XRT_S_HTR_A_ENA	0	04-BC	
2012/01/11	08:00:24.0	XRT_Custom_444_OG [0x1bc]			
2012/01/11	10:00:24.0	XRT_Custom_444_OG [0x1bc]			
2012/01/11	12:00:24.0	XRT_Custom_444_OG [0x1bc]			
2012/01/11	14:00:24.0	XRT_Custom_448_OG [0x1c0]			
2012/01/11	14:00:34.0	XRT_Custom_444_OG [0x1bc]			
2012/01/11	16:00:34.0	XRT_Custom_444_OG [0x1bc]			
2012/01/11	18:00:34.0	XRT_Custom_444_OG [0x1bc]			
2012/01/11	20:00:34.0	XRT_Custom_444_OG [0x1bc]			
2012/01/11	22:00:34.0	XRT_Custom_427_OG [0x1ab]			
2012/01/12	00:00:12.0	XRT_Custom_448_OG [0x1c0]			
2012/01/12	00:00:22.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/12	00:00:24.0	XRT_TCIB_XRT_S_HTR_A_DIS_436_OG [0x1b4]			
		TCIB_XRT_S_HTR_A_DIS	0	04-C0	
2012/01/12	06:18:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/12	06:18:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/01/12	06:19:00.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/01/12	06:19:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/01/12	06:19:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/01/12	06:19:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/12	06:21:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2012/01/12	06:22:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/12	06:28:54.0	XRT_CTRL_MANU_430_OG [0x1ae]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/12	06:29:00.0	AOCS_Ore-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 2e f9 2e f9
2012/01/12	06:31:32.0	XRT_FOCUS_POSITION_416_OG [0x1a0]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/01/12	06:31:52.0	XRT_QT_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2012/01/12	06:31:54.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/01/12	06:31:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/01/12	06:31:58.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/12	06:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/12	06:38:54.0	XRT_CTRL_MANU_430_OG [0x1ae]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/12	06:39:00.0	AOCS_Ore-point_Start_5_OG [0x09b]			
		AOCU_NM	5	02-76	00 2e f9 d1 07
2012/01/12	06:41:32.0	XRT_FOCUS_POSITION_416_OG [0x1a0]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/01/12	06:41:52.0	XRT_QT_PROG_SET_446_OG [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2012/01/12	06:41:54.0	XRT_FLD_DIS_402_OG [0x192]			

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2012/01/12	06:41:56.0	XRT_FLRCTRL_DIS_433_0G [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/01/12	06:41:58.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/01/12	06:42:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/12	06:48:54.0	XRT_CTRL_MANU_430_0G [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/12	06:49:00.0	AOCS_ORe-point_Start_6_0G [0x09c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/12	06:51:32.0	XRT_FOCUS_POSITION_416_0G [0x1a0]	AOCU_NM	5	02-76	00 d1 07 d1 07
2012/01/12	06:51:52.0	XRT_QT_PROG_SET_417_0G [0x1a1]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/01/12	06:51:54.0	XRT_FLD_DIS_402_0G [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2012/01/12	06:51:56.0	XRT_FLRCTRL_DIS_433_0G [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/01/12	06:51:58.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/01/12	06:52:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/12	06:58:54.0	XRT_CTRL_MANU_430_0G [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/12	06:59:00.0	AOCS_ORe-point_Start_7_0G [0x09d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/12	07:01:32.0	XRT_FOCUS_POSITION_416_0G [0x1a0]	AOCU_NM	5	02-76	00 d1 07 2e f9
2012/01/12	07:01:52.0	XRT_QT_PROG_SET_432_0G [0x1b0]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/01/12	07:01:54.0	XRT_FLD_DIS_402_0G [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2012/01/12	07:01:56.0	XRT_FLRCTRL_DIS_433_0G [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/01/12	07:01:58.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/01/12	07:02:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/12	07:08:54.0	XRT_CTRL_MANU_439_0G [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/12	07:09:00.0	AOCS_ORe-point_Start_2_0G [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/12	07:11:26.0	XRT_FOCUS_POSITION_409_0G [0x199]	AOCU_NM	5	02-76	03 00 00 00 00
2012/01/12	07:11:46.0	XRT_FLD_ENA_411_0G [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/01/12	07:11:48.0	XRT_FLRCTRL_ENA_413_0G [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/01/12	07:11:50.0	XRT_AEC_RESET_443_0G [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/01/12	07:11:52.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/01/12	07:11:54.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/01/12	07:11:56.0	XRT_QT_PROG_SET_405_0G [0x195]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/12	07:11:58.0	XRT_FL_PROG_SET_414_0G [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2012/01/12	07:12:00.0	XRT_CTRL_AUTO_406_0G [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/01/12	07:23:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/12	07:23:32.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/12	07:23:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/12	07:26:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/12	07:46:30.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/12	07:47:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	XRT_Custom_418_0G [0x1a2]			
2012/01/12	09:03:00.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/01/12	09:03:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/01/12	09:03:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/01/12	09:06:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/01/12	09:30:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/01/12	09:56:00.0	AOCS_ORe-point_Start_3_0G [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00