XRT Timeline to be uploaded on 2012/02/02

Period: 2012/02/02 10:53:00 - 2012/02/07 10:41:00

* * * * * **Normal mode** * * * * *

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
02/03 00:13:00 - 02/03 00:19:54	Fixed (-528.4, -528.4)	* XRT Post-bakeout Four-Quadrant Pointings.

PROG= 18 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 38 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec
Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh,Ti/Poly -long

Term	Pointing (x, y)	Comment
02/03 00:23:00 - 02/03 00:29:54	Fixed (528.4, -528.4)	

PROG= 14 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 36 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec
Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
02/03 00:33:00 - 02/03 00:39:54	Fixed (528.4, 528.4)	

PROG= 19 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 39 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec
Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long

Term	Pointing (x, y)	Comment
02/03 00:43:00 - 02/03 00:49:54	Fixed (-528.4, 528.4)	

PROG= 17 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 40 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec
Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #18C7: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 30s cad

Term	Pointing (x, y)	Comment
02/03 00:53:00 - 02/03 05:39:24	Track (260.3, 397.8) @ 02/03 00:50:00	* AR 11410
02/04 06:13:00 - 02/04 08:25:00	Track (496.0, 387.3) @ 02/04 06:10:00	# Cont.

PROG= 11 Inf.-time(s)
Subr= 1 1-time(s) 2.0sec

Seqn= 19 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 96 4-time(s) 2.0sec													
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 15-time(s) 2.0sec													
Seqn= 42 1-time(s) 2.0sec													
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	7.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	7.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	7.0sec
Seqn= 43 1-time(s) 2.0sec													
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	7.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	7.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	7.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

XOB #18A8: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be(18

Term	Pointing (x, y)	Comment
02/03 05:42:30 - 02/03 05:49:24	Fixed (0.0, 0.0)	synoptic, shifted -20.5 min
02/03 17:45:30 - 02/03 17:52:24	Fixed (0.0, 0.0)	synoptic, shifted -17.5 min
02/04 06:03:00 - 02/04 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 05 1-time(s)													
Subr= 1 1-time(s) 12.0sec													
Seqn= 7 1-time(s) 4.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 8 1-time(s) 4.0sec													
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 3 1-time(s) 2.0sec													
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad

Term	Pointing (x, y)	Comment
02/03 05:52:30 - 02/03 09:27:30	Track (302.4, 396.6) @ 02/03 05:49:30	# Cont.
02/03 16:03:00 - 02/03 17:42:24	Track (386.0, 393.3) @ 02/03 16:00:00	* AR 11410.
02/03 17:55:30 - 02/04 05:59:54	Track (401.0, 392.6) @ 02/03 17:52:30	# Cont.

PROG= 13 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 19 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 96 4-time(s) 2.0sec													
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 62 15-time(s) 150.0sec													
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384

Term	Pointing (x, y)	Comment
02/03 09:53:00 - 02/03 15:59:54	Fixed (-14.0, 885.0)	* HOP 206, polar panorama, N pole.

PROG= 07 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 51 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

Subr= 2 30-time(s) 2.0sec

Seqn= 58 2-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

* * * * * Flare mode * * * * *

XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops

Term	Pointing (x, y)	Comment
02/03 00:53:00 - 02/03 05:39:24	Track (260.3, 397.8) @ 02/03 00:50:00	* AR 11410
02/03 05:52:30 - 02/03 09:27:30	Track (302.4, 396.6) @ 02/03 05:49:30	# Cont.
02/03 09:53:00 - 02/03 15:59:54	Fixed (-14.0, 885.0)	* HOP 206, polar panorama, N pole.
02/03 16:03:00 - 02/03 17:42:24	Track (386.0, 393.3) @ 02/03 16:00:00	* AR 11410.
02/03 17:55:30 - 02/04 05:59:54	Track (401.0, 392.6) @ 02/03 17:52:30	# Cont.
02/04 06:13:00 - 02/04 08:25:00	Track (496.0, 387.3) @ 02/04 06:10:00	# Cont.

PROG= 03 12-time(s)

Subr= 1 45-time(s) 10.0sec

Seqn= 20 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 63 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 77 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Subr= 2 1-time(s) 10.0sec

Seqn= 90 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

* * * * * Active Region Search * * * * *

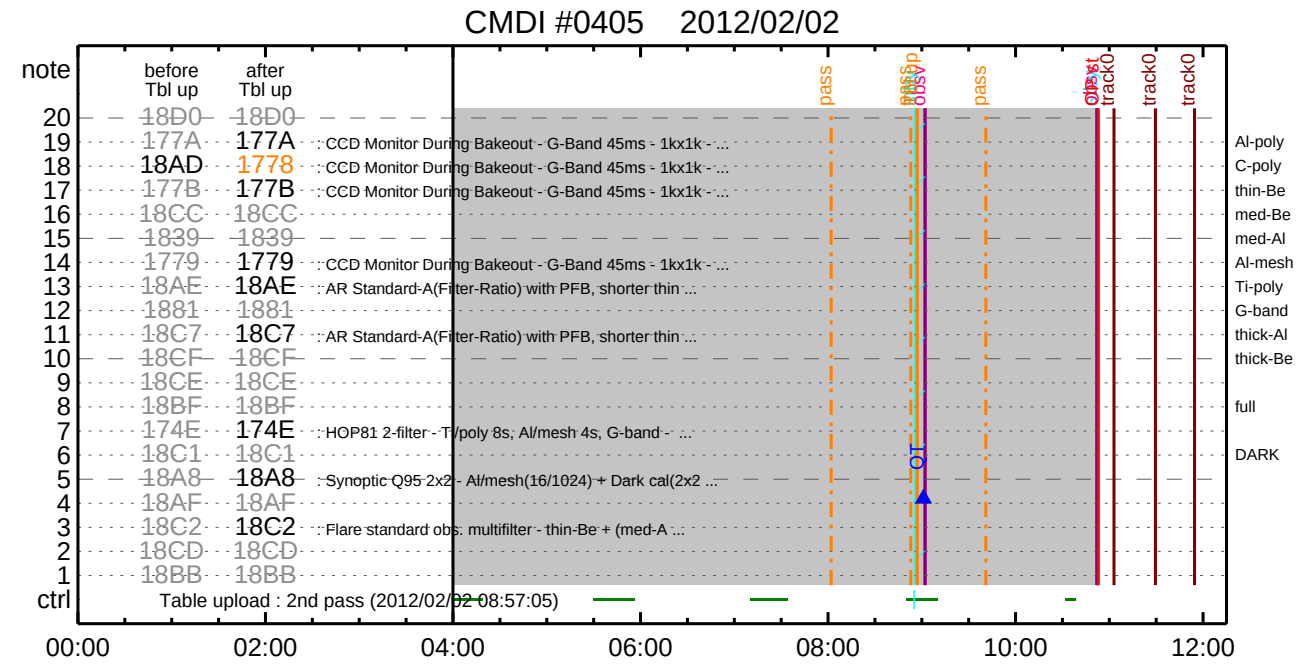
NOT USED

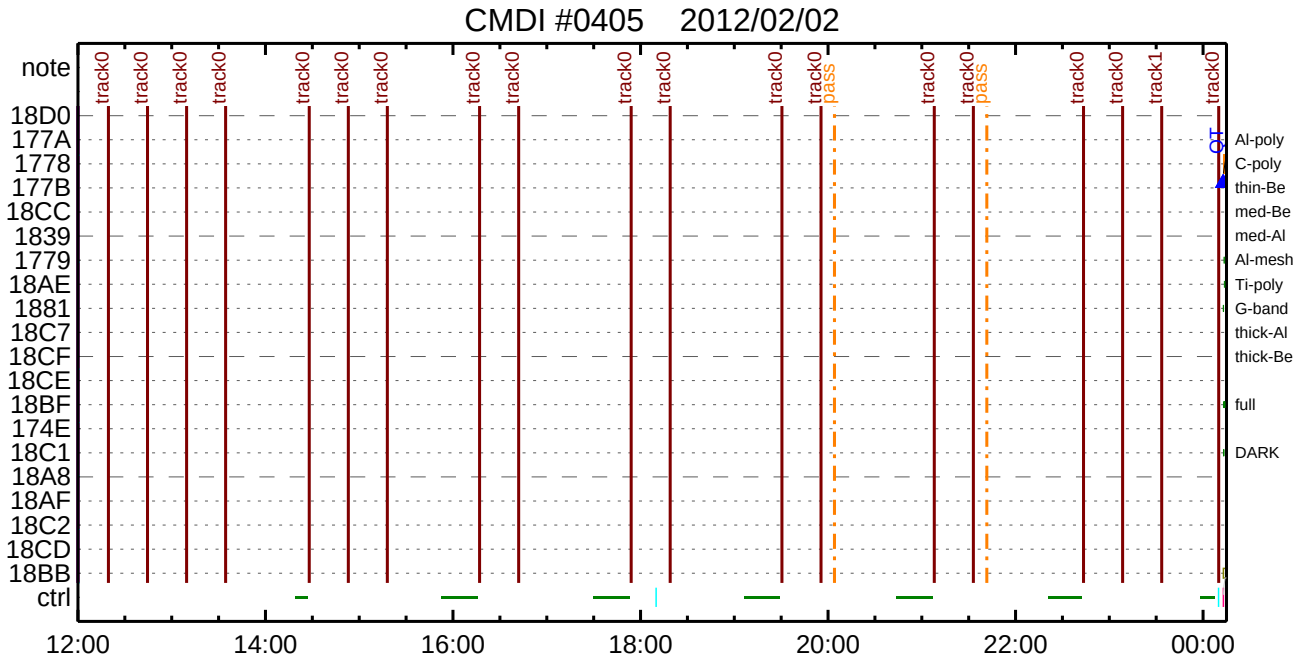
* * * * * Flare Detection * * * * *

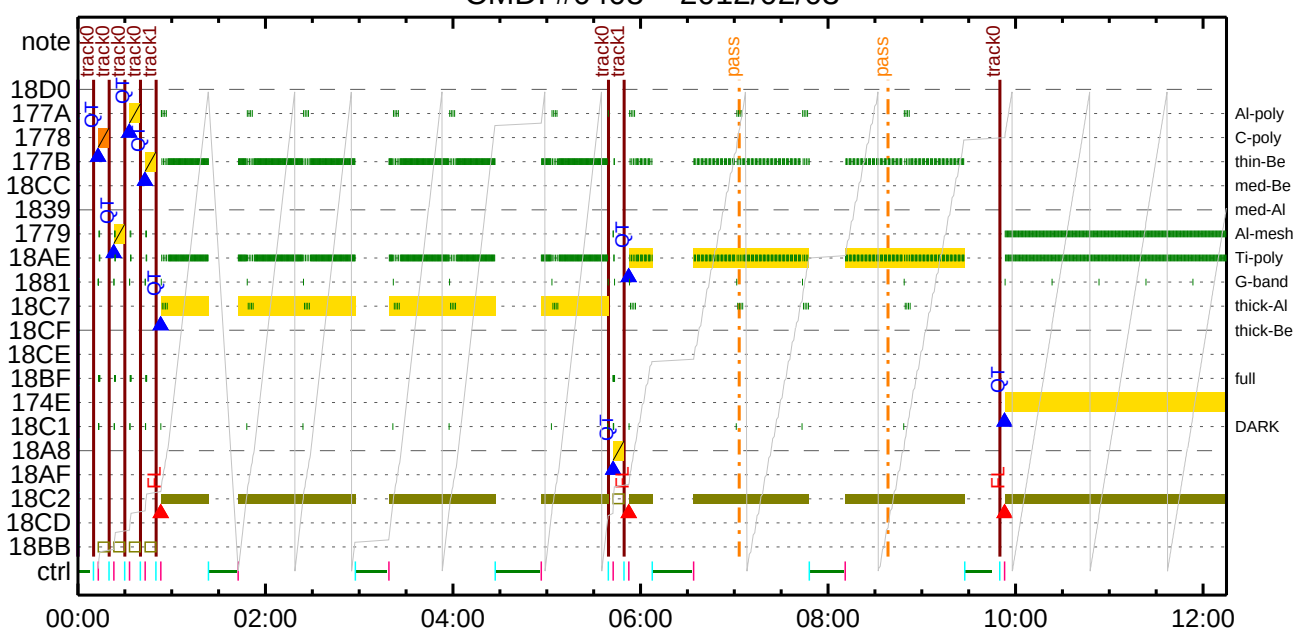
FLD Patrol

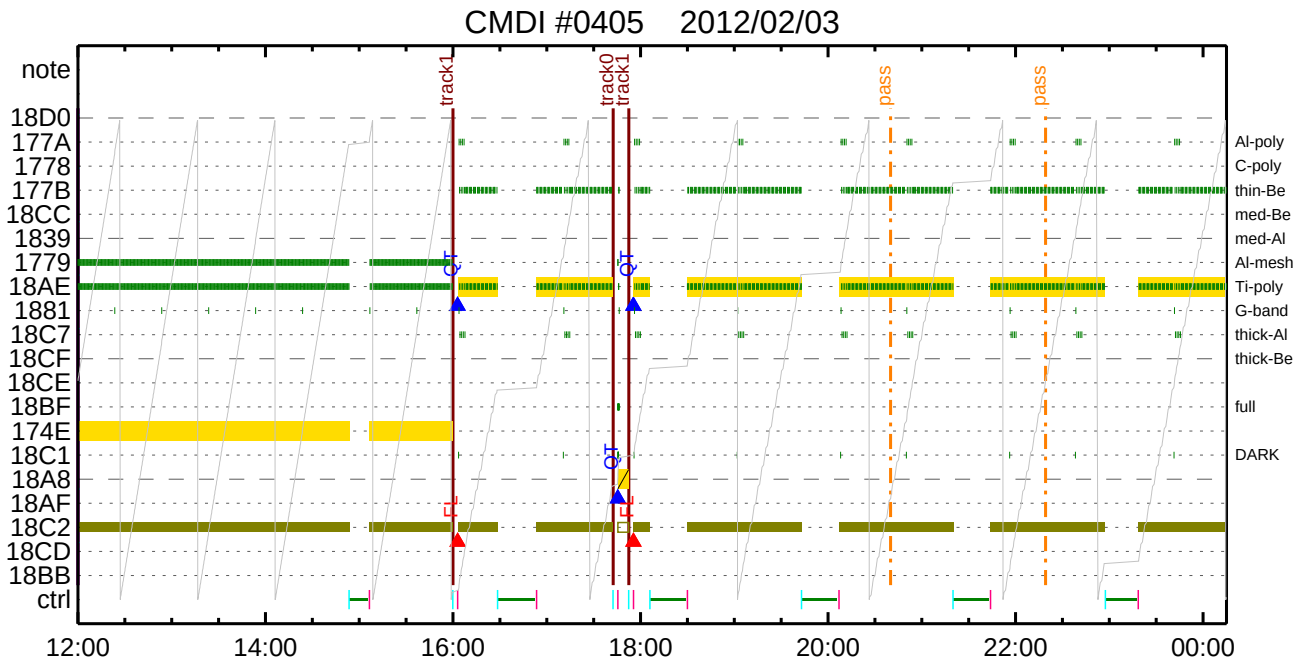
Term	Pointing (x, y)	Comment
02/03 00:52:46 - 02/03 05:39:46	Track (260.3, 397.8) @ 02/03 00:50:00	* AR 11410
02/03 05:52:16 - 02/03 17:42:46	Track (302.4, 396.6) @ 02/03 05:49:30	# Cont.
02/03 17:55:16 - 02/04 06:00:16	Track (401.0, 392.6) @ 02/03 17:52:30	# Cont.
02/04 06:12:46 - 02/07 10:41:00	Track (496.0, 387.3) @ 02/04 06:10:00	# Cont.

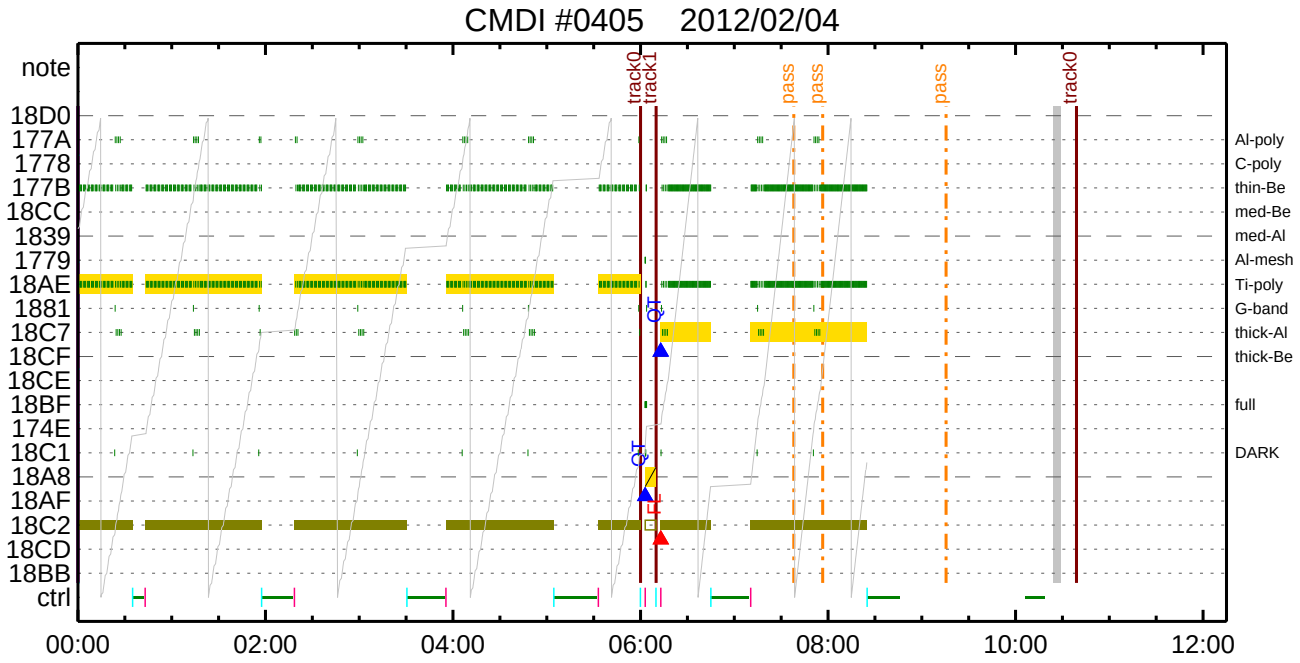
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec

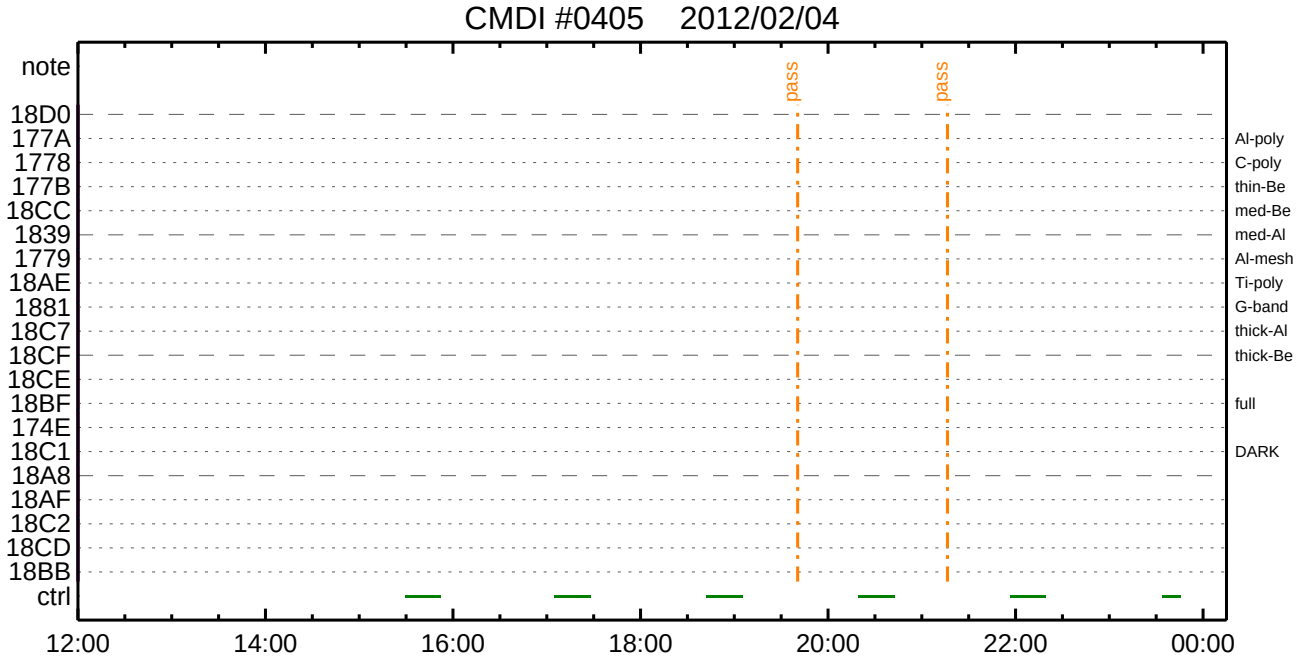

CMDI #0405 2012/02/02
note
before Tbl up
after Tbl up
20 18D0 18D0
19 177A 177A : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
18 18AD 1778 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
17 177B 177B : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
16 18CC 18CC
15 1839 1839
14 1779 1779 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
13 18AE 18AE : AR Standard-A(Filter-Ratio) with PFB, shorter thin ...
12 1881 1881
11 18C7 18C7 : AR Standard-A(Filter-Ratio) with PFB, shorter thin ...
10 18CF 18CF
9 18CE 18CE
8 18BF 18BF
7 174E 174E : HOP81 2-filter - T/poly 8s, Al/mesh 4s, G-band - ...
6 18C1 18C1
5 18A8 18A8 : Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 ...
4 18AF 18AF
3 18C2 18C2 : Flare standard obs: multifilter - thin-Be + (med-A ...
2 18CD 18CD
1 18BB 18BB
ctrl Table upload : 2nd pass (2012/02/02 08:57:05)
pass
pass
obsvp
pass
obsvt
track0
track0
track0
EOT
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

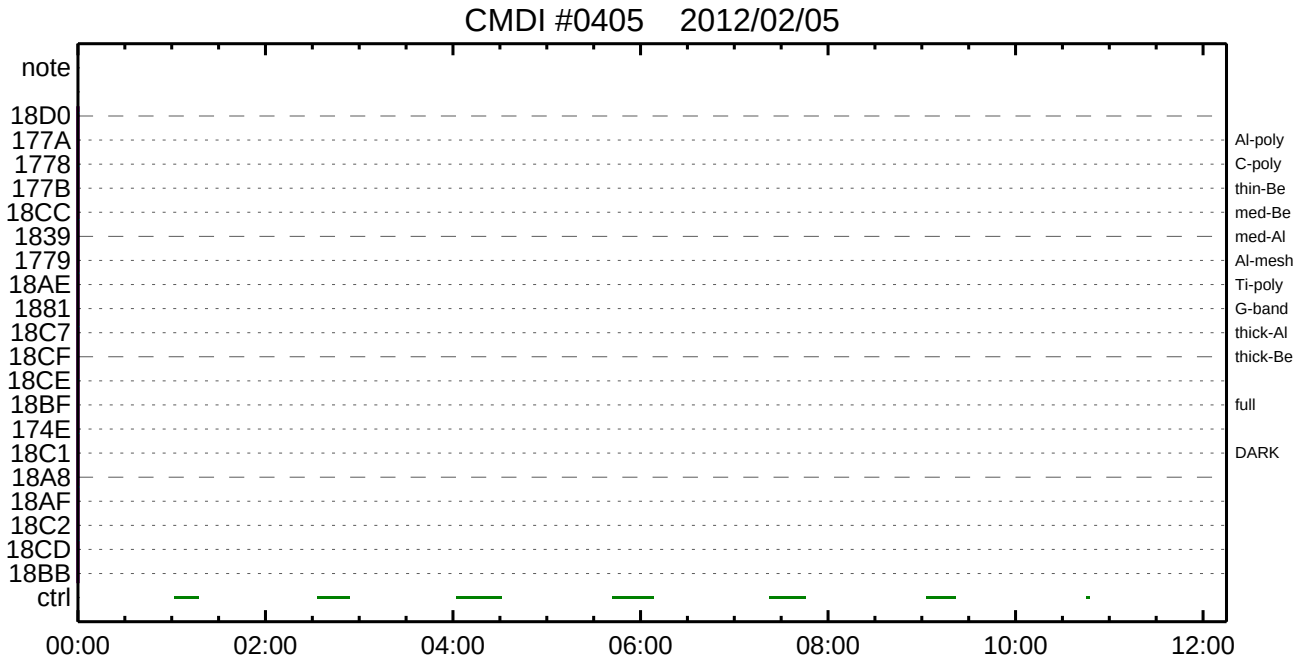

CMDI #0405 2012/02/02
note
track0
track0
track0
track0
track0
track0
track0
track0
track0
track0
track0
track0
pass
track0
track0
pass
track0
track0
track1
track0
EOT
18D0
177A
1778
177B
18CC
1839
1779
18AE
1881
18C7
18CF
18CE
18BF
174E
18C1
18A8
18AF
18C2
18CD
18BB
ctrl
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

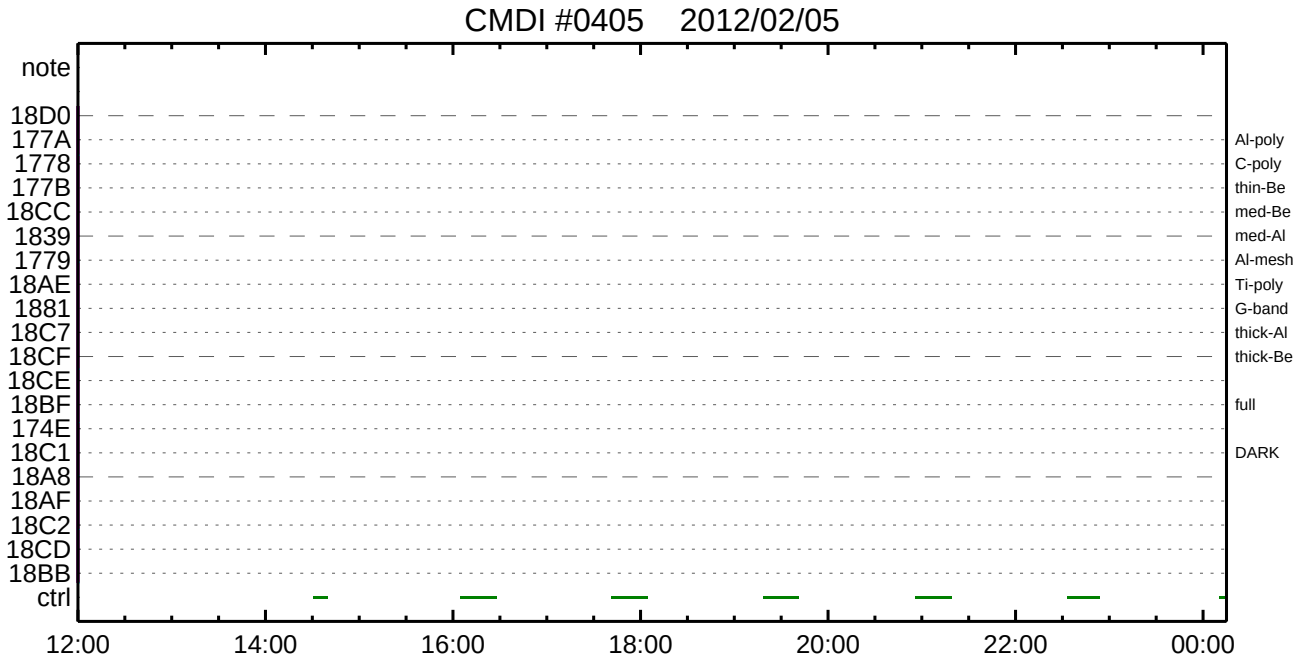

CMDI #0405 2012/02/03
note
track0
track0
track0
track0
track1
track0
track1
pass
pass
track0
EOT
EOT
EOT
EOT
EOT
EOT
EOT
EOT
FL
FL
FL
18D0
177A
1778
177B
18CC
1839
1779
18AE
1881
18C7
18CF
18CE
18BF
174E
18C1
18A8
18AF
18C2
18CD
18BB
ctrl
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

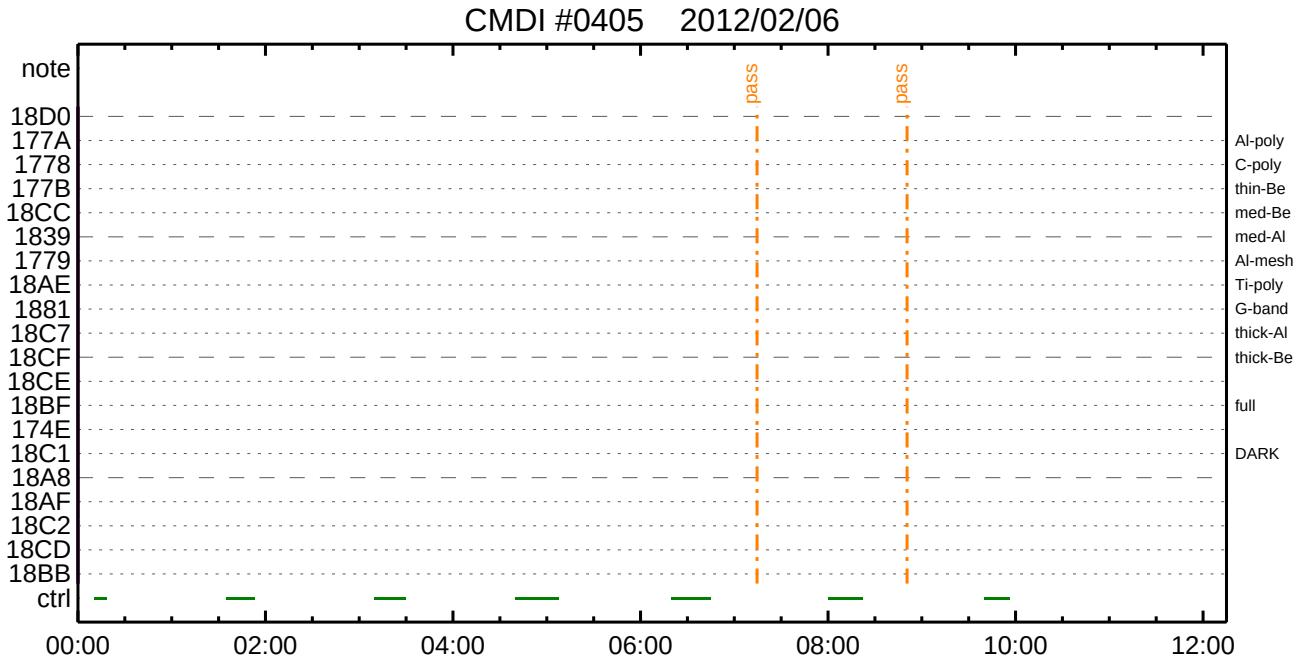

CMDI #0405 2012/02/03
note
18D0
177A
1778
177B
18CC
1839
1779
18AE
1881
18C7
18CF
18CE
18BF
174E
18C1
18A8
18AF
18C2
18CD
18BB
ctrl
track1
track0
track1
pass
pass
cr
cr
cr
FL
FL
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

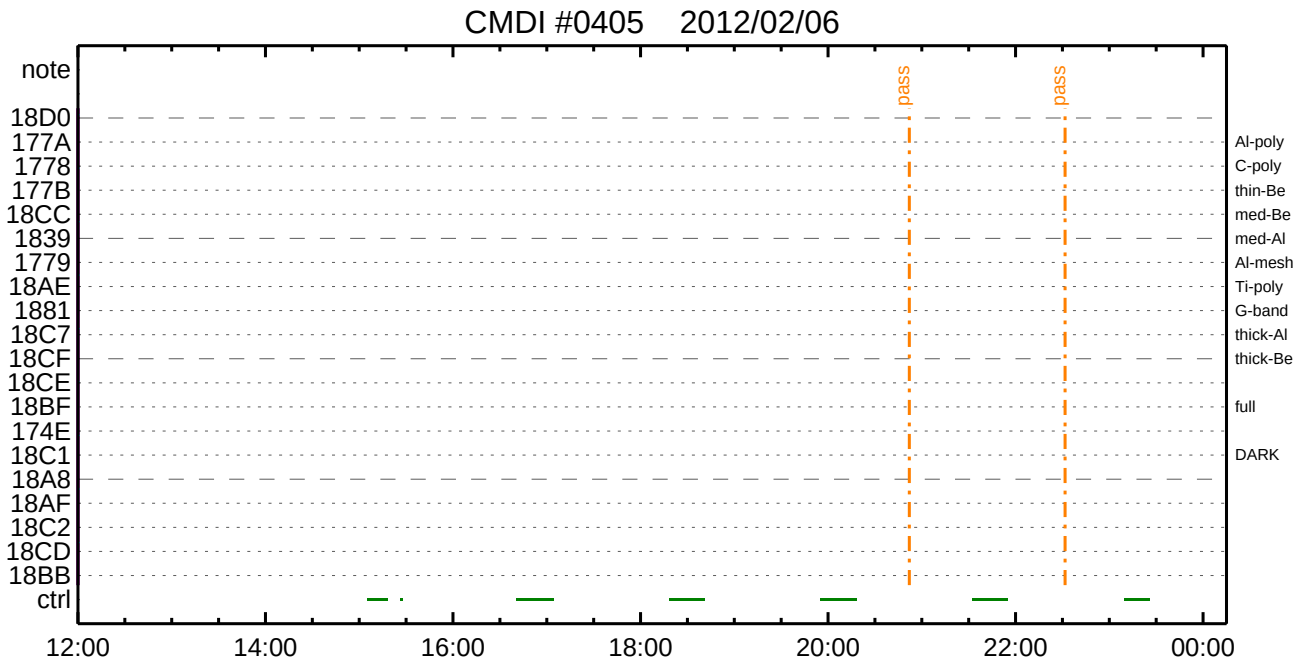

CMDI #0405 2012/02/04
note
18D0
177A
1778
177B
18CC
1839
1779
18AE
1881
18C7
18CF
18CE
18BF
174E
18C1
18A8
18AF
18C2
18CD
18BB
ctrl
track0
track1
pass
pass
pass
track0
cr
cr
FL
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

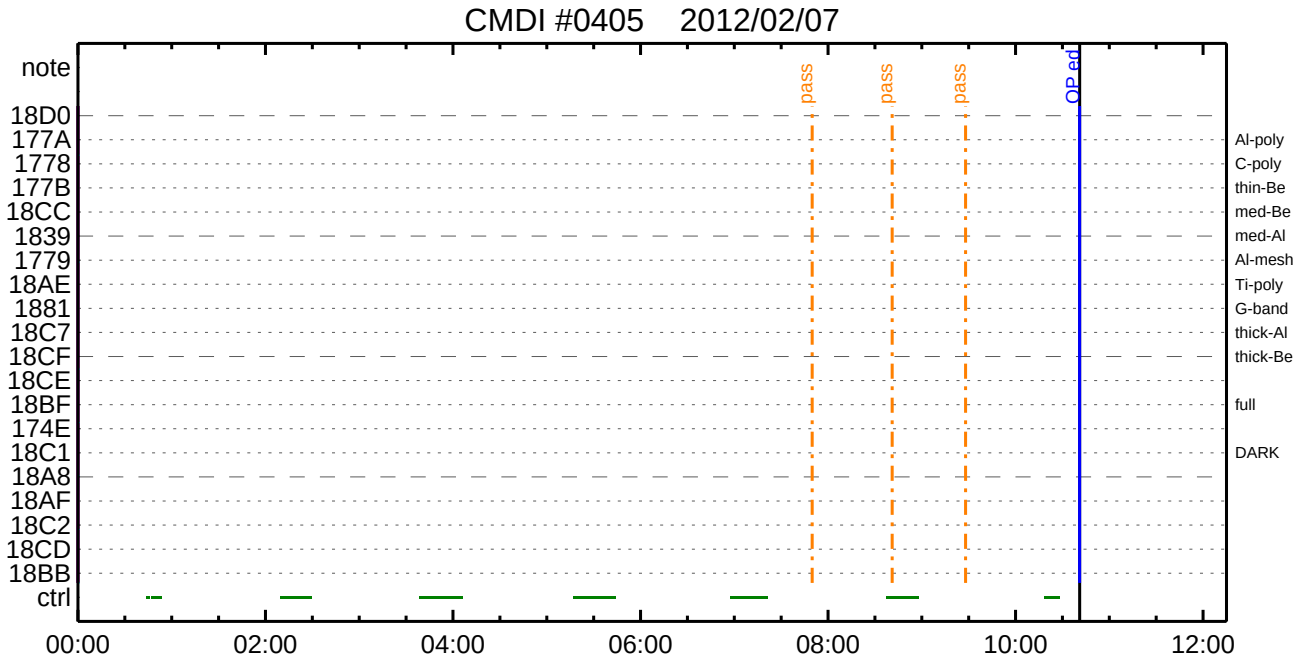

CMDI #0405 2012/02/04
note
18D0
177A
1778
177B
18CC
1839
1779
18AE
1881
18C7
18CF
18CE
18BF
174E
18C1
18A8
18AF
18C2
18CD
18BB
ctrl
pass
pass
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

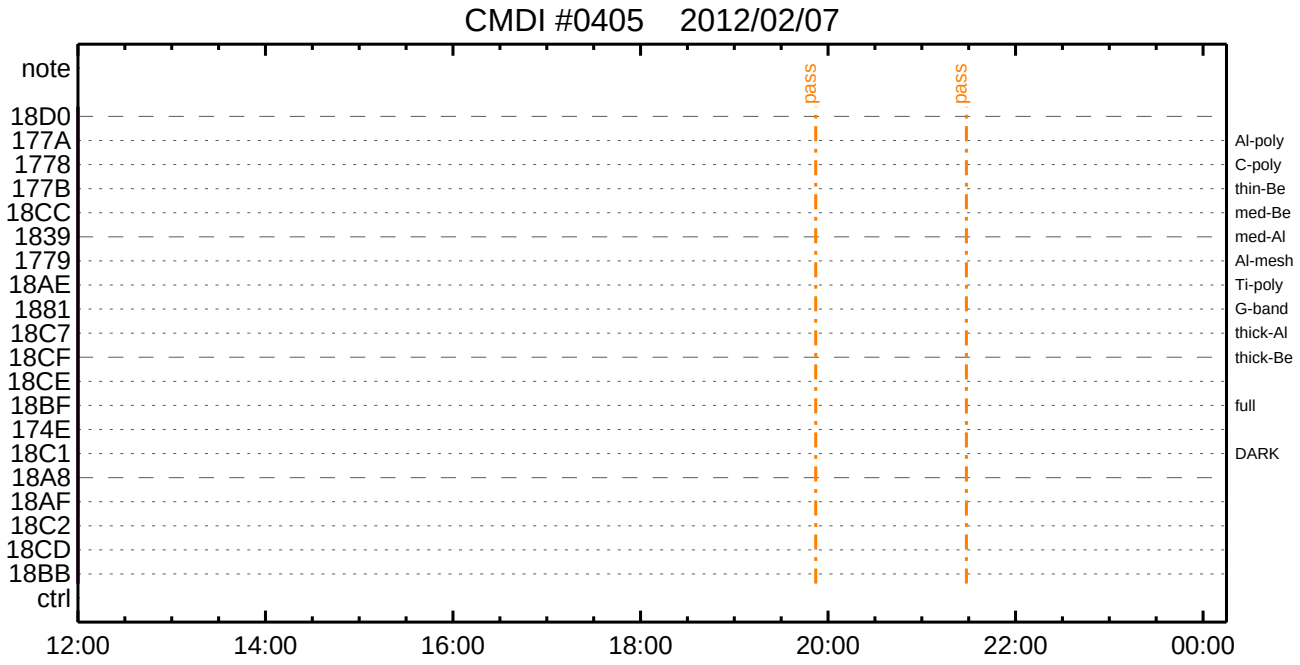

CMDI #0405 2012/02/05
note
18D0
177A
1778
177B
18CC
1839
1779
18AE
1881
18C7
18CF
18CE
18BF
174E
18C1
18A8
18AF
18C2
18CD
18BB
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0405 2012/02/05
note
18D0
177A
1778
177B
18CC
1839
1779
18AE
1881
18C7
18CF
18CE
18BF
174E
18C1
18A8
18AF
18C2
18CD
18BB
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0405 2012/02/06
note
pass
pass
18D0
177A
1778
177B
18CC
1839
1779
18AE
1881
18C7
18CF
18CE
18BF
174E
18C1
18A8
18AF
18C2
18CD
18BB
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0405 2012/02/06
note
18D0
177A
1778
177B
18CC
1839
1779
18AE
1881
18C7
18CF
18CE
18BF
174E
18C1
18A8
18AF
18C2
18CD
18BB
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0405 2012/02/07
note
18D0
177A
1778
177B
18CC
1839
1779
18AE
1881
18C7
18CF
18CE
18BF
174E
18C1
18A8
18AF
18C2
18CD
18BB
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0405 2012/02/07
note
18D0
177A
1778
177B
18CC
1839
1779
18AE
1881
18C7
18CF
18CE
18BF
174E
18C1
18A8
18AF
18C2
18CD
18BB
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-631 2012-02-02 11:48:21 278 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
   C.
   C.
 . C. ****************************
   C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
   C. ****************************
   C.
 . C. ¡ãOP/OG¥í¡¼¥É¡ä
 . S. OP    op-631:OP
   ()
 . S. OG    og-631:OG
   ()
   C.
 . C. ¡ãNMOG&OPÎÎºè¥À¥ó¥×¡ä
   C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
+. DC 01-23 DHU_DMA_DMP_PRM_SET
   BC       (20 00 7f 01 02)
   C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
   C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
   C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
   C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
   C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
+. DC 01-22 DHU_MODE_CHNG
   BC       (07 0b f8)
   C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
   C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
   C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
   C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
   C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
 . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
   C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
 . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
   C.
   C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
+. DC 01-23 DHU_DMA_DMP_PRM_SET
   BC       (20 80 7f 01 02)
   C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
   C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
   C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
   C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
   C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
+. DC 01-22 DHU_MODE_CHNG
   BC       (07 0b f8)
   C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
   C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
   C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
   C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
   C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
 . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
   C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
 . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
   C.
   C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
+. DC 01-23 DHU_DMA_DMP_PRM_SET
   BC       (21 00 41 01 02)
   C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
   C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
   C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
   C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
   C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
+. DC 01-22 DHU_MODE_CHNG
   BC       (07 0b f8)
   C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
   C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
   C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
   C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
   C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
 . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
   C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
 . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
   C.
 . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
   C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
+. DC 01-22 DHU_MODE_CHNG
   BC       (02 0a f8)
   C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     2
   C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
   C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
   C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
   C.
 . C. *********************************
   C. TI-CMD SET (OPOG STOP/COPY/START)
   C. *********************************
   C.
 . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
   C.          ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
   C.
 . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
+. TI 2012-02-02 10:48:00.0
   DC 01-B3 DHU_OP_STOP
   C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
   C.
+. TI 2012-02-02 10:48:01.0
   DC 01-B4 DHU_OP_COPY
   C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
   C.
+. TI 2012-02-02 10:48:01.0
   DC 01-B5 DHU_OPOG_COPY
   C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2012-02-02 10:52:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2012-02-02 10:52:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2012-02-02 10:52:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ***********
0252     C.
0253     C.
0254     C.
0255     C. ************ XRT START ***********
0256     C. Execute, after the success of OP upload.
0257   +. TI 2012-02-02 10:52:00.0
0258     DC 07-F0 MDP_XRT_MODE_STBY
0259     BC       (c3)
0260    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0261     C.
0262     C. ************ XRT END ***********
0263     C.
0264    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0265     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0266    . S. DC-BC dcbc-402:DCBC
0267     (MDP_known_event)
0268     C.
0269     C.
0270    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0271    . S. DC-BC dcbc-153:DCBC
0272     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273     C.
0274     C.
0275    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0276     C.
0277    . C. ********** LOS **********
0278     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-632 2012-02-02 11:48:21 157 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¼É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017    . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034    . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044    . C.
0045    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046   +. DC 07-FC EIS_MODE_MANU
0047     BC       (21 02)
0048    . C. Verify EIS in MANUAL mode
0049    . C. Estimated OBSTBL upload time is 46s
0050     C. ********************************
0051     C. EIS START OBSTBL LOAD
0052     C. ********************************
0053    . S. RAM   ram-820:EIS_OBSTBL
0054     ()
0055   +. DC 07-FC EIS_DUMP_OBSTBL
0056     BC       (07 07 07 00 00 70 00)
0057     C.
0058     C. Execute, after the success of OBSTBL upload.
0059     C. Set EIS TI-commands
0060   +. TI 2012-02-02 10:52:50.0
0061     DC 07-FC EIS_MODE_CHG_ENA
0062     BC       (20)
0063    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0064     C. ********************************
0065     C. EIS END OBSTBL LOAD
0066     C. ********************************
0067     C.
0068     C. ************ XRT START ************
0069     C.
0070   +. DC 07-F0 MDP_XRT_CTRL_MANU
0071     BC       (c1)
0072   +  DC 07-F0 MDP_XRT_MODE_STBY
0073     BC       (c3)
0074    . C. ------------ Success Verify ?              OK / NG____
0075     C.
0076     C. XRT Obs. Table Upload
0077    . S. RAM   ram-291:MDP_OBS_X
0078     ()
0079     C.
0080   +. DC 07-F0 MDP_DUMP_XRTTBL
0081     BC       (84 07 00 00 00 3a d4)
0082    . C. ------------ Comparison Check ?                        OK / ERR ____
0083     C.
0084     C.
0085   +. DC 07-F0 MDP_XRT_ROI_SET
0086     BC       (cd 01 b1 b1 04 04)
0087   +  DC 07-F0 MDP_XRT_ROI_SET
0088     BC       (cd 02 b1 b1 08 08)
0089   +  DC 07-F0 MDP_XRT_ROI_SET
0090     BC       (cd 03 b1 b1 08 08)
0091   +  DC 07-F0 MDP_XRT_ROI_SET
0092     BC       (cd 04 b1 b1 06 06)
0093   +  DC 07-F0 MDP_XRT_ROI_SET
0094     BC       (cd 05 85 83 06 06)
0095   +  DC 07-F0 MDP_XRT_ROI_SET
```

```
0096     BC       (cd 06 80 80 20 20)
0097  +  DC 07-F0 MDP_XRT_ROI_SET
0098     BC       (cd 07 80 80 20 08)
0099  +  DC 07-F0 MDP_XRT_ROI_SET
0100     BC       (cd 08 80 80 08 20)
0101  +  DC 07-F0 MDP_XRT_ROI_SET
0102     BC       (cd 09 c0 c0 10 10)
0103  +  DC 07-F0 MDP_XRT_ROI_SET
0104     BC       (cd 0a 40 c0 10 10)
0105  +  DC 07-F0 MDP_XRT_ROI_SET
0106     BC       (cd 0b 40 40 10 10)
0107  +  DC 07-F0 MDP_XRT_ROI_SET
0108     BC       (cd 0c c0 40 10 10)
0109  +  DC 07-F0 MDP_XRT_ROI_SET
0110     BC       (cd 0d 85 83 06 06)
0111  +  DC 07-F0 MDP_XRT_ROI_SET
0112     BC       (cd 0e 85 83 08 08)
0113  +  DC 07-F0 MDP_XRT_ROI_SET
0114     BC       (cd 0f 80 80 06 06)
0115  +  DC 07-F0 MDP_XRT_ROI_SET
0116     BC       (cd 10 80 80 08 08)
0117  +  DC 07-F0 MDP_XRT_FLD_ENA
0118     BC       (d8)
0119  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0120     BC       (c8)
0121  +  DC 07-F0 MDP_XRT_AEC_RESET
0122     BC       (d0)
0123  +  DC 07-F0 MDP_XRT_ARS_DIS
0124     BC       (d5)
0125  +  DC 07-F0 MDP_XRT_FLD_RESET
0126     BC       (da)
0127  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0128     BC       (c4 05)
0129   . C. ---------- Success Verify ?           OK / NG ____
0130     C.
0131     C.
0132   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0133     C.
0134  +. DC 07-F0 MDP_XRT_MODE_OBSV
0135     BC       (c2)
0136  +. TI 2012-02-02 10:52:02.0
0137     DC 07-F0 MDP_XRT_MODE_OBSV
0138     BC       (c2)
0139   . C. ---------- Success Verify ?           OK / NG ____
0140     C.
0141     C. ************ XRT END ***********
0142     C.
0143   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0144     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0145   . S. DC-BC dcbc-402:DCBC
0146     (MDP_known_event)
0147     C.
0148     C.
0149   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0150   . S. DC-BC dcbc-153:DCBC
0151     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0152     C.
0153     C.
0154   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0155     C.
0156   . C. *********** LOS ***********
0157     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-633 2012-02-02 11:48:21 113 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098     C.
0099   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0100     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0101   . S. DC-BC dcbc-402:DCBC
0102     (MDP_known_event)
0103     C.
0104     C.
0105   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0106   . S. DC-BC dcbc-153:DCBC
0107     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0108     C.
0109     C.
0110   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0111     C.
0112   . C. ********** LOS **********
0113     C.
```

```
*** OP Sequence for XRT ***

2012/02/02  11:03:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 54 00 01 3f
2012/02/02  11:29:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 4c 72 01 3f
2012/02/02  11:54:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 43 8d 01 3f
2012/02/02  12:19:30.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 3a a7 01 3f
2012/02/02  12:44:30.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 31 ca 01 3f
2012/02/02  13:09:30.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 28 e5 01 3f
2012/02/02  13:34:30.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 20 00 01 3f
2012/02/02  14:28:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                AOCU_NM              5 02-76 00 17 1a 01 3f
2012/02/02  14:53:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                AOCU_NM              5 02-76 00 0e 35 01 3f
2012/02/02  15:18:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM              5 02-76 00 05 58 01 3f
2012/02/02  16:17:00.0  AOCS_ORe-point_Start_11_OG [0x0a1]
                                AOCU_NM              5 02-76 00 fd 59 01 3f
2012/02/02  16:42:00.0  AOCS_ORe-point_Start_12_OG [0x0a2]
                                AOCU_NM              5 02-76 00 f4 73 01 3f
2012/02/02  17:54:00.0  AOCS_ORe-point_Start_13_OG [0x0a3]
                                AOCU_NM              5 02-76 00 eb 8e 01 3f
2012/02/02  18:09:58.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/02  18:10:00.0  XRT_TCIB_XRT_S_HTR_A_DIS_436_OG [0x1b4]
                                TCIB_XRT_S_HTR_A_DIS 0 04-C0
2012/02/02  18:19:00.0  AOCS_ORe-point_Start_14_OG [0x0a4]
                                AOCU_NM              5 02-76 00 e2 a8 01 3f
2012/02/02  19:30:30.0  AOCS_ORe-point_Start_15_OG [0x0a5]
                                AOCU_NM              5 02-76 00 d9 cb 01 3f
2012/02/02  19:55:30.0  AOCS_ORe-point_Start_16_OG [0x0a6]
                                AOCU_NM              5 02-76 00 d0 e6 01 3f
2012/02/02  21:08:00.0  AOCS_ORe-point_Start_17_OG [0x0a7]
                                AOCU_NM              5 02-76 00 c8 01 01 3f
2012/02/02  21:33:00.0  AOCS_ORe-point_Start_18_OG [0x0a8]
                                AOCU_NM              5 02-76 00 bf 1b 01 3f
2012/02/02  22:43:30.5  AOCS_ORe-point_Start_19_OG [0x0a9]
                                AOCU_NM              5 02-76 00 b6 36 01 3f
2012/02/02  23:08:30.0  AOCS_ORe-point_Start_20_OG [0x0aa]
                                AOCU_NM              5 02-76 00 ad 59 01 3f
2012/02/02  23:33:30.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                AOCU_NM              5 02-76 01 00 00 00 00
2012/02/03  00:09:54.0  XRT_CTRL_MANU_430_OG [0x1ae]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  00:10:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                AOCU_NM              5 02-76 00 2e f9 2e f9
2012/02/03  00:12:32.0  XRT_FOCUS_POSITION_417_OG [0x1a1]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/02/03  00:12:52.0  XRT_QT_PROG_SET_423_OG [0x1a7]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2012/02/03  00:12:54.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/02/03  00:12:56.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/02/03  00:12:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/02/03  00:13:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  00:19:54.0  XRT_CTRL_MANU_430_OG [0x1ae]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  00:20:00.0  AOCS_ORe-point_Start_23_OG [0x0ad]
                                AOCU_NM              5 02-76 00 2e f9 d1 07
2012/02/03  00:22:32.0  XRT_FOCUS_POSITION_417_OG [0x1a1]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/02/03  00:22:52.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2012/02/03  00:22:54.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/02/03  00:22:56.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/02/03  00:22:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/02/03  00:23:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  00:29:54.0  XRT_CTRL_MANU_430_OG [0x1ae]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  00:30:00.0  AOCS_ORe-point_Start_24_OG [0x0ae]
                                AOCU_NM              5 02-76 00 d1 07 d1 07
2012/02/03  00:32:32.0  XRT_FOCUS_POSITION_417_OG [0x1a1]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/02/03  00:32:52.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2012/02/03  00:32:54.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/02/03  00:32:56.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/02/03  00:32:58.0  XRT_ARS_DIS_431_OG [0x1af]
```

```
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/02/03  00:33:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  00:39:54.0  XRT_CTRL_MANU_430_OG [0x1ae]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  00:40:00.0  AOCS_ORe-point_Start_25_OG [0x0af]
                                    AOCU_NM              5 02-76 00 d1 07 2e f9
2012/02/03  00:42:32.0  XRT_FOCUS_POSITION_417_OG [0x1a1]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/02/03  00:42:52.0  XRT_QT_PROG_SET_432_OG [0x1b0]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2012/02/03  00:42:54.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2012/02/03  00:42:56.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/02/03  00:42:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/02/03  00:43:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  00:49:54.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  00:50:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                    AOCU_NM              5 02-76 01 00 00 00 00
2012/02/03  00:52:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/02/03  00:52:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2012/02/03  00:52:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/02/03  00:52:50.0  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2012/02/03  00:52:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/02/03  00:52:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  00:52:56.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2012/02/03  00:52:58.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 03
2012/02/03  00:53:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  01:23:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  01:23:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  01:23:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/03  01:26:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/03  01:41:30.0  XRT_Custom_418_OG [0x1a2]
2012/02/03  01:42:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  02:57:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  02:57:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  02:57:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/03  03:00:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/03  03:18:00.0  XRT_Custom_418_OG [0x1a2]
2012/02/03  03:19:00.5  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  04:27:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  04:27:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  04:27:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/03  04:30:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/03  04:55:30.5  XRT_Custom_418_OG [0x1a2]
2012/02/03  04:56:30.5  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  05:39:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  05:39:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/02/03  05:39:30.0  AOCS_ORe-point_Start_26_OG [0x0b0]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2012/02/03  05:39:46.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2012/02/03  05:39:48.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/02/03  05:39:50.0  XRT_ARS_DIS_438_OG [0x1b6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/02/03  05:42:28.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2012/02/03  05:42:30.5  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  05:49:24.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  05:49:30.0  AOCS_ORe-point_Start_21_OG [0x0ab]
```

```
                                    AOCU_NM              5 02-76 01 00 00 00 00
2012/02/03  05:51:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/02/03  05:52:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2012/02/03  05:52:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/02/03  05:52:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2012/02/03  05:52:22.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/02/03  05:52:24.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  05:52:26.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2012/02/03  05:52:28.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 03
2012/02/03  05:52:30.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  06:07:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  06:07:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  06:07:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/03  06:10:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/03  06:33:00.0  XRT_Custom_418_OG [0x1a2]
2012/02/03  06:34:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  07:48:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  07:48:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  07:48:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/03  07:51:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/03  08:10:00.0  XRT_Custom_418_OG [0x1a2]
2012/02/03  08:11:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  09:27:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  09:27:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  09:27:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/03  09:30:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/03  09:49:54.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  09:50:00.0  AOCS_ORe-point_Start_27_OG [0x0b1]
                                    AOCU_NM              5 02-76 00 b1 59 01 3f
2012/02/03  09:52:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/02/03  09:52:46.0  XRT_FLD_ENA_435_OG [0x1b3]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2012/02/03  09:52:48.0  XRT_FLRCTRL_ENA_403_OG [0x193]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/02/03  09:52:50.0  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2012/02/03  09:52:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/02/03  09:52:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  09:52:56.0  XRT_QT_PROG_SET_441_OG [0x1b9]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2012/02/03  09:52:58.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 03
2012/02/03  09:53:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  14:53:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  14:53:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  14:53:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/03  14:56:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/03  15:05:30.0  XRT_Custom_418_OG [0x1a2]
2012/02/03  15:06:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  15:59:54.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  16:00:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                    AOCU_NM              5 02-76 01 00 00 00 00
2012/02/03  16:02:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/02/03  16:02:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2012/02/03  16:02:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/02/03  16:02:50.0  XRT_AEC_RESET_443_OG [0x1bb]
```

```
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/02/03  16:02:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/02/03  16:02:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  16:02:56.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2012/02/03  16:02:58.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 03
2012/02/03  16:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  16:28:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  16:28:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  16:28:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/03  16:31:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/03  16:52:30.0  XRT_Custom_418_OG [0x1a2]
2012/02/03  16:53:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  17:42:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  17:42:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/02/03  17:42:30.0  AOCS_ORe-point_Start_26_OG [0x0b0]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2012/02/03  17:42:46.0  XRT_FLD_DIS_402_OG [0x192]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2012/02/03  17:42:48.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/02/03  17:42:50.0  XRT_ARS_DIS_438_OG [0x1b6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/02/03  17:45:28.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2012/02/03  17:45:30.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  17:52:24.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  17:52:30.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                        AOCU_NM              5 02-76 01 00 00 00 00
2012/02/03  17:54:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/02/03  17:55:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/02/03  17:55:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/02/03  17:55:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/02/03  17:55:22.0  XRT_ARS_DIS_431_OG [0x1af]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/02/03  17:55:24.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  17:55:26.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2012/02/03  17:55:28.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 03
2012/02/03  17:55:30.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  18:06:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  18:06:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  18:06:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/03  18:09:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/03  18:29:00.0  XRT_Custom_418_OG [0x1a2]
2012/02/03  18:30:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  19:43:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  19:43:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  19:43:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/03  19:46:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/03  20:06:00.0  XRT_Custom_418_OG [0x1a2]
2012/02/03  20:07:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/03  21:20:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  21:20:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  21:20:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/03  21:23:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/03  21:43:00.0  XRT_Custom_418_OG [0x1a2]
2012/02/03  21:44:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
```

```
2012/02/03  22:57:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/03  22:57:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/03  22:57:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/03  23:00:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/03  23:17:30.0  XRT_Custom_418_OG [0x1a2]
2012/02/03  23:18:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/04  00:35:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/04  00:35:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/04  00:35:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/04  00:38:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/04  00:42:00.0  XRT_Custom_418_OG [0x1a2]
2012/02/04  00:43:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/04  01:57:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/04  01:57:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/04  01:57:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/04  02:00:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/04  02:17:30.0  XRT_Custom_418_OG [0x1a2]
2012/02/04  02:18:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/04  03:30:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/04  03:30:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/04  03:30:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/04  03:33:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/04  03:54:30.5  XRT_Custom_418_OG [0x1a2]
2012/02/04  03:55:30.5  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/04  05:04:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/04  05:04:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/04  05:04:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/04  05:07:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/04  05:32:00.5  XRT_Custom_418_OG [0x1a2]
2012/02/04  05:33:00.5  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/04  05:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/04  05:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/02/04  06:00:00.0  AOCS_ORe-point_Start_26_OG [0x0b0]
                                AOCU_NM              5 02-76 00 00 00 00 00
2012/02/04  06:00:16.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/02/04  06:00:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/02/04  06:00:20.0  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/02/04  06:02:58.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2012/02/04  06:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/04  06:09:54.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/04  06:10:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                AOCU_NM              5 02-76 01 00 00 00 00
2012/02/04  06:12:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/02/04  06:12:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2012/02/04  06:12:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/02/04  06:12:50.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2012/02/04  06:12:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/02/04  06:12:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/04  06:12:56.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2012/02/04  06:12:58.0  XRT_FL_PROG_SET_414_OG [0x19e]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 03
2012/02/04  06:13:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
```

```
2012/02/04  06:45:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/04  06:45:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/04  06:45:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/04  06:48:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/04  07:09:30.0  XRT_Custom_418_OG [0x1a2]
2012/02/04  07:10:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/02/04  08:25:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/02/04  08:25:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/02/04  08:25:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/02/04  08:28:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/02/04  10:39:00.0  AOCS_ORe-point_Start_26_OG [0x0b0]
                                AOCU_NM              5 02-76 00 00 00 00 00
```