

XRT Timeline to be uploaded on 2012/02/18

Period: 2012/02/18 10:53:00 - 2012/02/23 10:40:00

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Normal mode

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XOB #18D6: HOP208 AR - Thin-Be Fix 8sec - Ti-poly - G-band - 384FOV - 1min cadence												
Term	Pointing (x, y)		Comment									
02/18 11:06:00 - 02/18 17:28:30	Track (-133.6, 276.2)	[Ⓜ] 02/18 11:03:00	# OP start + 10min HOP208; Track AR 11420									
02/18 18:07:00 - 02/19 05:38:54	Track (-68.8, 277.2)	[Ⓜ] 02/18 18:04:00	# HOP208; Track AR 11420									
02/19 05:52:00 - 02/19 17:41:24	Track (40.2, 277.7)	[Ⓜ] 02/19 05:49:00	# HOP208; Track AR 11420									
02/19 17:54:30 - 02/20 05:59:54	Track (151.3, 276.5)	[Ⓜ] 02/19 17:51:30	# HOP208; Track AR 11420									
02/20 06:13:00 - 02/20 17:59:54	Track (262.7, 273.8)	[Ⓜ] 02/20 06:10:00	# HOP208; Track AR 11420									
02/20 18:13:00 - 02/21 06:17:24	Track (367.6, 269.5)	[Ⓜ] 02/20 18:10:00	# HOP208; Track AR 11420									
02/21 06:30:30 - 02/21 09:01:30	Track (470.0, 263.5)	[Ⓜ] 02/21 06:27:30	# HOP208; Track AR 11420									
PROG= 11 Inf-time(s)												
Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 97	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Subr= 3	13-time(s)	2.0sec										
└─ Seqn= 27	2-time(s)	60.0sec										
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 30	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1024, 1024)	Q=92	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18A8: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be(18)												
Term	Pointing (x, y)		Comment									
02/18 17:57:00 - 02/18 18:03:54	Fixed (0.0, 0.0)		synoptic, shifted -6.0 min									
02/19 05:42:00 - 02/19 05:48:54	Fixed (0.0, 0.0)		synoptic, shifted -21.0 min									
02/19 17:44:30 - 02/19 17:51:24	Fixed (0.0, 0.0)		synoptic, shifted -18.5 min									
02/20 06:03:00 - 02/20 06:09:54	Fixed (0.0, 0.0)		synoptic									
02/20 18:03:00 - 02/20 18:09:54	Fixed (0.0, 0.0)		synoptic									
02/21 06:20:30 - 02/21 06:27:24	Fixed (0.0, 0.0)		synoptic, shifted 17.5 min									

PROG= 05 1-time(s)												
Subr= 1	1-time(s)	12.0sec										
└─ Seqn= 7	1-time(s)	4.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8	1-time(s)	4.0sec										
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 3	1-time(s)	2.0sec										
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops												
Term	Pointing (x, y)		Comment									
02/18 11:06:00 - 02/18 17:28:30	Track (-133.6, 276.2)	[Ⓜ] 02/18 11:03:00	# OP start + 10min HOP208; Track AR 11420									
02/18 18:07:00 - 02/19 05:38:54	Track (-68.8, 277.2)	[Ⓜ] 02/18 18:04:00	# HOP208; Track AR 11420									
02/19 05:52:00 - 02/19 17:41:24	Track (40.2, 277.7)	[Ⓜ] 02/19 05:49:00	# HOP208; Track AR 11420									
02/19 17:54:30 - 02/20 05:59:54	Track (151.3, 276.5)	[Ⓜ] 02/19 17:51:30	# HOP208; Track AR 11420									
02/20 06:13:00 - 02/20 17:59:54	Track (262.7, 273.8)	[Ⓜ] 02/20 06:10:00	# HOP208; Track AR 11420									
02/20 18:13:00 - 02/21 06:17:24	Track (367.6, 269.5)	[Ⓜ] 02/20 18:10:00	# HOP208; Track AR 11420									
02/21 06:30:30 - 02/21 09:01:30	Track (470.0, 263.5)	[Ⓜ] 02/21 06:27:30	# HOP208; Track AR 11420									

PROG= 03 12-time(s)												
Subr= 1	45-time(s)	10.0sec										
└─ Seqn= 20	1-time(s)	2.0sec										
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 63	1-time(s)	2.0sec										
└─ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 77	1-time(s)	2.0sec										
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	10.0sec										
└─ Seqn= 90	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs 2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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Active Region Search

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NOT USED

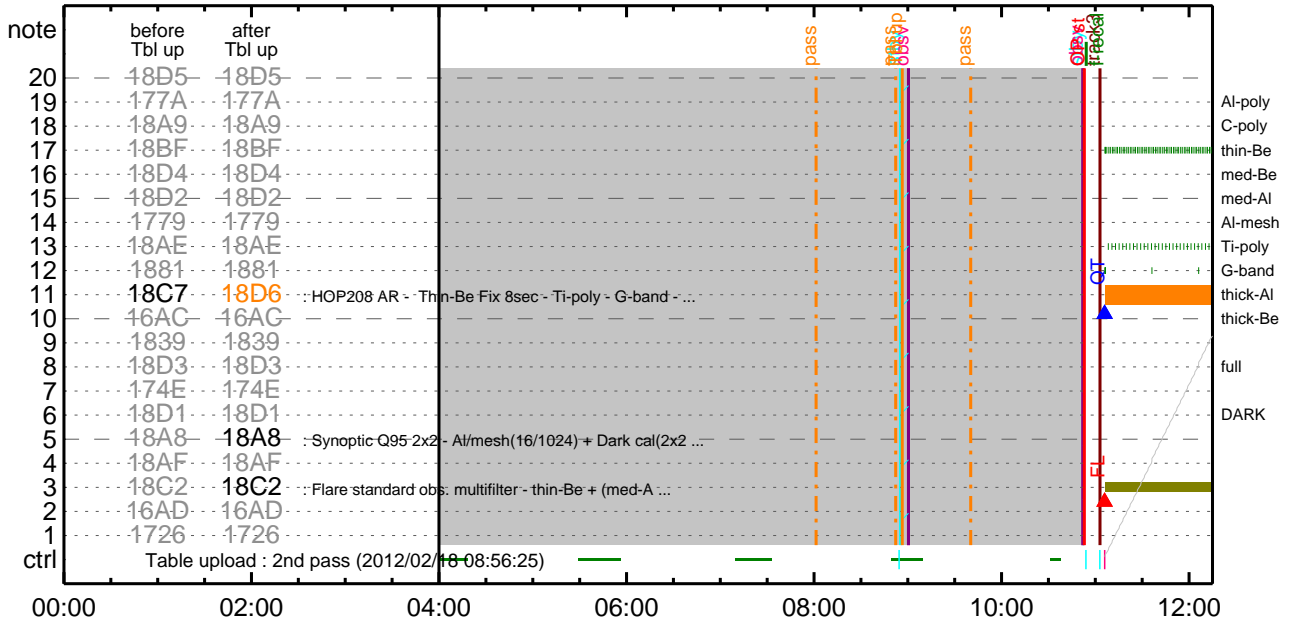
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Flare Detection

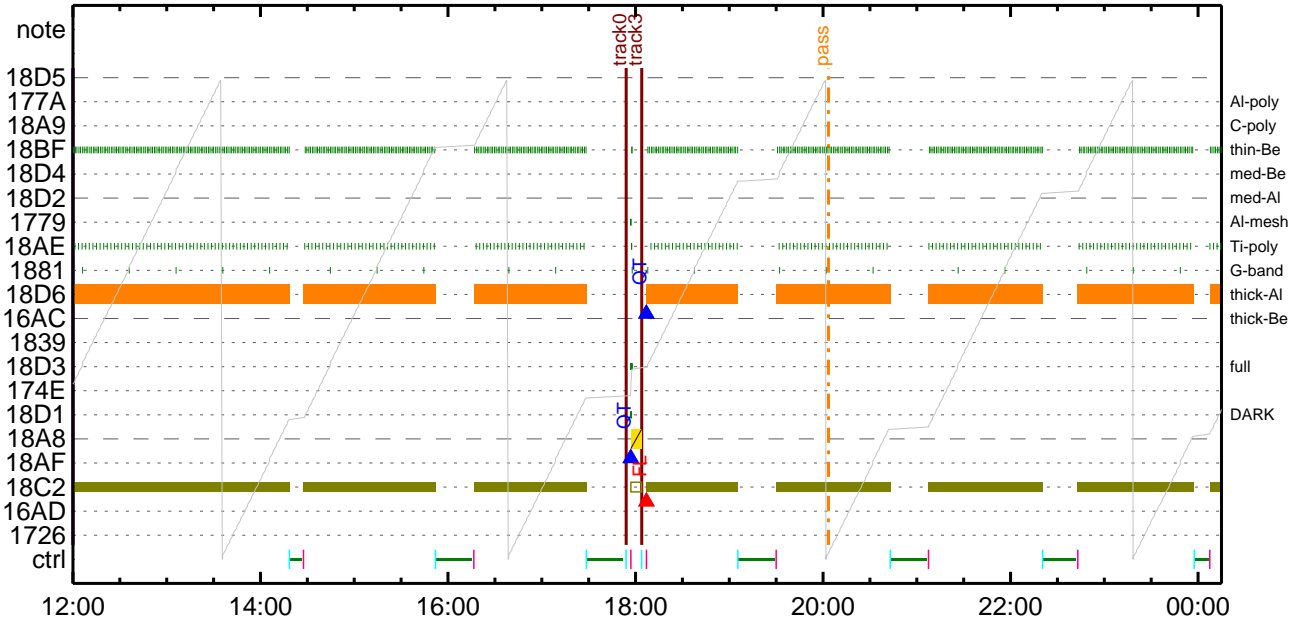
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FLD Patrol											
Term	Pointing (x, y)					Comment					
02/18 18:06:46 - 02/19 05:39:16	Track (-68.8,	277.2)	[Ⓜ] 02/18 18:04:00	# HOP208; Track AR 11420						
02/19 05:51:46 - 02/19 17:41:46	Track (40.2,	277.7)	[Ⓜ] 02/19 05:49:00	# HOP208; Track AR 11420						
02/19 17:54:16 - 02/20 06:00:16	Track (151.3,	276.5)	[Ⓜ] 02/19 17:51:30	# HOP208; Track AR 11420						
02/20 06:12:46 - 02/20 18:00:16	Track (262.7,	273.8)	[Ⓜ] 02/20 06:10:00	# HOP208; Track AR 11420						
02/20 18:12:46 - 02/21 06:17:46	Track (367.6,	269.5)	[Ⓜ] 02/20 18:10:00	# HOP208; Track AR 11420						
02/21 06:30:16 - 02/23 10:40:00	Track (470.0,	263.5)	[Ⓜ] 02/21 06:27:30	# HOP208; Track AR 11420						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

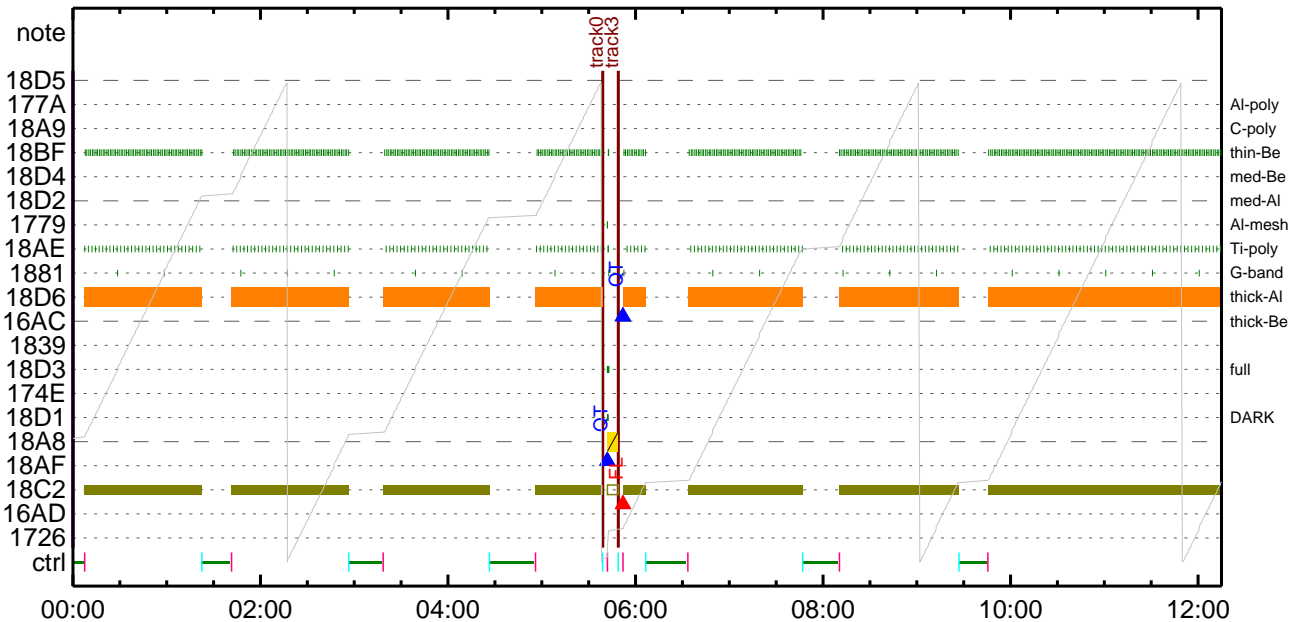
CMDI #0436 2012/02/18



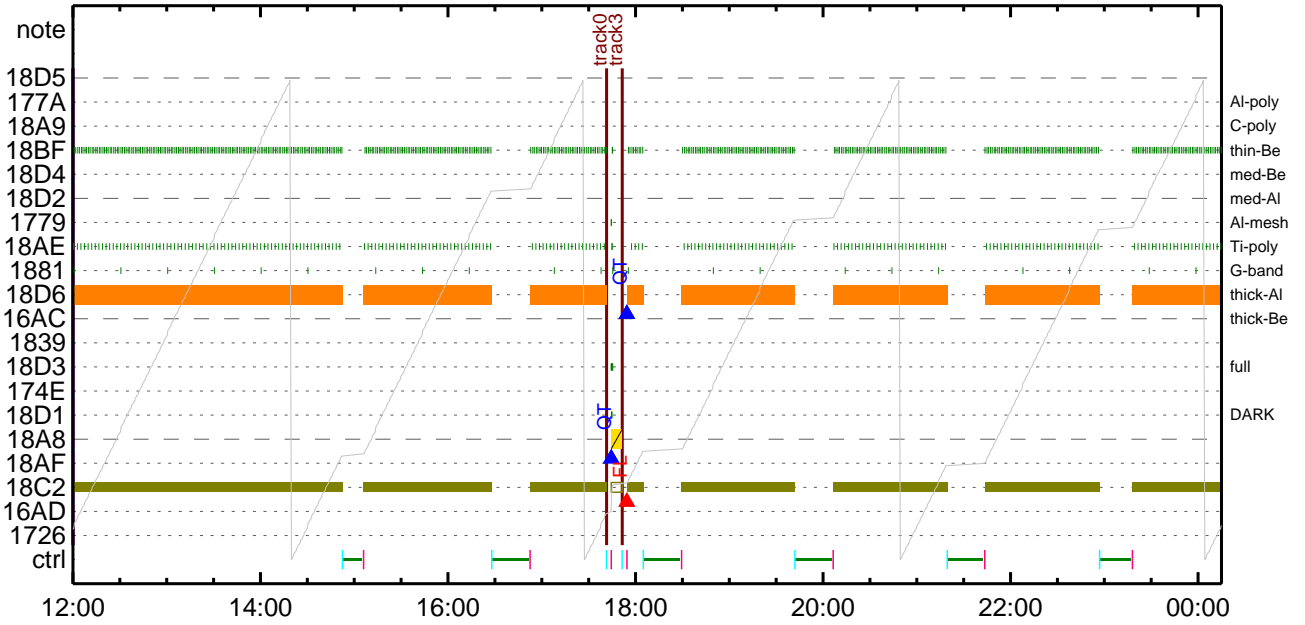
CMDI #0436 2012/02/18



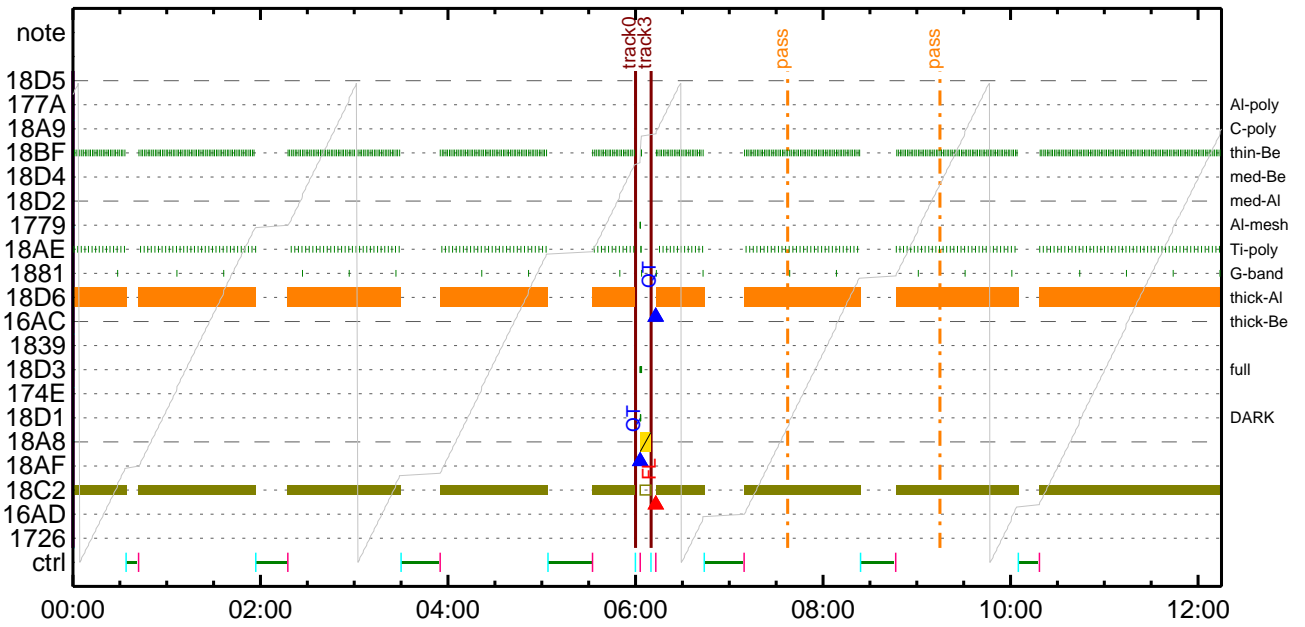
CMDI #0436 2012/02/19



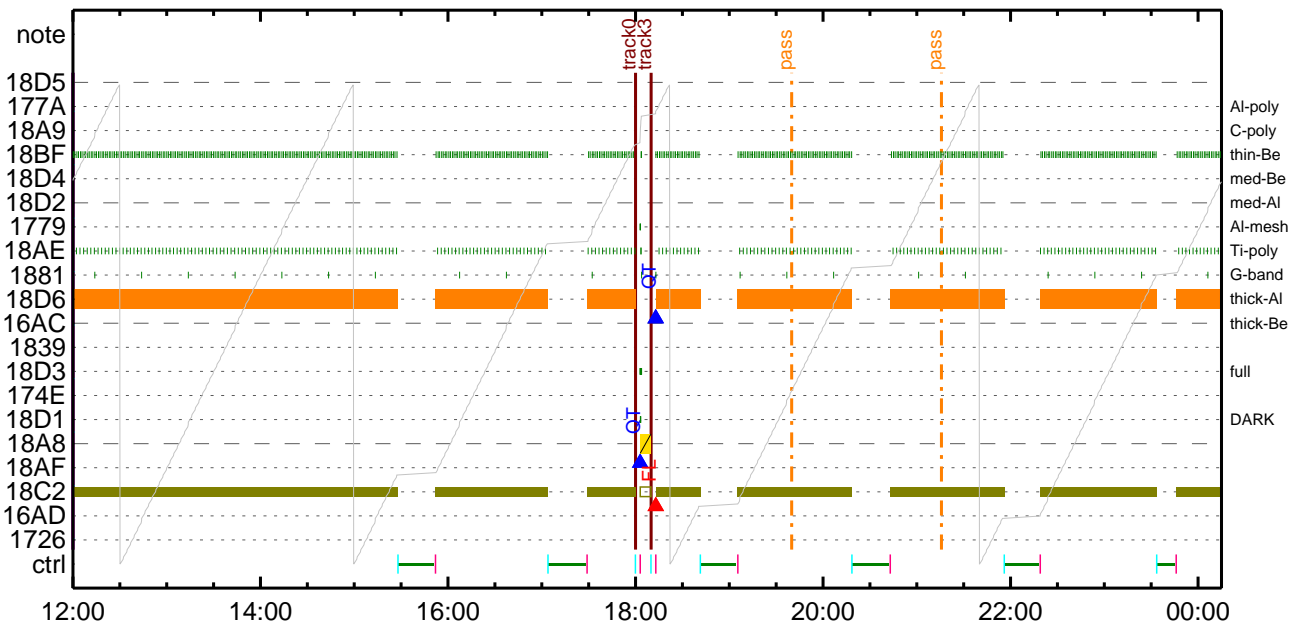
CMDI #0436 2012/02/19



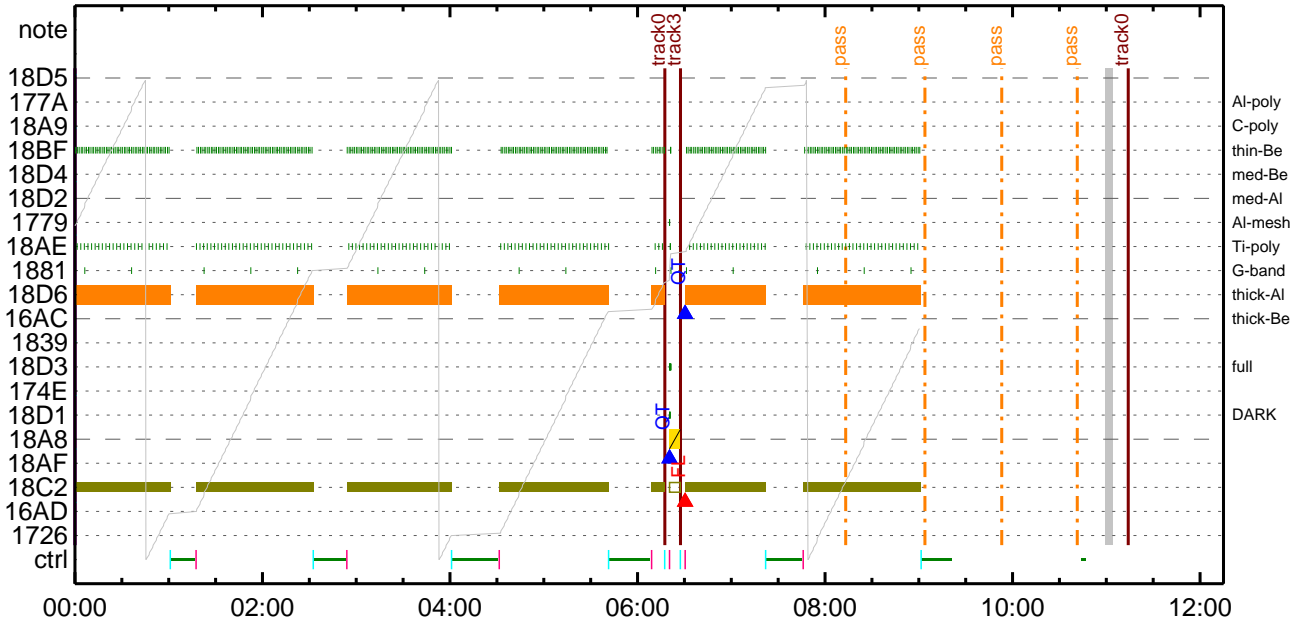
CMDI #0436 2012/02/20



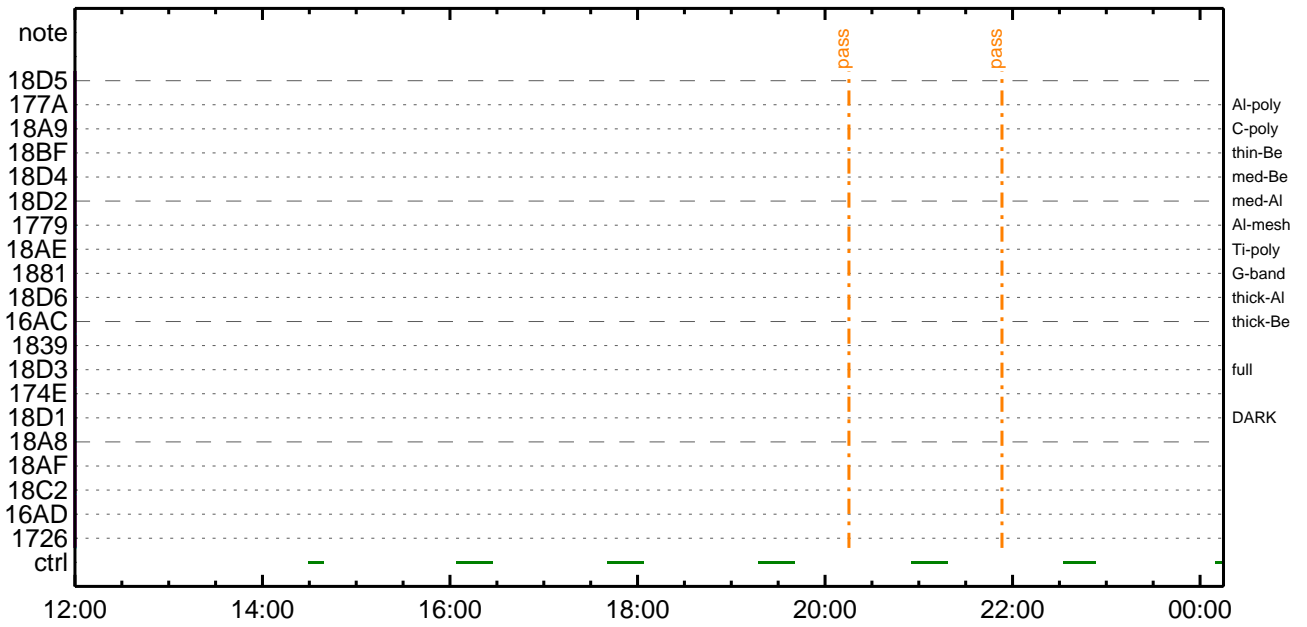
CMDI #0436 2012/02/20



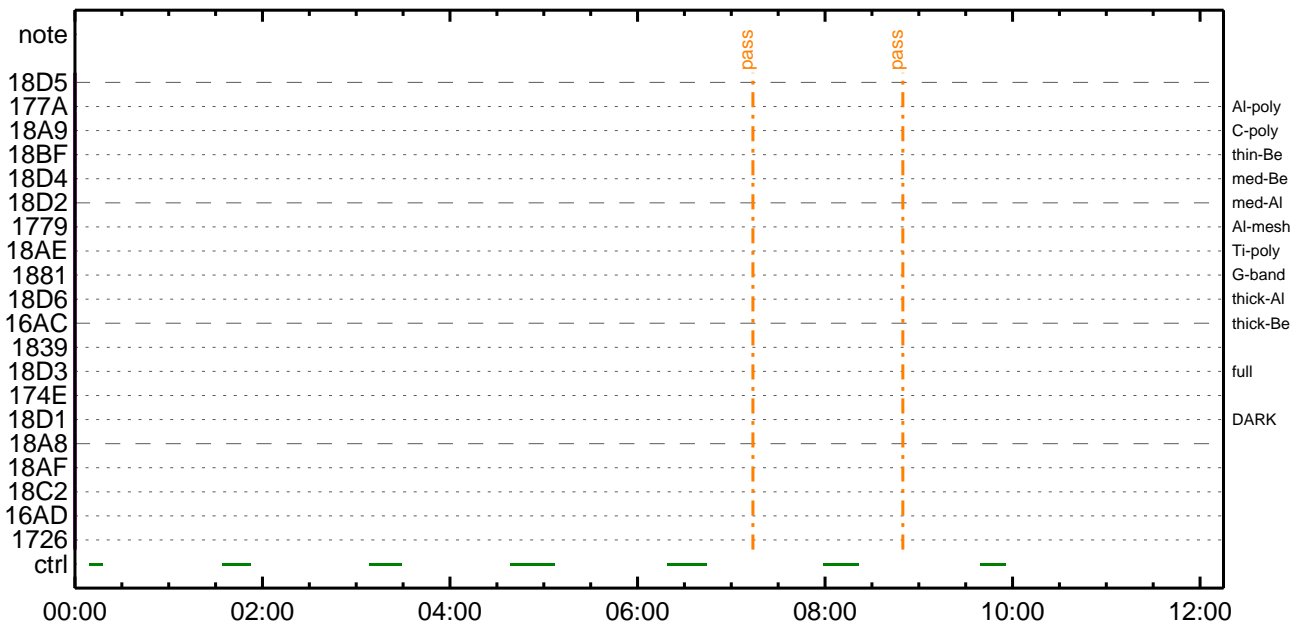
CMDI #0436 2012/02/21



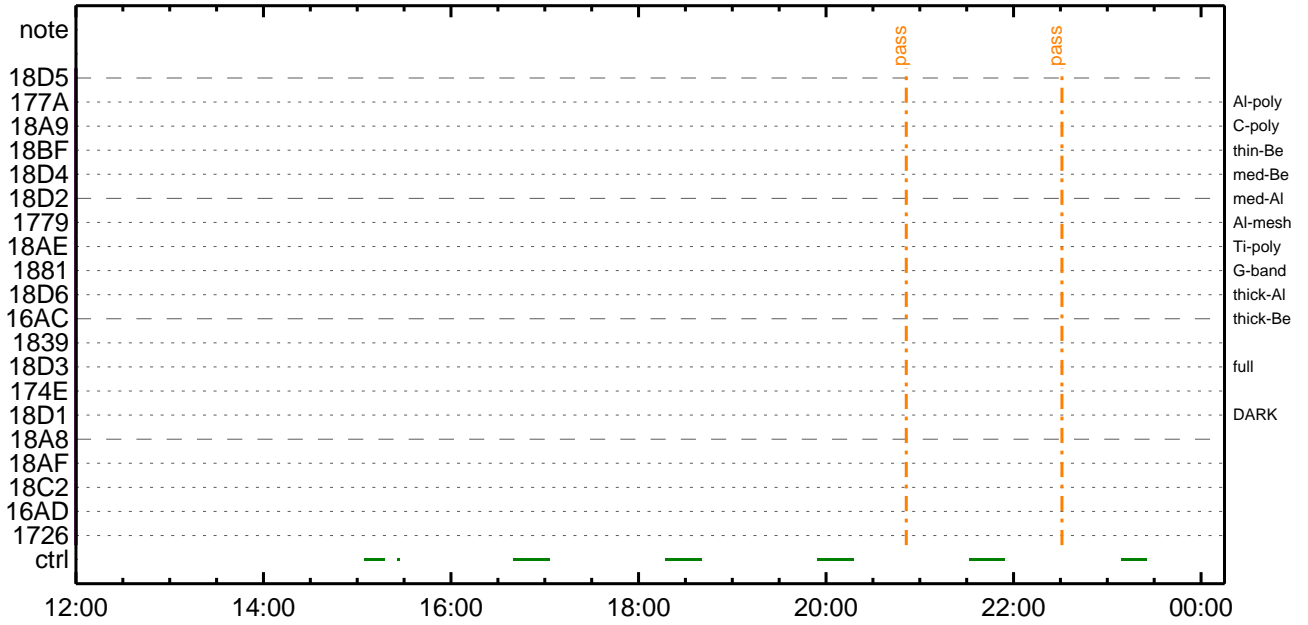
CMDI #0436 2012/02/21



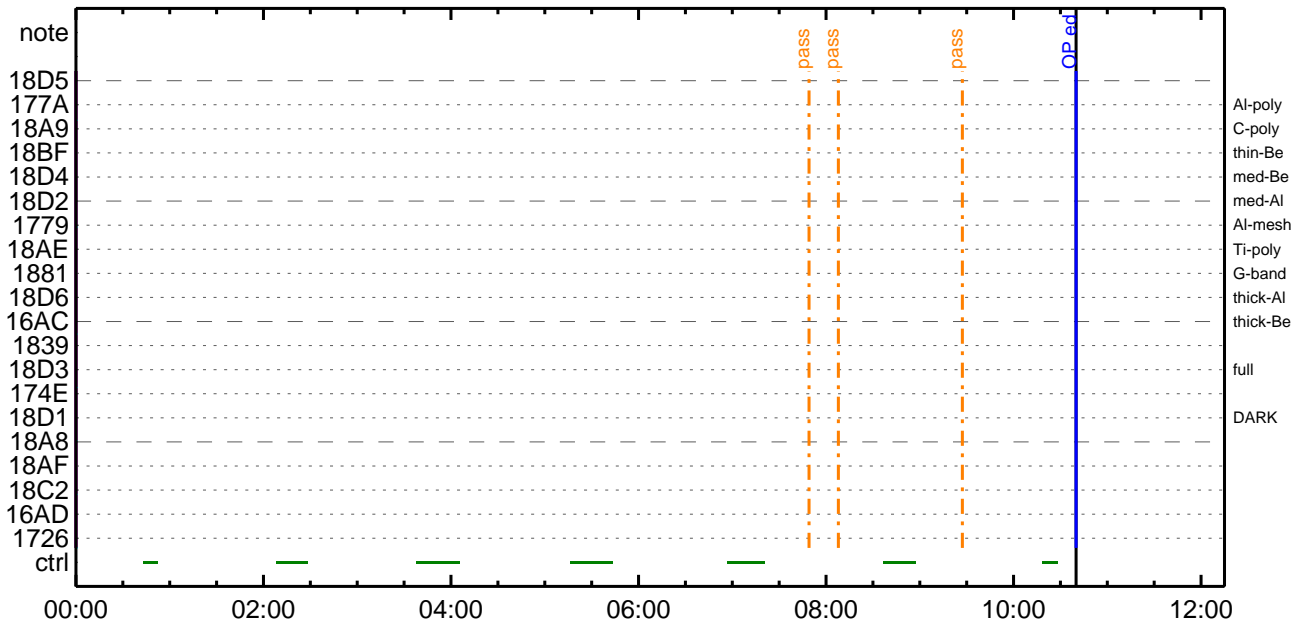
CMDI #0436 2012/02/22



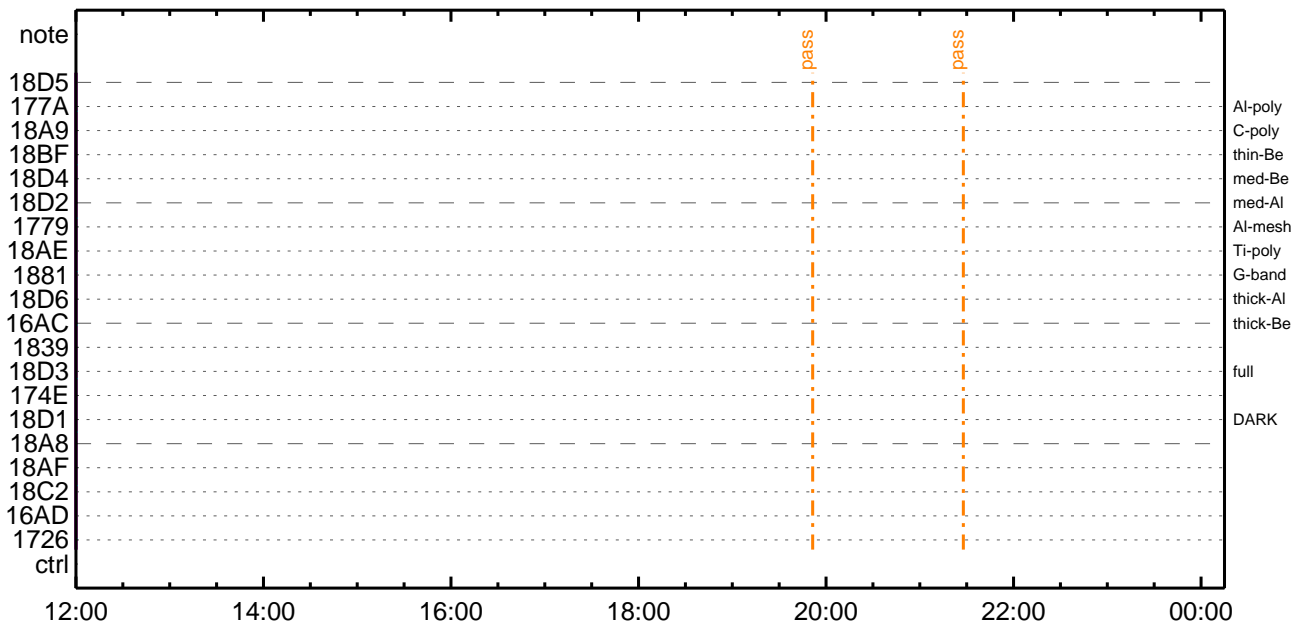
CMDI #0436 2012/02/22



CMDI #0436 2012/02/23



CMDI #0436 2012/02/23




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-664:OP
0104 ( )
0105 S. OG og-664:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ã´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°îYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²Y½YÉòðÁDî¿(UT)
0183 +. TI 2012-02-18 10:48:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-02-18 10:48:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-02-18 10:48:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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```

0096 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0097 C.
0098 +. DC 07-F0 MDP_XRT_MODE_OBSV
0099 BC (c2)
0100 +. TI 2012-02-18 10:52:02.0
0101 DC 07-F0 MDP_XRT_MODE_OBSV
0102 BC (c2)
0103 . C. ----- Success Verify ? OK / NG ____
0104 C.
0105 C. ***** XRT END *****
0106 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0107 +. DC 07-FC EIS_MODE_MANU
0108 BC (21 02)
0109 . C. Verify EIS in MANUAL mode
0110 . C. Estimated OBSTBL upload time is 10s
0111 C. *****
0112 C. EIS START OBSTBL LOAD
0113 C. *****
0114 . S. RAM ram-820:EIS_OBSTBL
0115 ( )
0116 +. DC 07-FC EIS_DUMP_OBSTBL
0117 BC (07 07 07 00 00 70 00)
0118 C.
0119 C. Execute, after the success of OBSTBL upload.
0120 C. Set EIS TI-commands
0121 +. TI 2012-02-18 10:52:50.0
0122 DC 07-FC EIS_MODE_CHG_ENA
0123 BC (20)
0124 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0125 C. *****
0126 C. EIS END OBSTBL LOAD
0127 C. *****
0128 C.
0129 . C. ***** MDP `úÃîñí»ò¼ÝñÉÃð¹ñèDCBC•x²è *****
0130 C. (¼á°îÿÓÿÃÿÈÿPÿËÿáÿçÿèñÉ¼ñ¼Ã»Ûñ¹ñè)
0131 . S. DC-BC dcbc-402:DCBC
0132 (MDP_known_event)
0133 C.
0134 C.
0135 . C. ***** ÿDÿ¹•Ï Daily±¿îññÉ´Øñ¹ñèDCBC•x²è *****
0136 . S. DC-BC dcbc-153:DCBC
0137 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0138 C.
0139 C.
0140 . C. ;ãLOSÿÁÿ$ÿÃÿ~¼Ã»Û;ã
0141 C.
0142 . C. ***** LOS *****
0143 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-666 2012-02-18 12:51:33 145 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈççðÁð•µ°È»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ÈÈÈµ•ííÈ;ÈÈÈ¼°ÇÖñ•çç¼í¹ççí;çÀ®, ùñ¹ññññççÁ+ç®ñ•ññññññÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç“ °ÈÀ, í×ÈYñäLOSñççççí»ç´Öññ¹íí, ç•; çÉÖÍ×çÈXÁÖONñí¹ÖñÈññññññÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY-¾ÖÁÖñ-°ÁÀÈñ•çççç; ç°È²¼ççí°ÈÀ, ¼È¼çççç¼Á¹Öñññ;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÈÀ,
0033 C. *****
0034 C. ç“ RESTART;ÈPT1;Èñ•çççç¼í¹ççí; ç°È²¼ççí°ÈÀ¹Öñññ°; çDCBC-150ççççÈññ;ç
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÈÁÚÁÖ;ÈÁ•Á°²óÈè;È, áñí°ÈÀ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, ç-¼«È°Áá»çççççç; ç°È²¼çççç¼Á¹Öñññ;ç
0055 C. YçYÓYÉYÈÁÚÁÖñññÁ•Á°²óÈèçççç¼í¹ççí°ÈÁ»ññññçççççççççç;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÈÀ,
0059 C. *****
0060 C. ç“ RESTART;ÈPT2;Èñ•çççç¼í¹ççí; ç°È²¼çççç¼Á¹Öñññ°; çDCBC-151ççççÈññ;ç
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÈÁÚÁÖ;ÈÁ•Á°²óÈè;È, áñí°ÈÀ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÀ, Áá»çç; çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÀ, Áá»çç;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
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*** OP Sequence for XRT ***

2012/02/18	10:54:00.5	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/18	10:54:10.5	XRT_FOCUS_RECALIBRATE_428_OG [0x1ac]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2012/02/18	10:58:10.5	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/02/18	11:02:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/18	11:03:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/02/18	11:05:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/02/18	11:05:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/02/18	11:05:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/02/18	11:05:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/02/18	11:05:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/18	11:05:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/18	11:05:56.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2012/02/18	11:05:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/02/18	11:06:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/18	14:18:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/18	14:18:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/18	14:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/18	14:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/18	14:26:30.0	XRT_Custom_418_OG [0x1a2]			
2012/02/18	14:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/18	15:52:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/18	15:52:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/18	15:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/18	15:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/18	16:15:30.0	XRT_Custom_418_OG [0x1a2]			
2012/02/18	16:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/18	17:28:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/18	17:28:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/18	17:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/18	17:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/18	17:53:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/18	17:53:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/02/18	17:54:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/02/18	17:54:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/02/18	17:54:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/02/18	17:54:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/18	17:56:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/02/18	17:57:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/18	18:03:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/18	18:04:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/02/18	18:06:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/02/18	18:06:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/02/18	18:06:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/02/18	18:06:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/02/18	18:06:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/18	18:06:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/18	18:06:56.0	XRT_QT_PROG_SET_410_OG [0x19a]			

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2012/02/18	18:06:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2012/02/18	18:07:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/18	19:05:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/18	19:05:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/18	19:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/02/18	19:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/02/18	19:29:00.0	XRT_Custom_418_OG [0x1a2]					
2012/02/18	19:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/18	20:43:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/18	20:43:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/18	20:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/02/18	20:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/02/18	21:06:30.0	XRT_Custom_418_OG [0x1a2]					
2012/02/18	21:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/18	22:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/18	22:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/18	22:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/02/18	22:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/02/18	22:42:00.0	XRT_Custom_418_OG [0x1a2]					
2012/02/18	22:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/18	23:57:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/18	23:57:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/18	23:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/02/19	00:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/02/19	00:06:30.0	XRT_Custom_418_OG [0x1a2]					
2012/02/19	00:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/19	01:22:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/19	01:22:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/19	01:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/02/19	01:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/02/19	01:40:30.0	XRT_Custom_418_OG [0x1a2]					
2012/02/19	01:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/19	02:56:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/19	02:56:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/19	02:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/02/19	02:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/02/19	03:17:30.0	XRT_Custom_418_OG [0x1a2]					
2012/02/19	03:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/19	04:26:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/19	04:26:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/19	04:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/02/19	04:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/02/19	04:55:00.0	XRT_Custom_418_OG [0x1a2]					
2012/02/19	04:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/19	05:38:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/19	05:38:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2012/02/19	05:39:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00
2012/02/19	05:39:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/02/19	05:39:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/02/19	05:39:20.0	XRT_ARS_DIS_438_OG [0x1b6]					

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2012/02/19	05:41:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/19	05:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/02/19	05:48:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/19	05:49:00.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/19	05:51:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	03 00 00 00 00
2012/02/19	05:51:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/02/19	05:51:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/02/19	05:51:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/02/19	05:51:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/02/19	05:51:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/19	05:51:56.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/19	05:51:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2012/02/19	05:52:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/02/19	06:06:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/19	06:06:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/19	06:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/19	06:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/19	06:32:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/19	06:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2012/02/19	07:47:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/19	07:47:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/19	07:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/19	07:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/19	08:09:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/19	08:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2012/02/19	09:27:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/19	09:27:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/19	09:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/19	09:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/19	09:44:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/19	09:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2012/02/19	14:52:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/19	14:52:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/19	14:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/19	14:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/19	15:05:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/19	15:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2012/02/19	16:28:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/19	16:28:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/19	16:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/19	16:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/19	16:51:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/19	16:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2012/02/19	17:41:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/19	17:41:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/19	17:41:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/02/19	17:41:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00
2012/02/19	17:41:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/02/19	17:41:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9

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2012/02/19	17:44:28.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/02/19	17:44:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/19	17:51:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/19	17:51:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00
2012/02/19	17:53:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/02/19	17:54:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/02/19	17:54:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/02/19	17:54:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/02/19	17:54:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/19	17:54:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/19	17:54:26.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2012/02/19	17:54:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/02/19	17:54:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/19	18:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/19	18:05:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/19	18:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/19	18:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/19	18:28:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/19	18:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/19	19:42:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/19	19:42:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/19	19:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/19	19:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/19	20:05:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/19	20:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/19	21:19:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/19	21:19:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/19	21:19:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/19	21:22:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/19	21:42:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/19	21:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/19	22:57:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/19	22:57:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/19	22:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/19	23:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/19	23:17:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/19	23:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/20	00:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/20	00:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/20	00:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/20	00:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/20	00:41:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/20	00:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/20	01:57:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/20	01:57:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/20	01:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/20	02:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/20	02:16:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/20	02:17:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2012/02/20	03:30:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/20	03:30:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/20	03:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/02/20	03:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/02/20	03:54:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/20	03:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/20	05:04:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/20	05:04:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/20	05:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/02/20	05:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/02/20	05:31:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/20	05:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/20	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/20	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/02/20	06:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2012/02/20	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/02/20	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/02/20	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/20	06:02:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
2012/02/20	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/20	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/20	06:10:00.0	AOCS_Orе-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00			
2012/02/20	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/02/20	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/02/20	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/20	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/20	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/20	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/20	06:12:56.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2012/02/20	06:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2012/02/20	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/20	06:44:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/20	06:44:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/20	06:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/02/20	06:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/02/20	07:08:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/20	07:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/20	08:24:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/20	08:24:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/20	08:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/02/20	08:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/02/20	08:45:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/20	08:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/20	10:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/20	10:05:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/20	10:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/02/20	10:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/02/20	10:17:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/20	10:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			

2012/02/20	15:28:00.0	XRT_CTRL_MANU_408_OG [0x198]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/02/20	15:28:02.0	XRT_FLD_RESET_412_OG [0x19c]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/02/20	15:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/02/20	15:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/02/20	15:51:00.0	XRT_Custom_418_OG [0x1a2]									
2012/02/20	15:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/02/20	17:04:00.0	XRT_CTRL_MANU_408_OG [0x198]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/02/20	17:04:02.0	XRT_FLD_RESET_412_OG [0x19c]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/02/20	17:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/02/20	17:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/02/20	17:28:00.0	XRT_Custom_418_OG [0x1a2]									
2012/02/20	17:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/02/20	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/02/20	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]									
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00						
2012/02/20	18:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]									
		AOCU_NM	5	02-76	00 00 00 00 00						
2012/02/20	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]									
		MDP_XRT_FLD_DIS	1	07-F0	d9						
2012/02/20	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]									
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9						
2012/02/20	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]									
		MDP_XRT_ARS_DIS	1	07-F0	d5						
2012/02/20	18:02:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]									
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05						
2012/02/20	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/02/20	18:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/02/20	18:10:00.0	AOCS_Orе-point_Start_1_OG [0x097]									
		AOCU_NM	5	02-76	03 00 00 00 00						
2012/02/20	18:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]									
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00						
2012/02/20	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b]									
		MDP_XRT_FLD_ENA	1	07-F0	d8						
2012/02/20	18:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]									
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8						
2012/02/20	18:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]									
		MDP_XRT_AEC_RESET	1	07-F0	d0						
2012/02/20	18:12:52.0	XRT_ARS_DIS_431_OG [0x1af]									
		MDP_XRT_ARS_DIS	1	07-F0	d5						
2012/02/20	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/02/20	18:12:56.0	XRT_QT_PROG_SET_410_OG [0x19a]									
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b						
2012/02/20	18:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]									
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03						
2012/02/20	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/02/20	18:41:30.0	XRT_CTRL_MANU_408_OG [0x198]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/02/20	18:41:32.0	XRT_FLD_RESET_412_OG [0x19c]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/02/20	18:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/02/20	18:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/02/20	19:04:30.0	XRT_Custom_418_OG [0x1a2]									
2012/02/20	19:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/02/20	20:18:30.0	XRT_CTRL_MANU_408_OG [0x198]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/02/20	20:18:32.0	XRT_FLD_RESET_412_OG [0x19c]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/02/20	20:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/02/20	20:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/02/20	20:42:00.5	XRT_Custom_418_OG [0x1a2]									
2012/02/20	20:43:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2012/02/20	21:56:00.0	XRT_CTRL_MANU_408_OG [0x198]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2012/02/20	21:56:02.0	XRT_FLD_RESET_412_OG [0x19c]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2012/02/20	21:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2012/02/20	21:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2012/02/20	22:18:00.0	XRT_Custom_418_OG [0x1a2]									
2012/02/20	22:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						

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2012/02/20	23:33:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/20	23:33:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/20	23:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/20	23:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/20	23:45:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/20	23:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/21	01:01:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/21	01:01:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/21	01:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/21	01:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/21	01:16:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/21	01:17:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/21	02:32:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/21	02:32:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/21	02:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/21	02:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/21	02:53:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/21	02:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/21	04:01:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/21	04:01:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/21	04:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/21	04:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/21	04:30:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/21	04:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/21	05:41:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/21	05:41:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/21	05:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/21	05:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/21	06:08:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/21	06:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/21	06:17:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/21	06:17:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/02/21	06:17:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/02/21	06:17:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/02/21	06:17:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/02/21	06:17:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/21	06:20:28.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/02/21	06:20:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/21	06:27:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/21	06:27:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/02/21	06:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/02/21	06:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/21	06:30:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/21	06:30:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/21	06:30:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/21	06:30:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/21	06:30:26.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/02/21	06:30:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/02/21	06:30:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2012/02/21	07:22:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/21	07:22:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/21	07:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/21	07:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/21	07:45:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/21	07:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/21	09:01:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/21	09:01:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/21	09:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/21	09:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/21	11:14:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				