

XRT Timeline to be uploaded on 2012/02/23

Period: 2012/02/23 10:40:00 - 2012/02/28 10:28:00

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Normal mode

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XOB #18D7: Coronal cavity (6 filters)-HOP114 with ARs-DPCM 2x2-768FOV-Thin-Be_16s-AEC0-3min cad													
Term		Pointing (x, y)					Comment						
02/23 10:53:00 - 02/23 13:49:54		Fixed (456.0, -844.0)					# OP start + 10min, HOP114 cavity						
PROG= 14 Inf.-time(s)													
└─ Subr= 1 5-time(s) 180.0sec													
└─ Seqn= 10 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	768x768 (1064, 1048)	DPCM	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.83s	Obs	2x2	768x768 (1064, 1048)	DPCM	0	0	2.0sec
	C-poly/Open	C-poly/Open	close	Safe	Norm	4.00s	Obs	2x2	768x768 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	768x768 (1064, 1048)	DPCM	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	768x768 (1064, 1048)	DPCM	0	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	768x768 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 14 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18AF: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 4min cad													
Term		Pointing (x, y)					Comment						
02/23 13:53:00 - 02/23 17:57:24		Track (655.1, 326.5) ^{Ⓢ 02/23 13:50:00}					AR11422 Obs.						
02/23 18:10:30 - 02/24 06:29:54		Track (682.2, 323.1) ^{Ⓢ 02/23 18:07:30}					#Cont.						
02/24 07:13:00 - 02/24 09:39:54		Track (757.4, 311.9) ^{Ⓢ 02/24 07:10:00}					#AR11422 Obs.						
02/24 15:43:00 - 02/24 17:52:30		Track (799.9, 304.0) ^{Ⓢ 02/24 15:40:00}					AR11422 Obs.						
02/24 18:29:00 - 02/25 06:01:54		Track (812.6, 301.3) ^{Ⓢ 02/24 18:26:00}					# Cont.						
02/25 06:15:00 - 02/25 10:25:54		Track (860.2, 289.5) ^{Ⓢ 02/25 06:12:00}					#Cont.						
PROG= 04 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 96 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 62 11-time(s) 240.0sec													
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18A8: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be(18)												
Term		Pointing (x, y)					Comment					
02/23 18:00:30 - 02/23 18:07:24		Fixed (0.0, 0.0)					synoptic, shifted -2.5 min					
02/24 06:33:00 - 02/24 06:44:54		Fixed (0.0, 0.0)					synoptic, shifted 30.0 min, SOT FG flat					
02/24 18:19:00 - 02/24 18:25:54		Fixed (0.0, 0.0)					synoptic, shifted 16.0 min					
02/25 06:05:00 - 02/25 06:11:54		Fixed (0.0, 0.0)					synoptic, shifted 2.0 min					

PROG= 05 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 7 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 3 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #18BF: Synoptic 9 Filter- 2x2 Q98 Short exp 2 loop

Term	Pointing (x, y)	Comment
02/24 06:48:00 - 02/24 07:09:54	Fixed (0.0, 0.0)	synoptic, shifted 30.0 min, SOT FG flat
PROG= 17 2-time(s)		
Subr= 1 1-time(s) 180.0sec		
Seqn= 82 1-time(s) 25.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 98 1-time(s) 25.0sec		
Open/Ti-poly	Open/Ti-poly close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 74 1-time(s) 25.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 18 1-time(s) 25.0sec		
C-poly/Open	C-poly/Open close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
C-poly/Open	C-poly/Open close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 24 1-time(s) 25.0sec		
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 9 1-time(s) 4.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 180.0sec		
Seqn= 33 1-time(s) 4.0sec		
med-Al/Open	med-Al/Open close	Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 71 1-time(s) 4.0sec		
Open/thick-Be	Open/thick-Be close	Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 67 1-time(s) 4.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Dark 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 4 1-time(s) 4.0sec		
Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384

Term	Pointing (x, y)	Comment
02/24 09:43:00 - 02/24 15:39:54	Fixed (0.0, 900.0)	HOP081 North
PROG= 07 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 51 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Subr= 2 30-time(s) 2.0sec		
Seqn= 58 2-time(s) 30.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 8.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec

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Flare mode

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XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops

Term	Pointing (x, y)	Comment
02/23 10:53:00 - 02/23 13:49:54	Fixed (456.0, -844.0)	# OP start + 10min, HOP114 cavity
02/23 13:53:00 - 02/23 17:57:24	Track (655.1, 326.5) ^{Ⓜ 02/23 13:50:00}	AR11422 Obs.
02/23 18:10:30 - 02/24 06:29:54	Track (682.2, 323.1) ^{Ⓜ 02/23 18:07:30}	#Cont.
02/24 07:13:00 - 02/24 09:39:54	Track (757.4, 311.9) ^{Ⓜ 02/24 07:10:00}	#AR11422 Obs.
02/24 09:43:00 - 02/24 15:39:54	Fixed (0.0, 900.0)	HOP081 North
02/24 15:43:00 - 02/24 17:52:30	Track (799.9, 304.0) ^{Ⓜ 02/24 15:40:00}	AR11422 Obs.
02/24 18:29:00 - 02/25 06:01:54	Track (812.6, 301.3) ^{Ⓜ 02/24 18:26:00}	# Cont.
02/25 06:15:00 - 02/25 10:25:54	Track (860.2, 289.5) ^{Ⓜ 02/25 06:12:00}	#Cont.
PROG= 03 12-time(s)		
Subr= 1 45-time(s) 10.0sec		
Seqn= 20 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 63 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 77 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Subr= 2 1-time(s) 10.0sec		
Seqn= 90 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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Active Region Search

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NOT USED

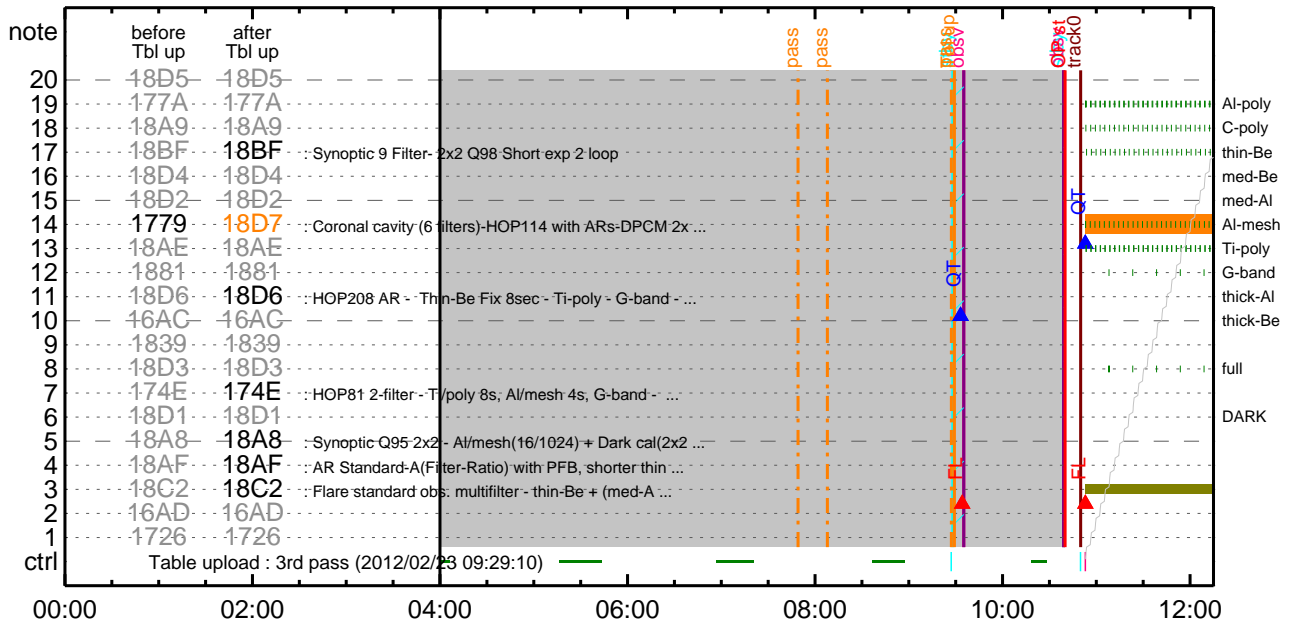
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Flare Detection

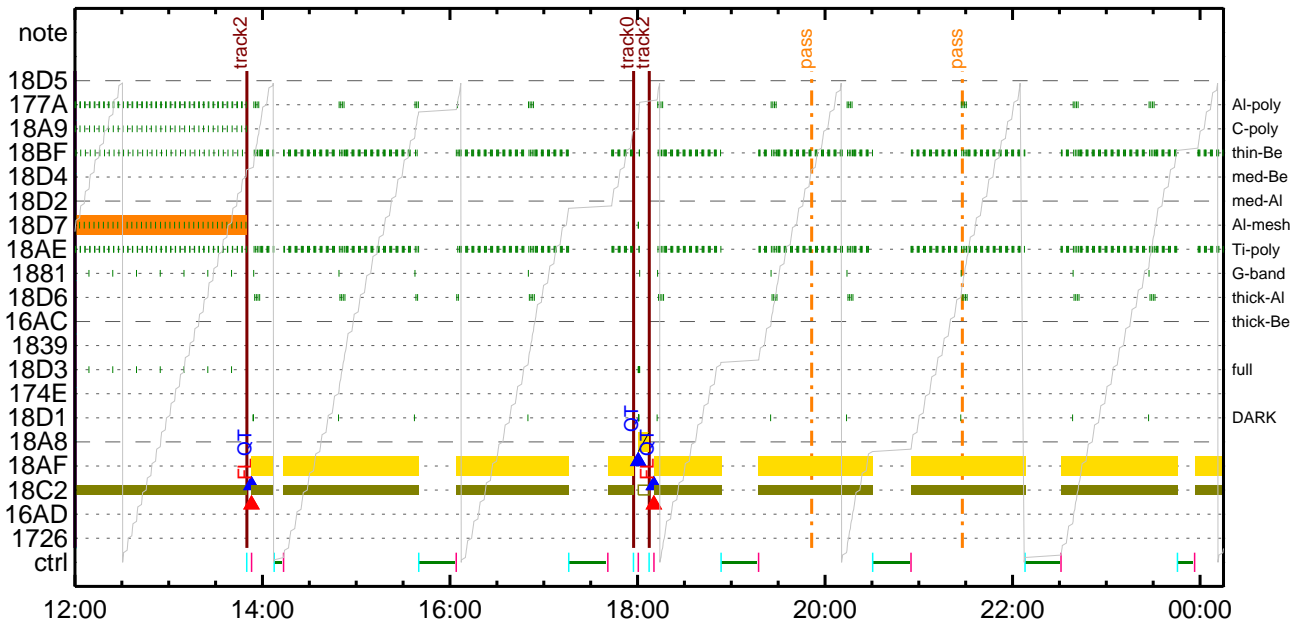
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FLD Patrol												
Term		Pointing (x, y)						Comment				
02/23 18:10:16 - 02/24 06:30:16	Track (682.2, 323.1)	④ 02/23 18:07:30				#Cont.						
02/24 07:12:46 - 02/24 18:16:16	Track (757.4, 311.9)	④ 02/24 07:10:00				#AR11422 Obs.						
02/24 18:28:46 - 02/25 06:02:16	Track (812.6, 301.3)	④ 02/24 18:26:00				# Cont.						
02/25 06:14:46 - 02/28 10:28:00	Track (860.2, 289.5)	④ 02/25 06:12:00				#Cont.						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval	

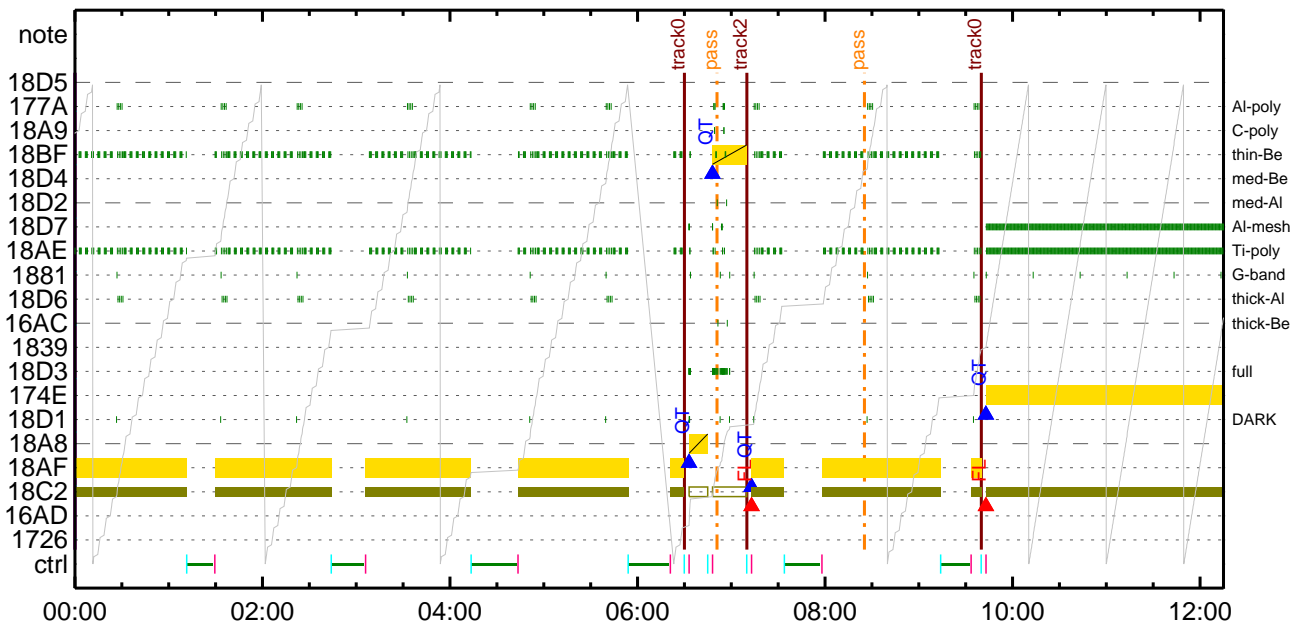
CMDI #0444 2012/02/23



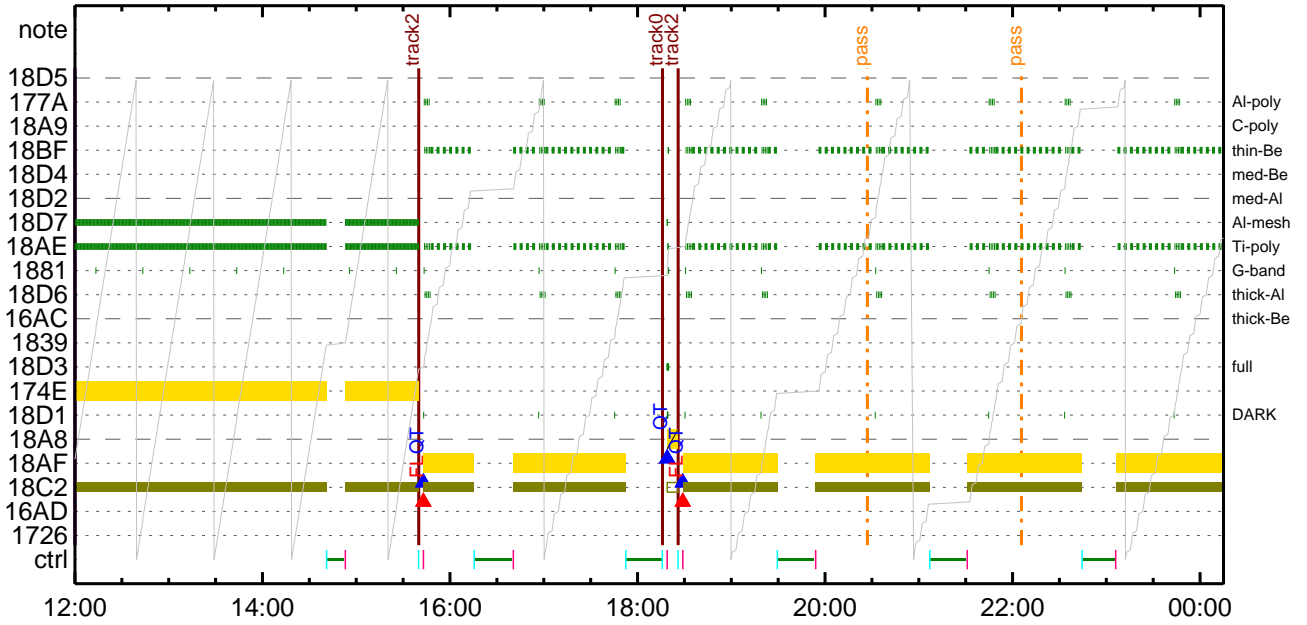
CMDI #0444 2012/02/23



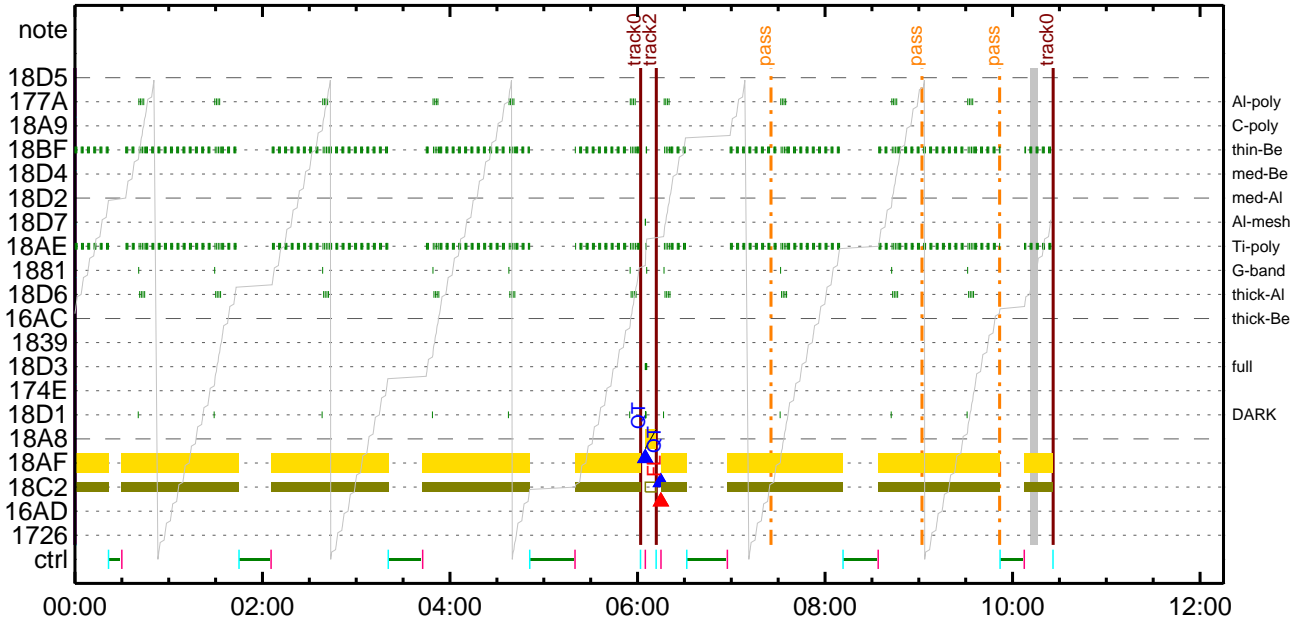
CMDI #0444 2012/02/24



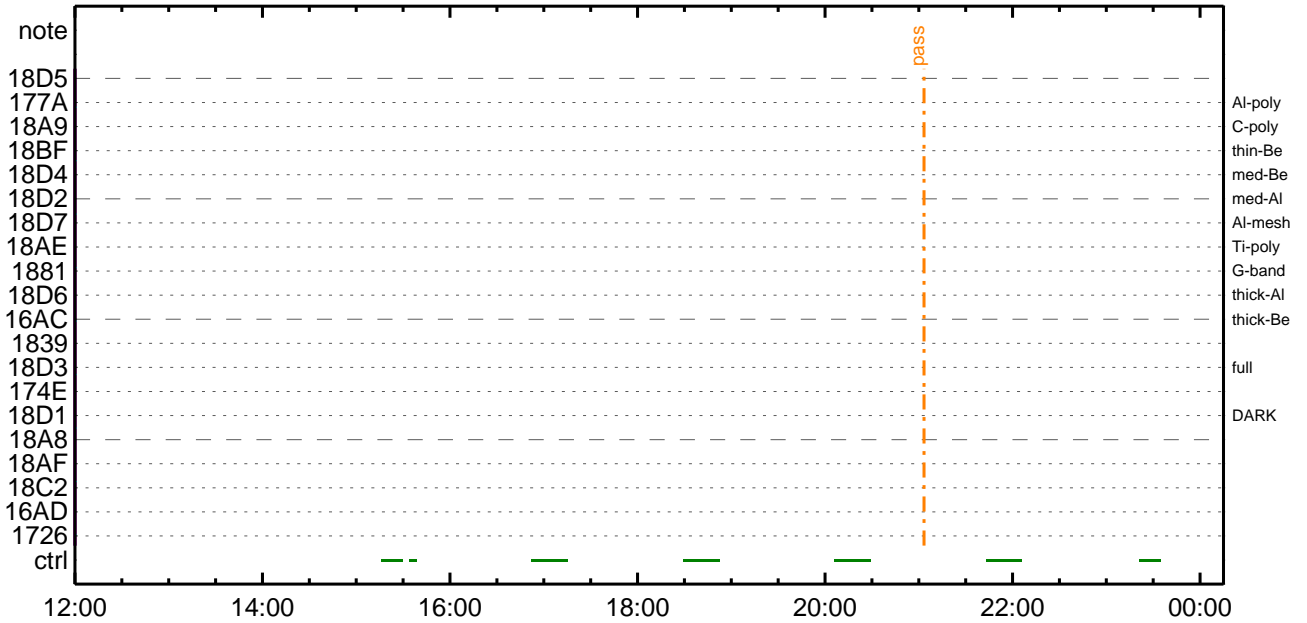
CMDI #0444 2012/02/24



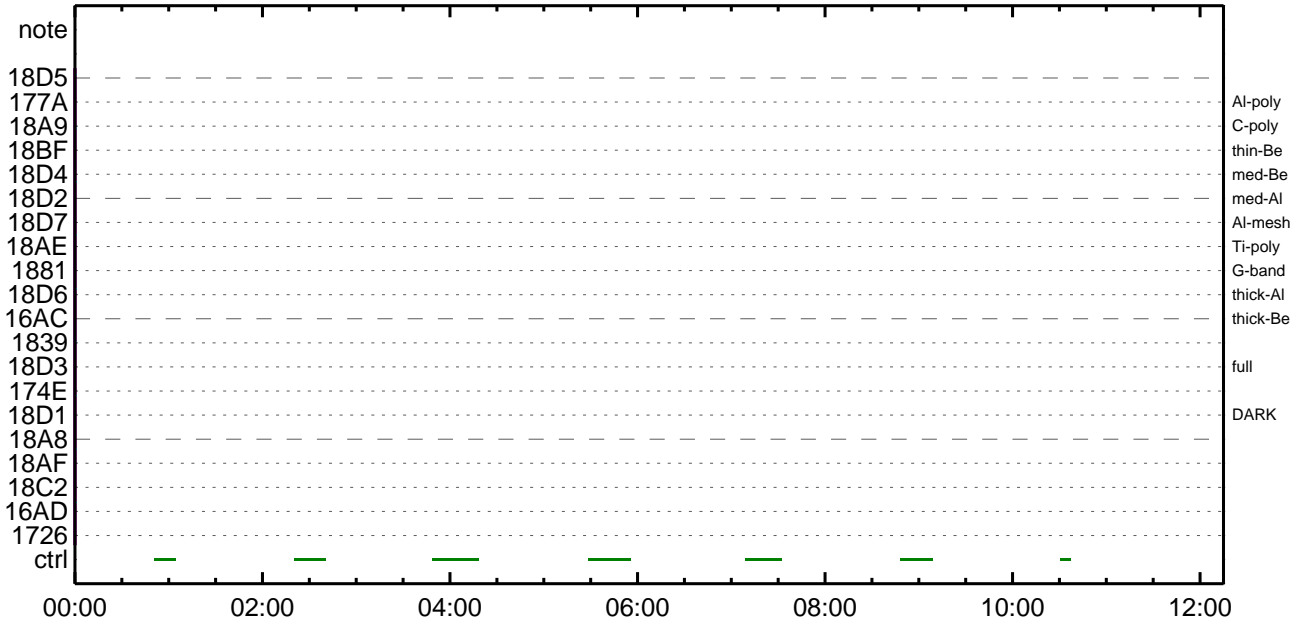
CMDI #0444 2012/02/25



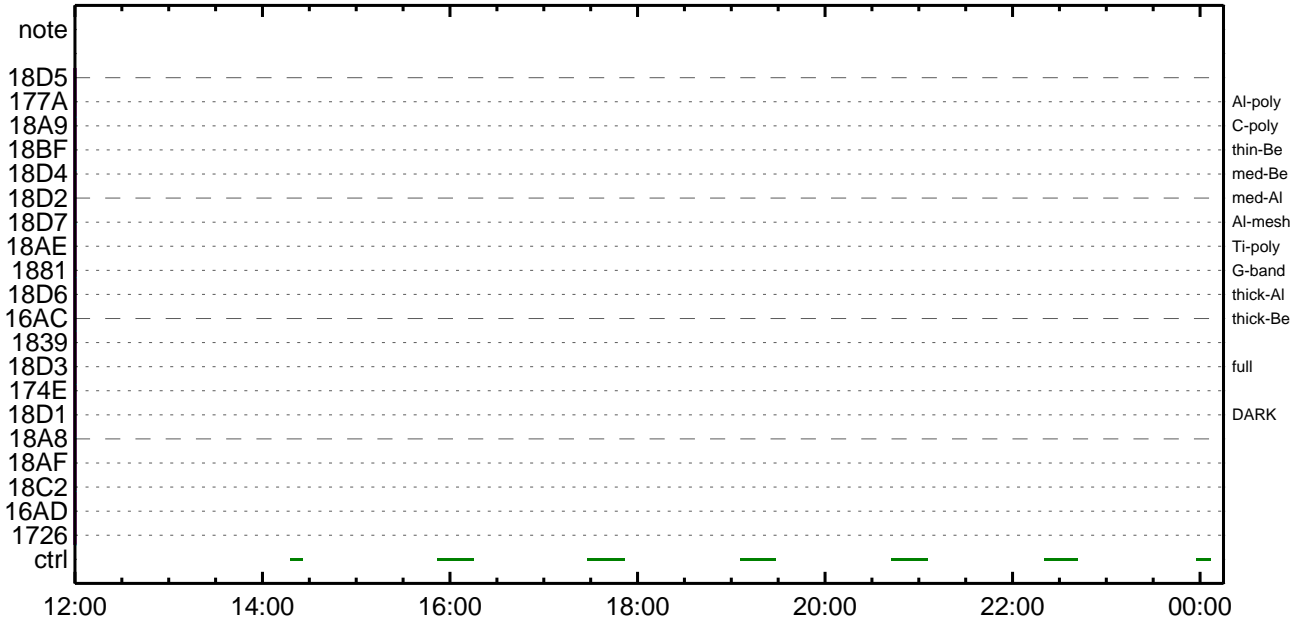
CMDI #0444 2012/02/25



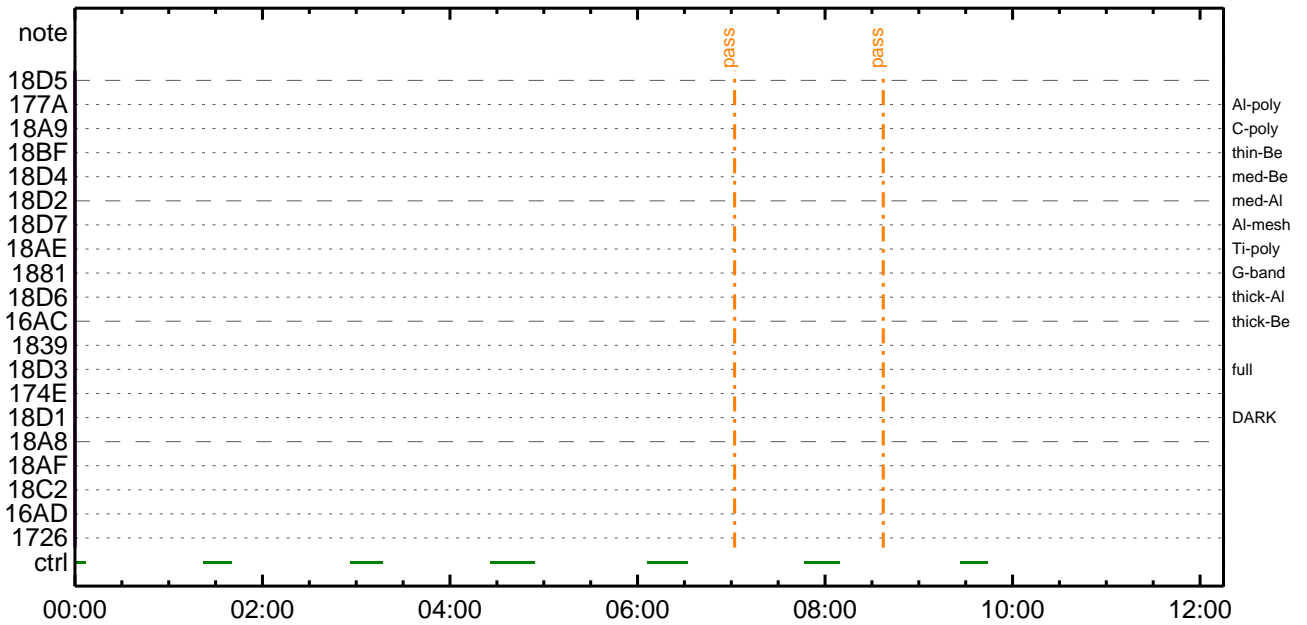
CMDI #0444 2012/02/26



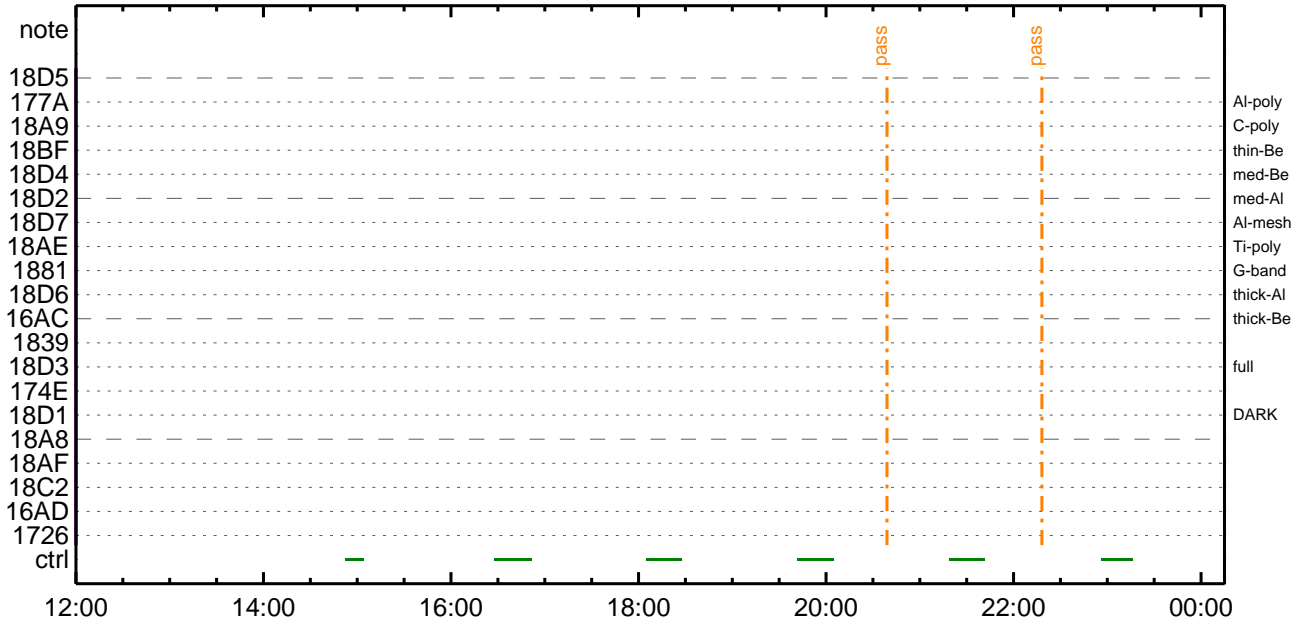
CMDI #0444 2012/02/26



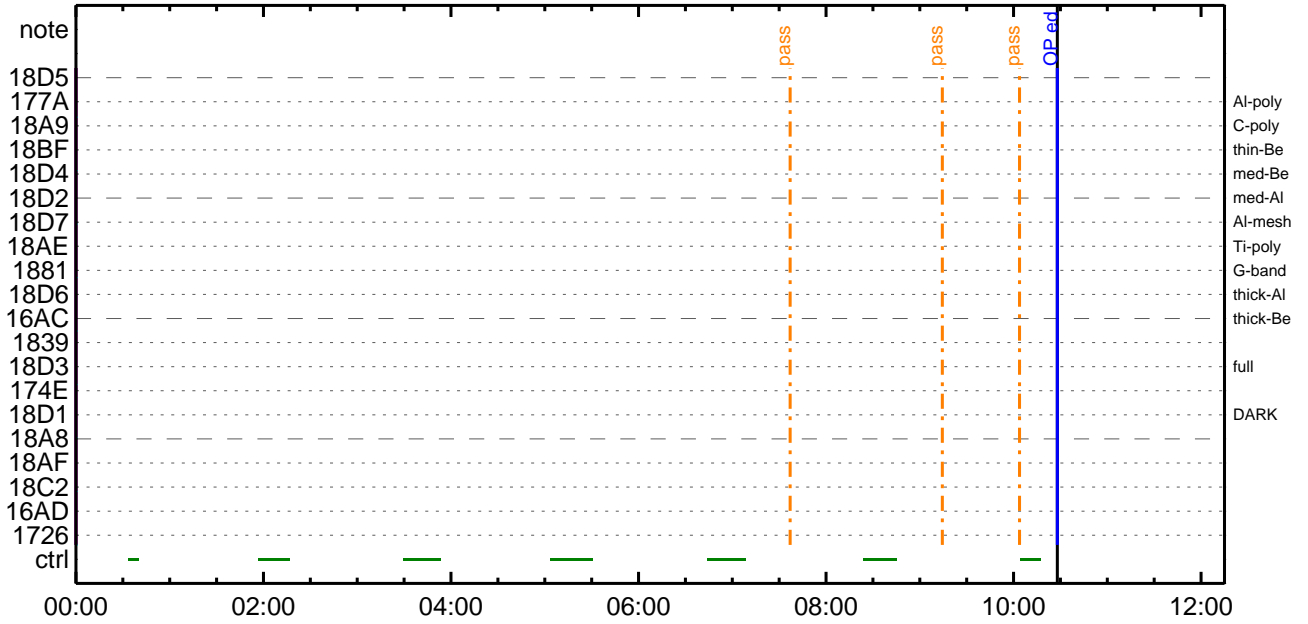
CMDI #0444 2012/02/27



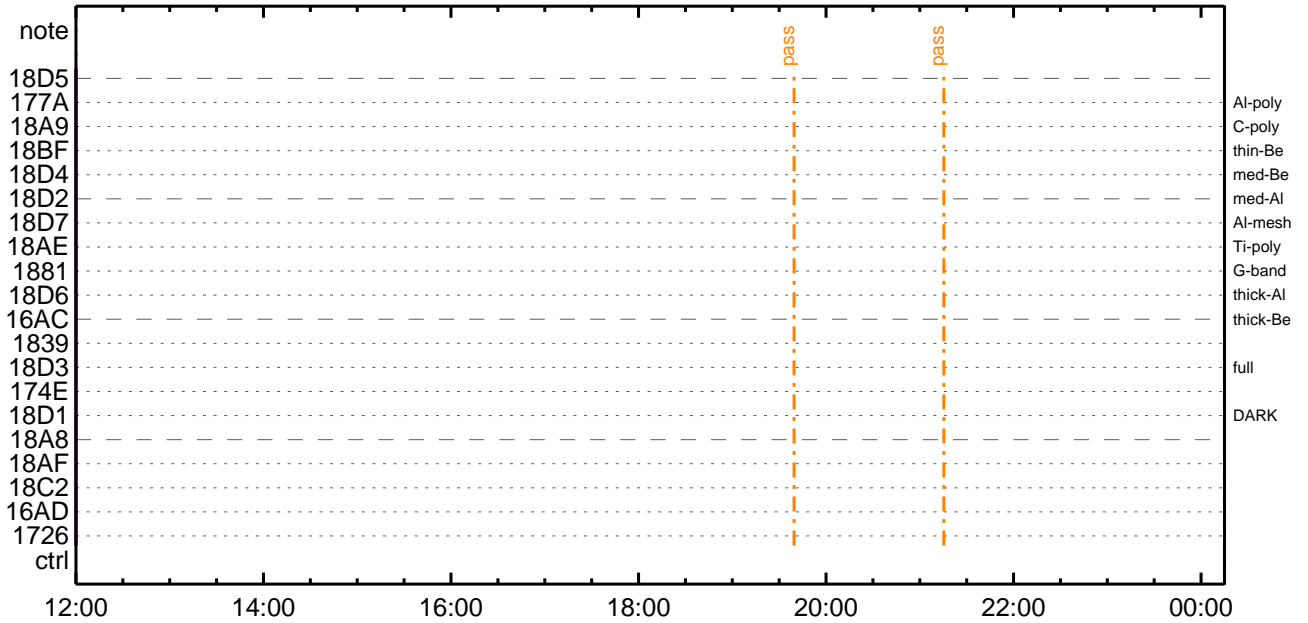
CMDI #0444 2012/02/27



CMDI #0444 2012/02/28



CMDI #0444 2012/02/28




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-674:OP
0104 ( )
0105 S. OG og-674:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî¼Ë¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî¼Ë¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPãî¼Ë¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °Ë²¼òî¼Ã´¶Á°òËË¬ò°Á÷¿® (¼åµ-YAYOYx½ªî»ò³îÇ§òðÃÏæòÇ¼ª°¬òè¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;Ë¼Y½;Yi;4YE;Ëòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD¬-Á÷¿®NGUî¼î¹ç;ç°Ë²¼òî¼TI-CMDÁ÷¿®¼î¼Á¹Ïò•òËòòò³òË;f
0180 C. ò³ò¿;çSETòEDUMPAîÆ±°îYNY¹ç¹Ïò|ò³òË;f
0181 C.
0182 C. TIY³YpYóYËòðÃî¿¿(UT)
0183 +. TI 2012-02-23 10:35:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-02-23 10:35:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-02-23 10:35:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-675 2012-02-23 12:40:02 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Ü;ã
0005 C.
0006 C. YÀYB;¼Y³YFÝÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Êõ¿òÃö•µ°Æ»Í×ÃÇõÍYçYÁY×Yí;¼YÉ;ËË%µ•íÉ;ÈõÈ¼°ÇÔö•õ¿¼ì¹çõí;çÀ®.ùò¹òèòÈòÇÁ+¿®ö•òÈöóó³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 28s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2012-02-23 10:39:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ûÁîõî»ó¼YõÈÃõ¹òèDCBC•x²è *****
0069 C. (¼á°ìYÖYÁYÉYÈYãYçYèõÈ%¼õ¼Á»Üò¹òè)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YDŸ¹.Ï Daily±¿îÑòÈ´Øò¹òèDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;ãLOSÁYŞYÁY~¼Á»Ü;ã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-269:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2012-02-23 10:39:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 80 80 06 06)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 85 83 0c 0c)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 20)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 85 83 06 06)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 85 83 08 08)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0b 80 80 20 08)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 0c 80 80 08 20)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 0f 80 80 06 06)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 10 80 80 08 08)
0176 + DC 07-F0 MDP_XRT_FLD_ENA
0177 BC (d8)
0178 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0179 BC (c8)
0180 + DC 07-F0 MDP_XRT_AEC_RESET
0181 BC (d0)
0182 + DC 07-F0 MDP_XRT_ARS_DIS
0183 BC (d5)
0184 + DC 07-F0 MDP_XRT_FLD_RESET
0185 BC (da)
0186 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0187 BC (c4 0b)
0188 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0189 BC (c5 03)
0190 . C. ----- Success Verify ? OK / NG ____
0191 C.
0192 C.
0193 . C. All OK? Yes--> Please Proceed. / No --> Stop here.

```


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*** OP Sequence for XRT ***

2012/02/23	10:49:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/23	10:50:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 4b 02 d7 75				
2012/02/23	10:52:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/02/23	10:52:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/23	10:52:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/23	10:52:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/23	10:52:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/23	10:52:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/23	10:52:56.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2012/02/23	10:52:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/02/23	10:53:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/23	13:49:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/23	13:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/02/23	13:52:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/02/23	13:52:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/23	13:52:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/23	13:52:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/23	13:52:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/23	13:52:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/23	13:52:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2012/02/23	13:52:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/02/23	13:53:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/23	14:07:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/23	14:07:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/23	14:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/23	14:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/23	14:12:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/23	14:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/23	15:40:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/23	15:40:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/23	15:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/23	15:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/23	16:03:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/23	16:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/23	17:16:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/23	17:16:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/23	17:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/23	17:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/23	17:40:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/23	17:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/23	17:57:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/23	17:57:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/02/23	17:57:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/02/23	17:57:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/02/23	17:57:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/02/23	17:57:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/23	18:00:28.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				

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2012/02/23	18:00:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/23	18:07:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/23	18:07:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/02/23	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/02/23	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/23	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/23	18:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/23	18:10:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/23	18:10:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/23	18:10:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2012/02/23	18:10:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/02/23	18:10:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/23	18:53:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/23	18:53:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/23	18:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/23	18:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/23	19:16:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/23	19:17:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/23	20:30:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/23	20:30:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/23	20:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/23	20:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/23	20:54:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/23	20:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/23	22:08:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/23	22:08:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/23	22:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/23	22:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/23	22:30:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/23	22:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/23	23:45:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/23	23:45:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/23	23:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/23	23:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/23	23:55:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/23	23:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/24	01:11:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/24	01:11:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/24	01:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/24	01:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/24	01:28:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/24	01:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/24	02:44:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/24	02:44:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/24	02:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/24	02:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/24	03:05:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/24	03:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/24	04:13:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/24	04:13:32.0	XRT_FLD_RESET_412_OG [0x19c]							

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2012/02/24	04:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/24	04:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/24	04:42:30.0	XRT_Custom_418_OG [0x1a2]								
2012/02/24	04:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/24	05:54:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/24	05:54:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/24	05:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/24	05:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/24	06:20:00.0	XRT_Custom_418_OG [0x1a2]								
2012/02/24	06:21:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/24	06:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/24	06:29:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/02/24	06:30:00.5	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
2012/02/24	06:30:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/02/24	06:30:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/02/24	06:30:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/24	06:32:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/02/24	06:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/24	06:44:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/24	06:44:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/02/24	06:45:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/02/24	06:45:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/02/24	06:45:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/24	06:47:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2012/02/24	06:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/24	07:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/24	07:10:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00				
2012/02/24	07:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/02/24	07:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/24	07:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/24	07:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/24	07:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/24	07:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/24	07:12:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2012/02/24	07:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/02/24	07:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/24	07:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/24	07:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/24	07:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/24	07:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/24	07:57:00.0	XRT_Custom_418_OG [0x1a2]								
2012/02/24	07:58:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/24	09:14:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/24	09:14:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/24	09:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/24	09:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/24	09:32:30.0	XRT_Custom_418_OG [0x1a2]								
2012/02/24	09:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2012/02/24	09:39:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/24	09:40:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 b0 00 00 00
2012/02/24	09:42:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/02/24	09:42:46.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/02/24	09:42:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/02/24	09:42:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/02/24	09:42:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/24	09:42:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/24	09:42:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2012/02/24	09:42:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/02/24	09:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/24	14:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/24	14:41:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/24	14:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/24	14:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/24	14:52:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/24	14:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/24	15:39:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/24	15:40:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00
2012/02/24	15:42:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/02/24	15:42:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/02/24	15:42:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/02/24	15:42:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/02/24	15:42:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/24	15:42:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/24	15:42:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/02/24	15:42:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/02/24	15:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/24	16:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/24	16:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/24	16:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/24	16:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/24	16:39:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/24	16:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/24	17:52:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/24	17:52:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/24	17:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/24	17:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/24	18:15:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/24	18:15:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/02/24	18:16:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2012/02/24	18:16:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/02/24	18:16:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/02/24	18:16:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/24	18:18:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/02/24	18:19:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/24	18:25:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/24	18:26:00.0	AOCS_Ore-point_Start_2_OG [0x098]				

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2012/02/24	18:28:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	02	00	00	00	00
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2012/02/24	18:28:46.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
2012/02/24	18:28:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	c8				
2012/02/24	18:28:50.5	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	d0				
2012/02/24	18:28:52.5	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d5				
2012/02/24	18:28:54.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	da				
2012/02/24	18:28:56.5	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_FLD_RESET	1	07-F0	c4	04			
2012/02/24	18:28:58.5	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c5	03			
2012/02/24	18:29:00.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c0				
2012/02/24	19:29:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c1				
2012/02/24	19:29:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	da				
2012/02/24	19:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	e8				
2012/02/24	19:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e9				
2012/02/24	19:53:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/24	19:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
2012/02/24	21:07:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c1				
2012/02/24	21:07:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	da				
2012/02/24	21:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	e8				
2012/02/24	21:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e9				
2012/02/24	21:30:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/24	21:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
2012/02/24	22:44:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c1				
2012/02/24	22:44:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	da				
2012/02/24	22:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	e8				
2012/02/24	22:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e9				
2012/02/24	23:05:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/24	23:06:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
2012/02/25	00:21:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c1				
2012/02/25	00:21:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	da				
2012/02/25	00:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	e8				
2012/02/25	00:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e9				
2012/02/25	00:29:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/25	00:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
2012/02/25	01:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c1				
2012/02/25	01:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	da				
2012/02/25	01:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	e8				
2012/02/25	01:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e9				
2012/02/25	02:04:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/25	02:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
2012/02/25	03:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c1				
2012/02/25	03:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	da				
2012/02/25	03:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	e8				
2012/02/25	03:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e9				
2012/02/25	03:41:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/25	03:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
2012/02/25	04:51:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c1				
2012/02/25	04:51:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	da				
2012/02/25	04:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	e8				
2012/02/25	04:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2012/02/25	05:19:00.0	XRT_Custom_418_OG [0x1a2]			
2012/02/25	05:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/25	06:01:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/25	06:01:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/02/25	06:02:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/02/25	06:02:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/02/25	06:02:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/02/25	06:02:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/25	06:04:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/02/25	06:05:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/25	06:11:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/25	06:12:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	02 00 00 00 00
2012/02/25	06:14:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/02/25	06:14:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/02/25	06:14:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/02/25	06:14:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/02/25	06:14:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/25	06:14:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/25	06:14:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/02/25	06:14:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/02/25	06:15:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/25	06:31:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/25	06:31:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/25	06:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/25	06:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/25	06:56:30.0	XRT_Custom_418_OG [0x1a2]			
2012/02/25	06:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/25	08:11:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/25	08:11:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/25	08:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/25	08:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/25	08:33:00.0	XRT_Custom_418_OG [0x1a2]			
2012/02/25	08:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/25	09:52:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/25	09:52:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/25	09:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/25	09:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/25	10:06:30.0	XRT_Custom_418_OG [0x1a2]			
2012/02/25	10:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/25	10:25:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/25	10:26:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00