

XRT Timeline to be uploaded on 2012/02/28

Period: 2012/02/28 10:28:00 - 2012/03/03 09:39:00

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Normal mode

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XOB #18B0: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(24/1443) + Thin-Be(88/2)

Term	Pointing (x, y)	Comment
02/28 15:55:00 - 02/28 17:04:00	Fixed (-14.0, 30.0)	# 11/20
02/29 06:20:30 - 02/29 06:27:24	Fixed (0.0, 0.0)	synoptic, shifted 17.5 min
02/29 18:07:00 - 02/29 18:13:54	Fixed (0.0, 0.0)	synoptic, shifted 4.0 min
03/01 05:53:30 - 03/01 06:00:24	Fixed (0.0, 0.0)	synoptic, shifted -9.5 min

PROG= 08 1-time(s)

- Subr= 1 1-time(s) 12.0sec
 - Seqn= 46 1-time(s) 4.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 5 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
 - Seqn= 69 1-time(s) 4.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 50 1-time(s) 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 4 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #18D7: Coronal cavity (6 filters)-HOP114 with ARs-DPCM 2x2-768FOV-Thin-Be_16s-AEC0-3min cad

Term	Pointing (x, y)	Comment
02/28 23:18:00 - 02/29 02:32:00	Track (797.1, -385.2) ^{02/28 23:15:00}	# Observe filament at SW limb.

PROG= 14 Inf.-time(s)

- Subr= 1 5-time(s) 180.0sec
 - Seqn= 10 1-time(s) 4.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec
 - Al-poly/Open Al-poly/Open close Safe Norm 2.83s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec
 - C-poly/Open C-poly/Open close Safe Norm 4.00s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Norm 4.00s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec
 - Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 5.66s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 16.0s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
 - Seqn= 14 1-time(s) 4.0sec
 - Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #18C7: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 30s cad

Term	Pointing (x, y)	Comment
02/29 03:03:00 - 02/29 06:17:24	Track (-400.4, 296.9) ^{02/29 03:00:00}	# AR 11424 observations.
02/29 06:30:30 - 02/29 08:59:54	Track (-371.3, 298.5) ^{02/29 06:27:30}	# AR obs.
02/29 09:21:30 - 02/29 17:40:00	Track (-349.7, 299.6) ^{02/29 09:00:00}	# Re-point to AR track 2 to allow possible change of target.
02/29 18:17:00 - 03/01 05:50:24	Track (-269.5, 303.2) ^{02/29 18:14:00}	# AR obs.
03/01 06:03:30 - 03/01 09:39:00	Track (-164.2, 306.4) ^{03/01 06:00:30}	# AR obs.

PROG= 16 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 19 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Seqn= 96 4-time(s) 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
 - Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
- Subr= 2 15-time(s) 2.0sec
 - Seqn= 42 1-time(s) 2.0sec
 - thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 7.0sec
 - thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 7.0sec
 - thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 7.0sec
 - Seqn= 43 1-time(s) 2.0sec
 - thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 7.0sec

thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	7.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	7.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops

Term	Pointing (x, y)	Comment
02/28 23:18:00 - 02/29 02:32:00	Track (797.1, -385.2) [Ⓜ] 02/28 23:15:00	# Observe filament at SW limb.
02/29 03:03:00 - 02/29 06:17:24	Track (-400.4, 296.9) [Ⓜ] 02/29 03:00:00	# AR 11424 observations.
02/29 06:30:30 - 02/29 08:59:54	Track (-371.3, 298.5) [Ⓜ] 02/29 06:27:30	# AR obs.
02/29 09:21:30 - 02/29 17:40:00	Track (-349.7, 299.6) [Ⓜ] 02/29 09:00:00	# Re-point to AR track 2 to allow possible change of target.
02/29 18:17:00 - 03/01 05:50:24	Track (-269.5, 303.2) [Ⓜ] 02/29 18:14:00	# AR obs.
03/01 06:03:30 - 03/01 09:39:00	Track (-164.2, 306.4) [Ⓜ] 03/01 06:00:30	# AR obs.

PROG= 03 12-time(s)

Subr= 1	45-time(s)	10.0sec										
Seqn= 20	1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 63	1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 77	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	10.0sec										
Seqn= 90	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

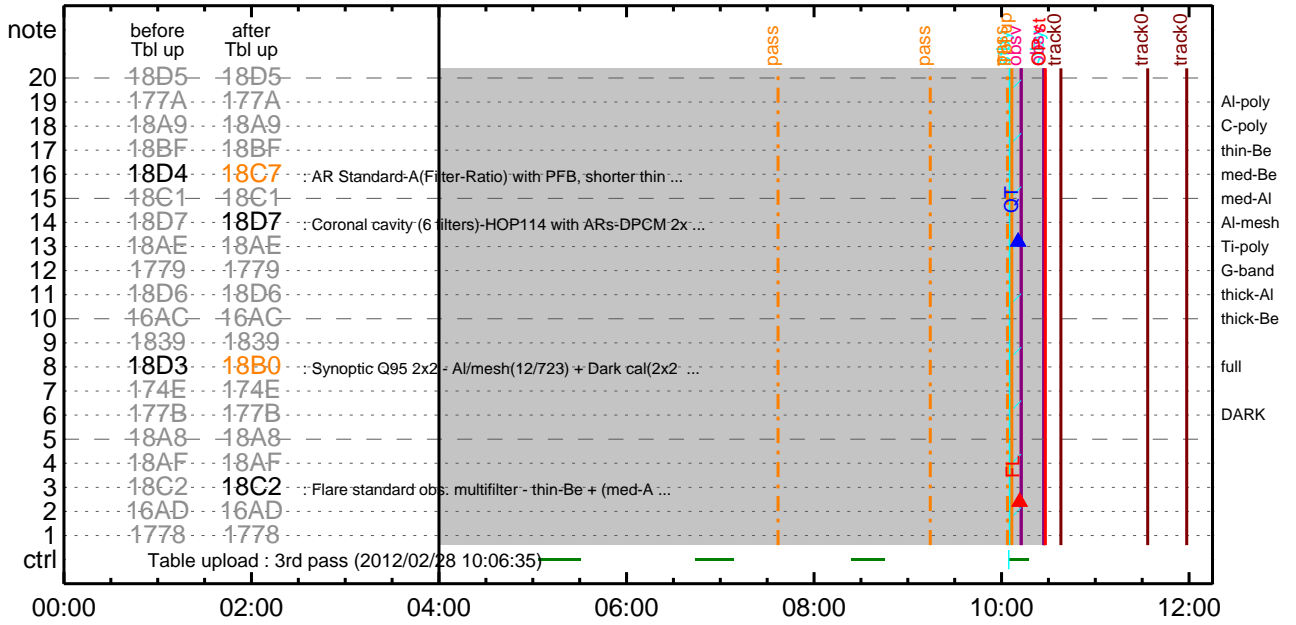
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Flare Detection

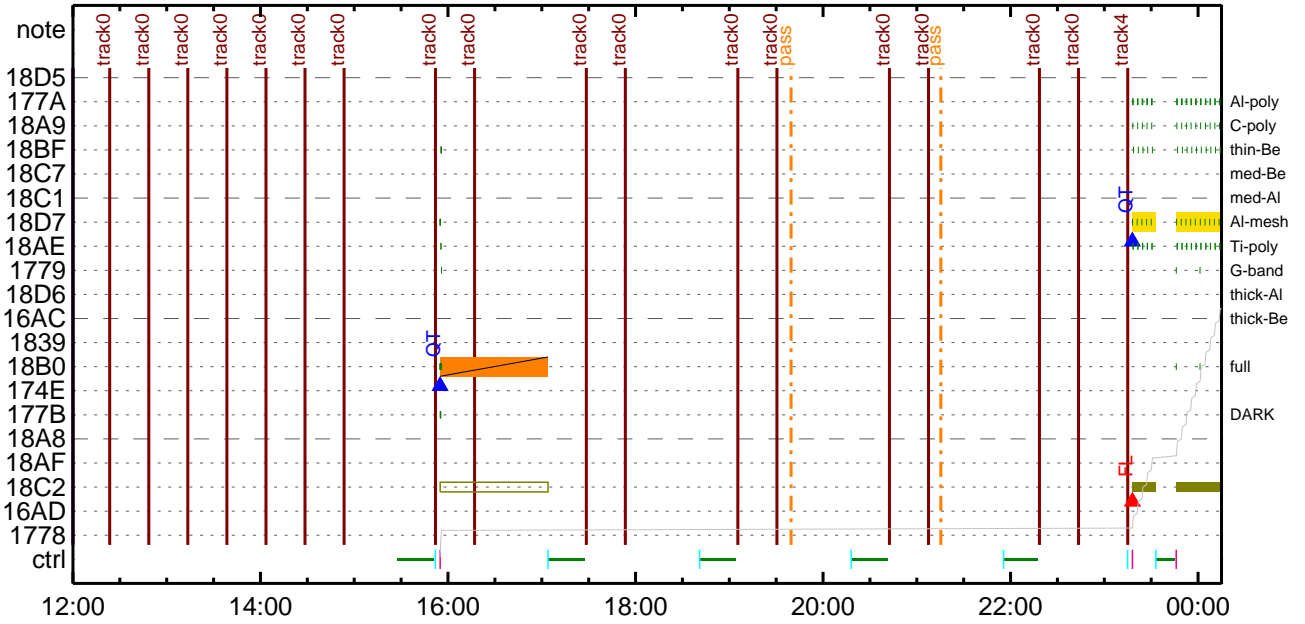
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Term	Pointing (x, y)	Comment										
02/28 23:17:46 - 02/29 06:17:46	Track (797.1, -385.2) [Ⓜ] 02/28 23:15:00	# Observe filament at SW limb.										
02/29 06:30:16 - 02/29 18:04:16	Track (-371.3, 298.5) [Ⓜ] 02/29 06:27:30	# AR obs.										
02/29 18:16:46 - 03/01 05:50:46	Track (-269.5, 303.2) [Ⓜ] 02/29 18:14:00	# AR obs.										
03/01 06:03:16 - 03/03 09:39:00	Track (-164.2, 306.4) [Ⓜ] 03/01 06:00:30	# AR obs.										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

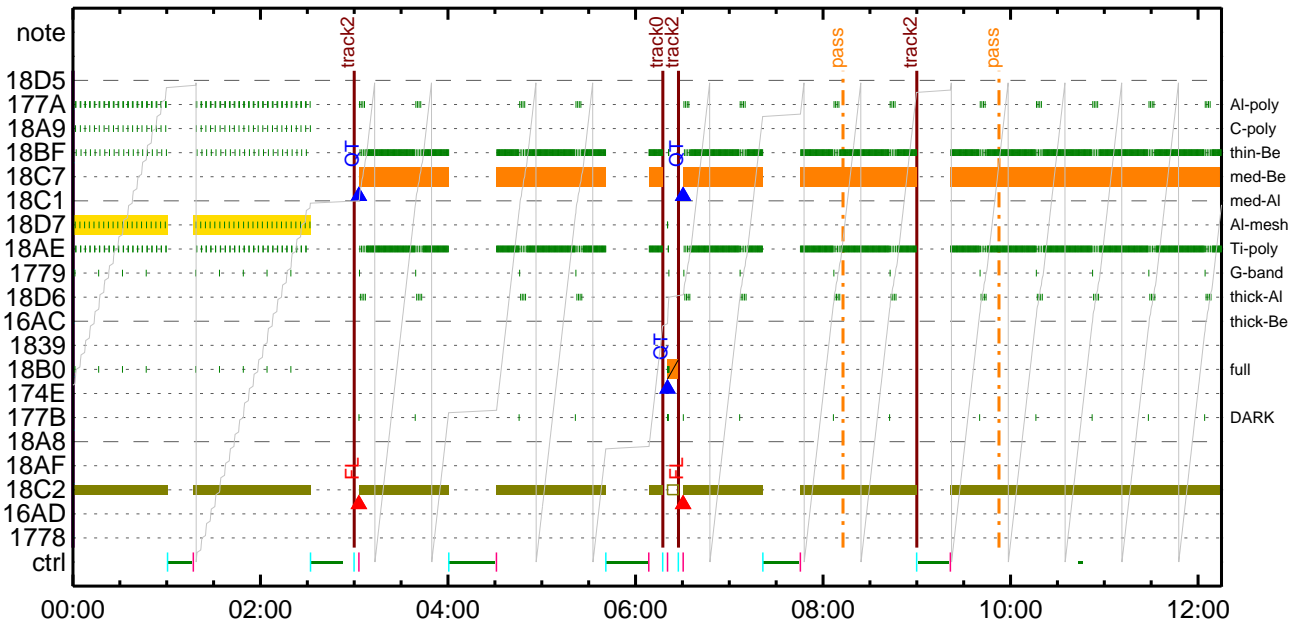
CMDI #0451 2012/02/28



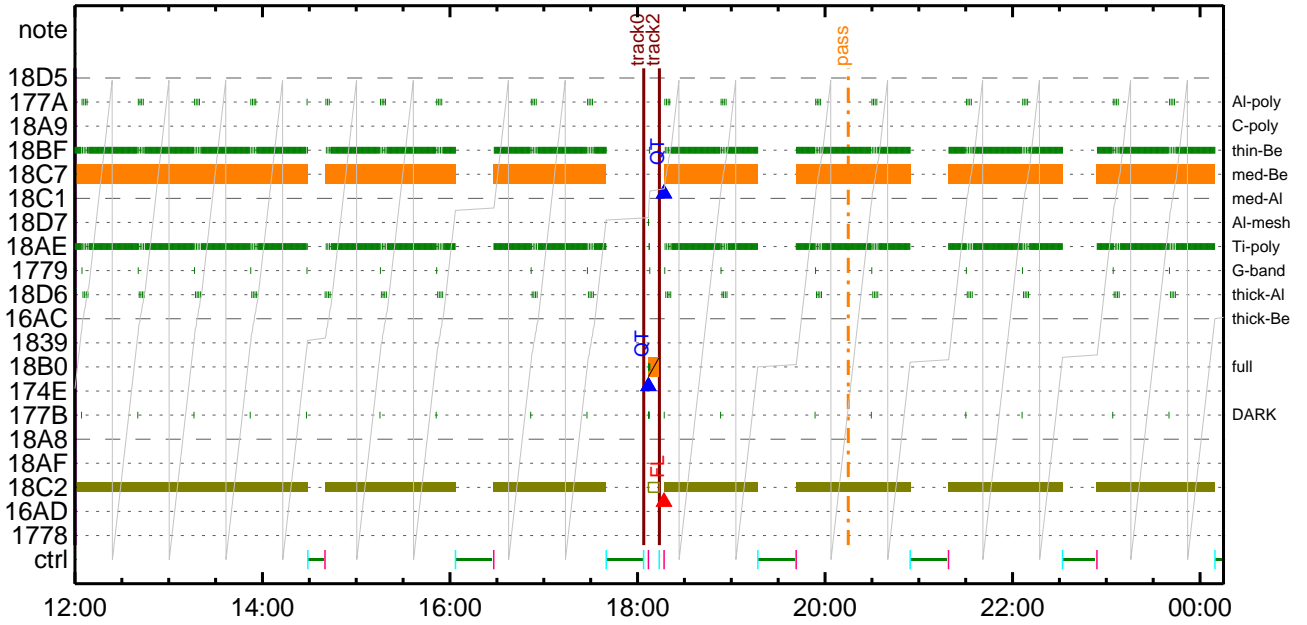
CMDI #0451 2012/02/28



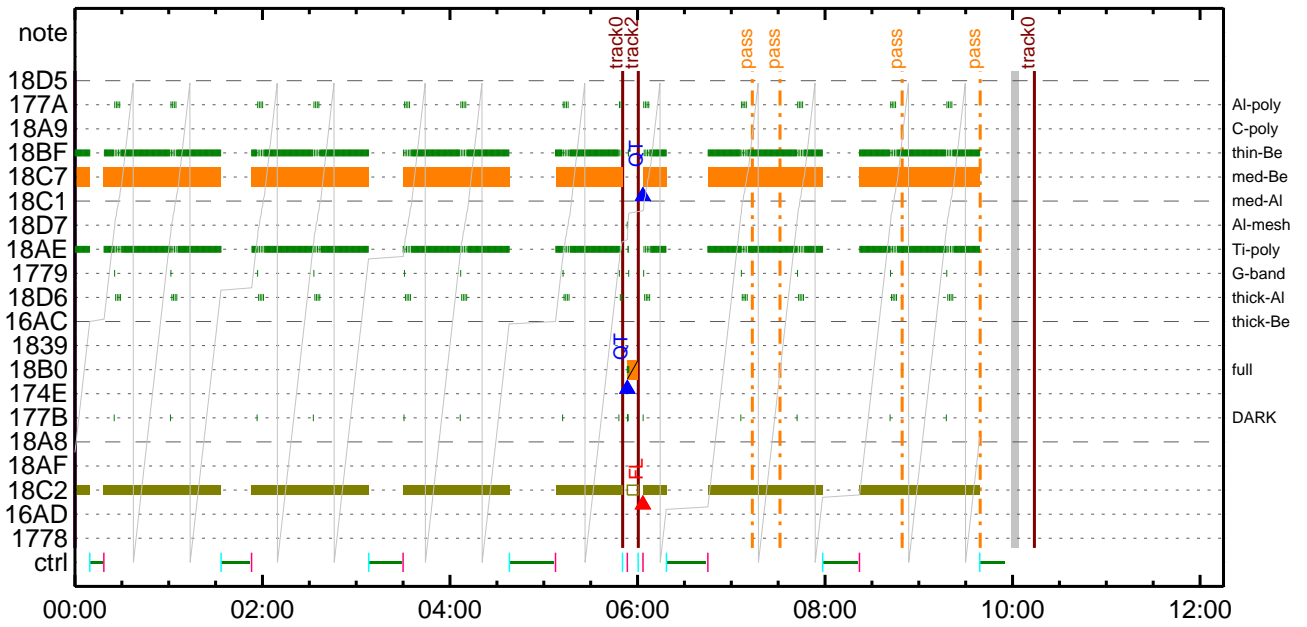
CMDI #0451 2012/02/29



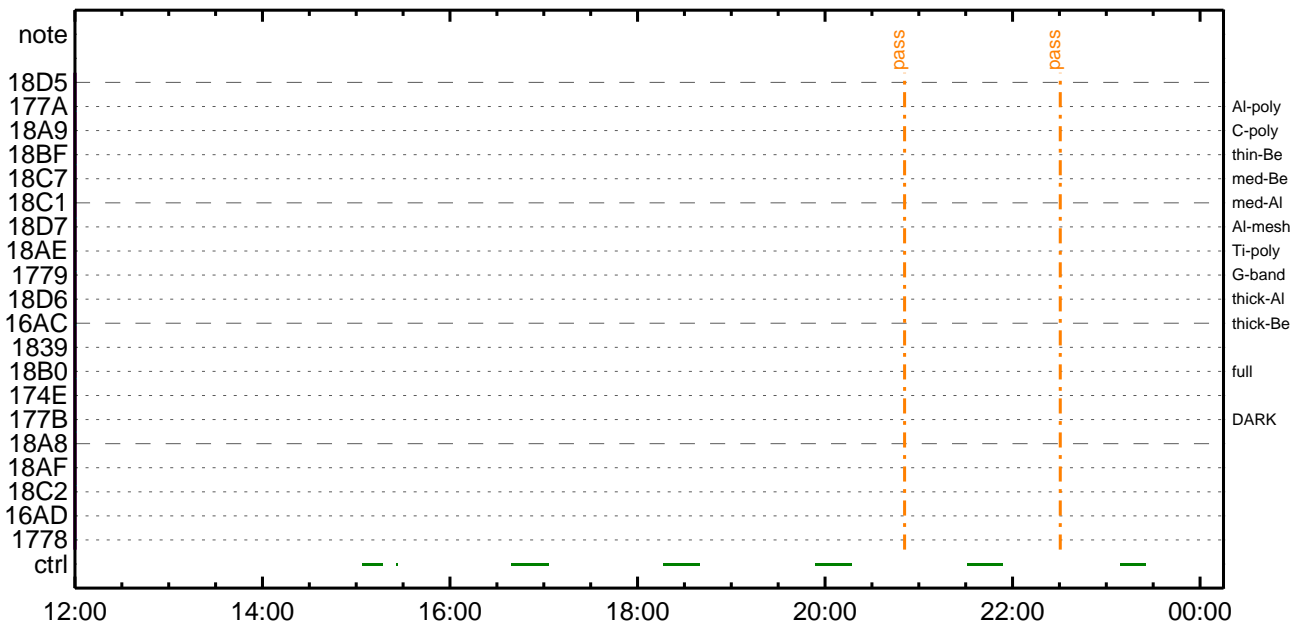
CMDI #0451 2012/02/29



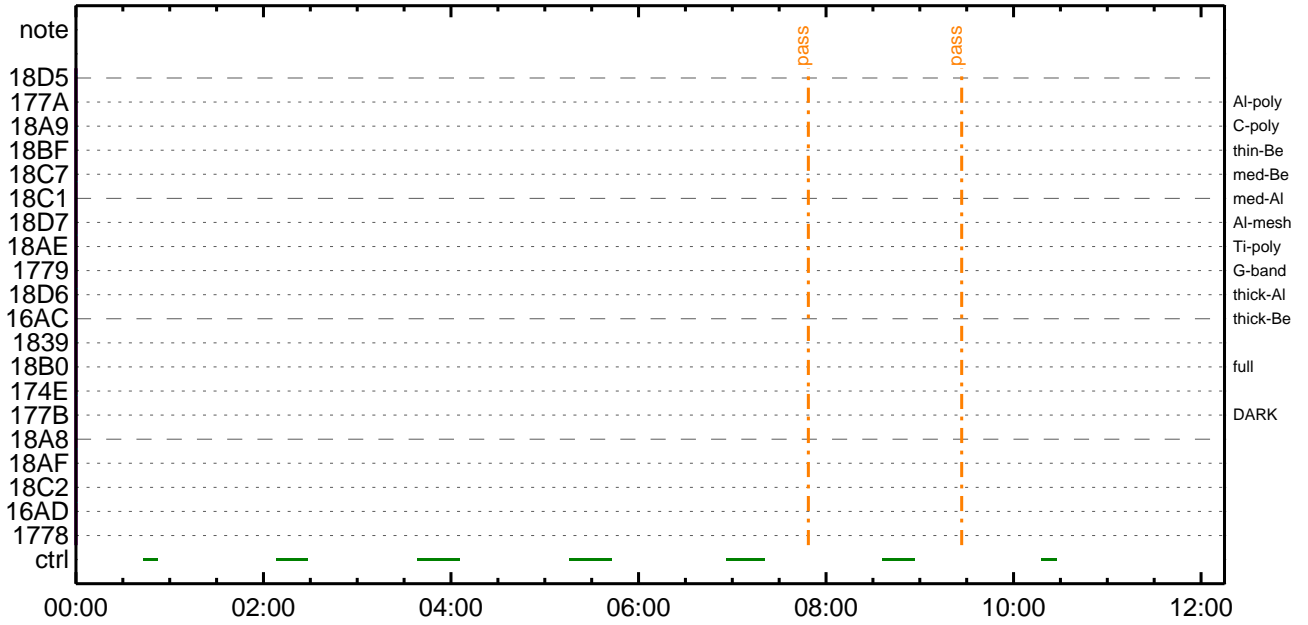
CMDI #0451 2012/03/01



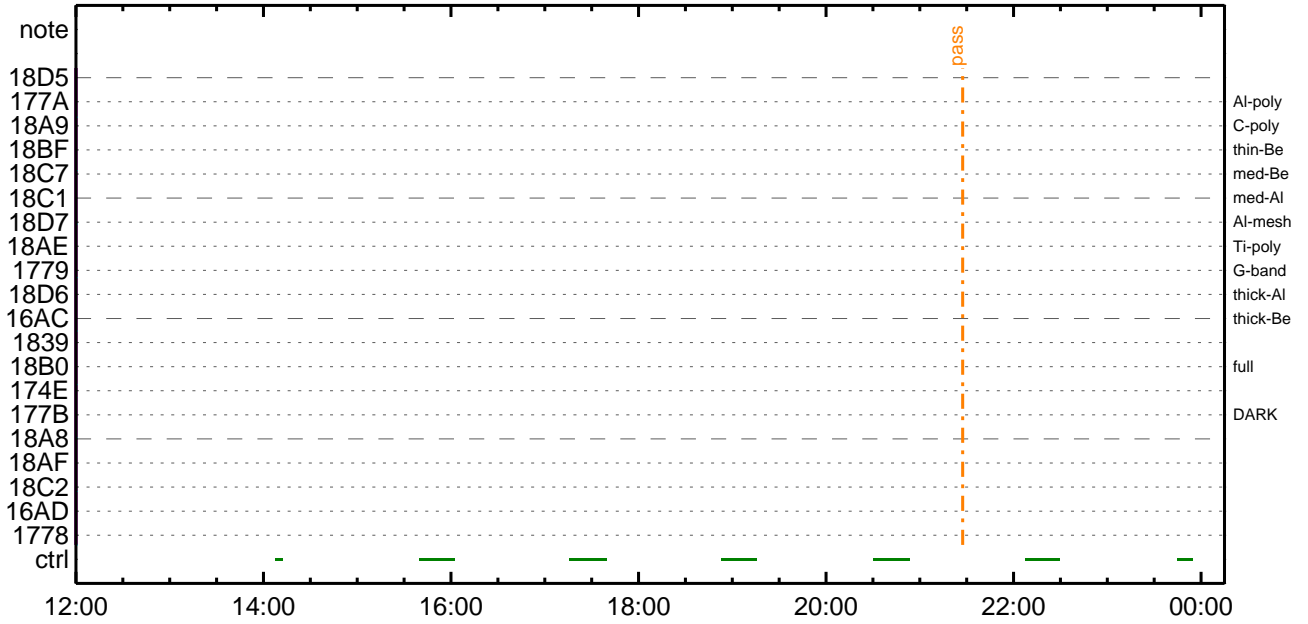
CMDI #0451 2012/03/01



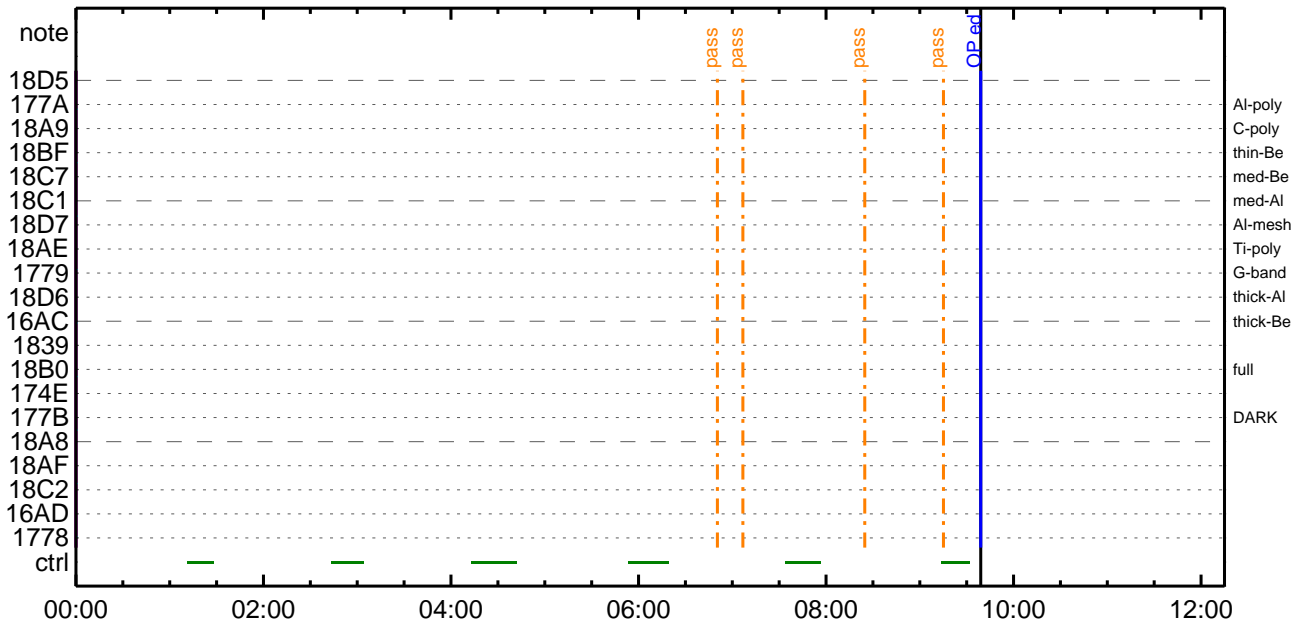
CMDI #0451 2012/03/02



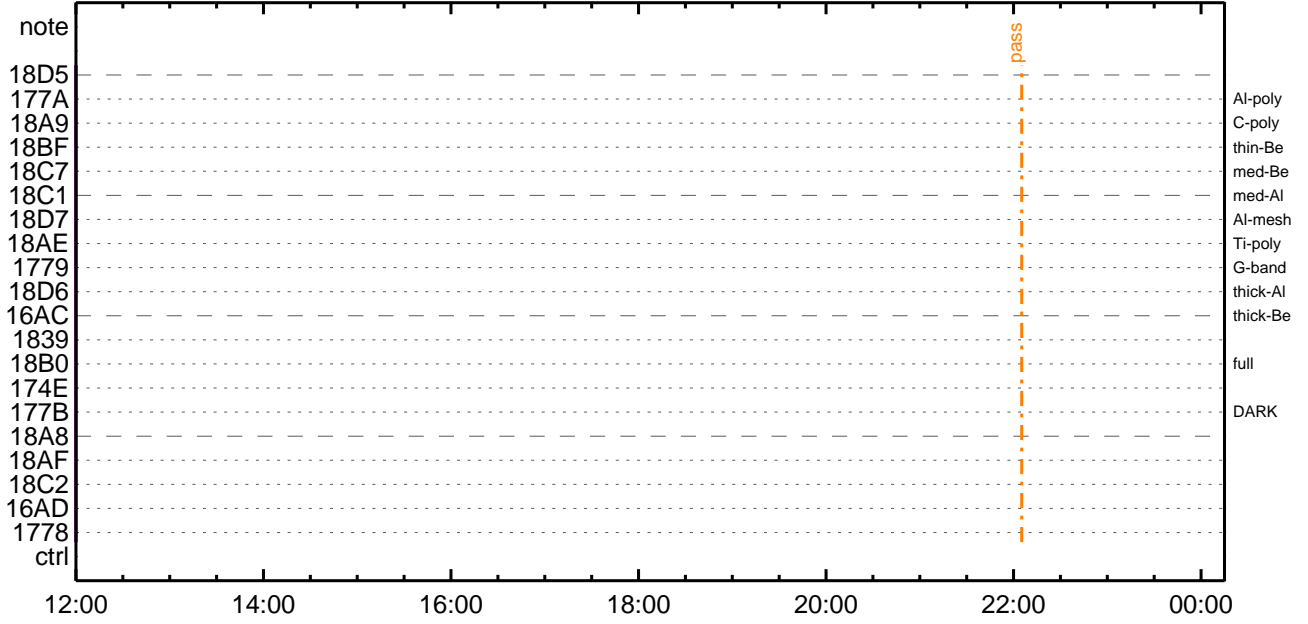
CMDI #0451 2012/03/02



CMDI #0451 2012/03/03



CMDI #0451 2012/03/03




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-683:OP
0104 ( )
0105 S. OG og-683:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî¼Ã´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î¼î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î¼Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²îÆ±°îYÑY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YóYÉòðÁDî¿(UT)
0183 +. TI 2012-02-28 10:23:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-02-28 10:23:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-02-28 10:23:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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(a) Spacecraft Operation Procedure (real-commands)

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main-685 2012-02-28 12:38:11 143 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈøÈ¼°ÇÒø•ø¿¼í¹çøÍ;çÀ®, ùø¹øÈøÈøÇÁ+¿®ø•øÈøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 0c 0c)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 85 83 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 85 83 08 08)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 10 80 80 08 08)
0058 + DC 07-F0 MDP_XRT_FLD_ENA
0059 BC (d8)
0060 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0061 BC (c8)
0062 + DC 07-F0 MDP_XRT_AEC_RESET
0063 BC (d0)
0064 + DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 + DC 07-F0 MDP_XRT_FLD_RESET
0067 BC (da)
0068 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069 BC (c4 0e)
0070 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071 BC (c5 03)
0072 . C. ----- Success Verify ? OK / NG ____
0073 C.
0074 C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076 C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078 BC (c2)
0079 +. TI 2012-02-28 10:27:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 . C. ----- Success Verify ? OK / NG ____
0083 C.
0084 C. ***** XRT END *****
0085 . C. *****
0086 C. SOT table upload
0087 C. *****
0088 . C. < Upload DPL table >
0089 +. DC 07-F0 MDP_FG_CTRL_MANU
0090 BC (51)
0091 . C. -----
0092 C. MDP_FG_CTRL_MODE = MANU [ ]
0093 C. -----
0094 C.
0095 C. YçYÁY×Yí;¼YÉøÍÁ°øÈÈSTS_CHKøøOFFøÈø¹øÈ
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*** OP Sequence for XRT ***

2012/02/28	10:38:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	54	00	01	3f
2012/02/28	11:33:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	4c	72	01	3f
2012/02/28	11:58:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	43	8d	01	3f
2012/02/28	12:23:30.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	3a	a7	01	3f
2012/02/28	12:48:30.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	31	ca	01	3f
2012/02/28	13:13:30.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	28	e5	01	3f
2012/02/28	13:38:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	20	00	01	3f
2012/02/28	14:03:30.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	17	1a	01	3f
2012/02/28	14:28:30.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	0e	35	01	3f
2012/02/28	14:53:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	05	58	01	3f
2012/02/28	15:51:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2012/02/28	15:51:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/02/28	15:52:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	fd	59	01	3f
2012/02/28	15:52:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2012/02/28	15:52:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2012/02/28	15:52:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2012/02/28	15:54:58.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	08		
2012/02/28	15:55:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2012/02/28	16:17:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	f4	73	01	3f
2012/02/28	17:04:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2012/02/28	17:04:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2012/02/28	17:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2012/02/28	17:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2012/02/28	17:28:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	eb	8e	01	3f
2012/02/28	17:53:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	e2	a8	01	3f
2012/02/28	18:41:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2012/02/28	18:41:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2012/02/28	18:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2012/02/28	18:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2012/02/28	19:05:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	d9	cb	01	3f
2012/02/28	19:30:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00	d0	e6	01	3f
2012/02/28	20:18:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2012/02/28	20:18:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2012/02/28	20:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2012/02/28	20:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2012/02/28	20:42:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00	c8	01	01	3f
2012/02/28	21:07:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00	bf	1b	01	3f
2012/02/28	21:55:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2012/02/28	21:55:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2012/02/28	21:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2012/02/28	21:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2012/02/28	22:18:30.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	b6	36	01	3f
2012/02/28	22:43:30.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	ad	59	01	3f
2012/02/28	23:14:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2012/02/28	23:15:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	04	00	00	00	00
2012/02/28	23:17:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							

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2012/02/28	23:17:46.0	XRT_FLD_ENA_411_OG [0x19b]	4	07-F8	22	fe	97	00
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/02/28	23:17:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/28	23:17:50.0	XRT_AEC_RESET_443_OG [0x1bb]	1	07-F0	d0			
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/28	23:17:52.0	XRT_ARS_DIS_431_OG [0x1af]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/28	23:17:54.0	XRT_FLD_RESET_412_OG [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/28	23:17:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	2	07-F0	c4	0e		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		
2012/02/28	23:17:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	2	07-F0	c5	03		
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2012/02/28	23:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/28	23:33:00.0	XRT_CTRL_MANU_408_OG [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/28	23:33:02.0	XRT_FLD_RESET_412_OG [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/28	23:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/02/28	23:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/02/28	23:45:00.0	XRT_Custom_418_OG [0x1a2]						
2012/02/28	23:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/29	01:00:30.0	XRT_CTRL_MANU_408_OG [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/29	01:00:32.0	XRT_FLD_RESET_412_OG [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/29	01:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/02/29	01:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/02/29	01:16:00.0	XRT_Custom_418_OG [0x1a2]						
2012/02/29	01:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/29	02:32:00.0	XRT_CTRL_MANU_408_OG [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/29	02:32:02.0	XRT_FLD_RESET_412_OG [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/29	02:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/02/29	02:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/02/29	02:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/29	03:00:00.0	AOCs_OrE-point_Start_22_OG [0x0ac]						
		AOCU_NM	5	02-76	02	00	00	00
2012/02/29	03:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	4	07-F8	22	fe	97	00
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2012/02/29	03:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	d8			
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/02/29	03:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/29	03:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	1	07-F0	d0			
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/29	03:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/29	03:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/29	03:02:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	2	07-F0	c4	10		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10		
2012/02/29	03:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	2	07-F0	c5	03		
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2012/02/29	03:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/29	04:00:30.0	XRT_CTRL_MANU_408_OG [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/29	04:00:32.0	XRT_FLD_RESET_412_OG [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/29	04:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/02/29	04:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/02/29	04:30:00.0	XRT_Custom_418_OG [0x1a2]						
2012/02/29	04:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/29	05:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/29	05:41:02.0	XRT_FLD_RESET_412_OG [0x19c]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/29	05:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/02/29	05:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/02/29	06:07:30.0	XRT_Custom_418_OG [0x1a2]						
2012/02/29	06:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/29	06:17:24.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2012/02/29	06:17:26.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2012/02/29	06:17:30.0	AOCs_OrE-point_Start_23_OG [0x0ad]					
		AOCU_NM	5	02-76	00	00	00 00 00
2012/02/29	06:17:46.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0			d9
2012/02/29	06:17:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0			c9
2012/02/29	06:17:50.0	XRT_ARS_DIS_438_OG [0x1b6]					
		MDP_XRT_ARS_DIS	1	07-F0			d5
2012/02/29	06:20:28.0	XRT_QT_PROG_SET_404_OG [0x194]					
		MDP_XRT_QT_PROG_SET	2	07-F0			c4 08
2012/02/29	06:20:30.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0			c0
2012/02/29	06:27:24.0	XRT_CTRL_MANU_439_OG [0x1b7]					
		MDP_XRT_CTRL_MANU	1	07-F0			c1
2012/02/29	06:27:30.0	AOCs_OrE-point_Start_22_OG [0x0ac]					
		AOCU_NM	5	02-76	02	00	00 00 00
2012/02/29	06:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2012/02/29	06:30:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0			d8
2012/02/29	06:30:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0			c8
2012/02/29	06:30:20.0	XRT_AEC_RESET_443_OG [0x1bb]					
		MDP_XRT_AEC_RESET	1	07-F0			d0
2012/02/29	06:30:22.0	XRT_ARS_DIS_431_OG [0x1af]					
		MDP_XRT_ARS_DIS	1	07-F0			d5
2012/02/29	06:30:24.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0			da
2012/02/29	06:30:26.0	XRT_QT_PROG_SET_415_OG [0x19f]					
		MDP_XRT_QT_PROG_SET	2	07-F0			c4 10
2012/02/29	06:30:28.0	XRT_FL_PROG_SET_414_OG [0x19e]					
		MDP_XRT_FL_PROG_SET	2	07-F0			c5 03
2012/02/29	06:30:30.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0			c0
2012/02/29	07:21:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0			c1
2012/02/29	07:21:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0			da
2012/02/29	07:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0			e8
2012/02/29	07:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0			e9
2012/02/29	07:44:30.0	XRT_Custom_418_OG [0x1a2]					
2012/02/29	07:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0			c0
2012/02/29	08:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0			c1
2012/02/29	09:00:00.0	AOCs_OrE-point_Start_22_OG [0x0ac]					
		AOCU_NM	5	02-76	02	00	00 00 00
2012/02/29	09:20:30.0	XRT_Custom_418_OG [0x1a2]					
2012/02/29	09:21:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0			c0
2012/02/29	14:29:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0			c1
2012/02/29	14:29:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0			da
2012/02/29	14:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0			e8
2012/02/29	14:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0			e9
2012/02/29	14:39:00.0	XRT_Custom_418_OG [0x1a2]					
2012/02/29	14:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0			c0
2012/02/29	16:03:30.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0			c1
2012/02/29	16:03:32.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0			da
2012/02/29	16:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0			e8
2012/02/29	16:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0			e9
2012/02/29	16:27:00.0	XRT_Custom_418_OG [0x1a2]					
2012/02/29	16:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0			c0
2012/02/29	17:40:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0			c1
2012/02/29	17:40:02.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0			da
2012/02/29	17:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
		MDP_XRT_PREFLR_STRT	1	07-F0			e8
2012/02/29	17:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
		MDP_XRT_PREFLR_STOP	1	07-F0			e9
2012/02/29	18:03:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0			c1
2012/02/29	18:03:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2012/02/29	18:04:00.0	AOCs_OrE-point_Start_23_OG [0x0ad]					
		AOCU_NM	5	02-76	00	00	00 00 00
2012/02/29	18:04:16.0	XRT_FLD_DIS_402_OG [0x192]					
		MDP_XRT_FLD_DIS	1	07-F0			d9
2012/02/29	18:04:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					

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2012/02/29	18:04:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
			MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/29	18:06:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2012/02/29	18:07:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/29	18:13:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/29	18:14:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	02 00 00 00 00
2012/02/29	18:16:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/02/29	18:16:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/02/29	18:16:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/02/29	18:16:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/02/29	18:16:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/29	18:16:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/29	18:16:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2012/02/29	18:16:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/02/29	18:17:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/29	19:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/29	19:17:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/29	19:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/29	19:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/29	19:40:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/29	19:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/29	20:54:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/29	20:54:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/29	20:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/29	20:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/29	21:18:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/29	21:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/29	22:32:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/29	22:32:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/29	22:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/29	22:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/29	22:53:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/29	22:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/03/01	00:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/01	00:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/03/01	00:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/03/01	00:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/03/01	00:17:30.0	XRT_Custom_418_OG [0x1a2]				
2012/03/01	00:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/03/01	01:33:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/01	01:33:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/03/01	01:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/03/01	01:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/03/01	01:52:00.0	XRT_Custom_418_OG [0x1a2]				
2012/03/01	01:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/03/01	03:08:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/01	03:08:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/03/01	03:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/03/01	03:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/03/01	03:29:00.0	XRT_Custom_418_OG [0x1a2]				

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2012/03/01	03:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/01	04:38:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/01	04:38:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/01	04:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/01	04:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/01	05:06:30.0	XRT_Custom_418_OG [0x1a2]							
2012/03/01	05:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/01	05:50:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/01	05:50:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/03/01	05:50:30.0	AOCS_Ore-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/03/01	05:50:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/03/01	05:50:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/03/01	05:50:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/03/01	05:53:28.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2012/03/01	05:53:30.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/01	06:00:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/01	06:00:30.0	AOCS_Ore-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/03/01	06:02:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/03/01	06:03:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/03/01	06:03:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/03/01	06:03:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/03/01	06:03:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/03/01	06:03:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/01	06:03:26.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2012/03/01	06:03:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/03/01	06:03:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/01	06:18:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/01	06:18:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/01	06:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/01	06:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/01	06:44:00.0	XRT_Custom_418_OG [0x1a2]							
2012/03/01	06:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/01	07:58:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/01	07:58:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/01	07:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/01	08:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/01	08:21:00.0	XRT_Custom_418_OG [0x1a2]							
2012/03/01	08:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/01	09:39:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/01	09:39:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/01	09:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/01	09:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/01	10:14:00.0	AOCS_Ore-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 00 00 00 00				