

# XRT Timeline to be uploaded on 2012/03/01

Period: 2012/03/01 10:04:00 - 2012/03/06 09:18:00

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Normal mode

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## XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad

Term	Pointing (x, y)	Comment
03/01 16:23:00 - 03/01 17:53:24	Track ( -70.0, 307.9) @ 03/01 16:20:00	track AR 11424
03/01 18:06:30 - 03/02 05:59:54	Track ( -54.2, 308.0) @ 03/01 18:03:30	track AR 11424
03/02 06:43:00 - 03/02 17:56:54	Track ( 61.6, 308.0) @ 03/02 06:40:00	track AR11424

**PROG= 13 Inf-time(s)**

<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 19</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 96</b>	<b>4-time(s)</b>	<b>2.0sec</b>											
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 62</b>	<b>15-time(s)</b>	<b>150.0sec</b>											
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

## XOB #18B0: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(24/1443) + Thin-Be(88/2)

Term	Pointing (x, y)	Comment
03/01 17:56:30 - 03/01 18:03:24	Fixed ( 0.0, 0.0)	synoptic, shifted -6.5 min
03/02 06:03:00 - 03/02 06:10:00	Fixed ( 0.0, 0.0)	extended synoptic (40 min) for SOT flat field and XRT long synop
03/02 18:00:00 - 03/02 18:06:54	Fixed ( 0.0, 0.0)	synoptic, shifted -3.0 min
03/03 06:32:30 - 03/03 06:39:24	Fixed ( 0.0, 0.0)	synoptic, shifted 29.5 min

**PROG= 08 1-time(s)**

<b>Subr= 1</b>	<b>1-time(s)</b>	<b>12.0sec</b>											
<b>Seqn= 46</b>	<b>1-time(s)</b>	<b>4.0sec</b>											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 5</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 69</b>	<b>1-time(s)</b>	<b>4.0sec</b>											
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 50</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 4</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

## XOB #18CC: Synoptic 9 Filter- 2x2 Q98 even Shorter exp

Term	Pointing (x, y)	Comment
03/02 06:13:06 - 03/02 06:39:54	Fixed ( 0.0, 0.0)	extended synoptic (40 min) for SOT flat field and XRT long synop

**PROG= 10 2-time(s)**

<b>Subr= 1</b>	<b>1-time(s)</b>	<b>180.0sec</b>											
<b>Seqn= 94</b>	<b>1-time(s)</b>	<b>25.0sec</b>											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 70</b>	<b>1-time(s)</b>	<b>25.0sec</b>											
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 74</b>	<b>1-time(s)</b>	<b>25.0sec</b>											
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 18</b>	<b>1-time(s)</b>	<b>25.0sec</b>											
	C-poly/Open	C-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	C-poly/Open	C-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 24</b>	<b>1-time(s)</b>	<b>25.0sec</b>											

Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 73 1-time(s) 4.0sec</b>												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 2 1-time(s) 180.0sec</b>												
<b>Seqn= 33 1-time(s) 4.0sec</b>												
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 71 1-time(s) 4.0sec</b>												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 67 1-time(s) 4.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 4 1-time(s) 4.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #18AF: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 4min cad**

Term	Pointing (x, y)	Comment
03/02 18:10:00 - 03/03 06:29:24	Track ( 166.0, 306.4) @ 03/02 18:07:00	track AR 11424
03/03 06:42:30 - 03/03 09:13:30	Track ( 277.9, 303.0) @ 03/03 06:39:30	track AR 11424

**PROG= 04 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>												
<b>Seqn= 19 1-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 96 4-time(s) 2.0sec</b>												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 62 11-time(s) 240.0sec</b>												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops**

Term	Pointing (x, y)	Comment
03/01 16:23:00 - 03/01 17:53:24	Track ( -70.0, 307.9) @ 03/01 16:20:00	track AR 11424
03/01 18:06:30 - 03/02 05:59:54	Track ( -54.2, 308.0) @ 03/01 18:03:30	track AR 11424
03/02 06:43:00 - 03/02 17:56:54	Track ( 61.6, 308.0) @ 03/02 06:40:00	track AR 11424
03/02 18:10:00 - 03/03 06:29:24	Track ( 166.0, 306.4) @ 03/02 18:07:00	track AR 11424
03/03 06:42:30 - 03/03 09:13:30	Track ( 277.9, 303.0) @ 03/03 06:39:30	track AR 11424

**PROG= 03 12-time(s)**

<b>Subr= 1 45-time(s) 10.0sec</b>												
<b>Seqn= 20 1-time(s) 2.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 63 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 77 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2 1-time(s) 10.0sec</b>												
<b>Seqn= 90 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Active Region Search**

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NOT USED

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**Flare Detection**

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**FLD Patrol**

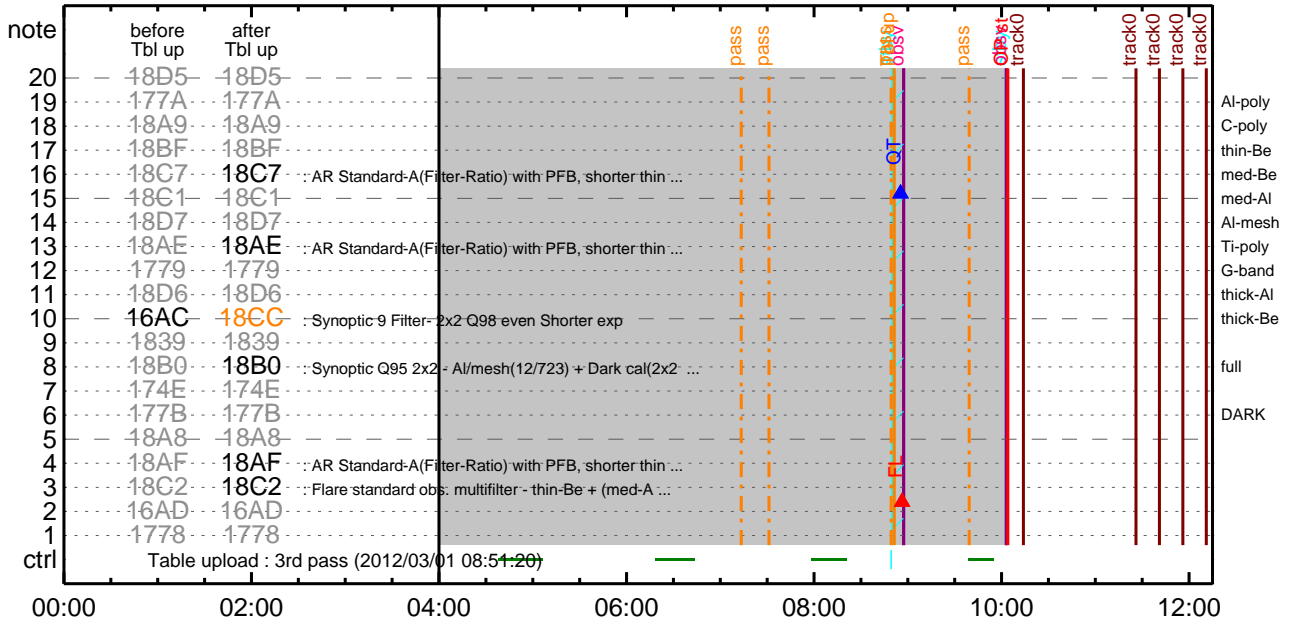
Term	Pointing (x, y)	Comment
03/01 18:06:16 - 03/02 06:00:16	Track ( -54.2, 308.0) @ 03/01 18:03:30	track AR 11424
03/02 06:42:46 - 03/02 17:57:16	Track ( 61.6, 308.0) @ 03/02 06:40:00	track AR 11424
03/02 18:09:46 - 03/03 06:29:46	Track ( 166.0, 306.4) @ 03/02 18:07:00	track AR 11424

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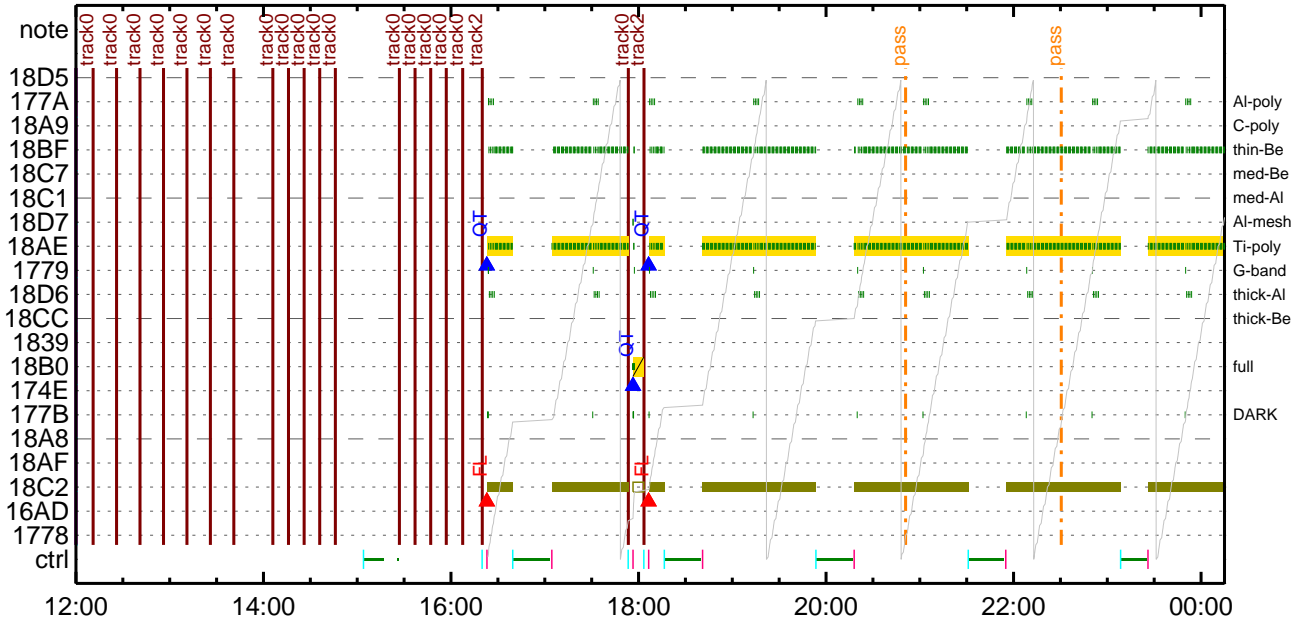
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

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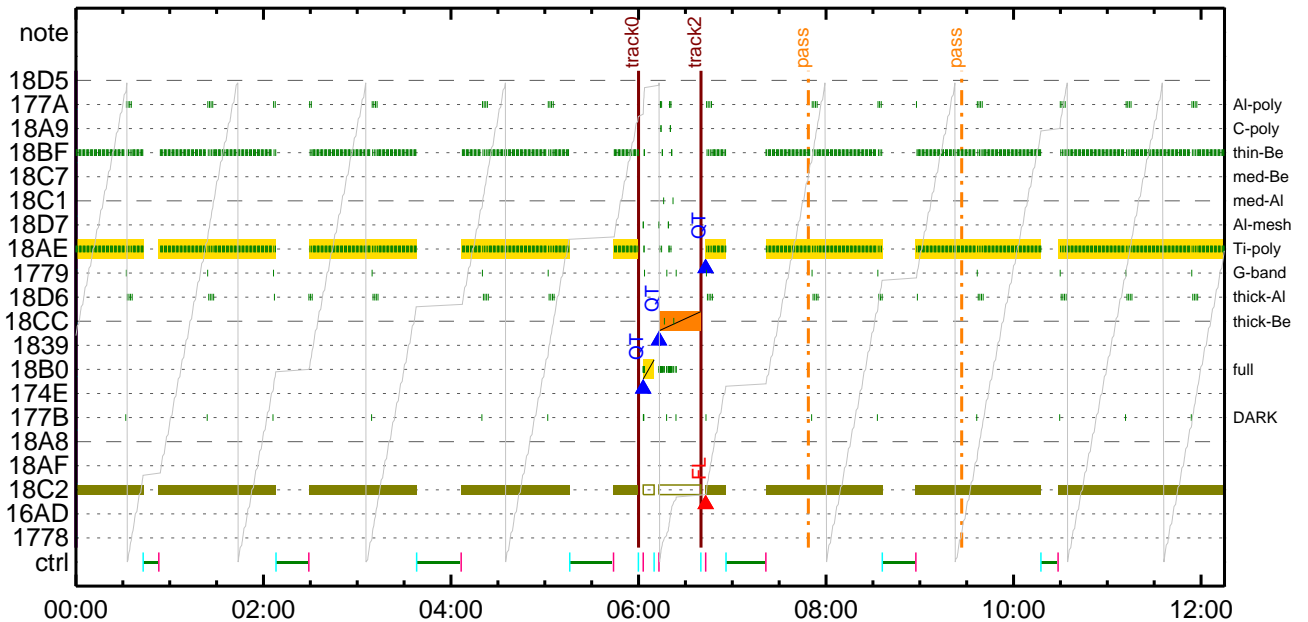
### CMDI #0456 2012/03/01



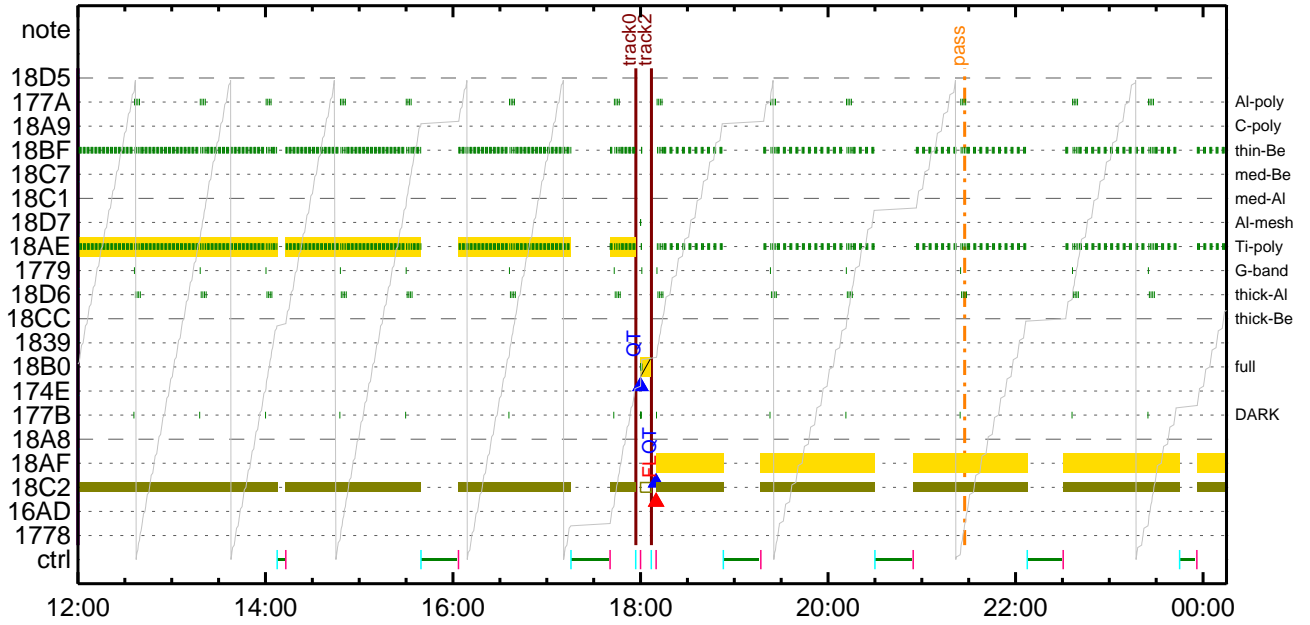
### CMDI #0456 2012/03/01



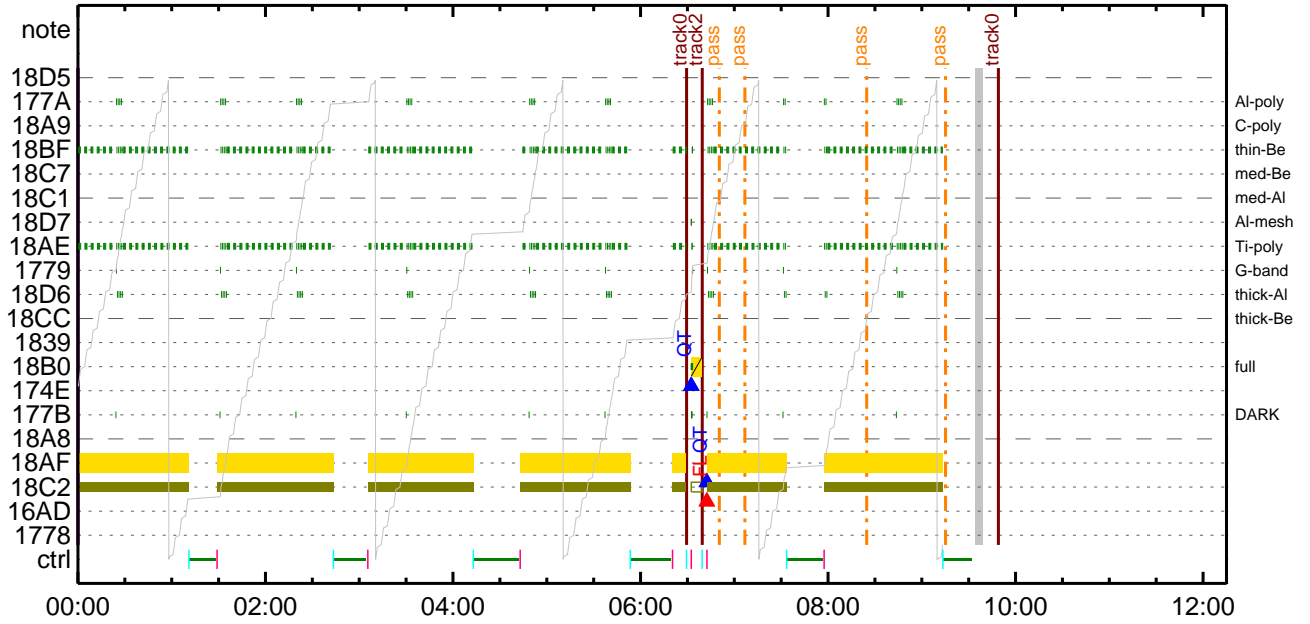
### CMDI #0456 2012/03/02



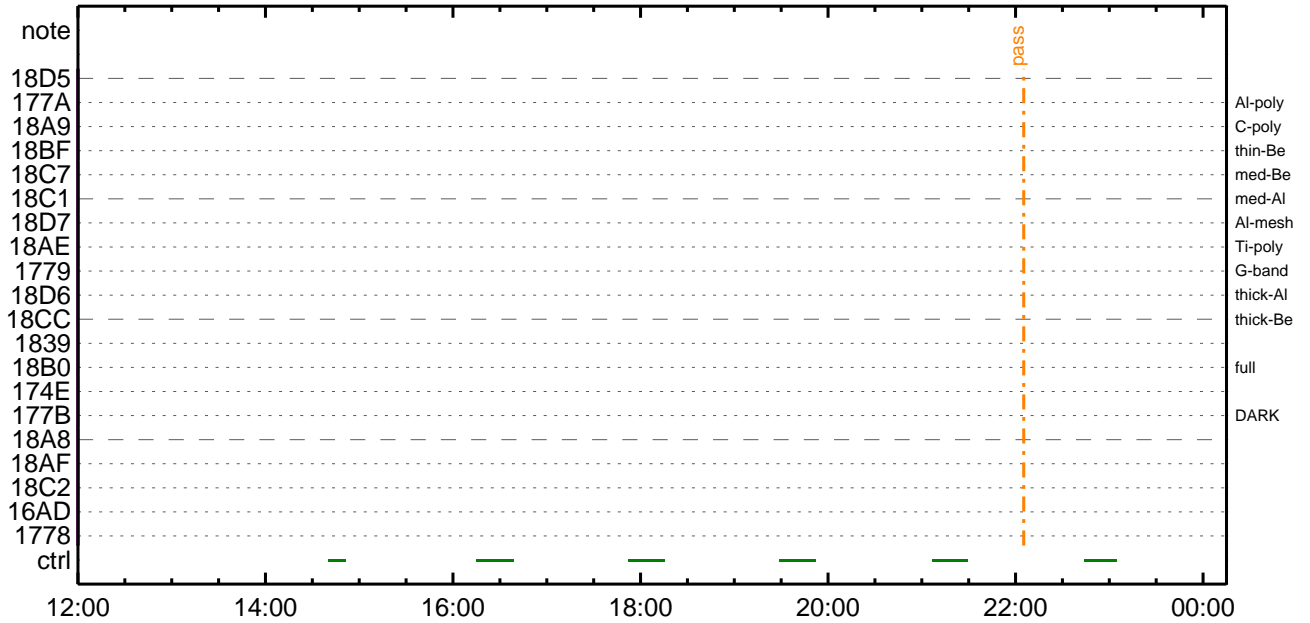
CMDI #0456 2012/03/02



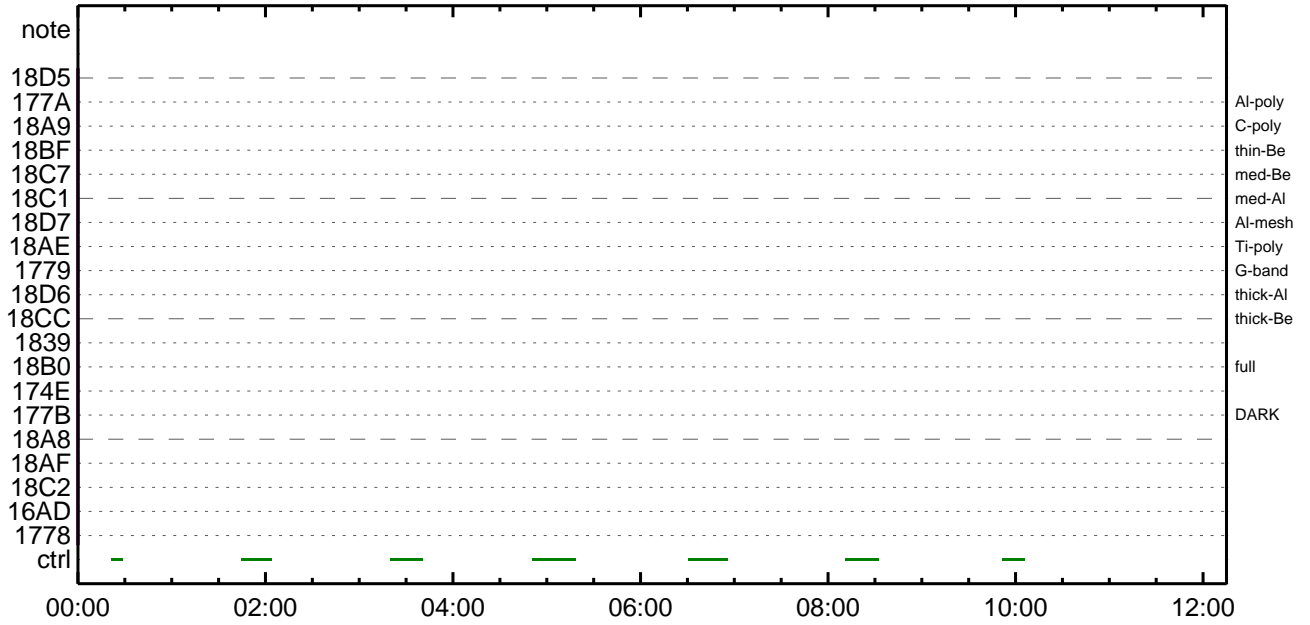
CMDI #0456 2012/03/03



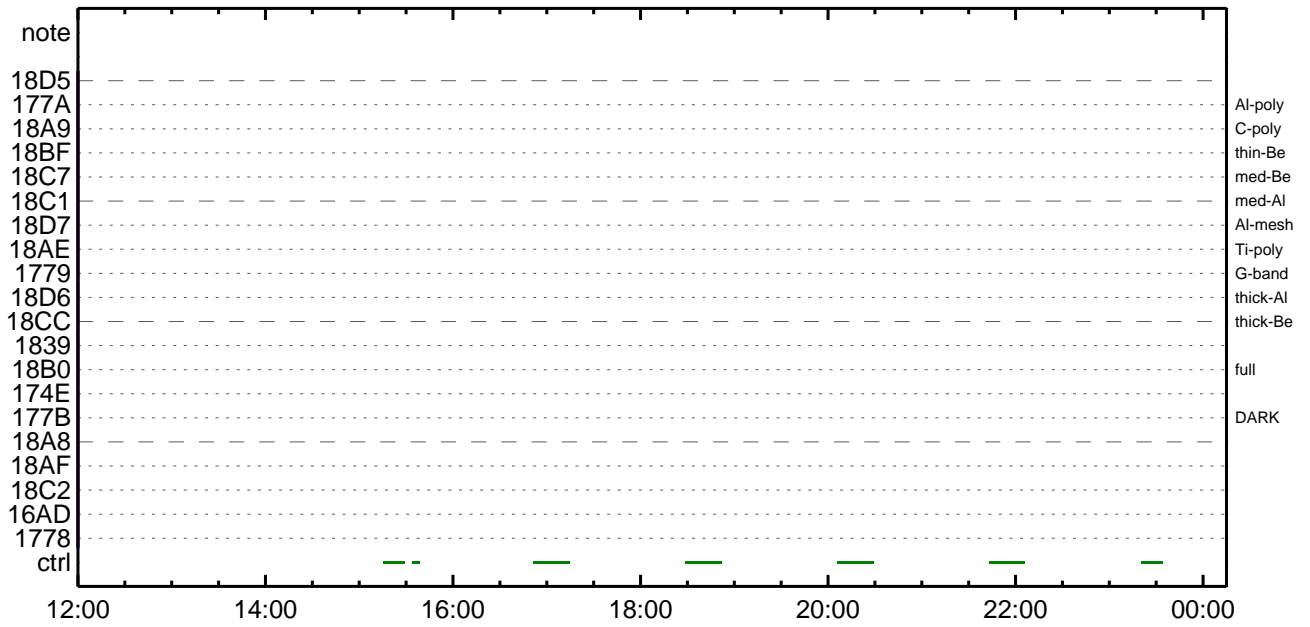
CMDI #0456 2012/03/03



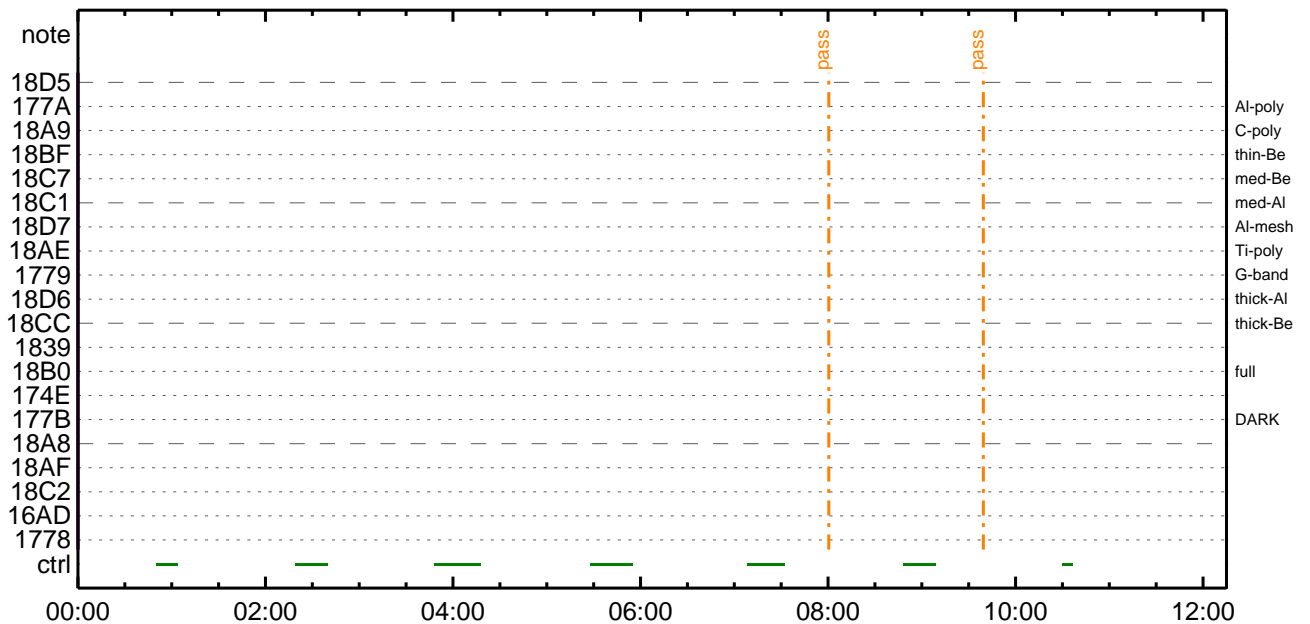
CMDI #0456 2012/03/04



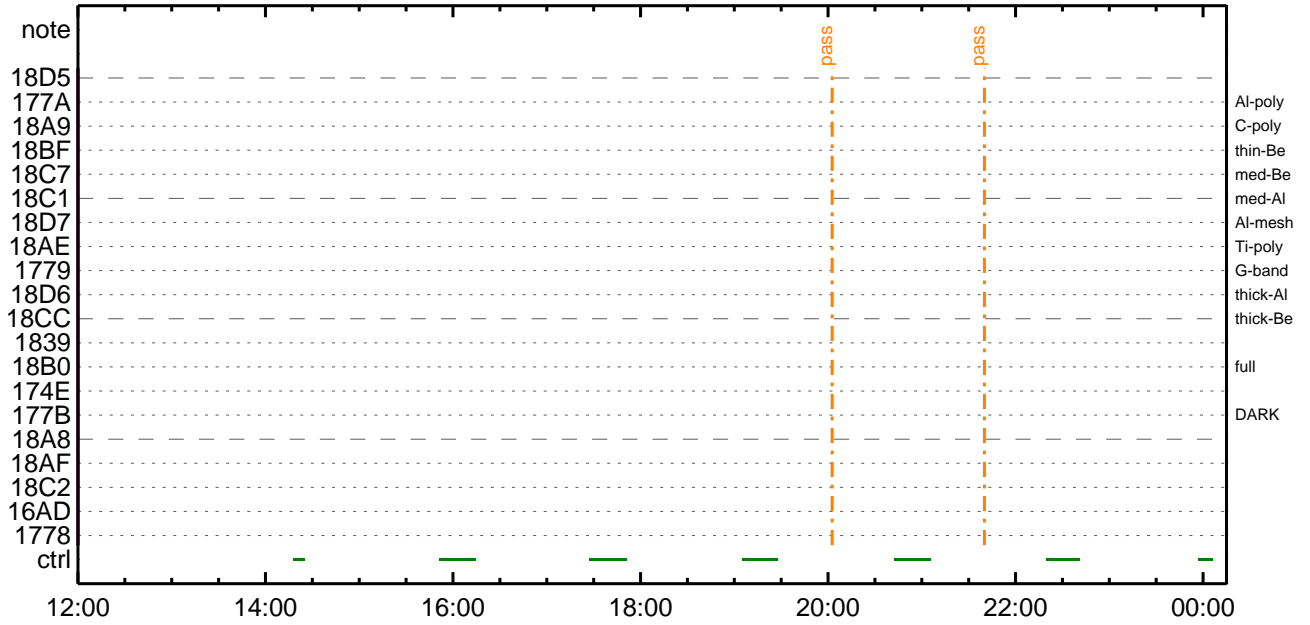
CMDI #0456 2012/03/04



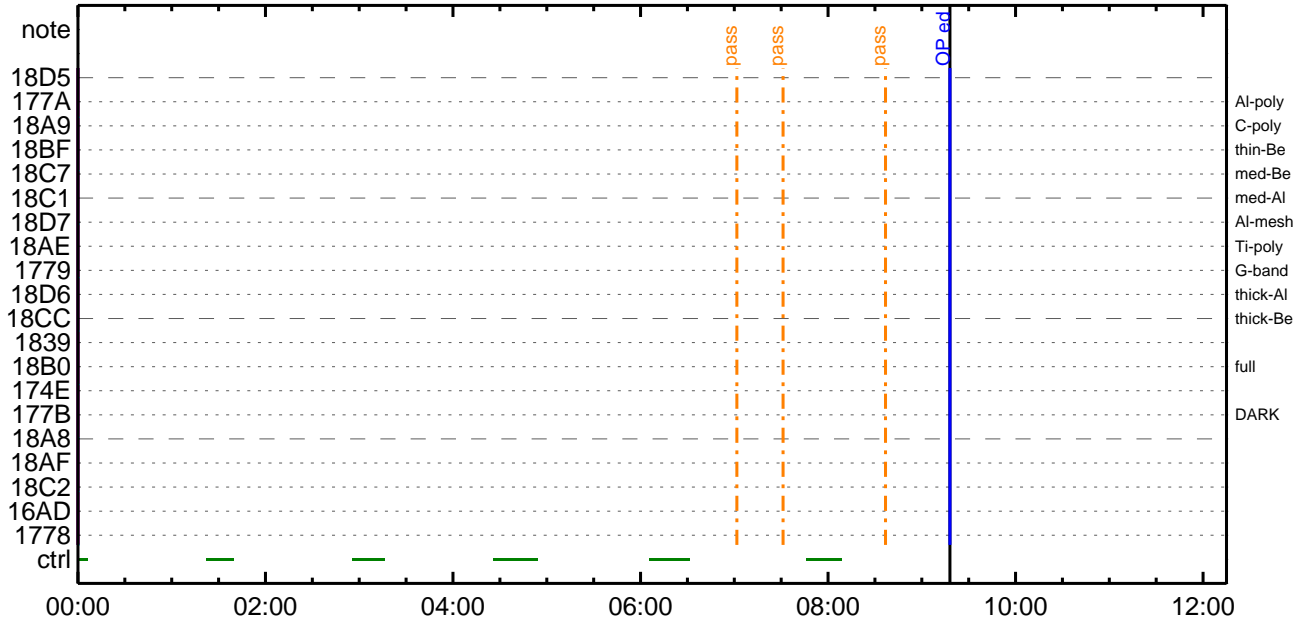
CMDI #0456 2012/03/05



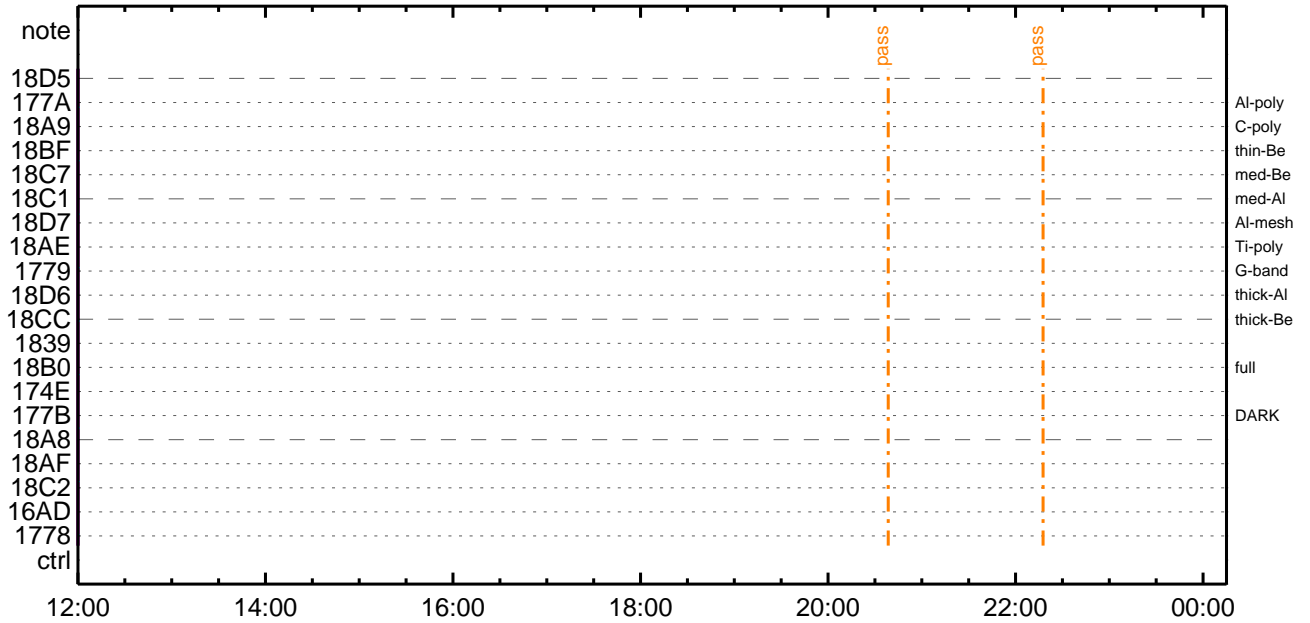
CMDI #0456 2012/03/05



CMDI #0456 2012/03/06



CMDI #0456 2012/03/06















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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_AEC_RESET
0145 BC (d0)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 10)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 03)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2012-03-01 10:03:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 C.
0168 . C. ***** MDP `úÃîï»ö%ÝðËÄð¹ñèDCBC•x²è *****
0169 C. (%á°îÿÓYÁYÈYÏYÉYáYçYèè%¼¼¼¼»Û¹ñè)
0170 . S. DC-BC dcbc-402:DCBC
0171 (MDP_known_event)
0172 C.
0173 C.
0174 . C. ***** ¥D¥¹•İ Daily±¿İÑñÈ´Øñ¹ñèDCBC•x²è *****
0175 . S. DC-BC dcbc-153:DCBC
0176 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0177 C.
0178 C.
0179 . C. ;ãLOS¥Á¥$¥Ã¥¹¼Å»Û;ã
0180 C.
0181 . C. ***** LOS *****
0182 C.

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\*\*\* OP Sequence for XRT \*\*\*

2012/03/01	10:14:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	eb	af	51	58
2012/03/01	11:26:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	e4	84	41	60
2012/03/01	11:41:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	e2	90	30	dd
2012/03/01	11:56:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	e1	71	20	28
2012/03/01	12:11:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	e0	d5	0f	22
2012/03/01	12:26:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	e0	b5	fd	cb
2012/03/01	12:41:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	e0	d5	f3	54
2012/03/01	12:56:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	e1	71	e2	57
2012/03/01	13:11:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	e2	90	d1	a2
2012/03/01	13:26:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	e4	84	c1	1f
2012/03/01	13:41:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	ed	07	af	e8
2012/03/01	14:06:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	01	58	51	68
2012/03/01	14:16:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	01	58	3f	b6
2012/03/01	14:26:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	01	58	2e	04
2012/03/01	14:36:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	01	58	1c	51
2012/03/01	14:46:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00	01	58	0a	9f
2012/03/01	15:04:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2012/03/01	15:04:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2012/03/01	15:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2012/03/01	15:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2012/03/01	15:27:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00	01	58	f8	ee
2012/03/01	15:37:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00	01	58	e7	3c
2012/03/01	15:47:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	01	58	d5	8a
2012/03/01	15:57:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	01	58	c3	e0
2012/03/01	16:07:30.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00	01	58	b2	2e
2012/03/01	16:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2012/03/01	16:20:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	02	00	00	00	00
2012/03/01	16:22:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/03/01	16:22:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0		d8			
2012/03/01	16:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2012/03/01	16:22:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0		d0			
2012/03/01	16:22:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2012/03/01	16:22:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2012/03/01	16:22:56.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	0d		
2012/03/01	16:22:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0		c5	03		
2012/03/01	16:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2012/03/01	16:39:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2012/03/01	16:39:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2012/03/01	16:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2012/03/01	16:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2012/03/01	17:03:30.0	XRT_Custom_418_OG [0x1a2]							
2012/03/01	17:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2012/03/01	17:53:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2012/03/01	17:53:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/03/01	17:53:30.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00	00	00	00	00
2012/03/01	17:53:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			

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2012/03/01	17:53:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/03/01	17:53:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/03/01	17:56:28.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2012/03/01	17:56:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/01	18:03:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/01	18:03:30.0	AOCS_OrE-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	02	00	00	00	00
2012/03/01	18:05:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/03/01	18:06:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/03/01	18:06:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/03/01	18:06:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/03/01	18:06:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/03/01	18:06:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/01	18:06:26.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2012/03/01	18:06:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2012/03/01	18:06:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/01	18:16:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/01	18:16:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/01	18:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/01	18:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/01	18:40:00.0	XRT_Custom_418_OG [0x1a2]							
2012/03/01	18:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/01	19:53:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/01	19:53:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/01	19:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/01	19:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/01	20:17:00.0	XRT_Custom_418_OG [0x1a2]							
2012/03/01	20:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/01	21:31:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/01	21:31:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/01	21:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/01	21:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/01	21:54:00.0	XRT_Custom_418_OG [0x1a2]							
2012/03/01	21:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/01	23:08:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/01	23:08:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/01	23:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/01	23:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/01	23:25:00.0	XRT_Custom_418_OG [0x1a2]							
2012/03/01	23:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/02	00:43:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/02	00:43:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/02	00:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/02	00:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/02	00:52:00.0	XRT_Custom_418_OG [0x1a2]							
2012/03/02	00:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/02	02:08:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/02	02:08:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/02	02:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/02	02:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				





2012/03/02	08:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/02	08:56:30.0	XRT_Custom_418_OG [0x1a2]					
2012/03/02	08:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/02	10:17:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/02	10:17:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/02	10:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/02	10:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/02	10:27:30.0	XRT_Custom_418_OG [0x1a2]					
2012/03/02	10:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/02	14:07:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/02	14:07:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/02	14:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/02	14:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/02	14:12:00.0	XRT_Custom_418_OG [0x1a2]					
2012/03/02	14:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/02	15:39:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/02	15:39:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/02	15:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/02	15:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/02	16:02:30.0	XRT_Custom_418_OG [0x1a2]					
2012/03/02	16:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/02	17:15:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/02	17:15:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/02	17:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/02	17:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/02	17:39:30.0	XRT_Custom_418_OG [0x1a2]					
2012/03/02	17:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/02	17:56:54.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/02	17:56:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/03/02	17:57:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2012/03/02	17:57:16.0	XRT_FLD_DIS_402_OG [0x192]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/03/02	17:57:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/03/02	17:57:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/03/02	17:59:58.0	XRT_QT_PROG_SET_404_OG [0x194]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2012/03/02	18:00:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/02	18:06:54.0	XRT_CTRL_MANU_439_OG [0x1b7]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/02	18:07:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]					
			AOCU_NM	5	02-76	02 00 00 00 00	
2012/03/02	18:09:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/03/02	18:09:46.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/03/02	18:09:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/03/02	18:09:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/03/02	18:09:52.0	XRT_ARS_DIS_431_OG [0x1af]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/03/02	18:09:54.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/02	18:09:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2012/03/02	18:09:58.0	XRT_FL_PROG_SET_414_OG [0x19e]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2012/03/02	18:10:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/02	18:53:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/02	18:53:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/02	18:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					

2012/03/02	18:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/02	19:16:00.0	XRT_Custom_418_OG [0x1a2]					
2012/03/02	19:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/02	20:30:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/02	20:30:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/02	20:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/02	20:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/02	20:53:30.0	XRT_Custom_418_OG [0x1a2]					
2012/03/02	20:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/02	22:07:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/02	22:07:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/02	22:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/02	22:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/02	22:29:30.0	XRT_Custom_418_OG [0x1a2]					
2012/03/02	22:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/02	23:45:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/02	23:45:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/02	23:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/02	23:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/02	23:55:00.0	XRT_Custom_418_OG [0x1a2]					
2012/03/02	23:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/03	01:11:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/03	01:11:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/03	01:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/03	01:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/03	01:28:00.0	XRT_Custom_418_OG [0x1a2]					
2012/03/03	01:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/03	02:43:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/03	02:43:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/03	02:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/03	02:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/03	03:04:30.0	XRT_Custom_418_OG [0x1a2]					
2012/03/03	03:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/03	04:13:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/03	04:13:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/03	04:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/03	04:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/03	04:42:00.0	XRT_Custom_418_OG [0x1a2]					
2012/03/03	04:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/03	05:53:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/03	05:53:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/03	05:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/03	05:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/03	06:19:30.0	XRT_Custom_418_OG [0x1a2]					
2012/03/03	06:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/03	06:29:24.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/03	06:29:26.0	XRT_FOCUS_POSITION_401_OG [0x191]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/03/03	06:29:30.0	AOCS_Ore-point_Start_23_OG [0x0ad]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2012/03/03	06:29:46.0	XRT_FLD_DIS_402_OG [0x192]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/03/03	06:29:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	

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2012/03/03	06:29:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/03/03	06:32:28.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2012/03/03	06:32:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/03	06:39:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/03	06:39:30.0	AOCS_Ore-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/03/03	06:41:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/03/03	06:42:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/03/03	06:42:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/03/03	06:42:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/03/03	06:42:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/03/03	06:42:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/03	06:42:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2012/03/03	06:42:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/03/03	06:42:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/03	07:33:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/03	07:33:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/03	07:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/03	07:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/03	07:56:30.0	XRT_Custom_418_OG [0x1a2]							
2012/03/03	07:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/03	09:13:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/03	09:13:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/03	09:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/03	09:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/03	09:49:00.0	AOCS_Ore-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 00 00 00 00				