

XRT Timeline to be uploaded on 2012/03/22

Period: 2012/03/22 09:25:00 - 2012/03/27 10:20:00

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Normal mode

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XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad

Term	Pointing (x, y)	Comment
03/22 09:51:06 - 03/22 09:59:54	Fixed (878.0, -335.0)	# OP start + 10min, AR 11434 at W limb, cont.
03/23 06:13:00 - 03/23 09:59:54	Fixed (878.0, -335.0)	# AR 11434.
03/23 15:59:06 - 03/23 18:06:24	Fixed (878.0, -335.0)	* AR 11434.
03/23 18:19:30 - 03/24 06:25:24	Fixed (878.0, -335.0)	# Cont.
03/24 06:38:30 - 03/24 09:09:30	Fixed (878.0, -335.0)	# Cont.
PROG= 13 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 19 1-time(s) 2.0sec		
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec		
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec		
└─ Seqn= 96 4-time(s) 2.0sec		
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec		
└─ thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec		
└─ Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 62 15-time(s) 150.0sec		
└─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec		
└─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec		
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec		
└─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec		
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec		
└─ thin-Be/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec		
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec		
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384

Term	Pointing (x, y)	Comment
03/22 10:03:00 - 03/22 15:29:54	Fixed (-14.0, -955.0)	* HOP 81, polar monitoring campaign, S pole.
03/23 10:03:00 - 03/23 15:35:00	Fixed (-14.0, -955.0)	* HOP 81, S pole.
PROG= 20 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 51 1-time(s) 2.0sec		
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec		
└─ Subr= 2 30-time(s) 2.0sec		
└─ Seqn= 58 2-time(s) 30.0sec		
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec		
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 8.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec		
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #18A9: Coronal cavity (6 filters)-HOP114 with ARs-DPCM 2x2-768FOV-AEC0-3min cad

Term	Pointing (x, y)	Comment
03/22 15:33:00 - 03/22 17:48:24	Fixed (500.0, -810.0)	* ToO HOP 114, coronal cavity.
PROG= 09 Inf.-time(s)		
└─ Subr= 1 5-time(s) 180.0sec		
└─ Seqn= 6 1-time(s) 4.0sec		
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec		
└─ Al-poly/Open Al-poly/Open close Safe Norm 2.83s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec		
└─ C-poly/Open C-poly/Open close Safe Norm 4.00s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec		
└─ Open/Ti-poly Open/thick-Al close Safe Norm 4.00s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec		
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 5.66s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec		
└─ thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec		
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 14 1-time(s) 4.0sec		
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec		
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #18AD: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be(12

Term	Pointing (x, y)	Comment
03/22 17:51:30 - 03/22 17:58:24	Fixed (0.0, 0.0)	synoptic, shifted -11.5 min
03/23 06:03:00 - 03/23 06:09:54	Fixed (0.0, 0.0)	synoptic
03/23 18:09:30 - 03/23 18:16:24	Fixed (0.0, 0.0)	synoptic, shifted 6.5 min
03/24 06:28:30 - 03/24 06:35:24	Fixed (0.0, 0.0)	synoptic, shifted 25.5 min
PROG= 17 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 7 1-time(s) 4.0sec		
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		

Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 8 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18C1: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 60s cad

Term	Pointing (x, y)	Comment
03/22 18:01:30 - 03/22 23:59:00	Fixed (878.0, -335.0)	# AR 11434.

PROG= 15 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 96 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 62 15-time(s) 2.0sec												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18E1: HOP130-Synoptic 3 Filter- 2x2 Q98 Short exp 2 loop-2

Term	Pointing (x, y)	Comment
03/23 00:56:30 - 03/23 05:59:54	Fixed (0.0, 0.0)	

PROG= 04 2-time(s)

Subr= 1 1-time(s) 180.0sec												
Seqn= 82 1-time(s) 25.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 98 1-time(s) 25.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 12 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops

Term	Pointing (x, y)	Comment
03/22 09:51:06 - 03/22 09:59:54	Fixed (878.0, -335.0)	# OP start + 10min, AR 11434 at W limb, cont.
03/22 10:03:00 - 03/22 15:29:54	Fixed (-14.0, -955.0)	* HOP 81, polar monitoring campaign, S pole.
03/22 15:33:00 - 03/22 17:48:24	Fixed (500.0, -810.0)	* ToO HOP 114, coronal cavity.
03/22 18:01:30 - 03/22 23:59:00	Fixed (878.0, -335.0)	# AR 11434.
03/23 06:13:00 - 03/23 09:59:54	Fixed (878.0, -335.0)	# AR 11434.
03/23 10:03:00 - 03/23 15:35:00	Fixed (-14.0, -955.0)	* HOP 81, S pole.
03/23 15:59:06 - 03/23 18:06:24	Fixed (878.0, -335.0)	* AR 11434.
03/23 18:19:30 - 03/24 06:25:24	Fixed (878.0, -335.0)	# Cont.
03/24 06:38:30 - 03/24 09:09:30	Fixed (878.0, -335.0)	# Cont.

PROG= 03 12-time(s)

Subr= 1 45-time(s) 10.0sec												
Seqn= 20 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 63 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 77 1-time(s) 2.0sec												

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

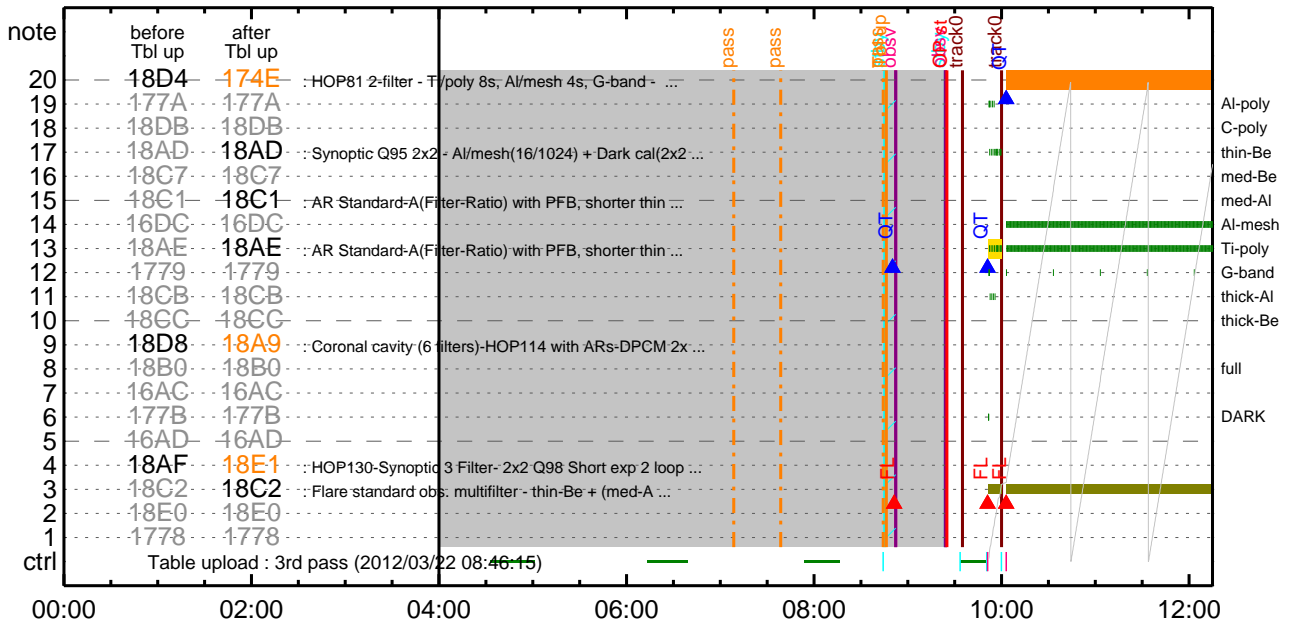
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Flare Detection

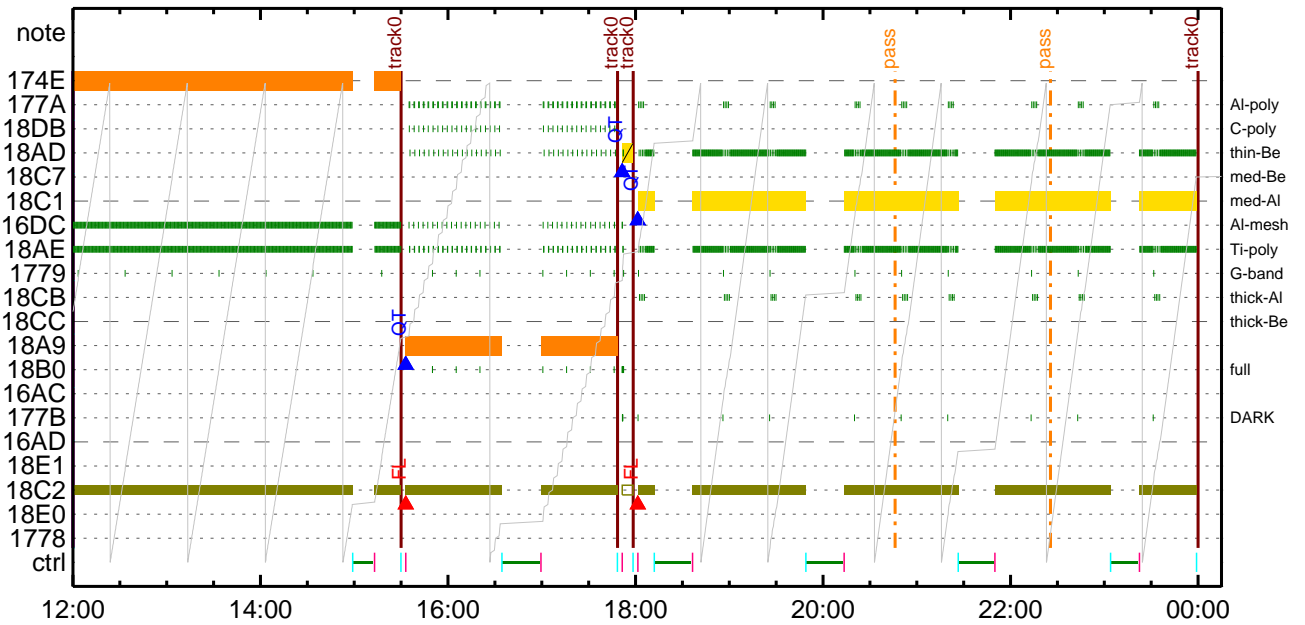
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FLD Patrol												
Term			Pointing (x, y)				Comment					
03/22 18:01:16 - 03/23 00:53:46			Fixed (878.0, -335.0)				# AR 11434.					
03/23 06:12:46 - 03/23 18:06:46			Fixed (878.0, -335.0)				# AR 11434.					
03/23 18:19:16 - 03/24 06:25:46			Fixed (878.0, -335.0)				# Cont.					
03/24 06:38:16 - 03/27 10:20:00			Fixed (878.0, -335.0)				# Cont.					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

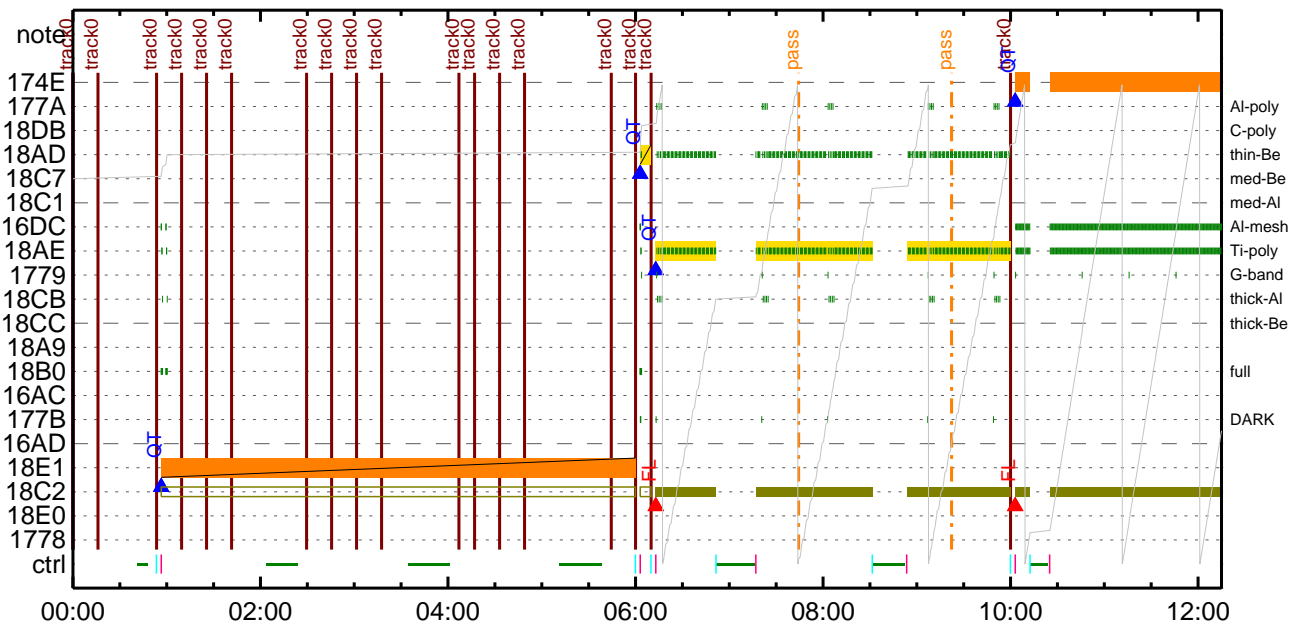
CMDI #0531 2012/03/22



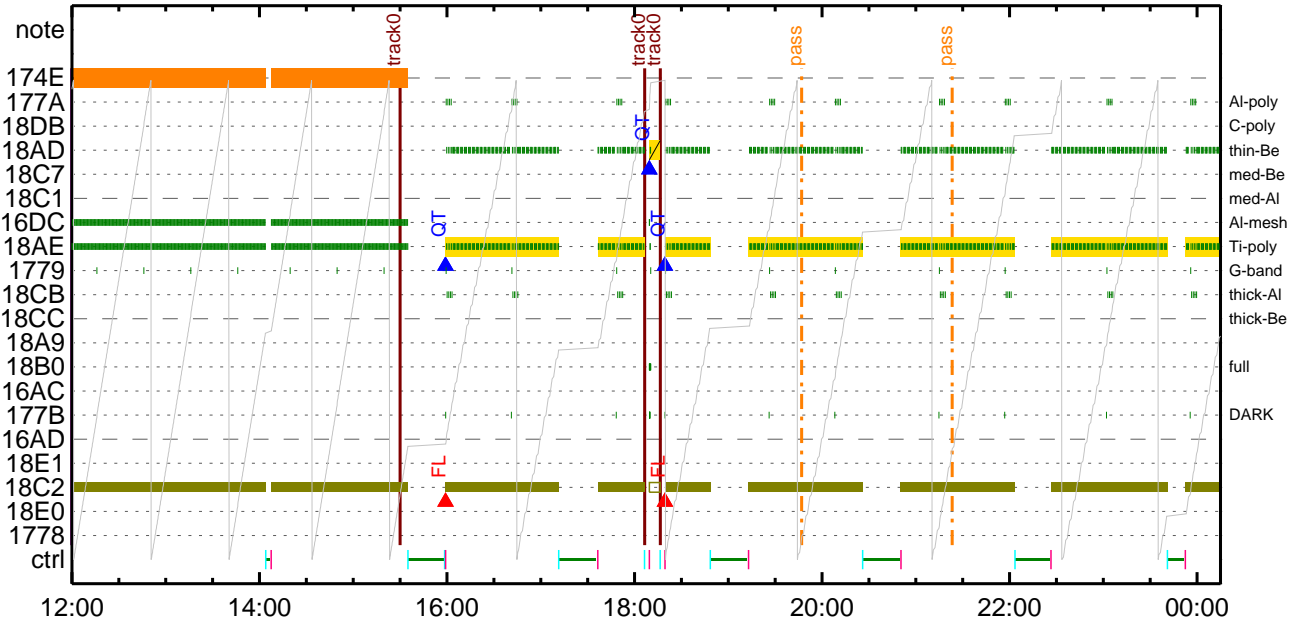
CMDI #0531 2012/03/22



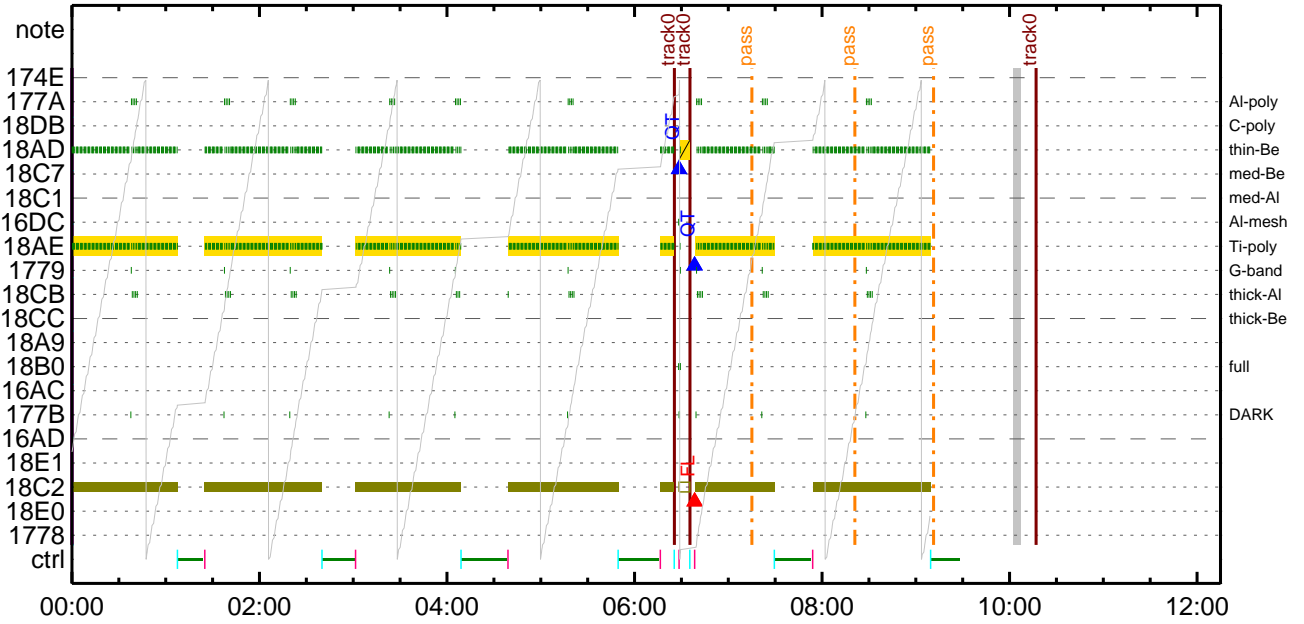
CMDI #0531 2012/03/23



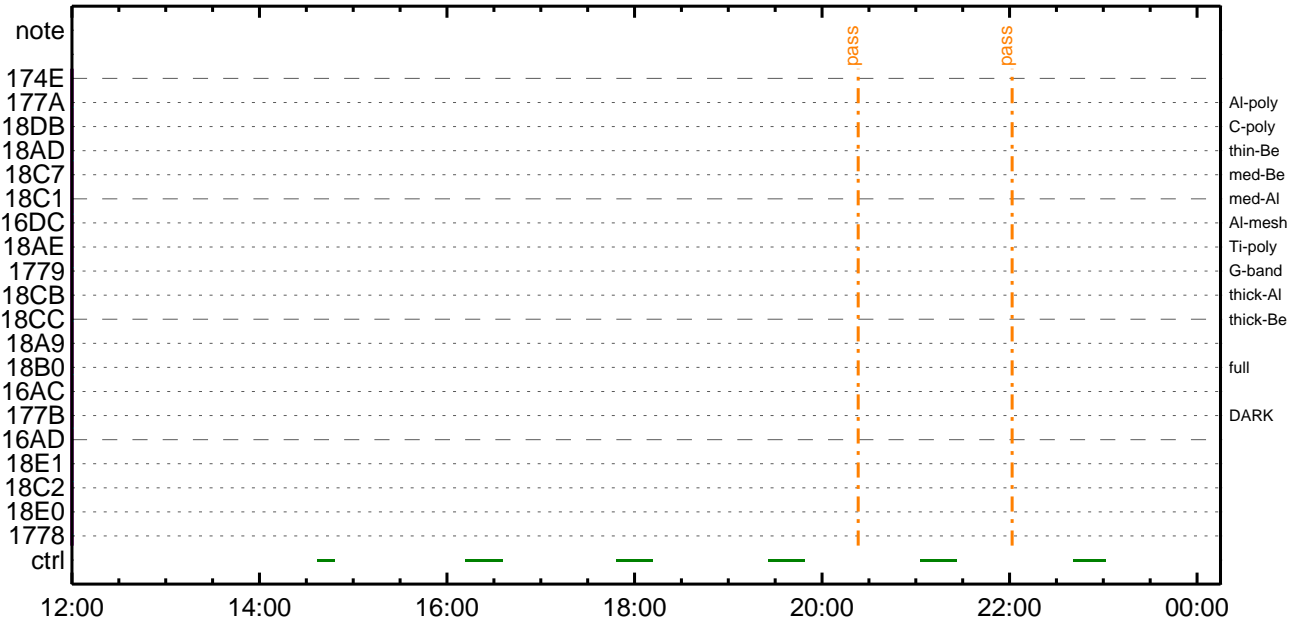
CMDI #0531 2012/03/23



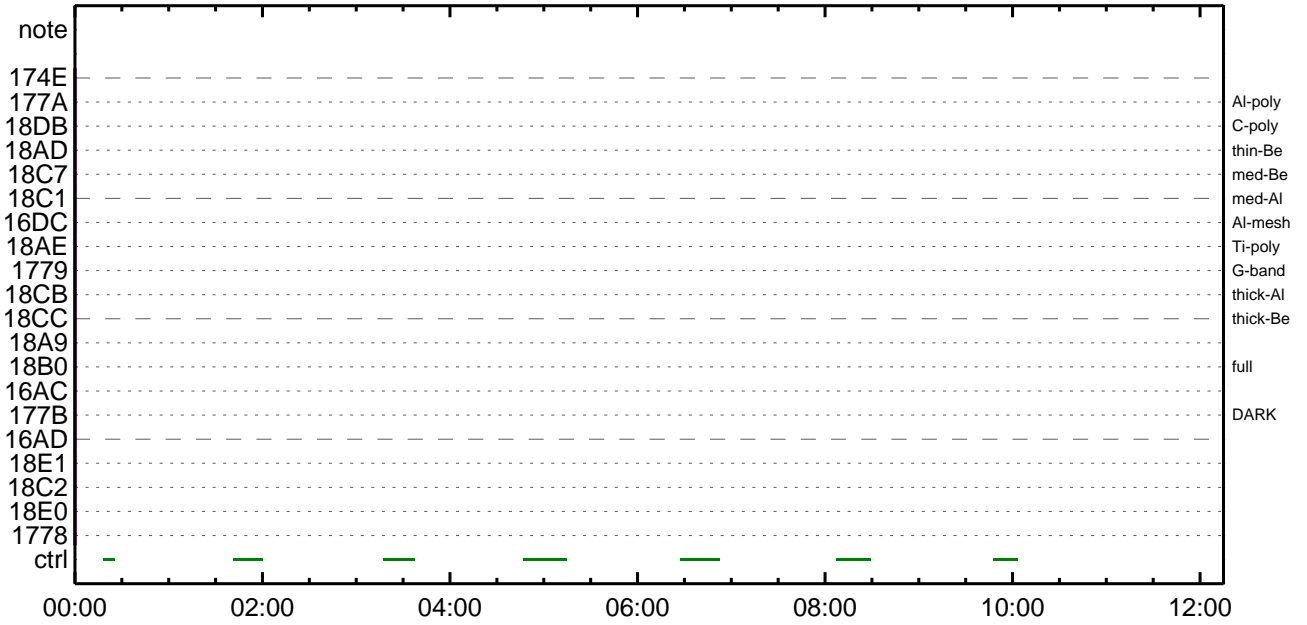
CMDI #0531 2012/03/24



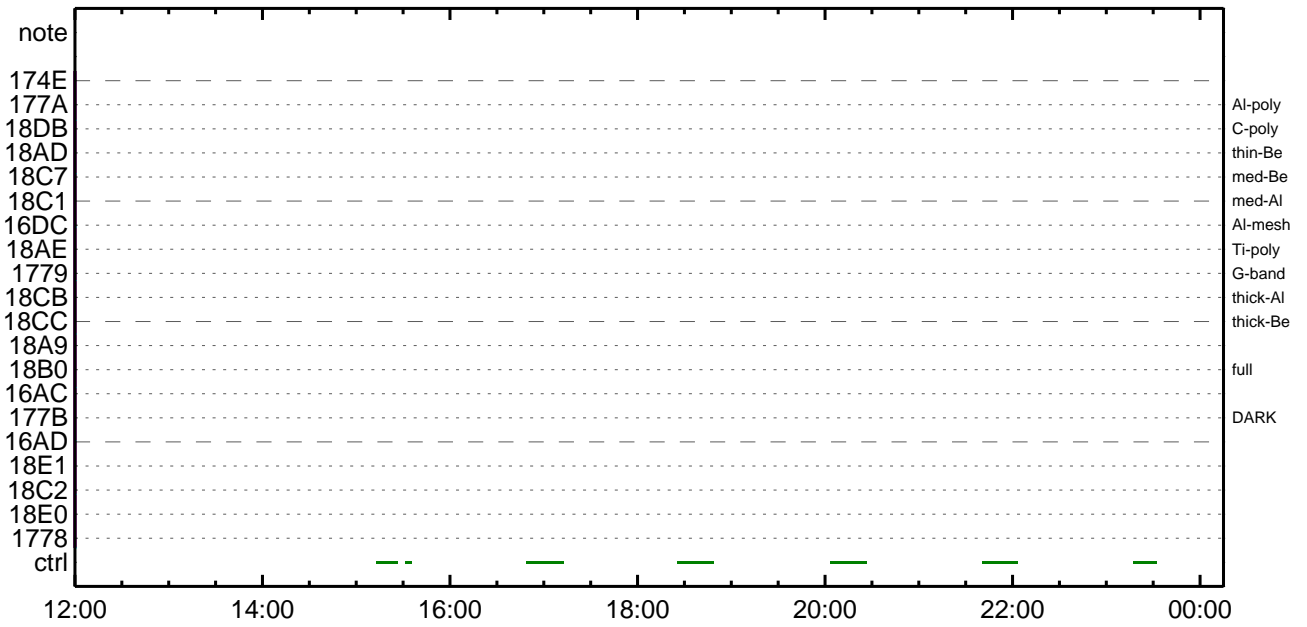
CMDI #0531 2012/03/24



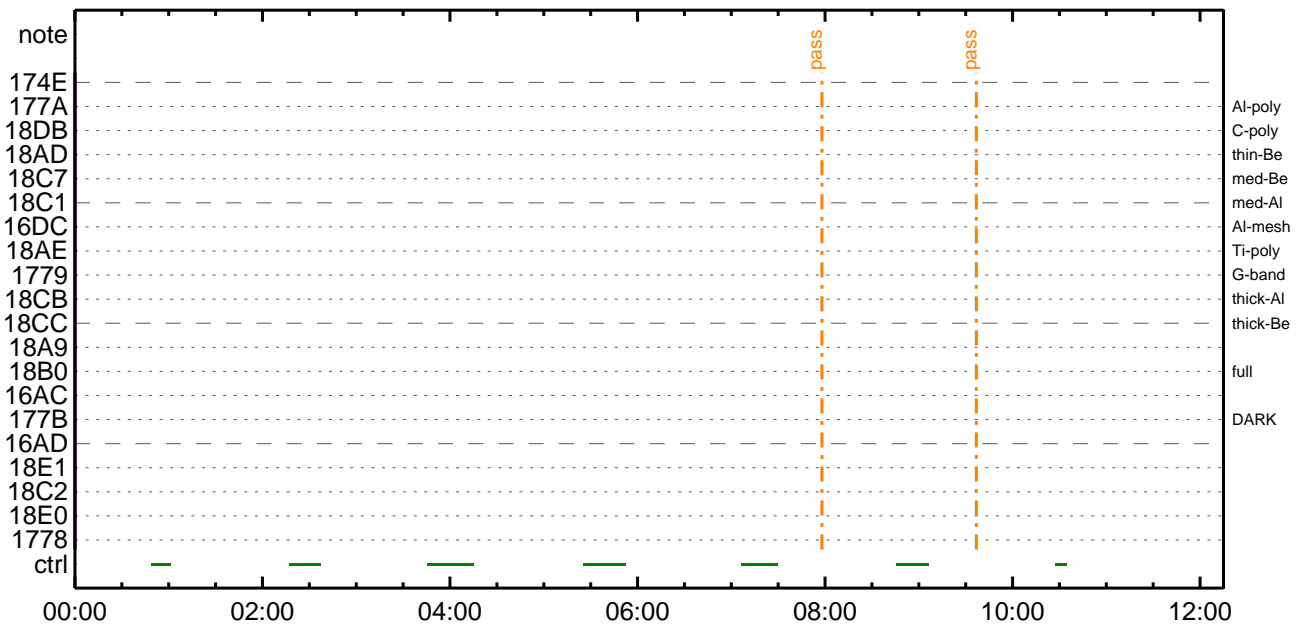
CMDI #0531 2012/03/25



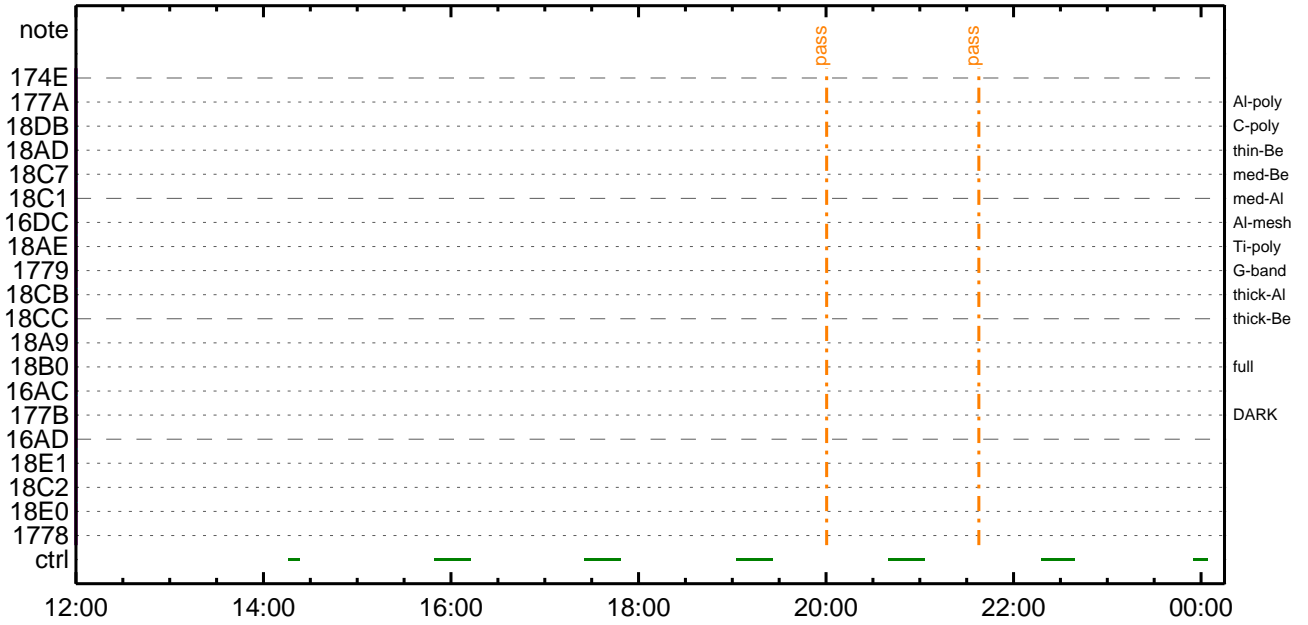
CMDI #0531 2012/03/25



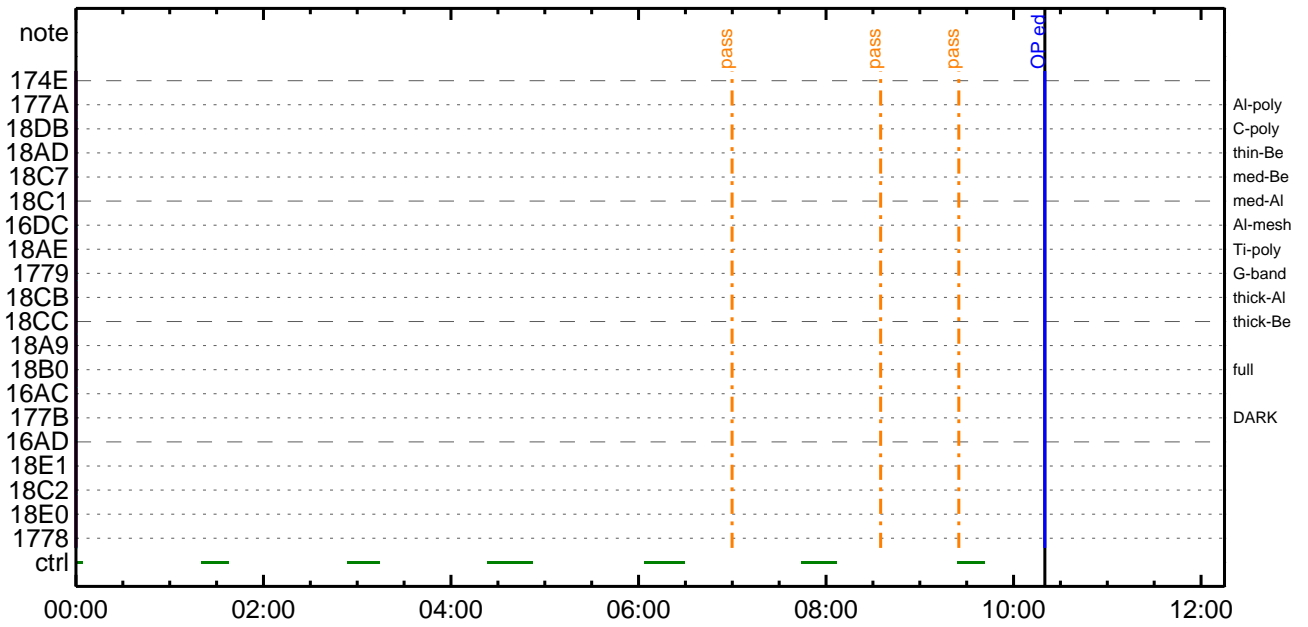
CMDI #0531 2012/03/26



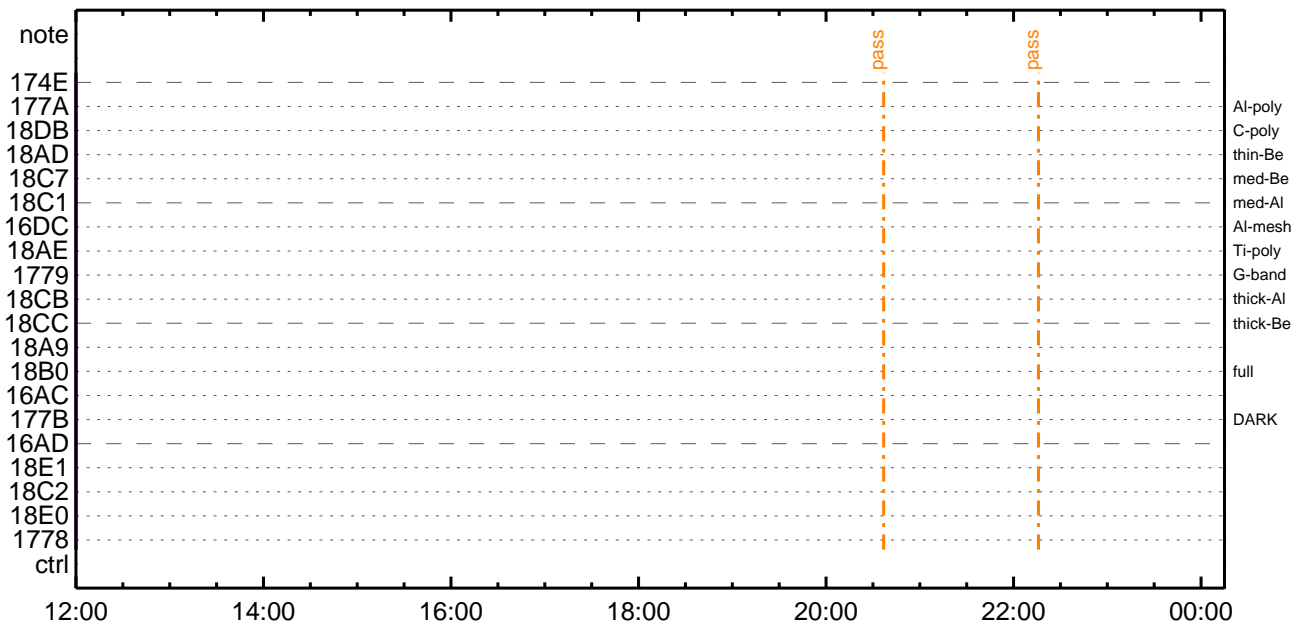
CMDI #0531 2012/03/26



CMDI #0531 2012/03/27



CMDI #0531 2012/03/27




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-761:OP
0104 ( )
0105 S. OG og-761:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °E²¼²î½E¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î½E¹ç•è²îOK²³îÇ§
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 C. TIY³YpYôYE²òðÁDîç(UT)
0183 +. TI 2012-03-22 09:20:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-03-22 09:20:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-03-22 09:20:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-267:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2012-03-22 09:24:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 85 83 08 08)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 85 83 0c 0c)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 20 20)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 80 80 20 08)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0b 80 80 08 20)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 0f 80 80 06 06)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 10 80 80 08 08)
0174 + DC 07-F0 MDP_XRT_FLD_ENA
0175 BC (d8)
0176 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0177 BC (c8)
0178 + DC 07-F0 MDP_XRT_AEC_RESET
0179 BC (d0)
0180 + DC 07-F0 MDP_XRT_ARS_DIS
0181 BC (d5)
0182 + DC 07-F0 MDP_XRT_FLD_RESET
0183 BC (da)
0184 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0185 BC (c4 0d)
0186 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0187 BC (c5 03)
0188 . C. ----- Success Verify ? OK / NG ____
0189 C.
0190 C.
0191 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0192 C.
0193 +. DC 07-F0 MDP_XRT_MODE_OBSV

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0194 BC (c2)
0195 +. TI 2012-03-22 09:24:02.0
0196 DC 07-F0 MDP_XRT_MODE_OBSV
0197 BC (c2)
0198 . C. ----- Success Verify ? OK / NG ____
0199 C.
0200 C. ***** XRT END *****
0201 C.
0202 . C. ***** MDP 'úÃîâî»ô¼ÝðËÂð¹æDCBC•x²è *****
0203 C. (¼ã°îÿÓÿÃÿËÿbÿËÿáÿçÿèæ¼¼¼¼¼»Û¹æè)
0204 . S. DC-BC dcbc-402:DCBC
0205 (MDP_known_event)
0206 C.
0207 C.
0208 . C. ***** ÿDÿ¹•ï Daily±¿îÑæÉ'Ø¹æDCBC•x²è *****
0209 . S. DC-BC dcbc-153:DCBC
0210 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0211 C.
0212 C.
0213 . C. ;ãLOSÿÁÿ§ÿËÿ-¼Â»Û;ä
0214 C.
0215 . C. ***** LOS *****
0216 C.

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*** OP Sequence for XRT ***

2012/03/22	09:33:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/03/22	09:33:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/03/22	09:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/03/22	09:35:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00 ld ca b1 f4			
2012/03/22	09:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/03/22	09:50:30.0	XRT_CTRL_MANU_429_OG [0x1ad]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/03/22	09:50:32.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/03/22	09:50:52.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/03/22	09:50:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/03/22	09:50:56.0	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/03/22	09:50:58.0	XRT_ARS_DIS_431_OG [0x1af]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/03/22	09:51:00.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/03/22	09:51:02.0	XRT_QT_PROG_SET_405_OG [0x195]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2012/03/22	09:51:04.0	XRT_FL_PROG_SET_414_OG [0x19e]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2012/03/22	09:51:06.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/03/22	09:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/03/22	10:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 54 e5 01 3f			
2012/03/22	10:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/03/22	10:02:46.0	XRT_FLD_ENA_435_OG [0x1b3]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/03/22	10:02:48.0	XRT_FLRCTRL_ENA_449_OG [0x1c1]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/03/22	10:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/03/22	10:02:52.0	XRT_ARS_DIS_431_OG [0x1af]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/03/22	10:02:54.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/03/22	10:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2012/03/22	10:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2012/03/22	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/03/22	14:59:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/03/22	14:59:02.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/03/22	14:59:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/03/22	15:02:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/03/22	15:12:00.0	XRT_Custom_418_OG [0x1a2]						
2012/03/22	15:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/03/22	15:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/03/22	15:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 48 00 d3 8e			
2012/03/22	15:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/03/22	15:32:46.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/03/22	15:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/03/22	15:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/03/22	15:32:52.0	XRT_ARS_DIS_431_OG [0x1af]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/03/22	15:32:54.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/03/22	15:32:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09			
2012/03/22	15:32:58.0	XRT_FL_PROG_SET_414_OG [0x19e]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2012/03/22	15:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/03/22	16:34:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/03/22	16:34:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/03/22	16:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			

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2012/03/22	16:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/22	16:58:30.0	XRT_Custom_418_OG [0x1a2]							
2012/03/22	16:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/22	17:48:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/22	17:48:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/03/22	17:48:30.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/03/22	17:48:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/03/22	17:48:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/03/22	17:48:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/03/22	17:51:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2012/03/22	17:51:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/22	17:58:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/22	17:58:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 1d ca b1 f4				
2012/03/22	18:00:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/03/22	18:01:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/03/22	18:01:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/03/22	18:01:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/03/22	18:01:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/03/22	18:01:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/22	18:01:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2012/03/22	18:01:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/03/22	18:01:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/22	18:12:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/22	18:12:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/22	18:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/22	18:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/22	18:35:30.0	XRT_Custom_418_OG [0x1a2]							
2012/03/22	18:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/22	19:49:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/22	19:49:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/22	19:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/22	19:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/22	20:12:30.0	XRT_Custom_418_OG [0x1a2]							
2012/03/22	20:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/22	21:26:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/22	21:26:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/22	21:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/22	21:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/22	21:49:00.0	XRT_Custom_418_OG [0x1a2]							
2012/03/22	21:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/22	23:04:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/22	23:04:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/22	23:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/22	23:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/22	23:21:30.0	XRT_Custom_418_OG [0x1a2]							
2012/03/22	23:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/22	23:59:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/23	00:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 00 00 00 ac cd				
2012/03/23	00:16:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 00 00 00 d6 67				

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2012/03/23	00:53:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/23	00:53:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/03/23	00:53:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00
2012/03/23	00:53:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/03/23	00:53:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/03/23	00:53:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/03/23	00:56:28.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/03/23	00:56:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/03/23	01:09:30.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00 00 29 99
2012/03/23	01:25:30.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 00 00 00 53 33
2012/03/23	01:41:30.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 d6 36 b7 8e
2012/03/23	02:29:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00 b4 b5 db 75
2012/03/23	02:45:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00 ac 5b 00 00
2012/03/23	03:01:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 b4 b5 24 8b
2012/03/23	03:17:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00 d6 36 48 72
2012/03/23	04:07:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00 29 ca b7 8e
2012/03/23	04:17:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 4b 4b db 75
2012/03/23	04:33:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 53 a5 00 00
2012/03/23	04:49:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00 4b 4b 24 8b
2012/03/23	05:44:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 29 db 48 72
2012/03/23	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/23	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/03/23	06:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00 00
2012/03/23	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/03/23	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/03/23	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/03/23	06:02:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2012/03/23	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/03/23	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/23	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 1d ca b1 f4
2012/03/23	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/03/23	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/03/23	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/03/23	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/03/23	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/03/23	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/03/23	06:12:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/03/23	06:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/03/23	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/03/23	06:51:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/23	06:51:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/03/23	06:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/03/23	06:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/03/23	07:16:00.0	XRT_Custom_418_OG [0x1a2]				
2012/03/23	07:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/03/23	08:31:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/23	08:31:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da

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2012/03/23	08:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/23	08:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/23	08:52:30.0	XRT_Custom_418_OG [0x1a2]							
2012/03/23	08:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/23	09:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/23	10:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 54 e5 01 3f				
2012/03/23	10:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/03/23	10:02:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/03/23	10:02:48.0	XRT_FLRCTRL_ENA_449_OG [0x1c1]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/03/23	10:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/03/23	10:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/03/23	10:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/23	10:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2012/03/23	10:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/03/23	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/23	10:12:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/23	10:12:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/23	10:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/23	10:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/23	10:24:00.0	XRT_Custom_418_OG [0x1a2]							
2012/03/23	10:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/23	14:04:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/23	14:04:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/23	14:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/23	14:06:30.0	XRT_Custom_418_OG [0x1a2]							
2012/03/23	14:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/23	14:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/23	15:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 ld ca b1 f4				
2012/03/23	15:35:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/23	15:35:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/23	15:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/23	15:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/23	15:58:30.0	XRT_CTRL_MANU_429_OG [0x1ad]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/23	15:58:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/03/23	15:58:52.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/03/23	15:58:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/03/23	15:58:56.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/03/23	15:58:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/03/23	15:59:00.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/23	15:59:02.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/03/23	15:59:04.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/03/23	15:59:06.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/23	17:11:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/03/23	17:11:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/03/23	17:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/03/23	17:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/03/23	17:35:30.0	XRT_Custom_418_OG [0x1a2]							
2012/03/23	17:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/03/23	18:06:24.0	XRT_CTRL_MANU_400_OG [0x190]							

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2012/03/23	18:06:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/23	18:06:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/03/23	18:06:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00
2012/03/23	18:06:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/03/23	18:06:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/03/23	18:09:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/03/23	18:09:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2012/03/23	18:16:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/03/23	18:16:30.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/23	18:18:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	00 1d ca b1 f4
2012/03/23	18:19:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/03/23	18:19:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/03/23	18:19:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/03/23	18:19:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/03/23	18:19:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/03/23	18:19:26.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/03/23	18:19:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/03/23	18:19:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/03/23	18:48:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/03/23	18:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/23	18:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/03/23	18:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/03/23	19:12:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/03/23	19:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2012/03/23	20:26:00.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2012/03/23	20:26:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/03/23	20:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/23	20:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/03/23	20:49:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/03/23	20:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/03/23	22:03:30.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_Custom_418_OG [0x1a2]			
2012/03/23	22:03:32.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2012/03/23	22:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/03/23	22:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/23	22:25:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/03/23	22:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/03/23	23:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/03/23	23:41:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/03/23	23:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	XRT_Custom_418_OG [0x1a2]			
2012/03/23	23:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2012/03/23	23:51:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/03/23	23:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/03/24	01:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/03/24	01:07:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/03/24	01:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/03/24	01:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/03/24	01:24:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/03/24	01:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2012/03/24	02:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0

2012/03/24	02:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/24	02:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/24	02:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/24	03:00:30.5	XRT_Custom_418_OG [0x1a2]					
2012/03/24	03:01:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/24	04:09:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/24	04:09:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/24	04:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/24	04:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/24	04:38:00.0	XRT_Custom_418_OG [0x1a2]					
2012/03/24	04:39:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/24	05:49:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/24	05:49:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/24	05:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/24	05:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/24	06:15:30.0	XRT_Custom_418_OG [0x1a2]					
2012/03/24	06:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/24	06:25:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/24	06:25:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/03/24	06:25:30.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00	
2012/03/24	06:25:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/03/24	06:25:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/03/24	06:25:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/03/24	06:28:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2012/03/24	06:28:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/24	06:35:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/24	06:35:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 1d ca b1 f4	
2012/03/24	06:37:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/03/24	06:38:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/03/24	06:38:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/03/24	06:38:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/03/24	06:38:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/03/24	06:38:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/24	06:38:26.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2012/03/24	06:38:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2012/03/24	06:38:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/24	07:29:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/24	07:29:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/24	07:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/24	07:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/24	07:53:00.0	XRT_Custom_418_OG [0x1a2]					
2012/03/24	07:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/03/24	09:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/03/24	09:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/03/24	09:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/03/24	09:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/03/24	10:17:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00	