

XRT Timeline to be uploaded on 2012/03/27

Period: 2012/03/27 10:20:00 - 2012/03/31 10:40:00

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Normal mode

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XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad

| Term | Pointing (x, y) | Comment |
|---------------------------------|---|----------------------------------|
| 03/27 15:03:30 - 03/27 17:38:54 | Track (-491.3, -299.8) ^{Ⓢ 03/27 10:30:00} | # OP start + 10min, Obs AR 11445 |
| 03/27 17:52:00 - 03/28 05:57:54 | Track (-439.6, -296.2) ^{Ⓢ 03/27 17:49:00} | Obs AR 11445 |
| 03/28 06:11:00 - 03/28 17:59:54 | Track (-348.1, -291.2) ^{Ⓢ 03/28 06:08:00} | Obs AR 11445 |

PROG= 13 Inf.-time(s)

| Subr= 1 | 1-time(s) | 2.0sec | | | | | | | | | | |
|-----------------|-------------------|-----------------|------|-------|-------|-----|-----|----------------------|-------|------------|----------|---------|
| Seqn= 19 | 1-time(s) | 2.0sec | | | | | | | | | | |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | open | Safe | Norm | 63ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |
| Seqn= 96 | 4-time(s) | 2.0sec | | | | | | | | | | |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Be | close | Safe | Norm | 1.00s | Obs | 1x1 | 512x512 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 5.66s | Obs | 1x1 | 512x512 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Al | close | Safe | Norm | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| Subr= 2 | 1-time(s) | 2.0sec | | | | | | | | | | |
| Seqn= 62 | 15-time(s) | 150.0sec | | | | | | | | | | |
| thin-Be/Open | Open/thick-Al | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Be | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 15.0sec |
| thin-Be/Open | Open/thick-Al | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 1 | 2.0sec |
| Open/Ti-poly | Open/thick-Be | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 1 | 15.0sec |
| thin-Be/Open | Open/thick-Al | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 2 | 2.0sec |
| Open/Ti-poly | Open/thick-Be | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 2 | 15.0sec |
| thin-Be/Open | Open/thick-Al | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 3 | 2.0sec |
| Open/Ti-poly | Open/thick-Be | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 3 | 15.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC Buffer | Interval | |

XOB #187C: Synoptic Q95 2x2 - Al/mesh(45/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2897) + Thin-Be(36)

| Term | Pointing (x, y) | Comment |
|---------------------------------|-------------------|-----------------------------|
| 03/27 17:42:00 - 03/27 17:48:54 | Fixed (0.0, 0.0) | synoptic, shifted -21.0 min |
| 03/28 06:01:00 - 03/28 06:07:54 | Fixed (0.0, 0.0) | synoptic, shifted -2.0 min |
| 03/28 18:03:00 - 03/28 18:09:54 | Fixed (0.0, 0.0) | synoptic |
| 03/29 06:19:00 - 03/29 06:25:54 | Fixed (0.0, 0.0) | synoptic, shifted 16.0 min |

PROG= 01 1-time(s)

| Subr= 1 | 1-time(s) | 12.0sec | | | | | | | | | | |
|-----------------|------------------|---------------|------|-------|-------|-----|-----|------------------------|-------|------------|----------|--------|
| Seqn= 44 | 1-time(s) | 4.0sec | | | | | | | | | | |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 44ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 2.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Seqn= 5 | 1-time(s) | 2.0sec | | | | | | | | | | |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 8x8 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 2048x512 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 512x2048 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |
| Seqn= 26 | 1-time(s) | 4.0sec | | | | | | | | | | |
| Open/Ti-poly | Open/Ti-poly | close | Safe | Norm | 86ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/Ti-poly | close | Safe | Norm | 2.83s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Seqn= 35 | 1-time(s) | 2.0sec | | | | | | | | | | |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 354ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 2.83s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Seqn= 4 | 1-time(s) | 2.0sec | | | | | | | | | | |
| Open/G-band | Open/G-band | open | Safe | Norm | 16ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC Buffer | Interval | |

XOB #18E2: AR Standard-B(Morphology) with PFB, thin-Be + thick Al and Al/Poly context, 384x384 at 1064 1048, 140s-cad

| Term | Pointing (x, y) | Comment |
|---------------------------------|---|--------------|
| 03/28 18:13:00 - 03/29 06:15:54 | Track (-254.2, -287.6) ^{Ⓢ 03/28 18:10:00} | Obs AR 11445 |
| 03/29 06:29:00 - 03/29 09:00:00 | Track (-155.0, -285.2) ^{Ⓢ 03/29 06:26:00} | Obs AR 11445 |

PROG= 08 Inf.-time(s)

| Subr= 1 | 1-time(s) | 2.0sec | | | | | | | | | | |
|-----------------|-------------------|---------------|------|------|-------|-----|-----|----------------------|------|---|---|---------|
| Seqn= 19 | 1-time(s) | 2.0sec | | | | | | | | | | |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | open | Safe | Norm | 63ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |
| Seqn= 96 | 4-time(s) | 2.0sec | | | | | | | | | | |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Be | close | Safe | Norm | 1.00s | Obs | 1x1 | 512x512 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 5.66s | Obs | 1x1 | 512x512 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Al | close | Safe | Norm | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| Subr= 2 | 1-time(s) | 2.0sec | | | | | | | | | | |
| Seqn= 11 | 30-time(s) | 2.0sec | | | | | | | | | | |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 20.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 1 | 20.0sec |

| | | | | | | | | | | | | |
|----------------|----------------|-------|------|-------|-------|-----|-----|----------------------|-------|------------|----------|---------|
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 2 | 20.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 3 | 20.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 1 | 20.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 2 | 20.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 3 | 20.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC Buffer | Interval | |

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Flare mode

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XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops

| Term | Pointing (x, y) | Comment |
|---------------------------------|---|----------------------------------|
| 03/27 15:03:30 - 03/27 17:38:54 | Track (-491.3, -299.8) ^{Ⓢ 03/27 10:30:00} | # OP start + 10min, Obs AR 11445 |
| 03/27 17:52:00 - 03/28 05:57:54 | Track (-439.6, -296.2) ^{Ⓢ 03/27 17:49:00} | Obs AR 11445 |
| 03/28 06:11:00 - 03/28 17:59:54 | Track (-348.1, -291.2) ^{Ⓢ 03/28 06:08:00} | Obs AR 11445 |
| 03/28 18:13:00 - 03/29 06:15:54 | Track (-254.2, -287.6) ^{Ⓢ 03/28 18:10:00} | Obs AR 11445 |
| 03/29 06:29:00 - 03/29 09:00:00 | Track (-155.0, -285.2) ^{Ⓢ 03/29 06:26:00} | Obs AR 11445 |

PROG= 03 12-time(s)

| | | | | | | | | | | | | |
|----------------|------------------|---------|------|-------|-------|-----|-----|----------------------|-------|------------|----------|--------|
| Subr= 1 | 45-time(s) | 10.0sec | | | | | | | | | | |
| Seqn= 20 | 1-time(s) | 2.0sec | | | | | | | | | | |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| Seqn= 63 | 1-time(s) | 2.0sec | | | | | | | | | | |
| med-Al/Open | med-Al/thick-Al | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| Open/thick-Be | Open/thick-Be | close | Safe | Norm | 2.00s | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| Seqn= 77 | 1-time(s) | 2.0sec | | | | | | | | | | |
| Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 125ms | Obs | 2x2 | 512x512 (1024, 1024) | Q=95 | 2 | 0 | 2.0sec |
| Subr= 2 | 1-time(s) | 10.0sec | | | | | | | | | | |
| Seqn= 90 | 1-time(s) | 2.0sec | | | | | | | | | | |
| Open/G-band | Open/G-band | open | Safe | Norm | 63ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Al | close | Safe | Dark | 1.00s | Obs | 1x1 | 384x384 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Al | close | Safe | Dark | 1.00s | Obs | 2x2 | 512x512 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC Buffer | Interval | |

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Active Region Search

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NOT USED

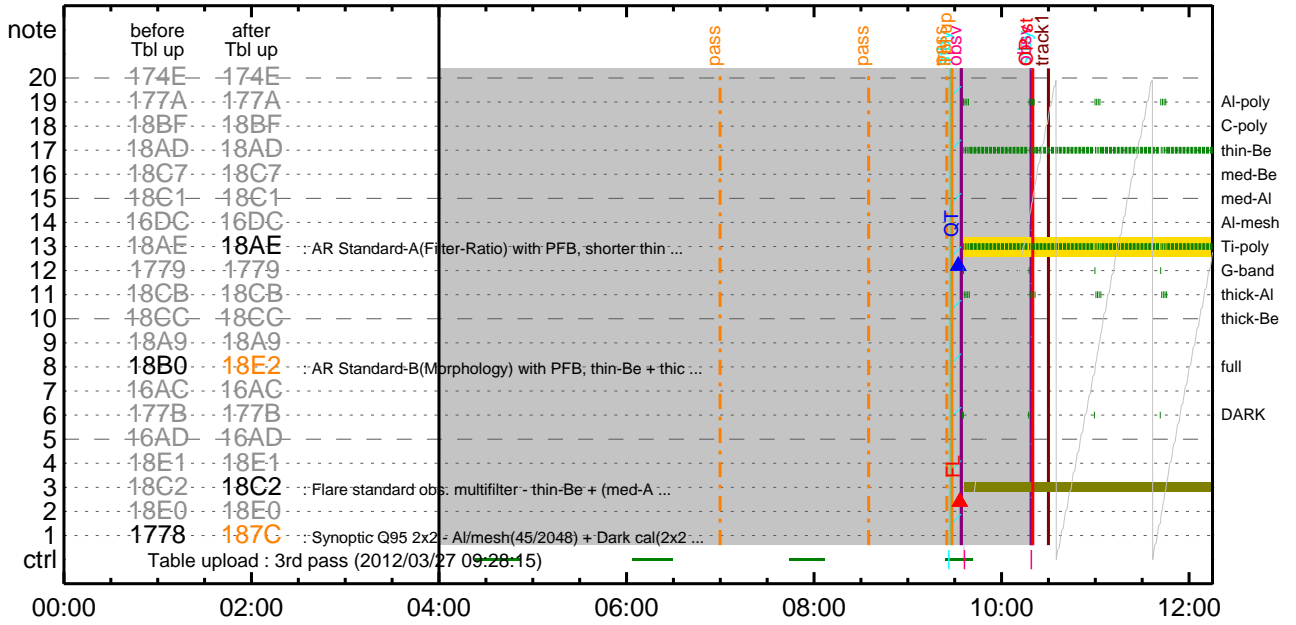
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Flare Detection

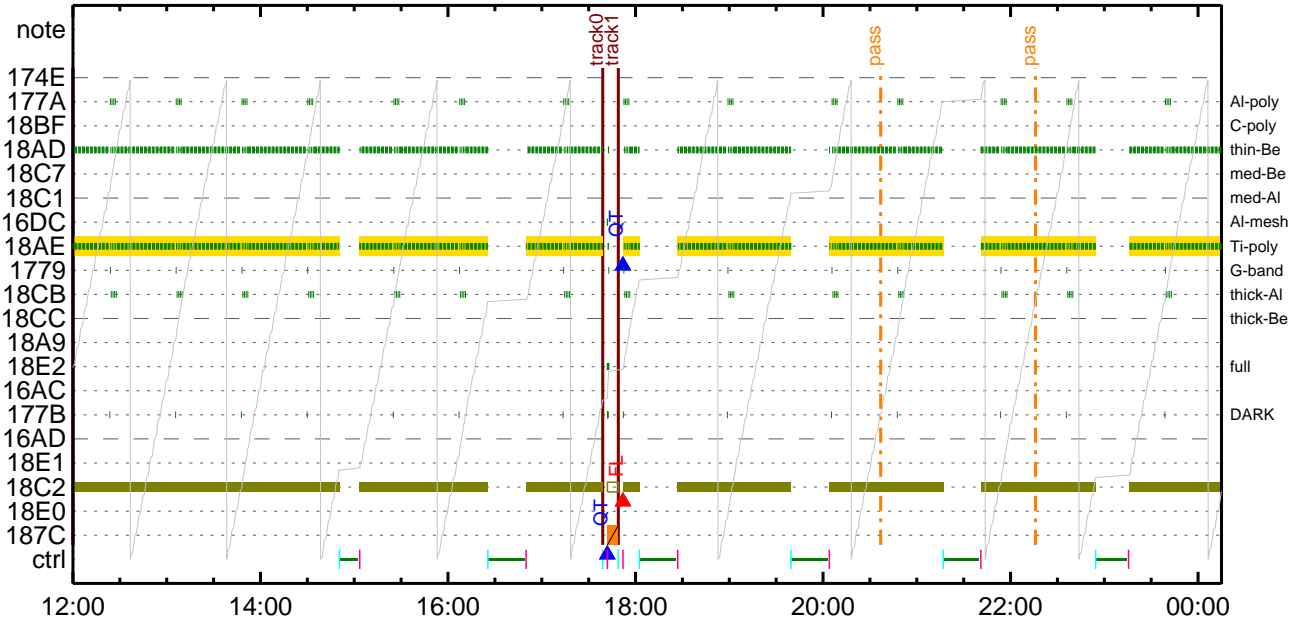
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| FLD Patrol | | | | | | | | | | | | |
|---------------------------------|---|--------------|------|-------|------|-----|-----|--------------------|-------|------------|----------|-------|
| Term | Pointing (x, y) | Comment | | | | | | | | | | |
| 03/27 17:51:46 - 03/28 05:58:16 | Track (-439.6, -296.2) ^{Ⓢ 03/27 17:49:00} | Obs AR 11445 | | | | | | | | | | |
| 03/28 06:10:46 - 03/28 18:00:16 | Track (-348.1, -291.2) ^{Ⓢ 03/28 06:08:00} | Obs AR 11445 | | | | | | | | | | |
| 03/28 18:12:46 - 03/29 06:16:16 | Track (-254.2, -287.6) ^{Ⓢ 03/28 18:10:00} | Obs AR 11445 | | | | | | | | | | |
| 03/29 06:28:46 - 03/31 10:40:00 | Track (-155.0, -285.2) ^{Ⓢ 03/29 06:26:00} | Obs AR 11445 | | | | | | | | | | |
| Open/Ti-poly | Open/thick-Al | close | Safe | Norm | 8ms | Obs | 8x8 | | Q=50 | | | 30sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC Buffer | Interval | |

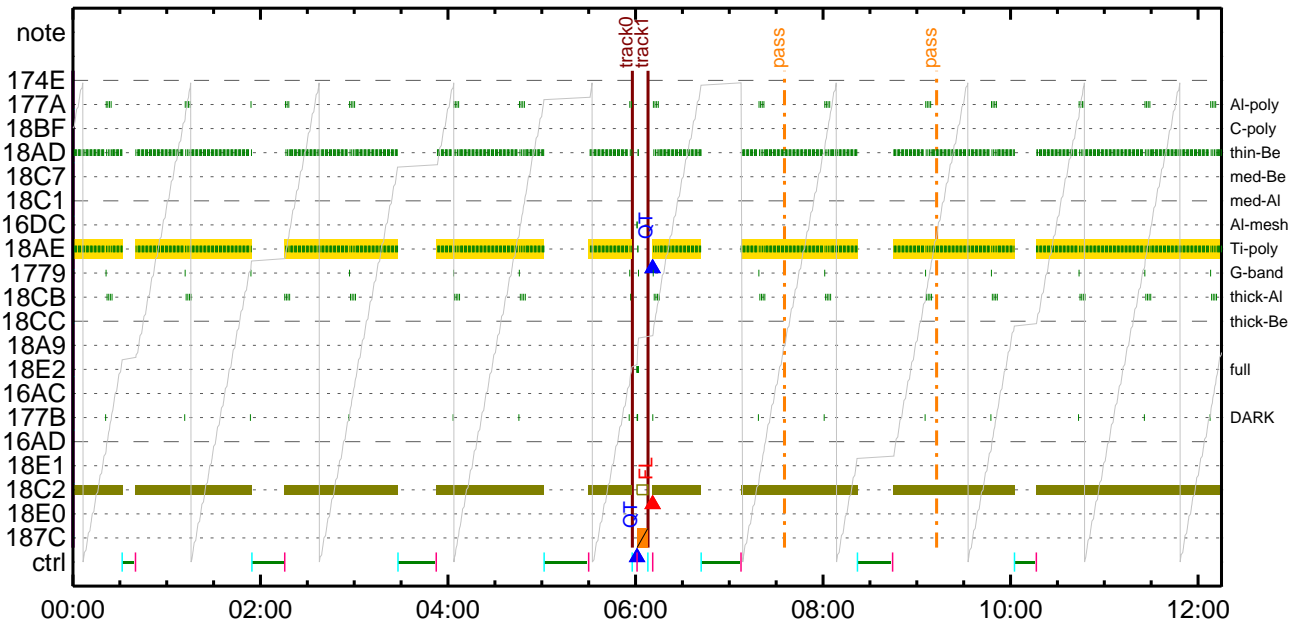
CMDI #0541 2012/03/27



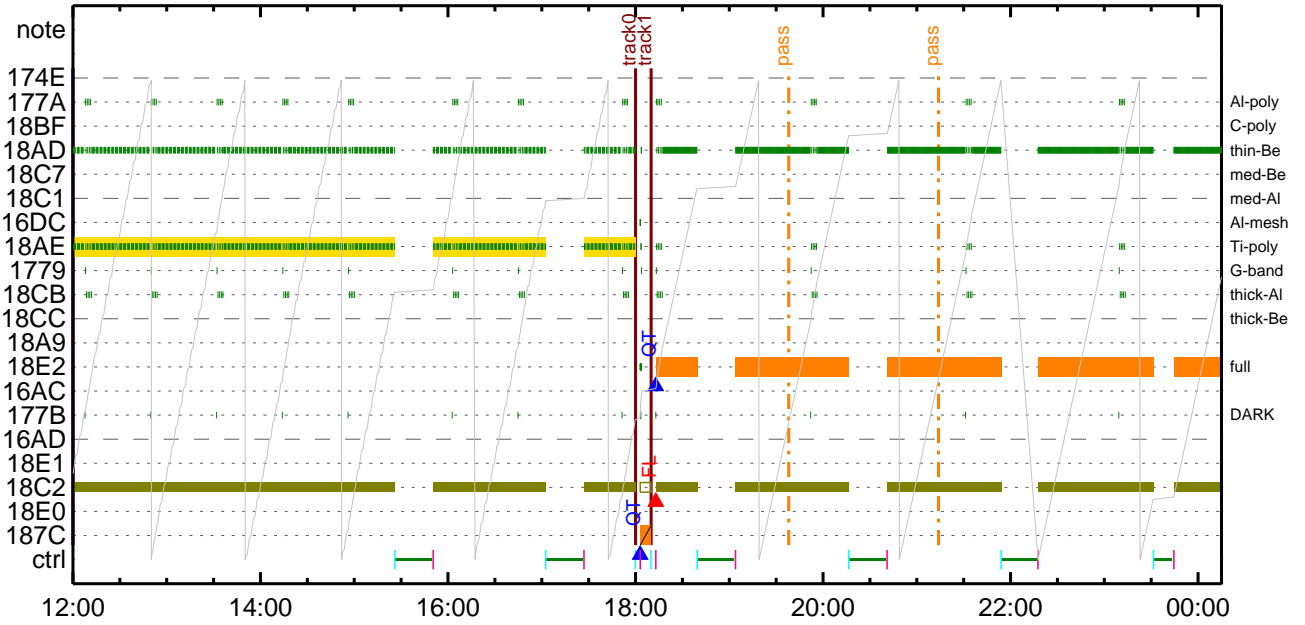
CMDI #0541 2012/03/27



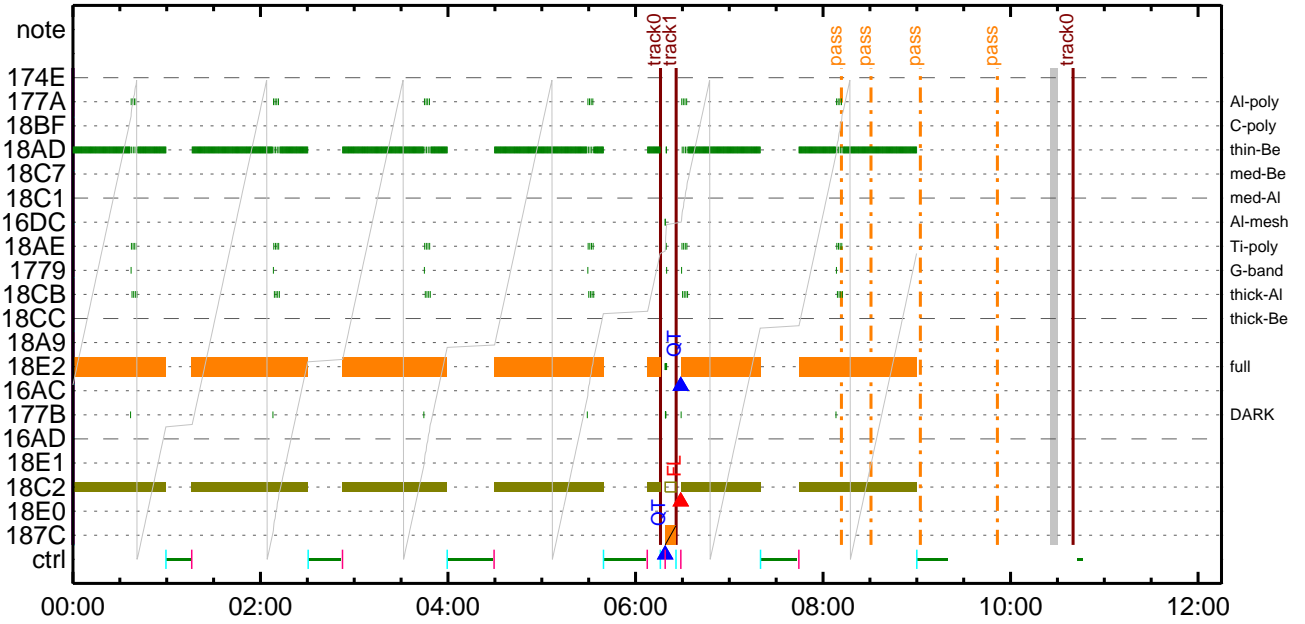
CMDI #0541 2012/03/28



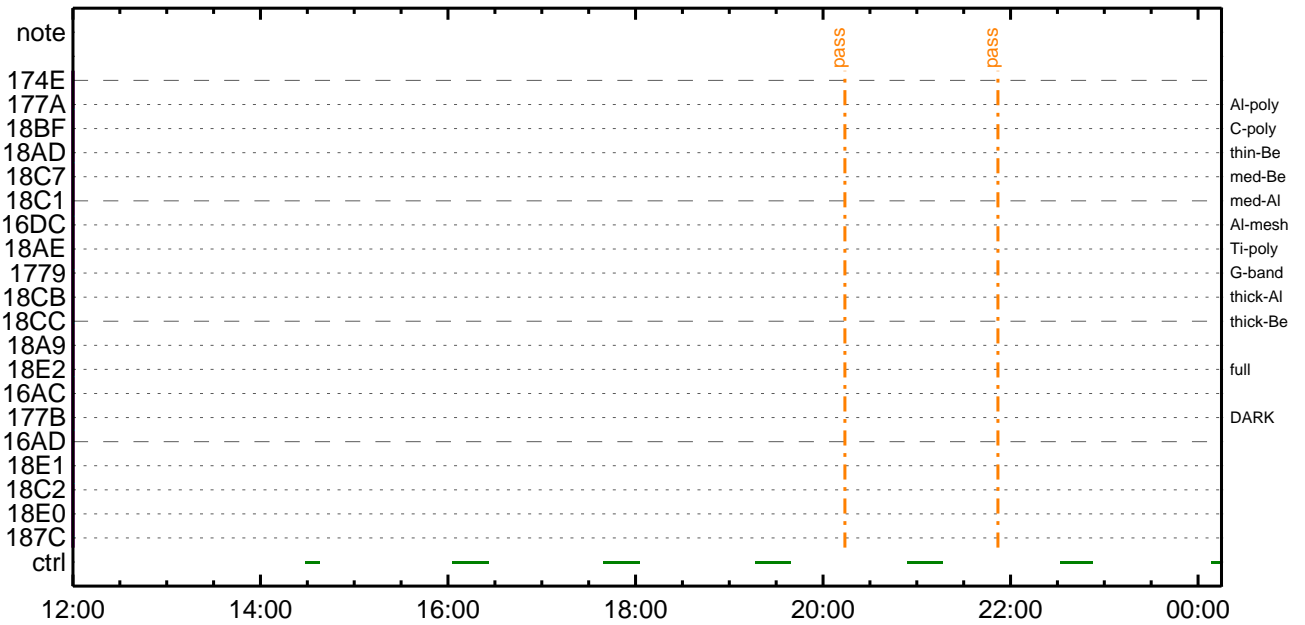
CMDI #0541 2012/03/28



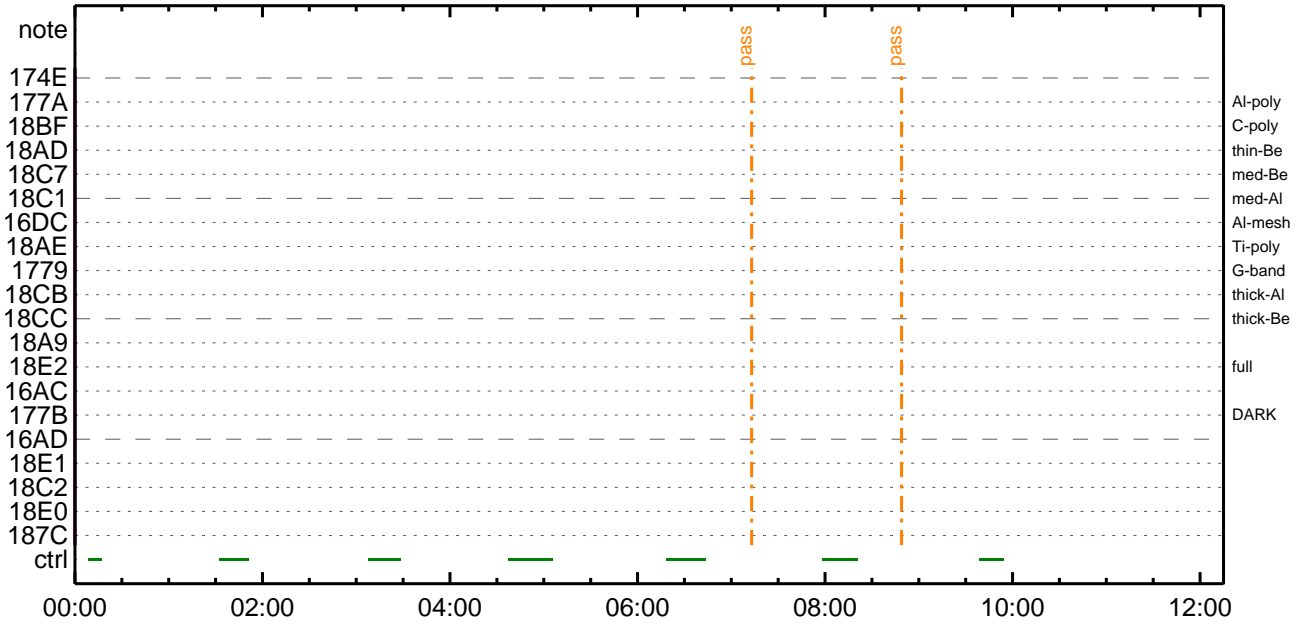
CMDI #0541 2012/03/29



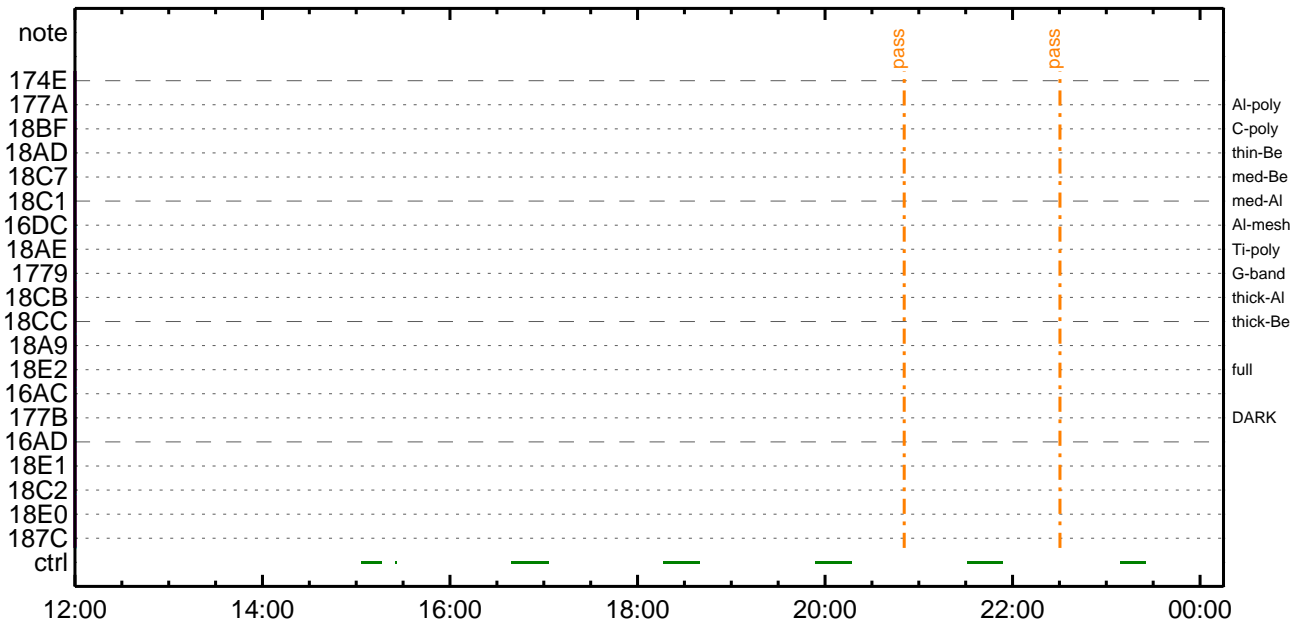
CMDI #0541 2012/03/29



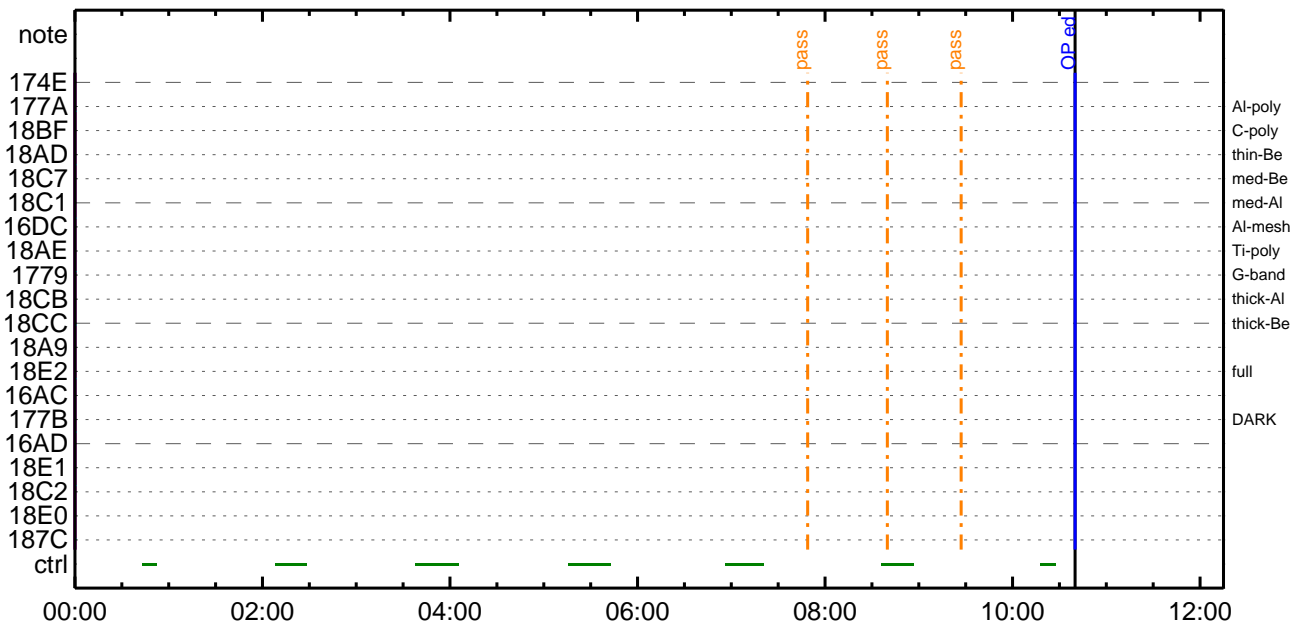
CMDI #0541 2012/03/30



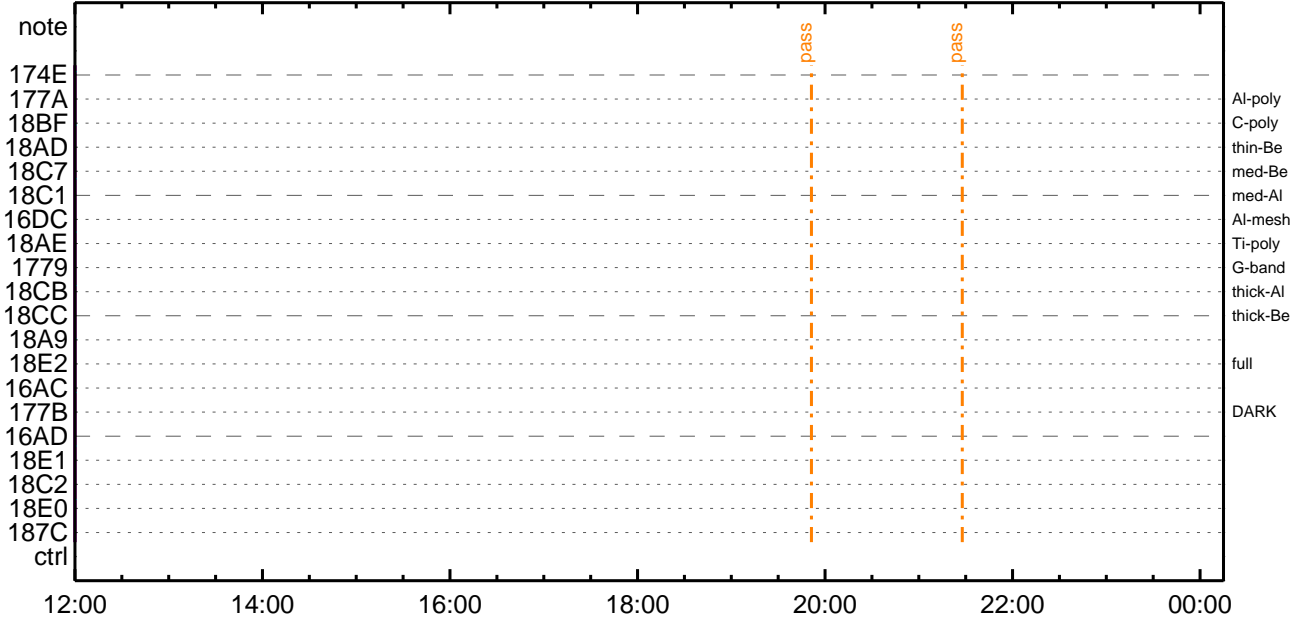
CMDI #0541 2012/03/30



CMDI #0541 2012/03/31



CMDI #0541 2012/03/31




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-771:OP
0104 ( )
0105 S. OG og-771:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °E²¼²î½E¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;E²ðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î½E¹ç•è²îOK²³îÇ§
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 C. TIY³YpYôYE²ð²ÁDîç(UT)
0183 +. TI 2012-03-27 10:15:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-03-27 10:15:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-03-27 10:15:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-773 2012-03-27 12:00:57 164 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿òÁò•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈòÈ¼°ÇÔò•ò¿¼í¹çòÍ;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-284:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 +. DC 07-F0 MDP_FG_CTRL_MANU
0037 BC (51)
0038 C. -----
0039 C. MDP_FG_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 C. YçYÁY×Yí;¼YÉòÎÁ°òÈSTS_CHKòðOFFòÈò¹òè
0043 C.
0044 . S. RAM ram-271:MDP_DPL
0045 ( )
0046 C.
0047 . C. < Dump RAMID=MDP_DPL >
0048 +. DC 07-F0 MDP_DUMP_FGTBL
0049 BC (82 07 00 38 b8 00 40)
0050 C. -----
0051 C. MDP_DPL verify = OK [ ]
0052 C. -----
0053 C.
0054 C. STS_CHKòðONòÈò¹òè
0055 C.
0056 . C. < Update MDP DSC PAR1 >
0057 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0058 BC (4c)
0059 C. MDP_CMD_CODE = F04C0700[ ]
0060 C. MDP_CMD_CNT (count-up 1) [ ]
0061 C. -----
0062 C.
0063 C.
0064 C. *****
0065 C. SOT TI command set
0066 C. *****
0067 C. Execute, after the success of TBL upload.
0068 +. TI 2012-03-27 10:19:18.0
0069 DC 07-F0 MDP_SOT_MODE_OBSV
0070 BC (40)
0071 C. -----
0072 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0073 C. -----
0074 C.
0075 C.
0076 C. ***** XRT START *****
0077 C.
0078 +. DC 07-F0 MDP_XRT_CTRL_MANU
0079 BC (c1)
0080 +. DC 07-F0 MDP_XRT_MODE_STBY
0081 BC (c3)
0082 . C. ----- Success Verify ? OK / NG_____
0083 C.
0084 C. XRT Obs. Table Upload
0085 . S. RAM ram-291:MDP_OBS_X
0086 ( )
0087 C.
0088 +. DC 07-F0 MDP_DUMP_XRTTBL
0089 BC (84 07 00 00 00 3a d4)
0090 . C. ----- Comparison Check ? OK / ERR _____
0091 C.
0092 C.
0093 +. DC 07-F0 MDP_XRT_ROI_SET
0094 BC (cd 01 b1 b1 04 04)
0095 +. DC 07-F0 MDP_XRT_ROI_SET
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0096 BC (cd 02 b1 b1 08 08)
0097 + DC 07-F0 MDP_XRT_ROI_SET
0098 BC (cd 03 b1 b1 08 08)
0099 + DC 07-F0 MDP_XRT_ROI_SET
0100 BC (cd 04 b1 b1 06 06)
0101 + DC 07-F0 MDP_XRT_ROI_SET
0102 BC (cd 05 85 83 06 06)
0103 + DC 07-F0 MDP_XRT_ROI_SET
0104 BC (cd 06 85 83 06 06)
0105 + DC 07-F0 MDP_XRT_ROI_SET
0106 BC (cd 07 85 83 08 08)
0107 + DC 07-F0 MDP_XRT_ROI_SET
0108 BC (cd 08 80 80 20 20)
0109 + DC 07-F0 MDP_XRT_ROI_SET
0110 BC (cd 09 80 80 20 08)
0111 + DC 07-F0 MDP_XRT_ROI_SET
0112 BC (cd 0a 80 80 08 20)
0113 + DC 07-F0 MDP_XRT_ROI_SET
0114 BC (cd 0f 80 80 06 06)
0115 + DC 07-F0 MDP_XRT_ROI_SET
0116 BC (cd 10 80 80 08 08)
0117 + DC 07-F0 MDP_XRT_FLD_ENA
0118 BC (d8)
0119 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0120 BC (c8)
0121 + DC 07-F0 MDP_XRT_AEC_RESET
0122 BC (d0)
0123 + DC 07-F0 MDP_XRT_ARS_DIS
0124 BC (d5)
0125 + DC 07-F0 MDP_XRT_FLD_RESET
0126 BC (da)
0127 + DC 07-F0 MDP_XRT_QT_PROG_SET
0128 BC (c4 0d)
0129 + DC 07-F0 MDP_XRT_FL_PROG_SET
0130 BC (c5 03)
0131 . C. ----- Success Verify ? OK / NG ____
0132 C.
0133 C.
0134 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0135 C.
0136 + DC 07-F0 MDP_XRT_MODE_OBSV
0137 BC (c2)
0138 + TI 2012-03-27 10:19:02.0
0139 DC 07-F0 MDP_XRT_MODE_OBSV
0140 BC (c2)
0141 + DC 07-F0 MDP_XRT_CTRL_AUTO
0142 BC (c0)
0143 + TI 2012-03-27 10:19:04.0
0144 DC 07-F0 MDP_XRT_CTRL_AUTO
0145 BC (c0)
0146 . C. ----- Success Verify ? OK / NG ____
0147 C.
0148 C. ***** XRT END *****
0149 C.
0150 . C. ***** MDP `uãîï»ö%ýðÊâð¹ðêDCBC•x²è *****
0151 C. (%á°îÿÓYÁYBYBYÿáÿçÿèðÊ%¼ðð%Á»Û¹ðé)
0152 . S. DC-BC dcbc-402:DCBC
0153 (MDP_known_event)
0154 C.
0155 C.
0156 . C. ***** ¥Dÿ¹•Ï Daily±¿ÍÑðË´Ø¹ðêDCBC•x²è *****
0157 . S. DC-BC dcbc-153:DCBC
0158 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0159 C.
0160 C.
0161 . C. ;ãLOS¥Á¥$¥Ã¥-¼Á»Û;ä
0162 C.
0163 . C. ***** LOS *****
0164 C.

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Mar 27, 12 12:01

XRT_OGLIST_0541.chk

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*** OP Sequence for XRT ***

| | | | | | | | | | |
|------------|------------|-----------------------------------|---|-------|----|----|----|----|----|
| 2012/03/27 | 10:30:00.0 | AOCS_Ore-point_Start_1_OG [0x097] | | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 01 | 00 | 00 | 00 | 00 |
| 2012/03/27 | 14:50:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/27 | 14:50:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/03/27 | 14:50:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/03/27 | 14:53:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/03/27 | 15:02:30.0 | XRT_Custom_418_OG [0x1a2] | | | | | | | |
| 2012/03/27 | 15:03:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/03/27 | 16:25:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/27 | 16:25:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/03/27 | 16:25:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/03/27 | 16:28:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/03/27 | 16:49:00.0 | XRT_Custom_418_OG [0x1a2] | | | | | | | |
| 2012/03/27 | 16:50:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/03/27 | 17:38:54.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/27 | 17:38:56.0 | XRT_FOCUS_POSITION_401_OG [0x191] | | | | | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 | ff | aa | 00 | |
| 2012/03/27 | 17:39:00.0 | AOCS_Ore-point_Start_2_OG [0x098] | | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 00 | 00 | 00 | 00 | 00 |
| 2012/03/27 | 17:39:16.0 | XRT_FLD_DIS_402_OG [0x192] | | | | | | | |
| | | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 | | | | |
| 2012/03/27 | 17:39:18.0 | XRT_FLRCTRL_DIS_433_OG [0x1b1] | | | | | | | |
| | | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 | | | | |
| 2012/03/27 | 17:39:20.0 | XRT_ARS_DIS_438_OG [0x1b6] | | | | | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2012/03/27 | 17:41:58.0 | XRT_QT_PROG_SET_444_OG [0x1bc] | | | | | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 | 01 | | | |
| 2012/03/27 | 17:42:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/03/27 | 17:48:54.0 | XRT_CTRL_MANU_439_OG [0x1b7] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/27 | 17:49:00.0 | AOCS_Ore-point_Start_1_OG [0x097] | | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 01 | 00 | 00 | 00 | 00 |
| 2012/03/27 | 17:51:26.0 | XRT_FOCUS_POSITION_409_OG [0x199] | | | | | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 | fe | 97 | 00 | |
| 2012/03/27 | 17:51:46.0 | XRT_FLD_ENA_411_OG [0x19b] | | | | | | | |
| | | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | | | |
| 2012/03/27 | 17:51:48.0 | XRT_FLRCTRL_ENA_413_OG [0x19d] | | | | | | | |
| | | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | | | |
| 2012/03/27 | 17:51:50.0 | XRT_AEC_RESET_443_OG [0x1bb] | | | | | | | |
| | | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | | | |
| 2012/03/27 | 17:51:52.0 | XRT_ARS_DIS_431_OG [0x1af] | | | | | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2012/03/27 | 17:51:54.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/03/27 | 17:51:56.0 | XRT_QT_PROG_SET_405_OG [0x195] | | | | | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 | 0d | | | |
| 2012/03/27 | 17:51:58.0 | XRT_FL_PROG_SET_414_OG [0x19e] | | | | | | | |
| | | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 | 03 | | | |
| 2012/03/27 | 17:52:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/03/27 | 18:02:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/27 | 18:02:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/03/27 | 18:02:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/03/27 | 18:05:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/03/27 | 18:26:00.0 | XRT_Custom_418_OG [0x1a2] | | | | | | | |
| 2012/03/27 | 18:27:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/03/27 | 19:39:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/27 | 19:39:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/03/27 | 19:39:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/03/27 | 19:42:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/03/27 | 20:03:00.0 | XRT_Custom_418_OG [0x1a2] | | | | | | | |
| 2012/03/27 | 20:04:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/03/27 | 21:17:00.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/27 | 21:17:02.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/03/27 | 21:17:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/03/27 | 21:20:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | | | |

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| | | | | | | | |
|------------|------------|-----------------------------------|---------------------|---|-------|----------------|--|
| 2012/03/27 | 21:40:00.0 | XRT_Custom_418_OG [0x1a2] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | |
| 2012/03/27 | 21:41:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | |
| 2012/03/27 | 22:54:30.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2012/03/27 | 22:54:32.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2012/03/27 | 22:54:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |
| 2012/03/27 | 22:57:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | |
| 2012/03/27 | 23:14:30.0 | XRT_Custom_418_OG [0x1a2] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | |
| 2012/03/27 | 23:15:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | |
| 2012/03/28 | 00:31:30.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2012/03/28 | 00:31:32.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2012/03/28 | 00:31:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |
| 2012/03/28 | 00:34:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | |
| 2012/03/28 | 00:39:00.0 | XRT_Custom_418_OG [0x1a2] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | |
| 2012/03/28 | 00:40:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | |
| 2012/03/28 | 01:54:30.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2012/03/28 | 01:54:32.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2012/03/28 | 01:54:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |
| 2012/03/28 | 01:54:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | |
| 2012/03/28 | 01:57:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | |
| 2012/03/28 | 02:14:30.0 | XRT_Custom_418_OG [0x1a2] | | | | | |
| 2012/03/28 | 02:15:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | |
| 2012/03/28 | 03:28:00.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2012/03/28 | 03:28:02.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2012/03/28 | 03:28:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |
| 2012/03/28 | 03:31:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | |
| 2012/03/28 | 03:51:30.0 | XRT_Custom_418_OG [0x1a2] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | |
| 2012/03/28 | 03:52:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | |
| 2012/03/28 | 05:01:30.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2012/03/28 | 05:01:32.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2012/03/28 | 05:01:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |
| 2012/03/28 | 05:01:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | |
| 2012/03/28 | 05:04:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | |
| 2012/03/28 | 05:29:00.0 | XRT_Custom_418_OG [0x1a2] | | | | | |
| 2012/03/28 | 05:30:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | |
| 2012/03/28 | 05:57:54.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2012/03/28 | 05:57:56.0 | XRT_FOCUS_POSITION_401_OG [0x191] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2012/03/28 | 05:58:00.0 | AOCS_Ore-point_Start_2_OG [0x098] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 ff aa 00 | |
| 2012/03/28 | 05:58:16.0 | XRT_FLD_DIS_402_OG [0x192] | AOCU_NM | 5 | 02-76 | 00 00 00 00 00 | |
| 2012/03/28 | 05:58:18.0 | XRT_FLRCTRL_DIS_433_OG [0x1b1] | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 | |
| 2012/03/28 | 05:58:20.0 | XRT_ARS_DIS_438_OG [0x1b6] | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 | |
| 2012/03/28 | 06:00:58.0 | XRT_QT_PROG_SET_444_OG [0x1bc] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | |
| 2012/03/28 | 06:01:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 01 | |
| 2012/03/28 | 06:07:54.0 | XRT_CTRL_MANU_439_OG [0x1b7] | | | | | |
| 2012/03/28 | 06:08:00.0 | AOCS_Ore-point_Start_1_OG [0x097] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2012/03/28 | 06:10:26.0 | XRT_FOCUS_POSITION_409_OG [0x199] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2012/03/28 | 06:10:46.0 | XRT_FLD_ENA_411_OG [0x19b] | AOCU_NM | 5 | 02-76 | 01 00 00 00 00 | |
| 2012/03/28 | 06:10:48.0 | XRT_FLRCTRL_ENA_413_OG [0x19d] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 | |
| 2012/03/28 | 06:10:50.0 | XRT_AEC_RESET_443_OG [0x1bb] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | |
| 2012/03/28 | 06:10:52.0 | XRT_ARS_DIS_431_OG [0x1af] | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | |
| 2012/03/28 | 06:10:54.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | |
| 2012/03/28 | 06:10:56.0 | XRT_QT_PROG_SET_405_OG [0x195] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | |
| 2012/03/28 | 06:10:56.0 | XRT_QT_PROG_SET_405_OG [0x195] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |
| 2012/03/28 | 06:10:56.0 | XRT_QT_PROG_SET_405_OG [0x195] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 0d | |

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| | | | | | | | | | |
|------------|------------|-----------------------------------|---|-------|----|----|----|----|----|
| 2012/03/28 | 06:10:58.0 | XRT_FL_PROG_SET_414_OG [0x19e] | | | | | | | |
| | | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 | 03 | | | |
| 2012/03/28 | 06:11:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/03/28 | 06:42:00.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/28 | 06:42:02.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/03/28 | 06:42:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/03/28 | 06:45:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/03/28 | 07:06:30.5 | XRT_Custom_418_OG [0x1a2] | | | | | | | |
| 2012/03/28 | 07:07:30.5 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/03/28 | 08:22:00.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/28 | 08:22:02.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/03/28 | 08:22:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/03/28 | 08:25:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/03/28 | 08:43:30.0 | XRT_Custom_418_OG [0x1a2] | | | | | | | |
| 2012/03/28 | 08:44:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/03/28 | 10:02:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/28 | 10:02:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/03/28 | 10:02:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/03/28 | 10:05:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/03/28 | 10:15:30.0 | XRT_Custom_418_OG [0x1a2] | | | | | | | |
| 2012/03/28 | 10:16:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/03/28 | 15:26:00.5 | XRT_CTRL_MANU_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/28 | 15:26:02.5 | XRT_FLD_RESET_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/03/28 | 15:26:04.5 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/03/28 | 15:29:14.5 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/03/28 | 15:49:30.0 | XRT_Custom_418_OG [0x1a2] | | | | | | | |
| 2012/03/28 | 15:50:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/03/28 | 17:02:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/28 | 17:02:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/03/28 | 17:02:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/03/28 | 17:05:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/03/28 | 17:26:00.0 | XRT_Custom_418_OG [0x1a2] | | | | | | | |
| 2012/03/28 | 17:27:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/03/28 | 17:59:54.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/28 | 17:59:56.0 | XRT_FOCUS_POSITION_401_OG [0x191] | | | | | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 | ff | aa | 00 | |
| 2012/03/28 | 18:00:00.0 | AOCS_Ore-point_Start_2_OG [0x098] | | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 00 | 00 | 00 | 00 | 00 |
| 2012/03/28 | 18:00:16.0 | XRT_FLD_DIS_402_OG [0x192] | | | | | | | |
| | | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 | | | | |
| 2012/03/28 | 18:00:18.0 | XRT_FLRCTRL_DIS_433_OG [0x1b1] | | | | | | | |
| | | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 | | | | |
| 2012/03/28 | 18:00:20.0 | XRT_ARS_DIS_438_OG [0x1b6] | | | | | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2012/03/28 | 18:02:58.0 | XRT_QT_PROG_SET_444_OG [0x1bc] | | | | | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 | 01 | | | |
| 2012/03/28 | 18:03:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/03/28 | 18:09:54.0 | XRT_CTRL_MANU_439_OG [0x1b7] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/03/28 | 18:10:00.0 | AOCS_Ore-point_Start_1_OG [0x097] | | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 01 | 00 | 00 | 00 | 00 |
| 2012/03/28 | 18:12:26.0 | XRT_FOCUS_POSITION_409_OG [0x199] | | | | | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 | fe | 97 | 00 | |
| 2012/03/28 | 18:12:46.0 | XRT_FLD_ENA_411_OG [0x19b] | | | | | | | |
| | | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | | | |
| 2012/03/28 | 18:12:48.0 | XRT_FLRCTRL_ENA_413_OG [0x19d] | | | | | | | |
| | | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | | | |
| 2012/03/28 | 18:12:50.0 | XRT_AEC_RESET_443_OG [0x1bb] | | | | | | | |
| | | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | | | |
| 2012/03/28 | 18:12:52.0 | XRT_ARS_DIS_431_OG [0x1af] | | | | | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2012/03/28 | 18:12:54.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/03/28 | 18:12:56.0 | XRT_QT_PROG_SET_416_OG [0x1a0] | | | | | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 | 08 | | | |

| | | | | | |
|------------|------------|--------------------------------|---|-------|-------|
| 2012/03/28 | 18:12:58.0 | XRT_FL_PROG_SET_414_OG [0x19e] | | | |
| | | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 03 |
| 2012/03/28 | 18:13:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/03/28 | 18:39:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/03/28 | 18:39:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/03/28 | 18:39:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/03/28 | 18:42:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/03/28 | 19:03:00.0 | XRT_Custom_418_OG [0x1a2] | | | |
| 2012/03/28 | 19:04:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/03/28 | 20:16:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/03/28 | 20:16:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/03/28 | 20:16:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/03/28 | 20:19:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/03/28 | 20:40:00.0 | XRT_Custom_418_OG [0x1a2] | | | |
| 2012/03/28 | 20:41:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/03/28 | 21:54:00.0 | XRT_CTRL_MANU_408_OG [0x198] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/03/28 | 21:54:02.0 | XRT_FLD_RESET_412_OG [0x19c] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/03/28 | 21:54:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/03/28 | 21:57:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/03/28 | 22:16:30.0 | XRT_Custom_418_OG [0x1a2] | | | |
| 2012/03/28 | 22:17:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/03/28 | 23:31:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/03/28 | 23:31:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/03/28 | 23:31:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/03/28 | 23:34:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/03/28 | 23:43:30.0 | XRT_Custom_418_OG [0x1a2] | | | |
| 2012/03/28 | 23:44:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/03/29 | 00:59:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/03/29 | 00:59:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/03/29 | 00:59:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/03/29 | 01:02:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/03/29 | 01:15:00.0 | XRT_Custom_418_OG [0x1a2] | | | |
| 2012/03/29 | 01:16:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/03/29 | 02:30:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/03/29 | 02:30:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/03/29 | 02:30:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/03/29 | 02:33:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/03/29 | 02:51:30.0 | XRT_Custom_418_OG [0x1a2] | | | |
| 2012/03/29 | 02:52:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/03/29 | 03:59:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/03/29 | 03:59:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/03/29 | 03:59:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/03/29 | 04:02:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/03/29 | 04:28:30.0 | XRT_Custom_418_OG [0x1a2] | | | |
| 2012/03/29 | 04:29:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/03/29 | 05:39:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/03/29 | 05:39:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/03/29 | 05:39:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/03/29 | 05:42:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/03/29 | 06:06:30.0 | XRT_Custom_418_OG [0x1a2] | | | |
| 2012/03/29 | 06:07:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/03/29 | 06:15:54.0 | XRT_CTRL_MANU_400_OG [0x190] | | | |

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| 2012/03/29 | 06:15:56.0 | XRT_FOCUS_POSITION_401_OG [0x191] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| | | XRT_FOCUS_POSITION | | 4 | 07-F8 | 22 ff aa 00 |
| 2012/03/29 | 06:16:00.0 | AOCS_ORe-point_Start_2_OG [0x098] | AOCU_NM | 5 | 02-76 | 00 00 00 00 00 |
| 2012/03/29 | 06:16:16.0 | XRT_FLD_DIS_402_OG [0x192] | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 |
| 2012/03/29 | 06:16:18.0 | XRT_FLRCTRL_DIS_433_OG [0x1b1] | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 |
| 2012/03/29 | 06:16:20.0 | XRT_ARS_DIS_438_OG [0x1b6] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 |
| 2012/03/29 | 06:18:58.0 | XRT_QT_PROG_SET_444_OG [0x1bc] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 01 |
| 2012/03/29 | 06:19:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/03/29 | 06:25:54.0 | XRT_CTRL_MANU_439_OG [0x1b7] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/03/29 | 06:26:00.0 | AOCS_ORe-point_Start_1_OG [0x097] | AOCU_NM | 5 | 02-76 | 01 00 00 00 00 |
| 2012/03/29 | 06:28:26.0 | XRT_FOCUS_POSITION_409_OG [0x199] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 |
| 2012/03/29 | 06:28:46.0 | XRT_FLD_ENA_411_OG [0x19b] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 |
| 2012/03/29 | 06:28:48.0 | XRT_FLRCTRL_ENA_413_OG [0x19d] | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 |
| 2012/03/29 | 06:28:50.0 | XRT_AEC_RESET_443_OG [0x1bb] | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 |
| 2012/03/29 | 06:28:52.0 | XRT_ARS_DIS_431_OG [0x1af] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 |
| 2012/03/29 | 06:28:54.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/03/29 | 06:28:56.0 | XRT_QT_PROG_SET_416_OG [0x1a0] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 08 |
| 2012/03/29 | 06:28:58.0 | XRT_FL_PROG_SET_414_OG [0x19e] | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 03 |
| 2012/03/29 | 06:29:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/03/29 | 07:20:00.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/03/29 | 07:20:02.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/03/29 | 07:20:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/03/29 | 07:23:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/03/29 | 07:43:30.0 | XRT_Custom_418_OG [0x1a2] | | 1 | 07-F0 | c0 |
| 2012/03/29 | 07:44:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/03/29 | 09:00:00.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/03/29 | 09:00:02.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/03/29 | 09:00:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/03/29 | 09:03:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/03/29 | 10:40:00.0 | AOCS_ORe-point_Start_2_OG [0x098] | AOCU_NM | 5 | 02-76 | 00 00 00 00 00 |