

XRT Timeline to be uploaded on 2012/04/07

Period: 2012/04/07 09:34:00 - 2012/04/12 09:25:00

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Normal mode

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XOB #18E5: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 90s-cad												
Term	Pointing (x, y)				Comment							
04/07 10:00:00 - 04/07 17:57:24	Track (414.4, 366.1) ^{Ⓜ 04/07 09:44:00}				# OP start + 10min, Continue HOP 208 on AR 11451.							
04/07 18:10:30 - 04/08 05:59:54	Track (478.8, 361.9) ^{Ⓜ 04/07 18:07:30}				# End of HOP 208.							
PROG= 16 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 29 2-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Be close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ C-poly/Open C-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ med-Be/Open med-Be/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ med-Al/Open med-Al/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 91 40-time(s) 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #187C: Synoptic Q95 2x2 - Al/mesh(45/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2897) + Thin-Be(36)												
Term	Pointing (x, y)				Comment							
04/07 18:00:30 - 04/07 18:07:24	Fixed (0.0, 0.0)				synoptic, shifted -2.5 min							
04/08 06:03:00 - 04/08 06:09:54	Fixed (0.0, 0.0)				synoptic							
04/08 18:03:00 - 04/08 18:09:54	Fixed (0.0, 0.0)				synoptic							
04/09 05:34:00 - 04/09 05:40:54	Fixed (0.0, 0.0)				synoptic, shifted -29.0 min							
04/09 18:24:00 - 04/09 18:30:54	Fixed (0.0, 0.0)				synoptic, shifted 21.0 min							
04/10 06:10:00 - 04/10 06:16:54	Fixed (0.0, 0.0)				synoptic, shifted 7.0 min							
PROG= 01 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 44 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 26 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 35 1-time(s) 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 4 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #18C1: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 60s cad												
Term	Pointing (x, y)				Comment							
04/08 06:13:00 - 04/08 17:59:54	Track (565.6, 354.9) ^{Ⓜ 04/08 06:10:00}				# Observe AR 11451.							
04/08 18:13:00 - 04/09 05:30:54	Track (644.5, 347.0) ^{Ⓜ 04/08 18:10:00}				# AR cont.							
04/09 05:44:00 - 04/09 11:59:54	Track (712.2, 338.6) ^{Ⓜ 04/09 05:41:00}				# AR cont.							
04/09 18:34:00 - 04/10 06:06:54	Track (777.4, 328.5) ^{Ⓜ 04/09 18:31:00}				# AR cont.							
04/10 06:24:00 - 04/10 09:57:30	Track (826.8, 318.7) ^{Ⓜ 04/10 06:17:00}				# AR cont.							
PROG= 15 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 96 4-time(s) 2.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												

Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 62 15-time(s) 2.0sec												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384												
Term			Pointing (x, y)					Comment				
04/09 12:03:00 - 04/09 17:58:00			Fixed (-13.0, -938.0)					# HOP 206 - Polar Panorama Map				
PROG= 20 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 51 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 2 30-time(s) 2.0sec												
Seqn= 58 2-time(s) 30.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops												
Term			Pointing (x, y)					Comment				
04/07 10:00:00 - 04/07 17:57:24			Track (414.4, 366.1) @ 04/07 09:44:00					# OP start + 10min, Continue HOP 208 on AR 11451.				
04/07 18:10:30 - 04/08 05:59:54			Track (478.8, 361.9) @ 04/07 18:07:30					# End of HOP 208.				
04/08 06:13:00 - 04/08 17:59:54			Track (565.6, 354.9) @ 04/08 06:10:00					# Observe AR 11451.				
04/08 18:13:00 - 04/09 05:30:54			Track (644.5, 347.0) @ 04/08 18:10:00					# AR cont.				
04/09 05:44:00 - 04/09 11:59:54			Track (712.2, 338.6) @ 04/09 05:41:00					# AR cont.				
04/09 12:03:00 - 04/09 17:58:00			Fixed (-13.0, -938.0)					# HOP 206 - Polar Panorama Map				
04/09 18:34:00 - 04/10 06:06:54			Track (777.4, 328.5) @ 04/09 18:31:00					# AR cont.				
04/10 06:24:00 - 04/10 09:57:30			Track (826.8, 318.7) @ 04/10 06:17:00					# AR cont.				
PROG= 03 12-time(s)												
Subr= 1 45-time(s) 10.0sec												
Seqn= 20 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 63 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 77 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

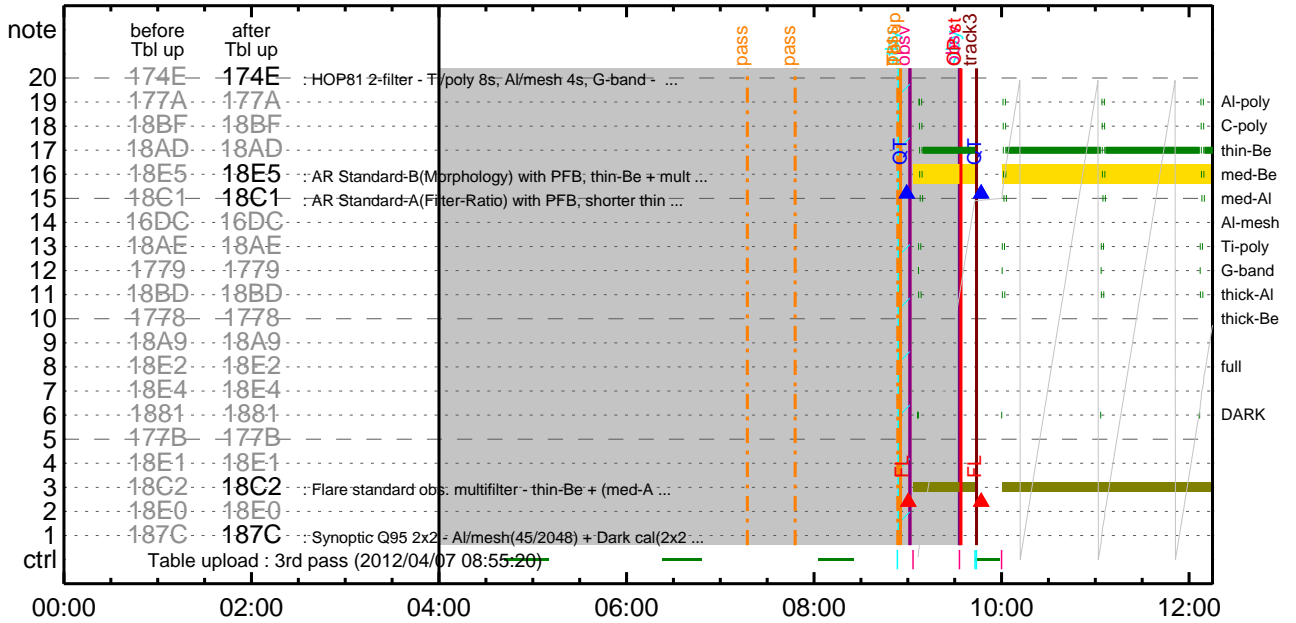
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Flare Detection

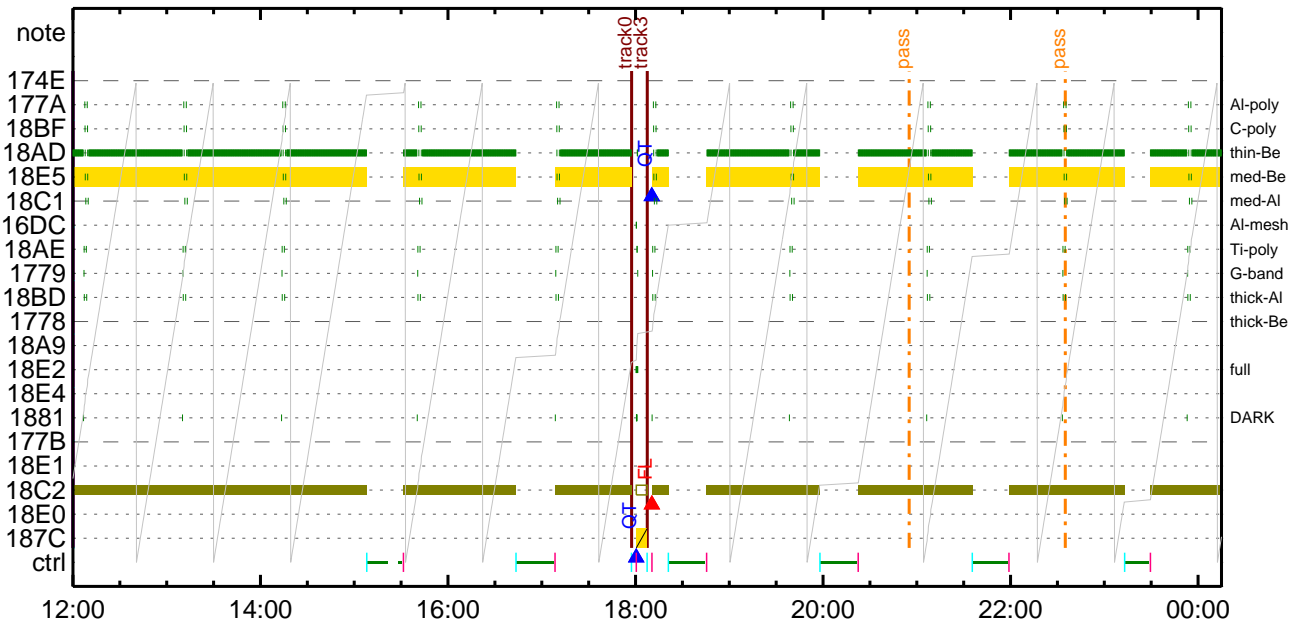
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FLD Patrol												
Term			Pointing (x, y)					Comment				
04/07 18:10:16 - 04/08 06:00:16			Track (478.8, 361.9) @ 04/07 18:07:30					# End of HOP 208.				
04/08 06:12:46 - 04/08 18:00:16			Track (565.6, 354.9) @ 04/08 06:10:00					# Observe AR 11451.				
04/08 18:12:46 - 04/09 05:31:16			Track (644.5, 347.0) @ 04/08 18:10:00					# AR cont.				
04/09 05:43:46 - 04/09 18:21:16			Track (712.2, 338.6) @ 04/09 05:41:00					# AR cont.				
04/09 18:33:46 - 04/10 06:07:16			Track (777.4, 328.5) @ 04/09 18:31:00					# AR cont.				
04/10 06:23:46 - 04/12 09:25:00			Track (826.8, 318.7) @ 04/10 06:17:00					# AR cont.				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

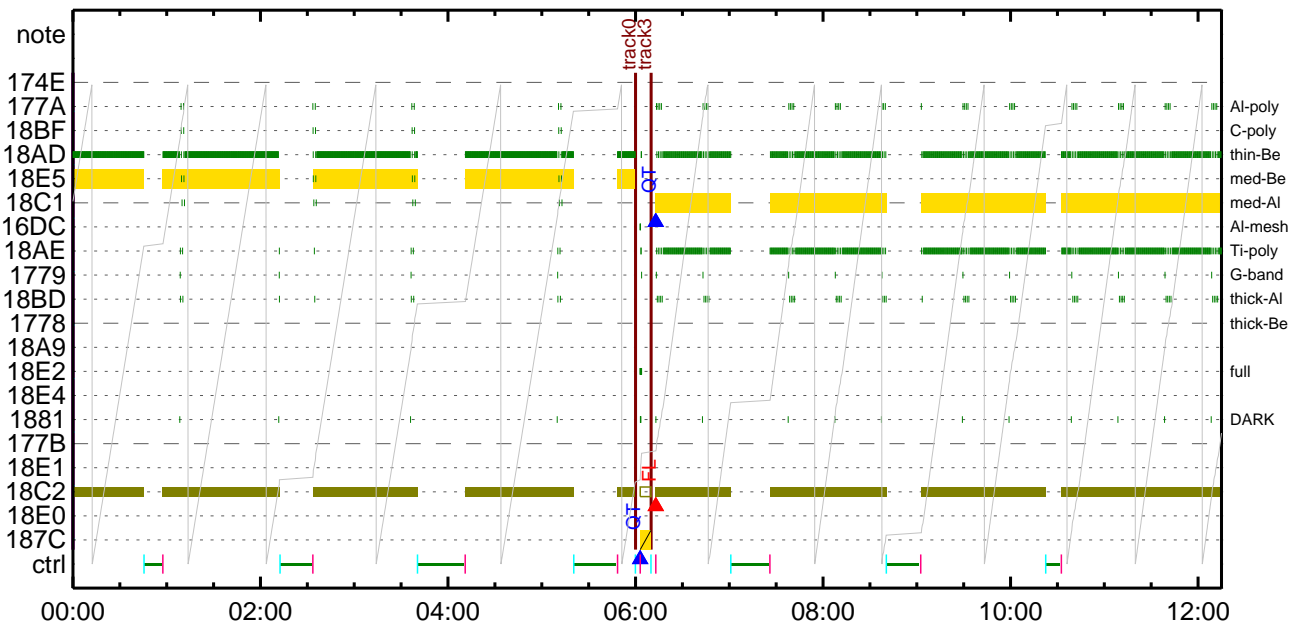
CMDI #0560 2012/04/07



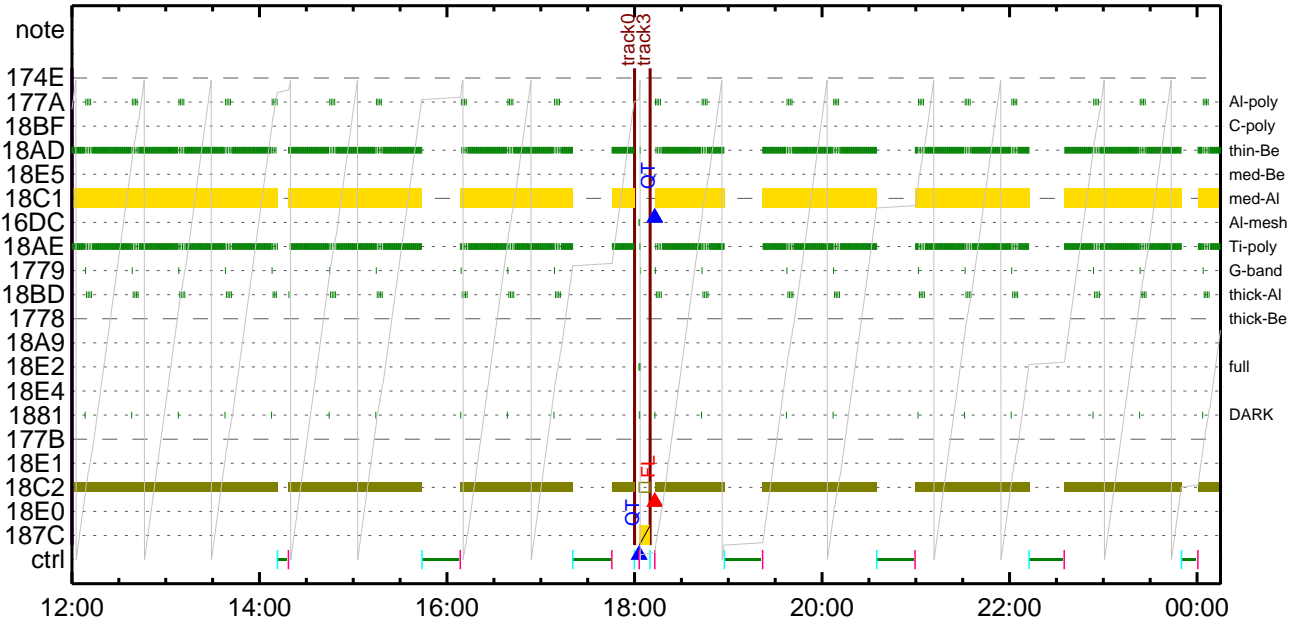
CMDI #0560 2012/04/07



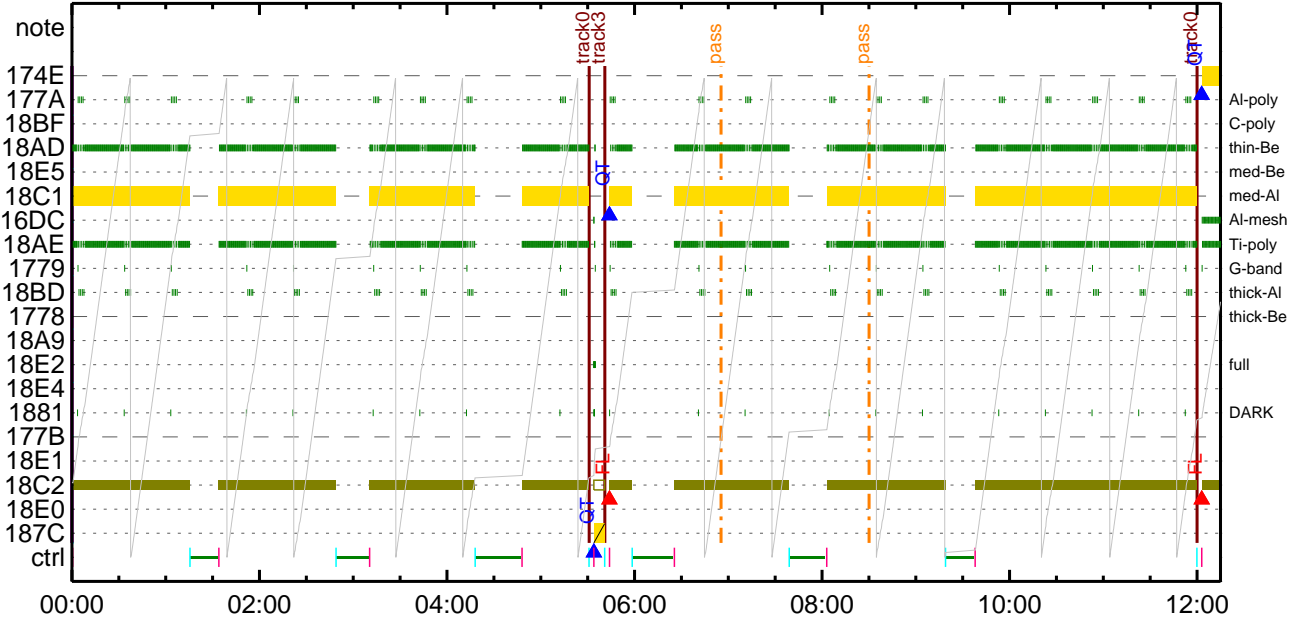
CMDI #0560 2012/04/08



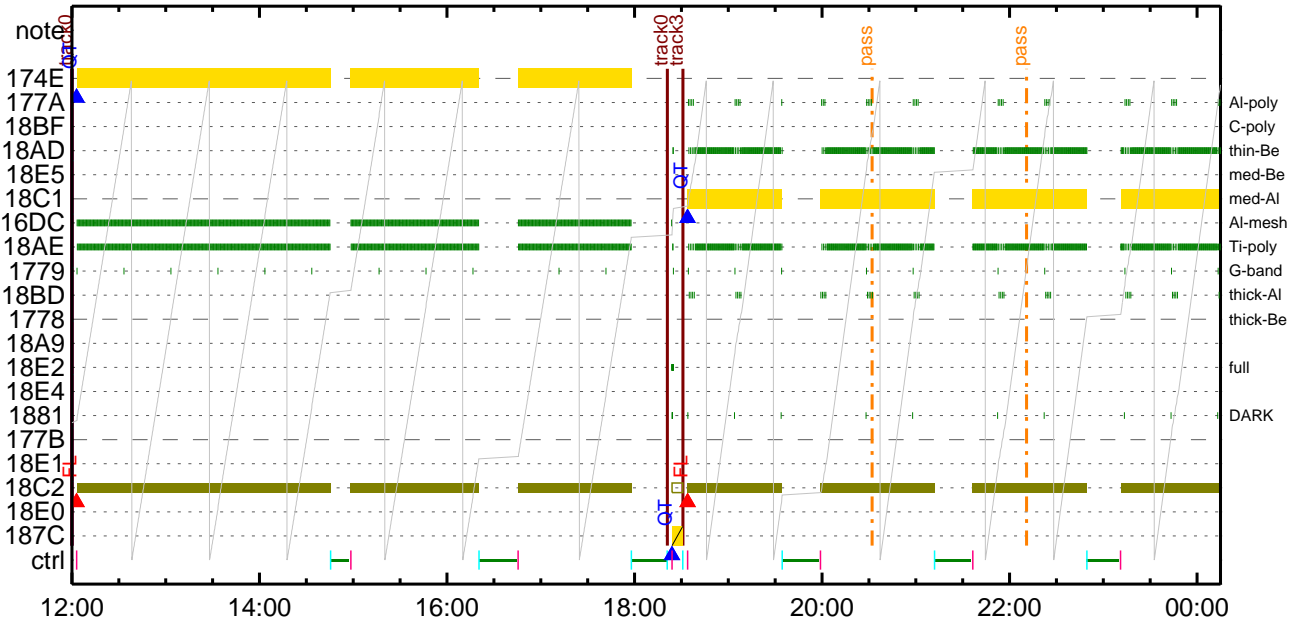
CMDI #0560 2012/04/08



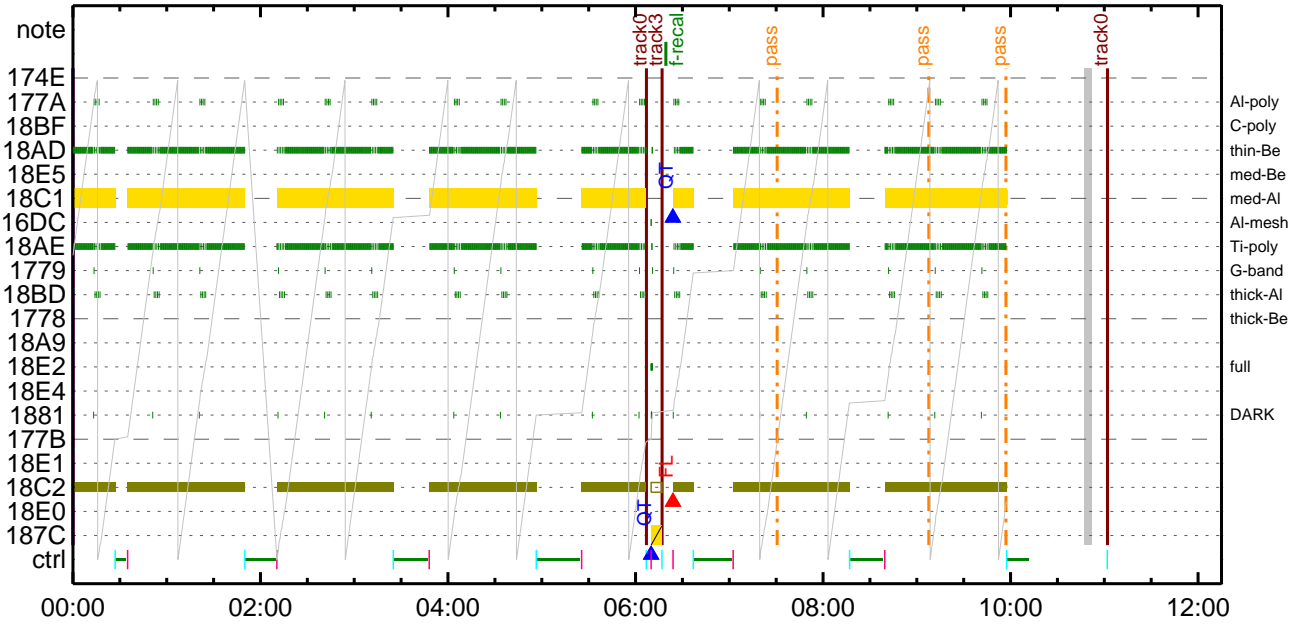
CMDI #0560 2012/04/09



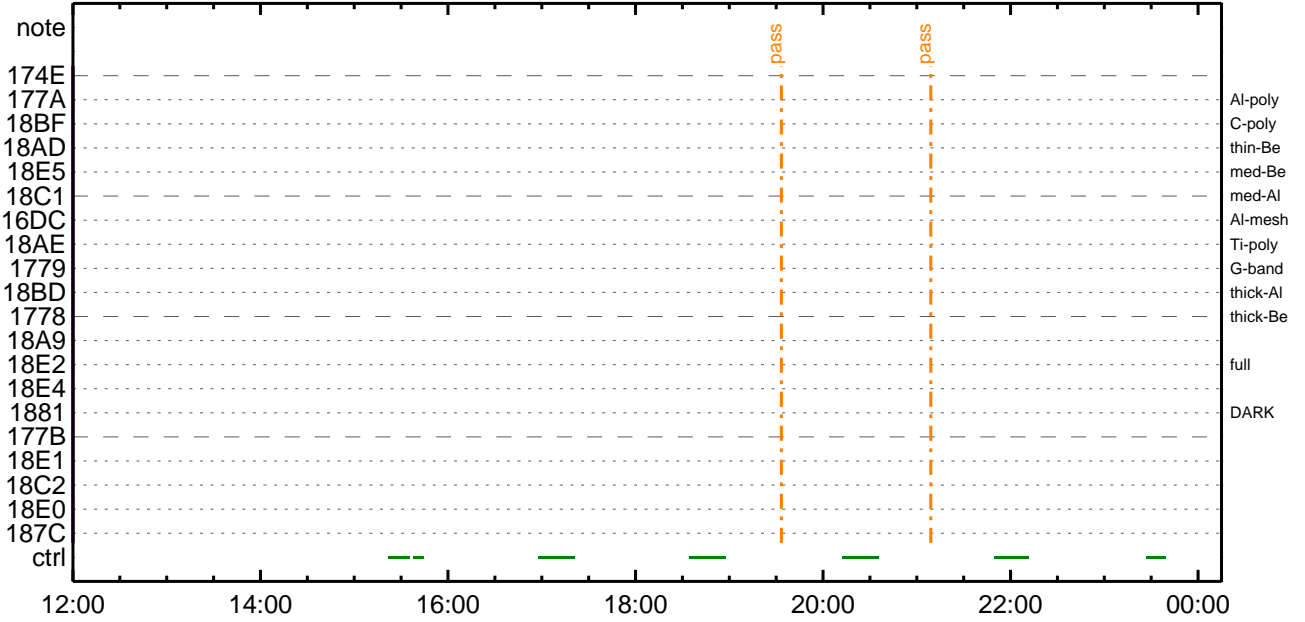
CMDI #0560 2012/04/09



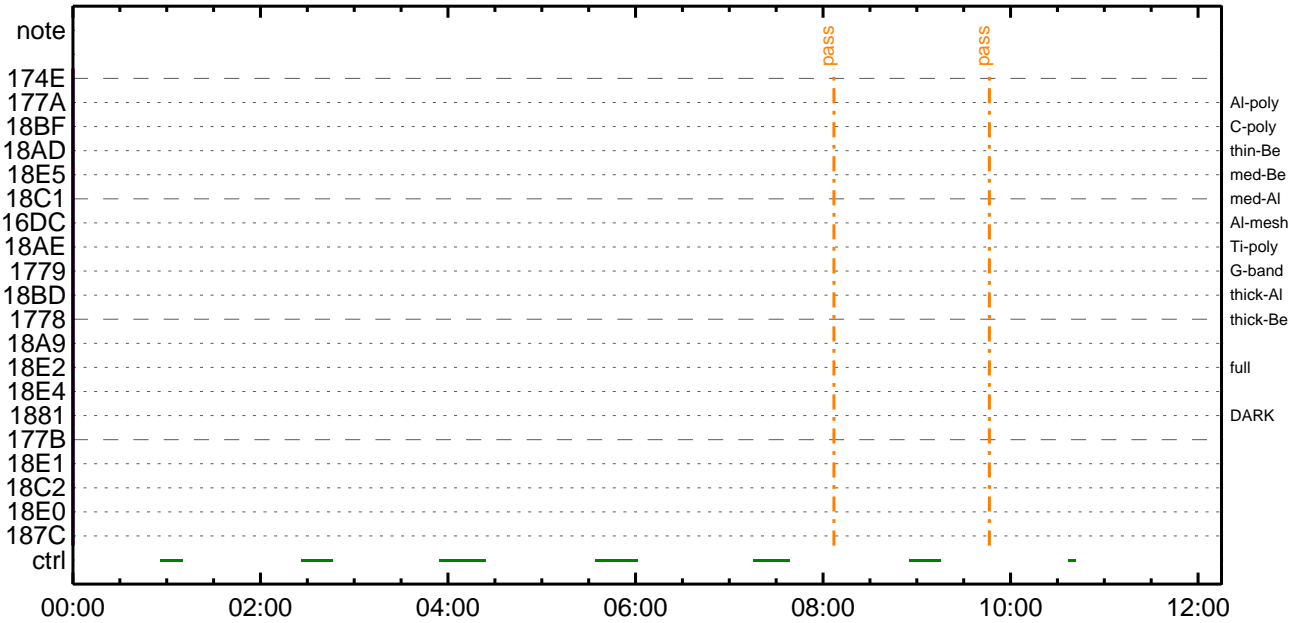
CMDI #0560 2012/04/10



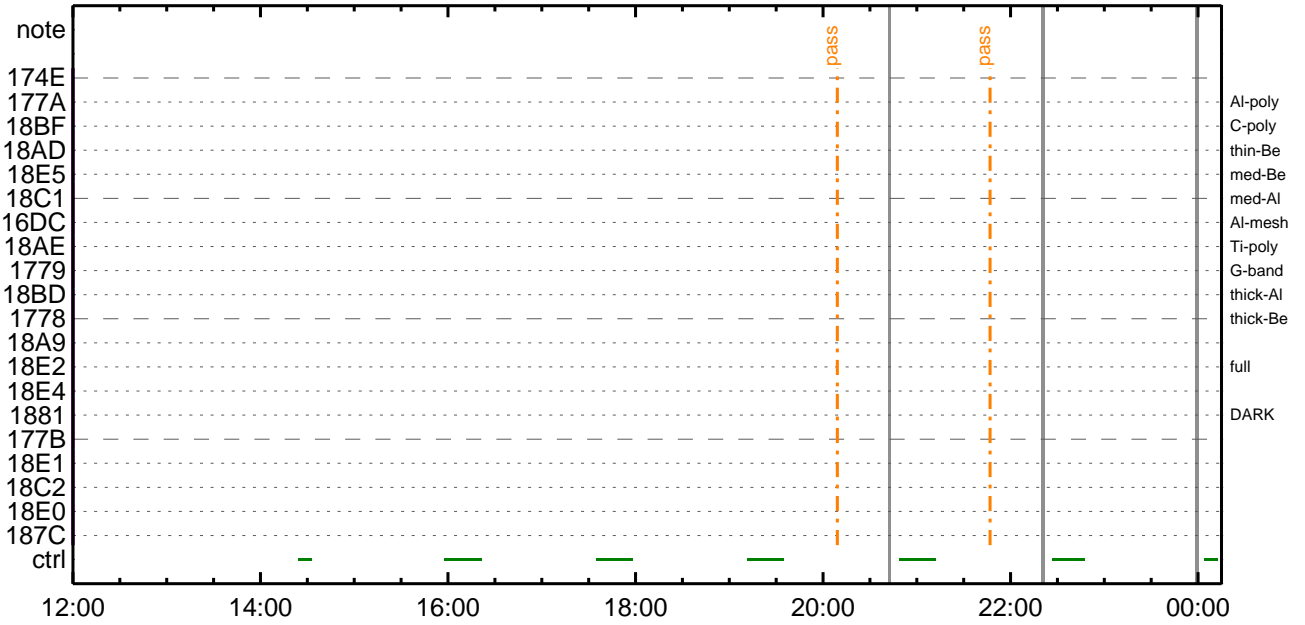
CMDI #0560 2012/04/10



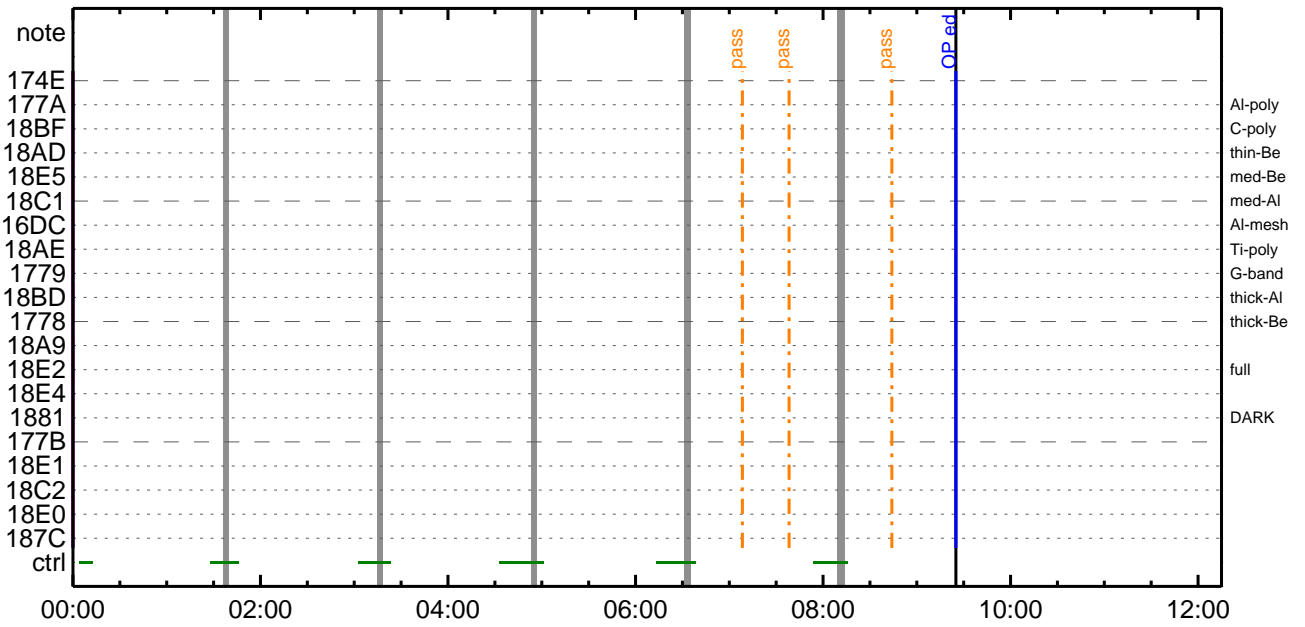
CMDI #0560 2012/04/11



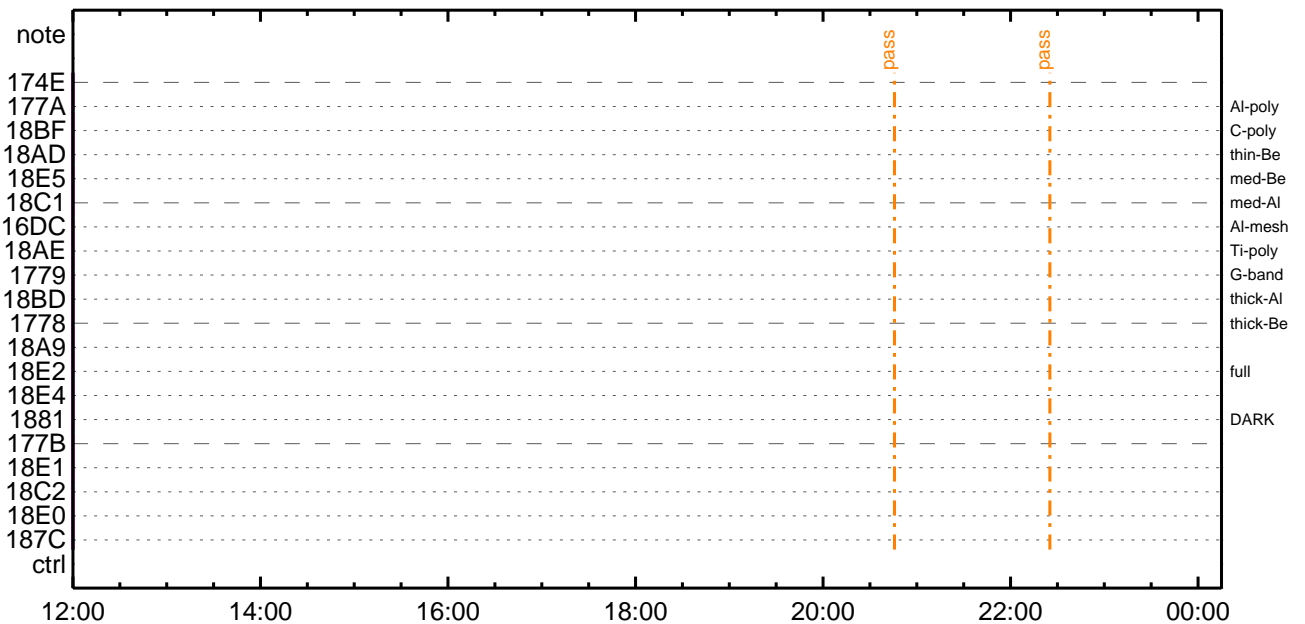
CMDI #0560 2012/04/11



CMDI #0560 2012/04/12



CMDI #0560 2012/04/12




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-798:OP
0104 ( )
0105 S. OG og-798:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°îYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YOYE²òðÁDî¿(UT)
0183 +. TI 2012-04-07 09:29:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-04-07 09:29:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-04-07 09:29:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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*** OP Sequence for XRT ***

2012/04/07	09:43:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/07	09:43:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/07	09:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/07	09:43:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/07	09:44:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/04/07	09:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/07	09:46:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/04/07	09:46:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/04/07	09:46:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/04/07	09:46:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/04/07	09:46:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/04/07	09:46:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/07	09:46:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2012/04/07	09:46:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/04/07	09:59:00.0	XRT_Custom_418_OG [0x1a2]							
2012/04/07	10:00:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/07	15:08:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/07	15:08:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/07	15:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/07	15:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/07	15:30:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/07	15:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/07	16:43:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/07	16:43:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/07	16:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/07	16:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/07	17:07:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/07	17:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/07	17:57:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/07	17:57:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/04/07	17:57:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/04/07	17:57:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/04/07	17:57:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/04/07	17:57:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/04/07	18:00:28.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2012/04/07	18:00:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/07	18:07:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/07	18:07:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/04/07	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/04/07	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/04/07	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/04/07	18:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/04/07	18:10:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/04/07	18:10:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/07	18:10:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2012/04/07	18:10:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/04/07	18:10:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2012/04/07	18:21:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/04/07	18:21:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/04/07	18:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/04/07	18:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/04/07	18:44:30.0	XRT_Custom_418_OG [0x1a2]			
2012/04/07	18:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/04/07	19:58:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/04/07	19:58:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/04/07	19:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/04/07	20:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/04/07	20:21:30.0	XRT_Custom_418_OG [0x1a2]			
2012/04/07	20:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/04/07	21:35:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/04/07	21:35:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/04/07	21:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/04/07	21:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/04/07	21:58:00.0	XRT_Custom_418_OG [0x1a2]			
2012/04/07	21:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/04/07	23:13:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/04/07	23:13:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/04/07	23:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/04/07	23:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/04/07	23:28:30.0	XRT_Custom_418_OG [0x1a2]			
2012/04/07	23:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/04/08	00:45:30.5	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/04/08	00:45:32.5	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/04/08	00:45:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/04/08	00:48:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/04/08	00:56:30.0	XRT_Custom_418_OG [0x1a2]			
2012/04/08	00:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/04/08	02:12:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/04/08	02:12:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/04/08	02:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/04/08	02:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/04/08	02:32:30.5	XRT_Custom_418_OG [0x1a2]			
2012/04/08	02:33:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/04/08	03:40:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/04/08	03:40:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/04/08	03:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/04/08	03:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/04/08	04:10:00.0	XRT_Custom_418_OG [0x1a2]			
2012/04/08	04:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/04/08	05:20:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/04/08	05:20:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/04/08	05:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/04/08	05:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/04/08	05:47:30.0	XRT_Custom_418_OG [0x1a2]			
2012/04/08	05:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/04/08	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/04/08	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/04/08	06:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]			

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2012/04/08	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2012/04/08	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2012/04/08	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2012/04/08	06:02:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]		2	07-F0	c4	01			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	01			
2012/04/08	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/04/08	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/04/08	06:10:00.0	AOCs_OrE-point_Start_1_OG [0x097]		5	02-76	03	00	00	00	00
		AOCU_NM		5	02-76	03	00	00	00	00
2012/04/08	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2012/04/08	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2012/04/08	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2012/04/08	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2012/04/08	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2012/04/08	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/04/08	06:12:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]		2	07-F0	c4	0f			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0f			
2012/04/08	06:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]		2	07-F0	c5	03			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	03			
2012/04/08	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/04/08	07:01:00.5	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/04/08	07:01:02.5	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/04/08	07:01:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/04/08	07:04:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/04/08	07:25:00.0	XRT_Custom_418_OG [0x1a2]								
2012/04/08	07:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/04/08	08:40:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/04/08	08:40:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/04/08	08:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/04/08	08:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/04/08	09:01:30.0	XRT_Custom_418_OG [0x1a2]								
2012/04/08	09:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/04/08	10:22:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/04/08	10:22:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/04/08	10:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/04/08	10:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/04/08	10:31:30.0	XRT_Custom_418_OG [0x1a2]								
2012/04/08	10:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/04/08	14:11:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/04/08	14:11:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/04/08	14:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/04/08	14:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/04/08	14:17:30.0	XRT_Custom_418_OG [0x1a2]								
2012/04/08	14:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/04/08	15:44:00.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/04/08	15:44:02.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/04/08	15:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/04/08	15:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/04/08	16:07:30.0	XRT_Custom_418_OG [0x1a2]								
2012/04/08	16:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/04/08	17:20:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/04/08	17:20:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/04/08	17:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				

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2012/04/08	17:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/04/08	17:44:30.5	XRT_Custom_418_OG [0x1a2]						
2012/04/08	17:45:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/04/08	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/04/08	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/04/08	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2012/04/08	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/04/08	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/04/08	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/04/08	18:02:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2012/04/08	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/04/08	18:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/04/08	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]						
			AOCU_NM	5	02-76	03 00 00 00 00		
2012/04/08	18:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/04/08	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/04/08	18:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/04/08	18:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/04/08	18:12:52.0	XRT_ARS_DIS_431_OG [0x1af]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/04/08	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/04/08	18:12:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2012/04/08	18:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03		
2012/04/08	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/04/08	18:57:30.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/04/08	18:57:32.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/04/08	18:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/04/08	19:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/04/08	19:21:00.0	XRT_Custom_418_OG [0x1a2]						
2012/04/08	19:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/04/08	20:35:00.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/04/08	20:35:02.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/04/08	20:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/04/08	20:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/04/08	20:58:30.0	XRT_Custom_418_OG [0x1a2]						
2012/04/08	20:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/04/08	22:12:30.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/04/08	22:12:32.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/04/08	22:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/04/08	22:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/04/08	22:34:00.0	XRT_Custom_418_OG [0x1a2]						
2012/04/08	22:35:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/04/08	23:50:00.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/04/08	23:50:02.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/04/08	23:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/04/08	23:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/04/08	23:59:30.0	XRT_Custom_418_OG [0x1a2]						
2012/04/09	00:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/04/09	01:15:30.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/04/09	01:15:32.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/04/09	01:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						

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2012/04/09	01:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2012/04/09	01:33:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/09	01:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/09	02:49:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/09	02:49:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/09	02:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/09	02:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/09	03:09:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/09	03:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/09	04:18:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/09	04:18:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/09	04:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/09	04:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	XRT_Custom_418_OG [0x1a2]							
2012/04/09	04:47:00.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]							
2012/04/09	04:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/09	05:30:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/09	05:30:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/04/09	05:31:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2012/04/09	05:31:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/04/09	05:31:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/04/09	05:31:20.5	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/04/09	05:33:58.5	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2012/04/09	05:34:00.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/09	05:40:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/09	05:41:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00				
2012/04/09	05:43:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/04/09	05:43:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/04/09	05:43:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/04/09	05:43:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/04/09	05:43:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/04/09	05:43:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/09	05:43:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2012/04/09	05:43:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/04/09	05:44:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/09	05:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/09	05:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/09	05:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/09	06:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/09	06:24:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/09	06:25:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/09	07:39:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/09	07:39:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/09	07:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/09	07:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	XRT_Custom_418_OG [0x1a2]							
2012/04/09	08:02:00.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]							
2012/04/09	08:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/09	09:19:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/09	09:19:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/09	09:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								

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2012/04/09	09:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/04/09	09:37:00.0	XRT_Custom_418_OG [0x1a2]							
2012/04/09	09:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/04/09	11:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/04/09	12:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
			AOCU_NM	5	02-76	00 53 64 01 26			
2012/04/09	12:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/04/09	12:02:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/04/09	12:02:48.0	XRT_FLRCTRL_ENA_449_OG [0x1c1]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/04/09	12:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/04/09	12:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/04/09	12:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/04/09	12:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2012/04/09	12:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2012/04/09	12:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/04/09	14:45:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/04/09	14:45:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/04/09	14:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/04/09	14:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/04/09	14:57:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/09	14:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/04/09	16:20:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/04/09	16:20:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/04/09	16:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/04/09	16:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/04/09	16:44:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/09	16:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/04/09	17:58:00.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/04/09	17:58:02.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/04/09	17:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/04/09	18:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/04/09	18:20:54.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/04/09	18:20:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/04/09	18:21:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
			AOCU_NM	5	02-76	00 00 00 00 00			
2012/04/09	18:21:16.0	XRT_FLD_DIS_402_OG [0x192]							
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/04/09	18:21:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/04/09	18:21:20.5	XRT_ARS_DIS_438_OG [0x1b6]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/04/09	18:23:58.5	XRT_QT_PROG_SET_444_OG [0x1bc]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01			
2012/04/09	18:24:00.5	XRT_CTRL_AUTO_406_OG [0x196]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/04/09	18:30:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/04/09	18:31:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
			AOCU_NM	5	02-76	03 00 00 00 00			
2012/04/09	18:33:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/04/09	18:33:46.0	XRT_FLD_ENA_411_OG [0x19b]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/04/09	18:33:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/04/09	18:33:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/04/09	18:33:52.0	XRT_ARS_DIS_431_OG [0x1af]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/04/09	18:33:54.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/04/09	18:33:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f			
2012/04/09	18:33:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							

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2012/04/09	18:34:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/09	19:34:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/09	19:34:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/09	19:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/09	19:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/09	19:58:00.0	XRT_Custom_418_OG [0x1a2]					
2012/04/09	19:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/09	21:12:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/09	21:12:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/09	21:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/09	21:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/09	21:35:30.0	XRT_Custom_418_OG [0x1a2]					
2012/04/09	21:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/09	22:49:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/09	22:49:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/09	22:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/09	22:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/09	23:10:00.0	XRT_Custom_418_OG [0x1a2]					
2012/04/09	23:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/10	00:27:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/10	00:27:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/10	00:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/10	00:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/10	00:34:00.0	XRT_Custom_418_OG [0x1a2]					
2012/04/10	00:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/10	01:50:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/10	01:50:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/10	01:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/10	01:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/10	02:09:30.0	XRT_Custom_418_OG [0x1a2]					
2012/04/10	02:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/10	03:25:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/10	03:25:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/10	03:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/10	03:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/10	03:47:00.0	XRT_Custom_418_OG [0x1a2]					
2012/04/10	03:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/10	04:56:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/10	04:56:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/10	04:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/10	04:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/10	05:24:30.0	XRT_Custom_418_OG [0x1a2]					
2012/04/10	05:25:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/10	06:06:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/10	06:06:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/04/10	06:07:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00
2012/04/10	06:07:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/04/10	06:07:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/04/10	06:07:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/04/10	06:09:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]					

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2012/04/10	06:10:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/10	06:16:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/10	06:17:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03	00 00 00 00
2012/04/10	06:19:26.0	XRT_FOCUS_RECALIBRATE_428_OG [0x1ac]	XRT_FOCUS_RECAL	2	07-F8	78	00
2012/04/10	06:23:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2012/04/10	06:23:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/04/10	06:23:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/04/10	06:23:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/04/10	06:23:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/04/10	06:23:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/10	06:23:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2012/04/10	06:23:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2012/04/10	06:24:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/10	06:37:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/10	06:37:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/10	06:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/10	06:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/10	07:01:30.0	XRT_Custom_418_OG [0x1a2]					
2012/04/10	07:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/10	08:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/10	08:17:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/10	08:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/10	08:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/10	08:38:30.0	XRT_Custom_418_OG [0x1a2]					
2012/04/10	08:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/10	09:57:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/10	09:57:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/10	09:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/10	10:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/10	11:01:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/10	11:02:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00