

# XRT Timeline to be uploaded on 2012/04/28

Period: 2012/04/28 10:07:00 - 2012/05/03 09:24:00

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**Normal mode**

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<b>XOB #18CC: Synoptic 9 Filter- 2x2 Q98 even Shorter exp</b>													
Term		Pointing (x, y)					Comment						
04/28 10:24:00 - 04/28 10:46:54		Track ( -25.1, 0.1) @ 04/28 10:17:00					# OP start + 10min, EIS sensitivity monitoring, and other EIS activities. Also, SOT to do ToV						
<b>PROG= 14 2-time(s)</b>													
└─ Subr= 1 1-time(s) 180.0sec													
└─ Seqn= 94 1-time(s) 25.0sec													
└─ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 70 1-time(s) 25.0sec													
└─ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 74 1-time(s) 25.0sec													
└─ Al-poly/Open		Al-poly/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Al-poly/Open		Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 18 1-time(s) 25.0sec													
└─ C-poly/Open		C-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ C-poly/Open		C-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 24 1-time(s) 25.0sec													
└─ Al-poly/Ti-poly		Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 73 1-time(s) 4.0sec													
└─ thin-Be/Open		thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ thin-Be/Open		thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 180.0sec													
└─ Seqn= 33 1-time(s) 4.0sec													
└─ med-Al/Open		med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 71 1-time(s) 4.0sec													
└─ Open/thick-Be		Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 67 1-time(s) 4.0sec													
└─ Open/Al-mesh		Open/Al-mesh	close	Safe	Dark	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 4 1-time(s) 4.0sec													
└─ Open/G-band		Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

<b>XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly</b>													
Term		Pointing (x, y)					Comment						
04/28 10:50:00 - 04/28 17:52:54		Track ( -25.1, 0.1) @ 04/28 10:17:00					# OP start + 10min, EIS sensitivity monitoring, and other EIS activities. Also, SOT to do ToV						
<b>PROG= 16 Inf-time(s)</b>													
└─ Subr= 1 1-time(s) 120.0sec													
└─ Seqn= 57 1-time(s) 4.0sec													
└─ Open/Ti-poly		Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

<b>XOB #18A8: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be(18</b>													
Term		Pointing (x, y)					Comment						
04/28 17:56:00 - 04/28 18:02:54		Fixed ( 0.0, 0.0)					synoptic, shifted -7.0 min						
04/29 06:03:00 - 04/29 06:09:54		Fixed ( 0.0, 0.0)					synoptic						
04/29 18:03:00 - 04/29 18:09:54		Fixed ( 0.0, 0.0)					synoptic						
04/30 05:33:30 - 04/30 05:40:24		Fixed ( 0.0, 0.0)					synoptic, shifted -29.5 min						
04/30 18:23:30 - 04/30 18:30:24		Fixed ( 0.0, 0.0)					synoptic, shifted 20.5 min						
05/01 06:09:30 - 05/01 06:16:24		Fixed ( 0.0, 0.0)					synoptic, shifted 6.5 min						
<b>PROG= 10 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 7 1-time(s) 4.0sec													
└─ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
└─ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8 1-time(s) 4.0sec													
└─ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 3 1-time(s) 2.0sec													
└─ thin-Be/Open		thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ thin-Be/Open		thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec													
└─ Open/G-band		Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

**XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad**

Term	Pointing (x, y)	Comment
04/28 18:44:30 - 04/29 05:59:54	Track ( 786.4, -276.6) @ 04/28 18:03:00	# Cont.
04/29 06:13:00 - 04/29 10:09:54	Track ( 831.6, -283.9) @ 04/29 06:10:00	# Cont.
04/29 18:13:00 - 04/30 02:29:54	Track ( 865.5, -291.3) @ 04/29 18:10:00	# Cont.
04/30 05:43:30 - 04/30 09:59:54	Fixed ( 882.0, -296.0)	# Cont.
04/30 18:33:30 - 05/01 06:06:24	Fixed ( 882.0, -296.0)	# Cont.

**PROG= 13 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec										
Seqn= 19	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 96	4-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 62	15-time(s)	150.0sec										
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384**

Term	Pointing (x, y)	Comment
04/29 10:13:00 - 04/29 16:09:54	Fixed ( -18.0, 882.0)	* HOP 81, polar monitoring campaign, N pole.
04/30 10:03:00 - 04/30 15:59:54	Fixed ( -13.0, -938.0)	* HOP 206, polar panorama, S pole.

**PROG= 20 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec										
Seqn= 51	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 2	30-time(s)	2.0sec										
Seqn= 58	2-time(s)	30.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #18E4: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 512x512 at 1064 1048, 24s-cad**

Term	Pointing (x, y)	Comment
04/29 16:13:00 - 04/29 17:59:54	Track ( 860.6, -290.1) @ 04/29 16:10:00	* Cont.
04/30 02:33:00 - 04/30 05:30:24	Fixed ( 882.0, -296.0)	* AR 11465, fixed pointing at W limb.
04/30 16:44:30 - 04/30 17:39:00	Fixed ( 882.0, -296.0)	* AR 11465 at W limb.
05/01 06:19:30 - 05/01 08:16:00	Fixed ( 882.0, -296.0)	# Cont.

**PROG= 07 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec										
Seqn= 19	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 29	2-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 28	150-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

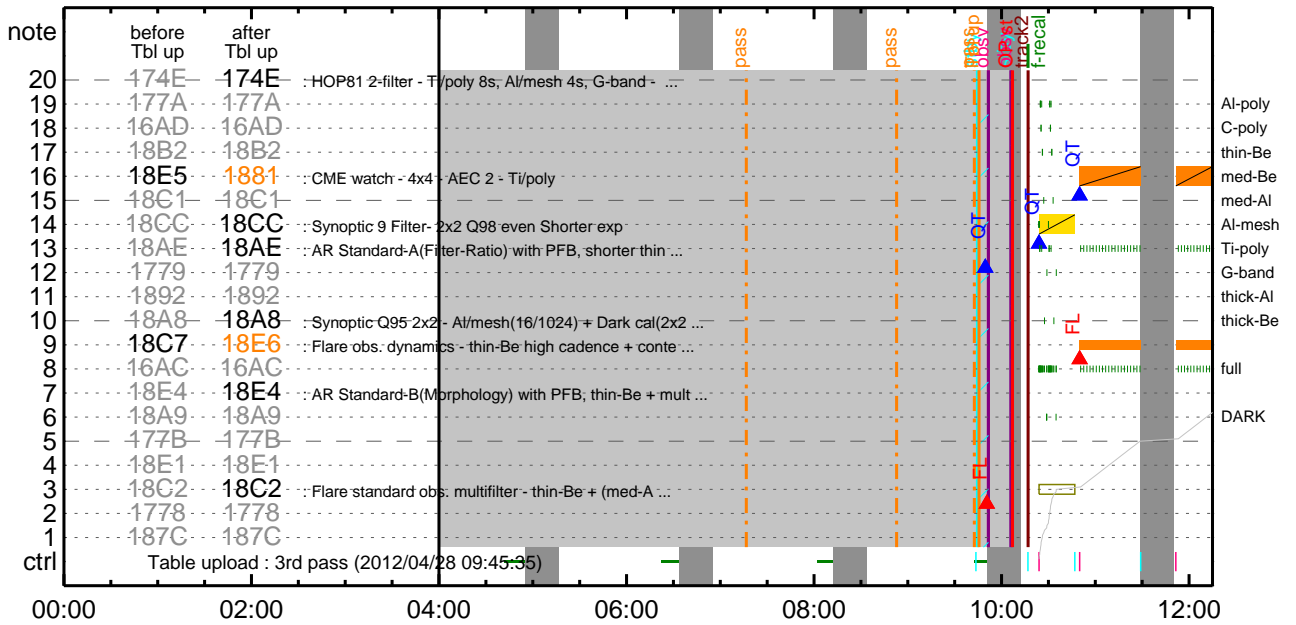
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**XOB #18E6: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-15 loops**

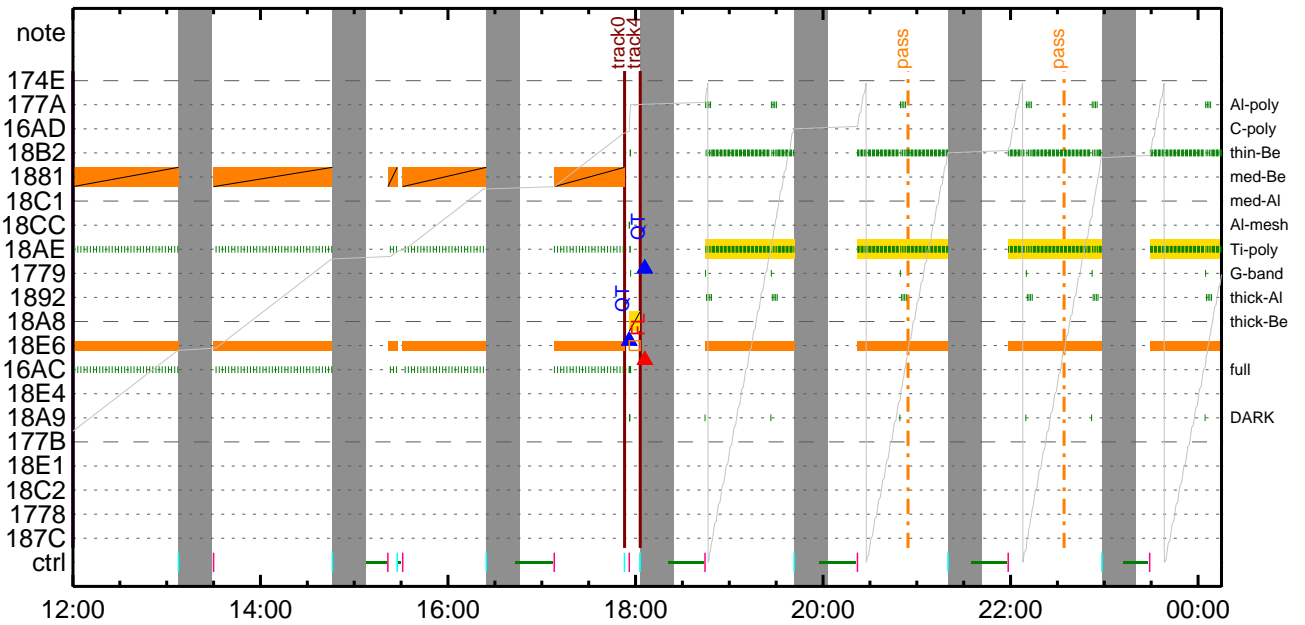
Term	Pointing (x, y)	Comment
04/28 10:50:00 - 04/28 17:52:54	Track ( -25.1, 0.1) @ 04/28 10:17:00	# OP start + 10min, EIS sensitivity monitoring, and other EIS activities. Also, SOT to do ToV
04/28 18:44:30 - 04/29 05:59:54	Track ( 786.4, -276.6) @ 04/28 18:03:00	# Cont.
04/29 06:13:00 - 04/29 10:09:54	Track ( 831.6, -283.9) @ 04/29 06:10:00	# Cont.



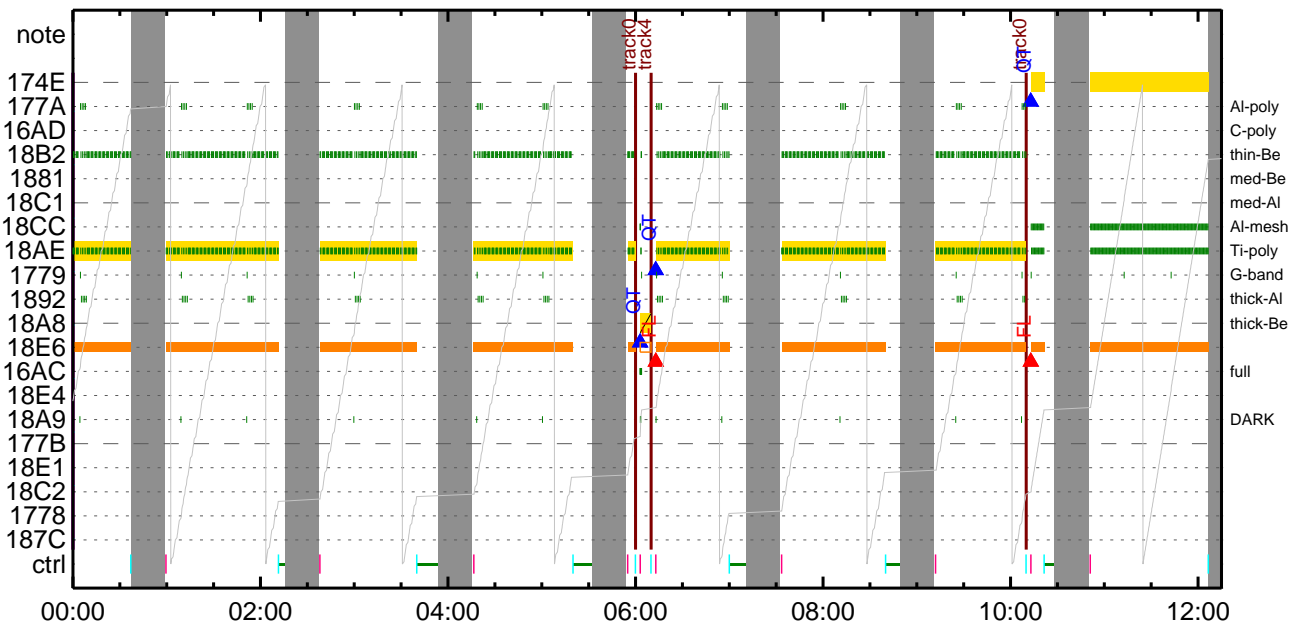
### CMDI #0594 2012/04/28



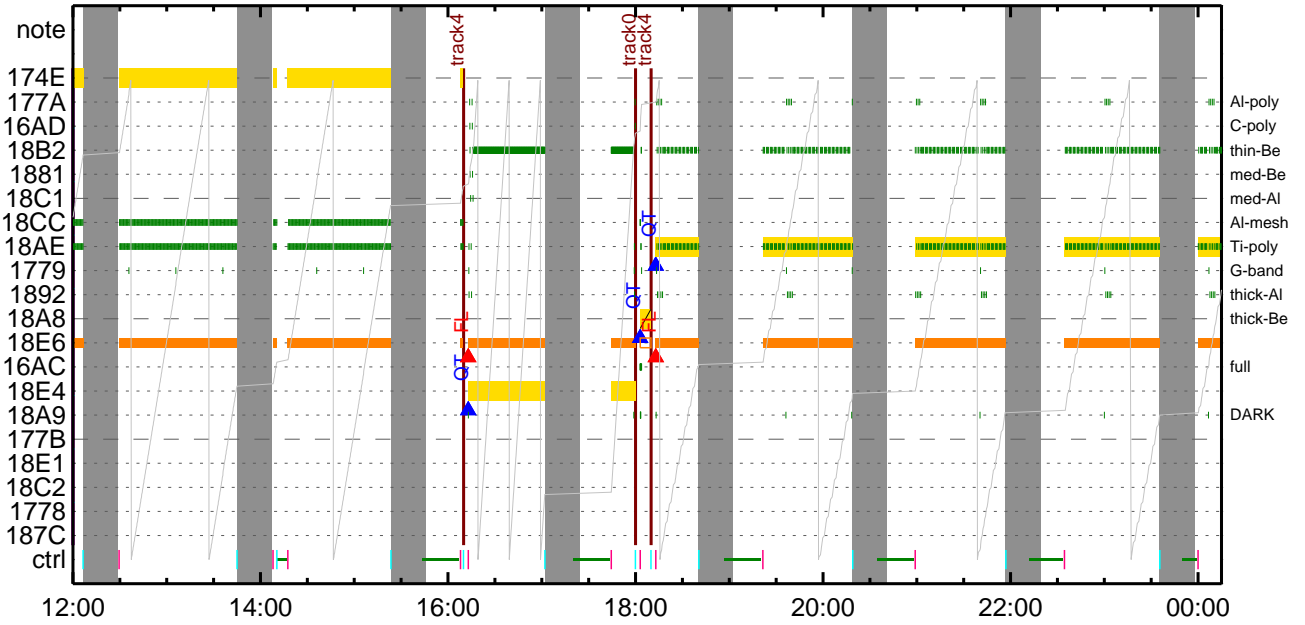
### CMDI #0594 2012/04/28



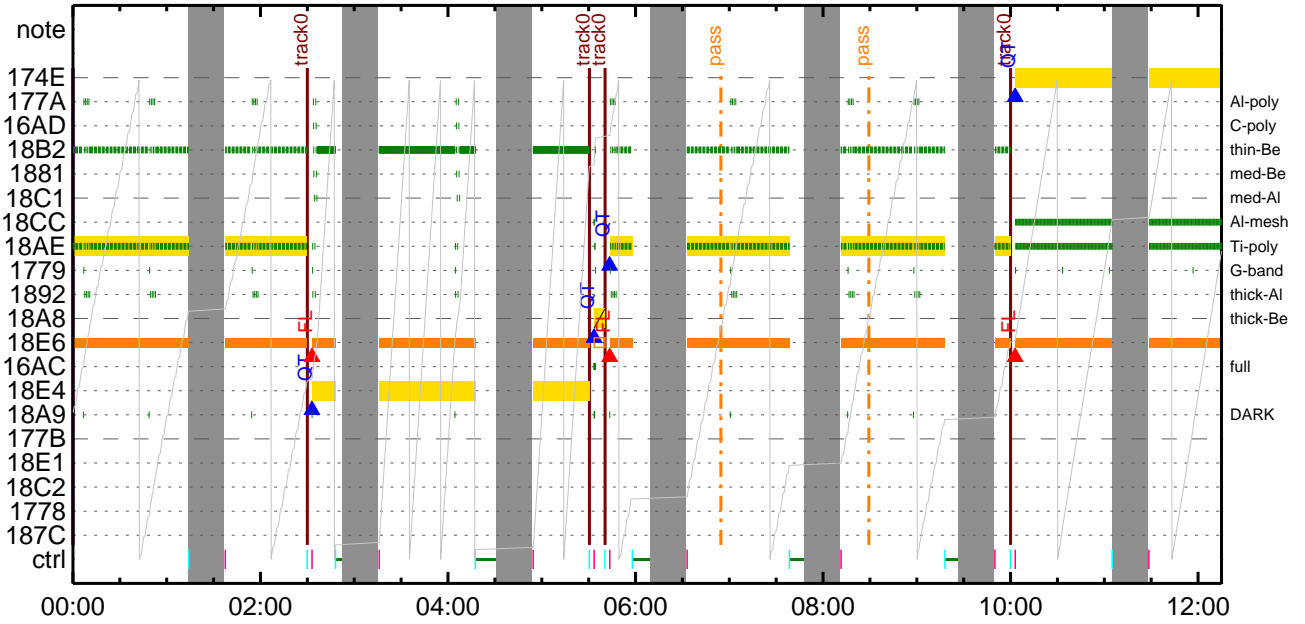
### CMDI #0594 2012/04/29



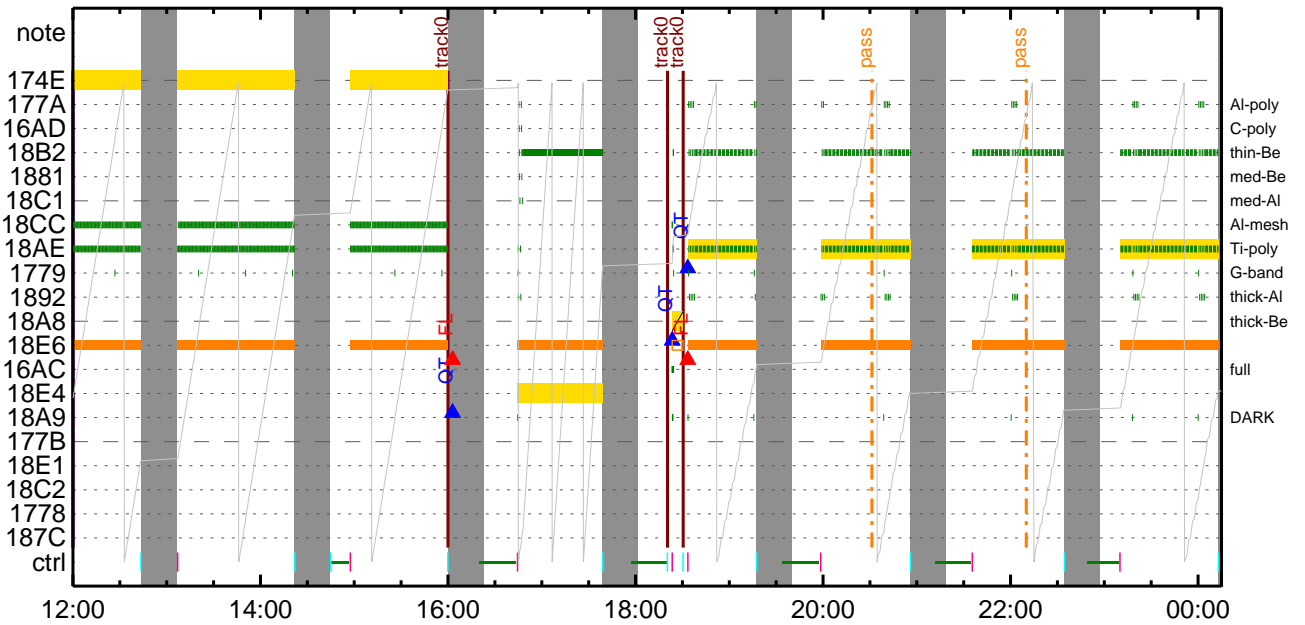
CMDI #0594 2012/04/29



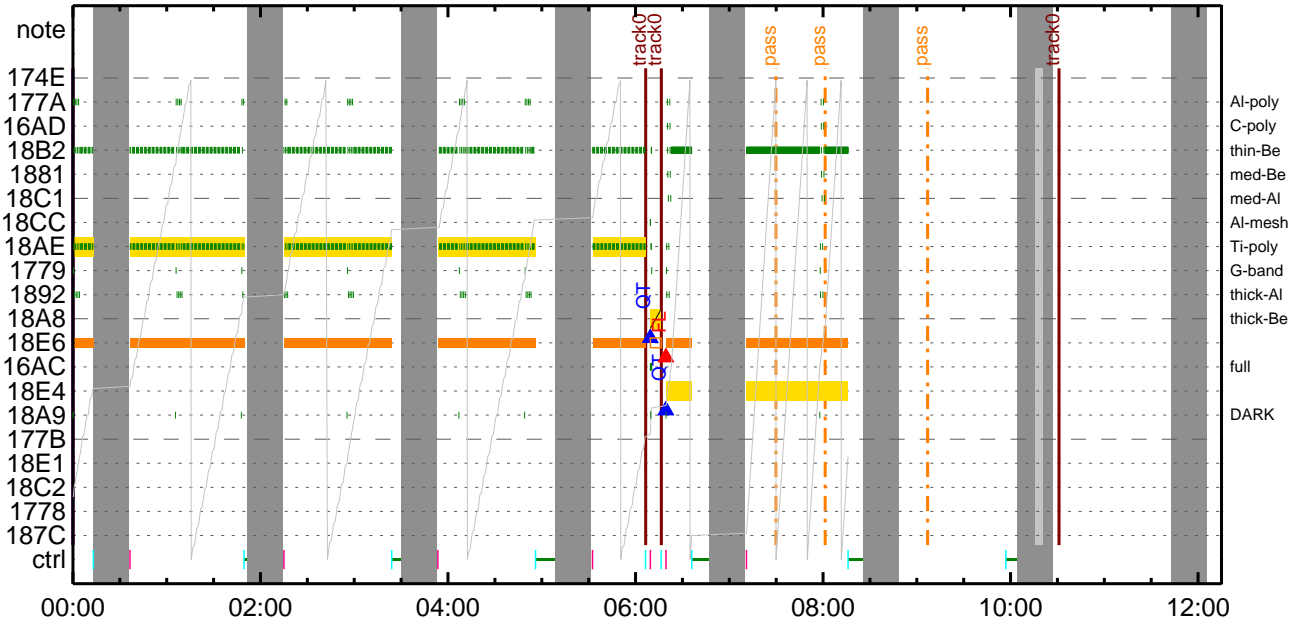
CMDI #0594 2012/04/30



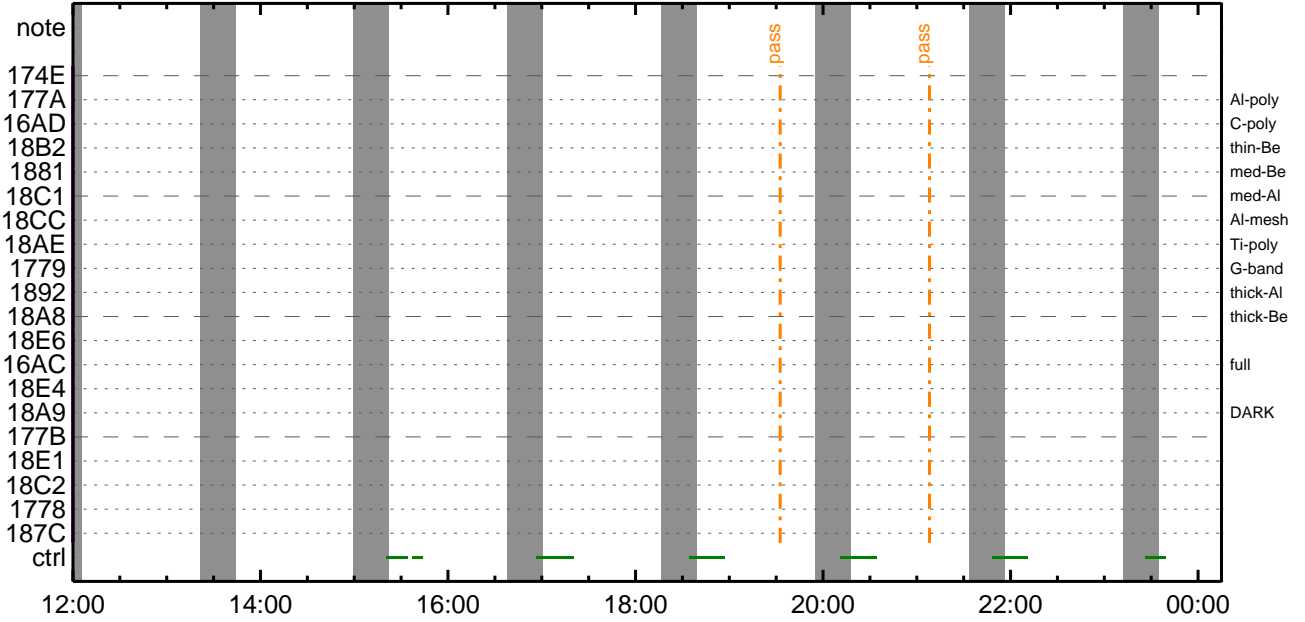
CMDI #0594 2012/04/30



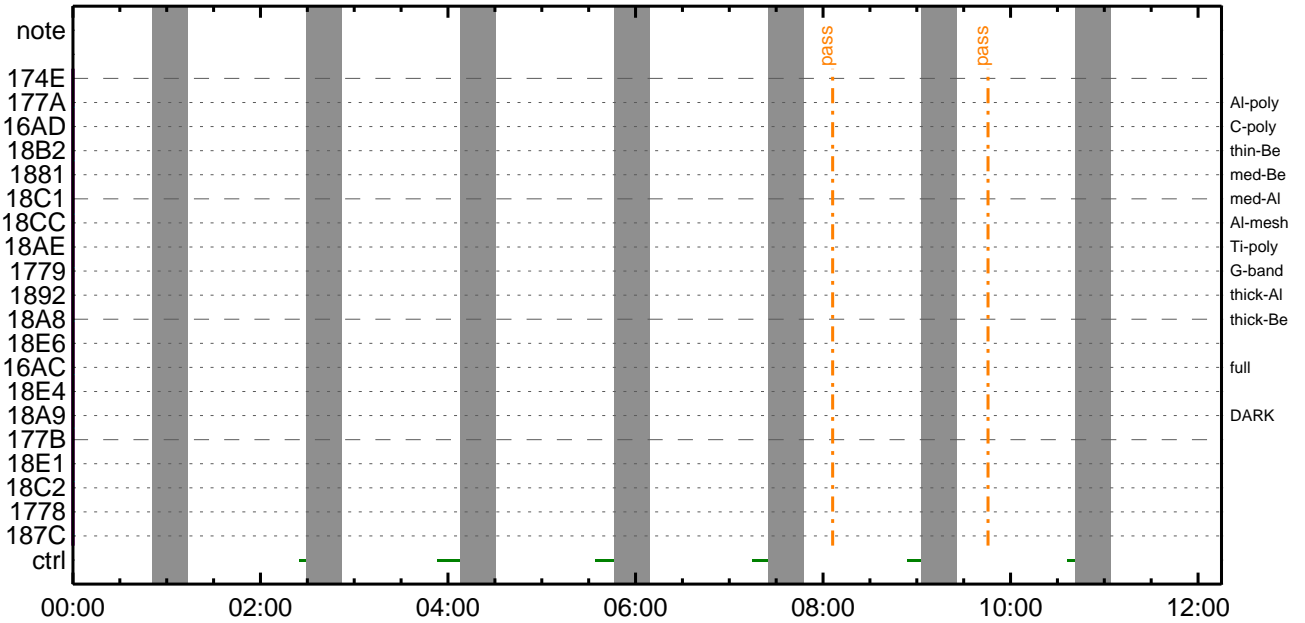
CMDI #0594 2012/05/01



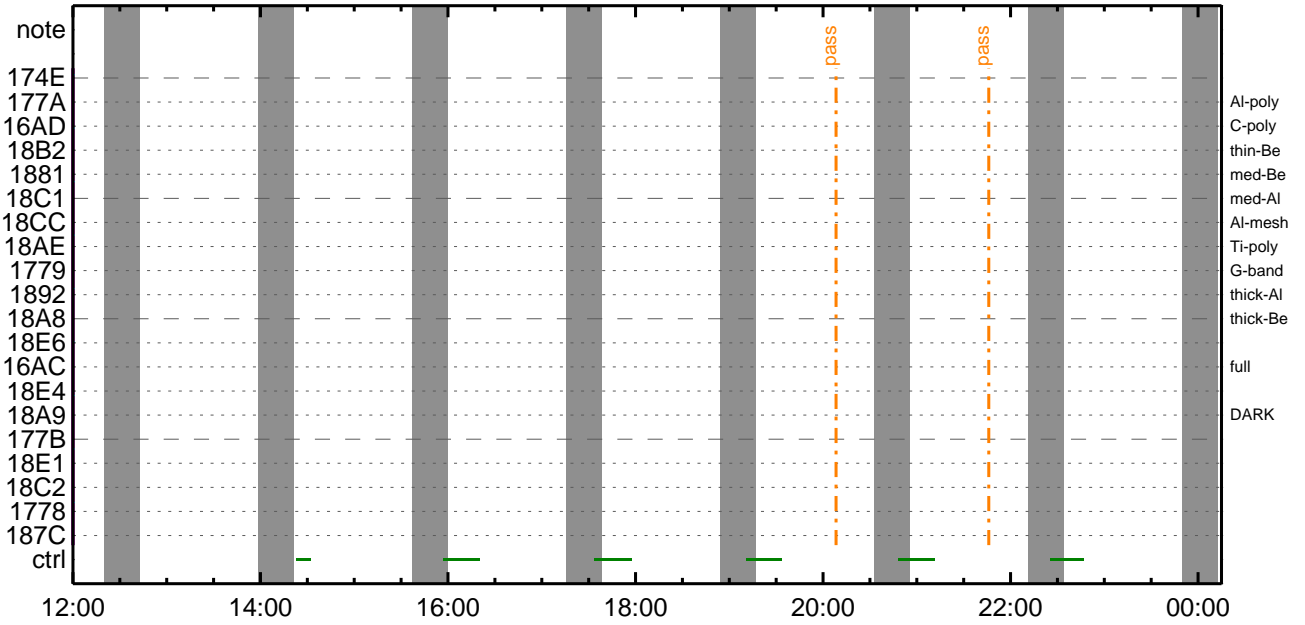
CMDI #0594 2012/05/01



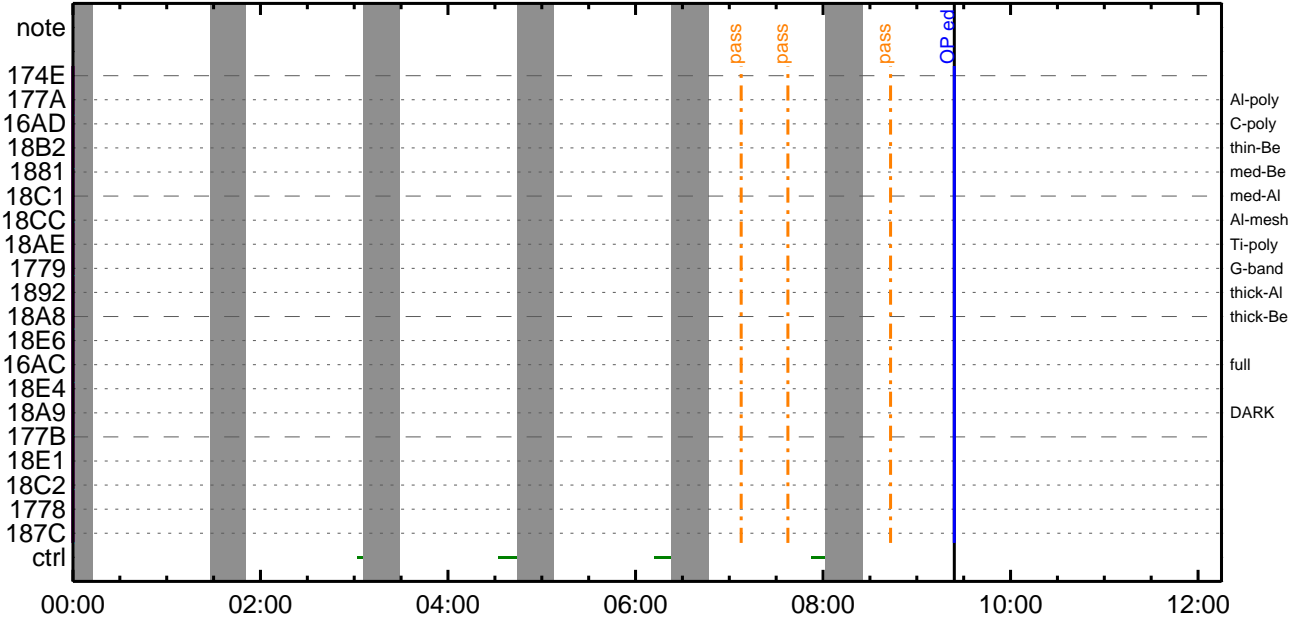
CMDI #0594 2012/05/02



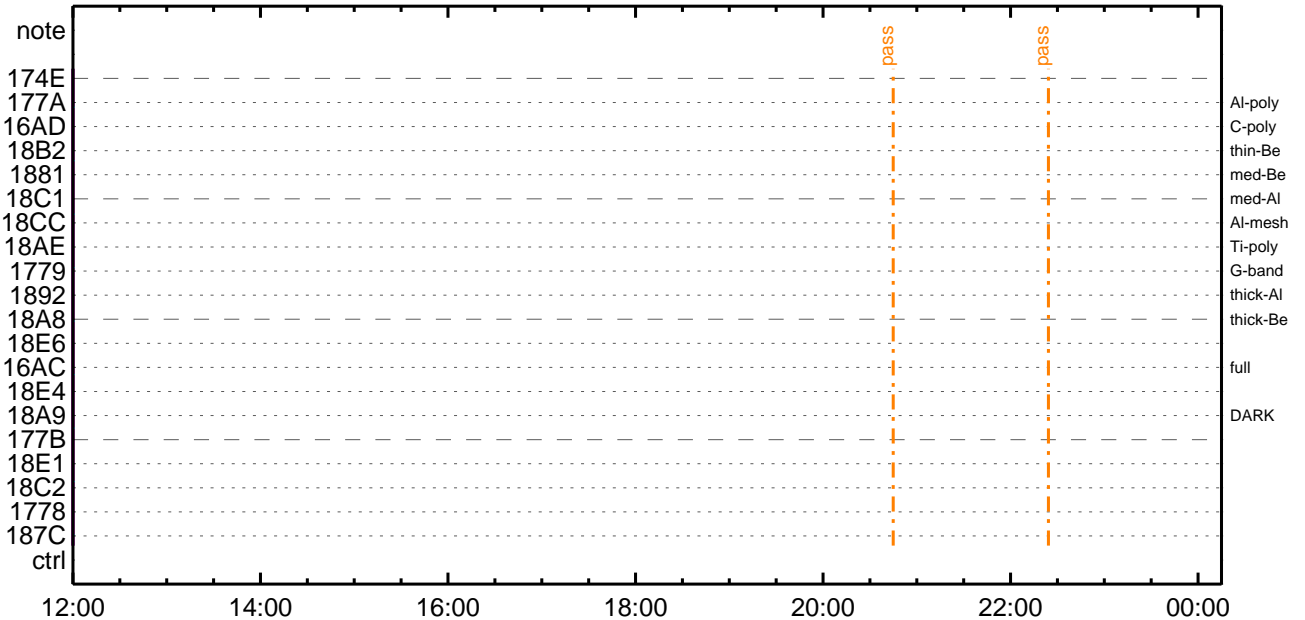
CMDI #0594 2012/05/02



CMDI #0594 2012/05/03



CMDI #0594 2012/05/03







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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-841:OP
0104 ( )
0105 S. OG og-841:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ã´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°²òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ô²°²E²ò²³òE;f
0180 C. ²²ò¿;çSET²E²DUMP²îE±²îY¹²ç¹Ô²|²³²E;f
0181 C.
0182 C. TIY³Y²YóYÉòðÁDî¿(UT)
0183 +. TI 2012-04-28 10:02:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-04-28 10:02:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-04-28 10:02:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```







(a) Spacecraft Operation Procedure (real-commands)

```
main-843 2012-04-28 12:02:37 145 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈÈÈ¼°ÇÔø•ø¿¼í¹çøÍ;çÀ®, ùø¹øèøøøÇÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-263:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Resume FG table (auto mode) >
0036 +. DC 07-F0 MDP_FG_CTRL_AUTO
0037 BC (50)
0038 . C. -----
0039 C. MDP_FG_CTRL_MODE = AUTO [ ]
0040 C. -----
0041 C.
0042 C. *****
0043 C. SOT TI command set
0044 C. *****
0045 C. Execute, after the success of TBL upload.
0046 +. TI 2012-04-28 10:06:18.0
0047 DC 07-F0 MDP_SOT_MODE_OBSV
0048 BC (40)
0049 . C. -----
0050 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0051 C. -----
0052 C.
0053 C. Only when FG_CTRL_AUTO is used in RT.
0054 +. TI 2012-04-28 10:06:20.0
0055 DC 07-F0 MDP_FG_CTRL_AUTO
0056 BC (50)
0057 . C. -----
0058 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0059 C. -----
0060 C. ***** SOT END *****
0061 C.
0062 C. ***** XRT START *****
0063 C.
0064 +. DC 07-F0 MDP_XRT_CTRL_MANU
0065 BC (c1)
0066 + DC 07-F0 MDP_XRT_MODE_STBY
0067 BC (c3)
0068 . C. ----- Success Verify ? OK / NG____
0069 C.
0070 C. XRT Obs. Table Upload
0071 . S. RAM ram-291:MDP_OBS_X
0072 ( )
0073 C.
0074 +. DC 07-F0 MDP_DUMP_XRTTBL
0075 BC (84 07 00 00 00 3a d4)
0076 . C. ----- Comparison Check ? OK / ERR ____
0077 C.
0078 C.
0079 +. DC 07-F0 MDP_XRT_ROI_SET
0080 BC (cd 01 b1 b1 04 04)
0081 + DC 07-F0 MDP_XRT_ROI_SET
0082 BC (cd 02 b1 b1 08 08)
0083 + DC 07-F0 MDP_XRT_ROI_SET
0084 BC (cd 03 b1 b1 08 08)
0085 + DC 07-F0 MDP_XRT_ROI_SET
0086 BC (cd 04 b1 b1 06 06)
0087 + DC 07-F0 MDP_XRT_ROI_SET
0088 BC (cd 05 85 83 06 06)
0089 + DC 07-F0 MDP_XRT_ROI_SET
0090 BC (cd 06 85 83 06 06)
0091 + DC 07-F0 MDP_XRT_ROI_SET
0092 BC (cd 07 85 83 08 08)
0093 + DC 07-F0 MDP_XRT_ROI_SET
0094 BC (cd 08 80 80 20 20)
0095 + DC 07-F0 MDP_XRT_ROI_SET
```



\*\*\* OP Sequence for XRT \*\*\*

```

2012/04/28 10:16:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/04/28 10:16:56.0 XRT_FOCUS_RECALIBRATE_428_OG [0x1ac]
                        XRT_FOCUS_RECAL 2 07-F8 78 00
2012/04/28 10:17:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 02 00 00 00 00
2012/04/28 10:20:56.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2012/04/28 10:21:16.0 XRT_FLD_DIS_402_OG [0x192]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2012/04/28 10:21:18.0 XRT_FLRCTRL_DIS_433_OG [0x1b1]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2012/04/28 10:21:20.0 XRT_ARS_DIS_438_OG [0x1b6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/04/28 10:23:58.0 XRT_QT_PROG_SET_417_OG [0x1a1]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0e
2012/04/28 10:24:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/04/28 10:46:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/04/28 10:49:26.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2012/04/28 10:49:46.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/04/28 10:49:48.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/04/28 10:49:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/04/28 10:49:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/04/28 10:49:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/04/28 10:49:56.0 XRT_QT_PROG_SET_420_OG [0x1a4]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 10
2012/04/28 10:49:58.0 XRT_FL_PROG_SET_441_OG [0x1b9]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 09
2012/04/28 10:50:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/04/28 11:29:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/04/28 11:29:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/04/28 11:29:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/04/28 11:32:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/04/28 11:50:30.0 XRT_Custom_418_OG [0x1a2]
2012/04/28 11:51:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/04/28 13:07:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/04/28 13:07:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/04/28 13:07:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/04/28 13:10:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/04/28 13:29:00.0 XRT_Custom_418_OG [0x1a2]
2012/04/28 13:30:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/04/28 14:46:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/04/28 14:46:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/04/28 14:46:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/04/28 14:49:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/04/28 15:20:30.0 XRT_Custom_418_OG [0x1a2]
2012/04/28 15:21:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/04/28 15:27:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/04/28 15:27:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/04/28 15:27:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/04/28 15:30:00.0 XRT_Custom_418_OG [0x1a2]
2012/04/28 15:30:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/04/28 15:31:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/04/28 16:24:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/04/28 16:24:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/04/28 16:24:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/04/28 16:27:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/04/28 17:07:00.0 XRT_Custom_418_OG [0x1a2]

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2012/04/28	17:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/28	17:52:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/28	17:52:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/04/28	17:53:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00
2012/04/28	17:53:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/04/28	17:53:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/04/28	17:53:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/04/28	17:55:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2012/04/28	17:56:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/28	18:02:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/28	18:03:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	04 00 00 00	00
2012/04/28	18:03:00.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/28	18:03:02.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/28	18:03:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/28	18:05:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2012/04/28	18:05:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/04/28	18:05:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/04/28	18:05:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/04/28	18:05:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/04/28	18:05:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/28	18:05:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2012/04/28	18:05:58.0	XRT_FL_PROG_SET_441_OG [0x1b9]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 09	
2012/04/28	18:06:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/28	18:43:30.0	XRT_Custom_418_OG [0x1a2]					
2012/04/28	18:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/28	19:41:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/28	19:41:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/28	19:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/28	19:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/28	20:21:00.0	XRT_Custom_418_OG [0x1a2]					
2012/04/28	20:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/28	21:20:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/28	21:20:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/28	21:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/28	21:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/28	21:57:30.0	XRT_Custom_418_OG [0x1a2]					
2012/04/28	21:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/28	22:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/28	22:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/28	22:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/28	23:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/28	23:28:00.0	XRT_Custom_418_OG [0x1a2]					
2012/04/28	23:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	00:37:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	00:37:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/29	00:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/29	00:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/29	00:58:30.0	XRT_Custom_418_OG [0x1a2]					
2012/04/29	00:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	



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2012/04/29	02:11:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/29	02:11:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/29	02:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/29	02:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/29	02:37:00.0	XRT_Custom_418_OG [0x1a2]							
2012/04/29	02:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/29	03:40:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/29	03:40:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/29	03:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/29	03:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/29	04:15:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/29	04:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/29	05:20:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/29	05:20:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/29	05:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/29	05:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/29	05:54:00.0	XRT_Custom_418_OG [0x1a2]							
2012/04/29	05:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/29	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/29	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/04/29	06:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/04/29	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/04/29	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/04/29	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/04/29	06:02:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2012/04/29	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/29	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/29	06:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2012/04/29	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/04/29	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/04/29	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/04/29	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/04/29	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/04/29	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/29	06:12:56.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/04/29	06:12:58.0	XRT_FL_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 09				
2012/04/29	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/29	07:00:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/29	07:00:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/29	07:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/29	07:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/29	07:32:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/29	07:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/29	08:40:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/29	08:40:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/29	08:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/29	08:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/29	09:11:00.0	XRT_Custom_418_OG [0x1a2]							
2012/04/29	09:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

2012/04/29	10:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	10:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 b1 9a 01 99	
2012/04/29	10:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/04/29	10:12:46.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/04/29	10:12:48.0	XRT_FLRCTRL_ENA_449_OG [0x1c1]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/04/29	10:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/04/29	10:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/04/29	10:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/29	10:12:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2012/04/29	10:12:58.0	XRT_FL_PROG_SET_441_OG [0x1b9]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 09	
2012/04/29	10:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	10:21:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	10:21:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/29	10:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/29	10:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/29	10:50:00.0	XRT_Custom_418_OG [0x1a2]					
2012/04/29	10:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	12:06:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	12:06:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/29	12:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/29	12:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/29	12:28:30.0	XRT_Custom_418_OG [0x1a2]					
2012/04/29	12:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	13:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	13:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/29	13:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/29	13:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/29	14:07:00.0	XRT_Custom_418_OG [0x1a2]					
2012/04/29	14:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	14:10:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	14:10:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/29	14:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/29	14:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/29	14:16:30.0	XRT_Custom_418_OG [0x1a2]					
2012/04/29	14:17:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	15:23:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	15:23:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/29	15:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/29	15:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/29	16:07:00.0	XRT_Custom_418_OG [0x1a2]					
2012/04/29	16:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	16:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	16:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	04 00 00 00 00	
2012/04/29	16:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/04/29	16:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/04/29	16:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/04/29	16:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/04/29	16:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/04/29	16:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	

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2012/04/29	16:12:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2012/04/29	16:12:58.0	XRT_FL_PROG_SET_441_OG [0x1b9]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	09
2012/04/29	16:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	17:02:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	17:02:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/29	17:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/29	17:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/29	17:43:30.0	XRT_Custom_418_OG [0x1a2]					
2012/04/29	17:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/04/29	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00
2012/04/29	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/04/29	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/04/29	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/04/29	18:02:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2012/04/29	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	18:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	18:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	04 00 00 00	00
2012/04/29	18:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2012/04/29	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/04/29	18:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/04/29	18:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/04/29	18:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/04/29	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/29	18:12:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2012/04/29	18:12:58.0	XRT_FL_PROG_SET_441_OG [0x1b9]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 09	
2012/04/29	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	18:40:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	18:40:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/29	18:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/29	18:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/29	19:20:30.0	XRT_Custom_418_OG [0x1a2]					
2012/04/29	19:21:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	20:19:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	20:19:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/29	20:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/29	20:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/29	20:58:00.0	XRT_Custom_418_OG [0x1a2]					
2012/04/29	20:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	21:57:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	21:57:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/29	21:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/04/29	22:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/04/29	22:33:30.0	XRT_Custom_418_OG [0x1a2]					
2012/04/29	22:34:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/04/29	23:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/04/29	23:35:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/04/29	23:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					

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2012/04/29	23:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/04/29	23:59:00.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	00:00:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/04/30	01:14:00.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/04/30	01:14:02.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/04/30	01:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/04/30	01:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/04/30	01:36:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	01:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/04/30	02:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/04/30	02:30:00.5	AOCS_Or-point_Start_5_OG [0x09b]							
			AOCU_NM	5	02-76	00 1a 4d b1 9a			
2012/04/30	02:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/04/30	02:32:46.0	XRT_FLD_ENA_411_OG [0x19b]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/04/30	02:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/04/30	02:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/04/30	02:32:52.0	XRT_ARS_DIS_431_OG [0x1af]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/04/30	02:32:54.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/04/30	02:32:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 07			
2012/04/30	02:32:58.0	XRT_FL_PROG_SET_441_OG [0x1b9]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 09			
2012/04/30	02:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/04/30	02:48:00.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/04/30	02:48:02.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/04/30	02:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/04/30	02:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/04/30	03:15:00.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	03:16:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/04/30	04:17:30.0	XRT_CTRL_MANU_408_OG [0x198]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/04/30	04:17:32.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/04/30	04:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/04/30	04:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/04/30	04:53:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	04:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/04/30	05:30:24.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/04/30	05:30:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/04/30	05:30:30.5	AOCS_Or-point_Start_2_OG [0x098]							
			AOCU_NM	5	02-76	00 00 00 00 00			
2012/04/30	05:30:46.0	XRT_FLD_DIS_402_OG [0x192]							
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/04/30	05:30:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/04/30	05:30:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/04/30	05:33:28.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
2012/04/30	05:33:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/04/30	05:40:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/04/30	05:40:30.0	AOCS_Or-point_Start_5_OG [0x09b]							
			AOCU_NM	5	02-76	00 1a 4d b1 9a			
2012/04/30	05:42:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/04/30	05:43:16.0	XRT_FLD_ENA_411_OG [0x19b]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/04/30	05:43:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/04/30	05:43:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/04/30	05:43:22.0	XRT_ARS_DIS_431_OG [0x1af]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/04/30	05:43:24.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			

2012/04/30	05:43:26.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2012/04/30	05:43:28.0	XRT_FL_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	09			
2012/04/30	05:43:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/30	05:58:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	05:58:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/30	05:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/30	06:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/30	06:32:00.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	06:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/30	07:38:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	07:38:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/30	07:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/30	07:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/30	08:10:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	08:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/30	09:18:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	09:18:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/30	09:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/30	09:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/30	09:49:00.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	09:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/30	09:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	10:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	53	64	01	26
2012/04/30	10:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/04/30	10:02:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/04/30	10:02:48.0	XRT_FLRCTRL_ENA_449_OG [0x1c1]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/04/30	10:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/04/30	10:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/04/30	10:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/30	10:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2012/04/30	10:02:58.0	XRT_FL_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	09			
2012/04/30	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/30	11:05:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	11:05:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/30	11:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/30	11:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/30	11:27:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	11:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/30	12:43:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	12:43:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/30	12:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/30	12:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/30	13:06:00.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	13:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/30	14:22:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	14:22:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/30	14:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/30	14:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/30	14:45:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2012/04/30	14:45:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/30	14:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/30	14:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/30	14:56:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	14:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/30	15:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	16:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 1a 4d b1 9a				
2012/04/30	16:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/04/30	16:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/04/30	16:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/04/30	16:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/04/30	16:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/04/30	16:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/30	16:02:56.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2012/04/30	16:02:58.0	XRT_FL_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 09				
2012/04/30	16:43:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	16:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/30	17:39:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	17:39:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/30	17:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/30	17:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/30	18:20:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	18:20:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/04/30	18:20:30.5	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/04/30	18:20:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/04/30	18:20:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/04/30	18:20:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/04/30	18:23:28.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2012/04/30	18:23:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/30	18:30:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	18:30:30.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 1a 4d b1 9a				
2012/04/30	18:32:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/04/30	18:33:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/04/30	18:33:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/04/30	18:33:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/04/30	18:33:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/04/30	18:33:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/30	18:33:26.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2012/04/30	18:33:28.0	XRT_FL_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 09				
2012/04/30	18:33:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/30	19:17:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	19:17:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/30	19:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/30	19:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/30	19:57:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	19:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/30	20:56:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	20:56:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2012/04/30	20:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/30	20:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/30	21:34:30.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	21:35:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/04/30	22:34:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/04/30	22:34:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/04/30	22:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/04/30	22:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/04/30	23:09:00.0	XRT_Custom_418_OG [0x1a2]							
2012/04/30	23:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/01	00:13:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/01	00:13:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/01	00:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/01	00:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/01	00:35:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/01	00:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/01	01:49:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/01	01:49:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/01	01:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/01	01:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/01	02:14:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/01	02:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/01	03:24:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/01	03:24:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/01	03:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/01	03:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/01	03:52:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/01	03:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/01	04:56:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/01	04:56:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/01	04:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/01	04:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/01	05:31:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/01	05:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/01	06:06:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/01	06:06:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/01	06:06:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/05/01	06:06:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/05/01	06:06:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/05/01	06:06:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/01	06:09:28.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2012/05/01	06:09:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/01	06:16:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/01	06:16:30.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 1a 4d b1 9a				
2012/05/01	06:18:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/05/01	06:19:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/01	06:19:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/01	06:19:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/01	06:19:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/01	06:19:24.0	XRT_FLD_RESET_412_OG [0x19c]							

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2012/05/01	06:19:26.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2012/05/01	06:19:28.0	XRT_FL_PROG_SET_441_OG [0x1b9]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	09
2012/05/01	06:19:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/01	06:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/01	06:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/01	06:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/01	06:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/01	07:10:00.0	XRT_Custom_418_OG [0x1a2]					
2012/05/01	07:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/01	08:16:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/01	08:16:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/01	08:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/01	08:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/01	09:57:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/01	09:57:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/01	09:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/01	10:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/01	10:31:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00 00