

# XRT Timeline to be uploaded on 2012/05/05

Period: 2012/05/05 09:34:00 - 2012/05/10 09:24:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

Term	Pointing (x, y)	Comment
05/05 09:41:30 - 05/05 09:43:54	Fixed ( , )	
05/05 09:47:00 - 05/05 10:01:24	Fixed ( -897.9, -18.0)	# OP start + 10min, HOP79 (1/21)
05/05 10:04:30 - 05/05 10:11:24	Fixed ( -698.9, -18.0)	HOP79 (2/21)
05/05 10:14:30 - 05/05 10:21:24	Fixed ( -499.8, -18.0)	HOP79 (3/21)
05/05 10:24:30 - 05/05 10:31:24	Fixed ( -300.8, -18.0)	HOP79 (4/21)
05/05 10:34:30 - 05/05 10:41:24	Fixed ( -101.7, -18.0)	HOP79 (5/21)
05/05 10:44:30 - 05/05 10:55:00	Fixed ( 97.4, -18.0)	HOP79 (6/21)
05/05 11:23:00 - 05/05 11:29:54	Fixed ( 296.5, -18.0)	HOP79 (7/21)
05/05 11:33:00 - 05/05 11:39:54	Fixed ( 495.5, -18.0)	HOP79 (8/21)
05/05 11:43:00 - 05/05 11:49:54	Fixed ( 694.6, -18.0)	HOP79 (9/21)
05/05 11:53:00 - 05/05 11:59:54	Fixed ( 893.7, -18.0)	HOP79 (10/21)
05/05 12:03:00 - 05/05 12:09:54	Fixed ( -913.8, 240.6)	HOP79 (11/21)
05/05 12:13:00 - 05/05 12:33:30	Fixed ( -740.7, 267.1)	HOP79 (12/21)
05/05 13:01:30 - 05/05 13:08:24	Fixed ( -369.7, 287.1)	HOP79 (14/21)
05/05 13:11:30 - 05/05 13:18:24	Fixed ( -178.9, 291.0)	HOP79 (15/21)
05/05 13:21:30 - 05/05 13:28:24	Fixed ( 16.2, 292.1)	HOP79 (16/21)
05/05 13:31:30 - 05/05 13:38:24	Fixed ( 152.9, 291.0)	HOP79 (17/21)
05/05 13:41:30 - 05/05 13:48:24	Fixed ( 343.7, 287.1)	HOP79 (18/21)
05/05 13:51:30 - 05/05 14:12:00	Fixed ( 530.8, 279.6)	HOP79 (19/21)
05/05 14:50:30 - 05/05 14:59:54	Fixed ( 900.8, 222.6)	HOP79 (21/21)

**PROG= 16 Inf.-time(s)**

Subr= 1	1-time(s)	120.0sec										
Seqn= 57	1-time(s)	4.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1839: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 120sec-cad**

Term	Pointing (x, y)	Comment
05/05 15:03:00 - 05/05 17:29:00	Track ( 177.6, -323.1) <sup>Ⓜ 05/05 15:00:00</sup>	AR11471 Obs.
05/05 18:24:00 - 05/06 05:58:24	Track ( 204.6, -323.7) <sup>Ⓜ 05/05 18:21:00</sup>	#Cont.
05/06 06:11:30 - 05/06 17:55:54	Track ( 297.8, -326.2) <sup>Ⓜ 05/06 06:08:30</sup>	#Cont.
05/06 18:51:28 - 05/07 05:24:00	Track ( 388.4, -329.2) <sup>Ⓜ 05/06 18:06:00</sup>	#Cont.

**PROG= 11 Inf.-time(s)**

Subr= 2	1-time(s)	2.0sec										
Seqn= 19	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 41	4-time(s)	2.0sec										
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 1	1-time(s)	2.0sec										
Seqn= 76	35-time(s)	120.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	30.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	30.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	30.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	30.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #18E8: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)**

Term	Pointing (x, y)	Comment
05/05 18:14:00 - 05/05 18:20:54	Fixed ( 0.0, 0.0)	synoptic, shifted 11.0 min
05/06 06:01:30 - 05/06 06:08:24	Fixed ( 0.0, 0.0)	synoptic, shifted -1.5 min
05/06 17:59:00 - 05/06 18:06:00	Fixed ( 0.0, 0.0)	synoptic, shifted -4.0 min
05/07 06:04:00 - 05/07 06:10:54	Fixed ( 0.0, 0.0)	synoptic, shifted 1.0 min
05/07 18:06:30 - 05/07 18:13:24	Fixed ( 0.0, 0.0)	synoptic, shifted 3.5 min
05/08 05:37:30 - 05/08 05:44:24	Fixed ( 0.0, 0.0)	synoptic, shifted -25.5 min

**PROG= 12 1-time(s)**

Subr= 1	1-time(s)	12.0sec										
Seqn= 7	1-time(s)	4.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 8	1-time(s)	4.0sec										
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 4 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #182A: AR Standard-B(Morphology) with PFB, FW1=Open, Al/Mesh, 384x384 at 1064 1048, 60sec-cad**

Term	Pointing (x, y)	Comment
05/07 06:14:00 - 05/07 18:03:24	Track ( 475.0, -332.9) @ 05/07 06:11:00	#Cont.
<b>PROG= 06 Inf.-time(s)</b>		
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 19 1-time(s) 2.0sec</b>		
Open/Ti-poly	Open/thick-Al	close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
<b>Seqn= 41 4-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Ti-poly	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 68 45-time(s) 30.0sec</b>		
Open/Al-mesh	Open/Ti-poly	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
Open/Al-mesh	Open/Ti-poly	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
Open/Al-mesh	Open/Ti-poly	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
Open/Al-mesh	Open/Ti-poly	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1835: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 40sec-cad**

Term	Pointing (x, y)	Comment
05/07 18:16:30 - 05/08 05:34:24	Track ( 555.0, -336.9) @ 05/07 18:13:30	#Cont.
05/08 05:47:30 - 05/08 09:07:30	Track ( 624.8, -341.2) @ 05/08 05:44:30	#Cont.
<b>PROG= 04 Inf.-time(s)</b>		
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 19 1-time(s) 2.0sec</b>		
Open/Ti-poly	Open/thick-Al	close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
<b>Seqn= 41 4-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Ti-poly	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 52 45-time(s) 24.0sec</b>		
Open/Ti-poly	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 6.0sec
Open/Ti-poly	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec
Open/Ti-poly	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec
Open/Ti-poly	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #18E7: Flare obs. dynamics - Ti\_poly high cadence + context (thick-Al-384x384)-15 loops**

Term	Pointing (x, y)	Comment
05/05 09:41:30 - 05/05 09:43:54	Fixed ( , )	
05/05 09:47:00 - 05/05 10:01:24	Fixed ( -897.9, -18.0)	# OP start + 10min, HOP79 (1/21)
05/05 10:04:30 - 05/05 10:11:24	Fixed ( -698.9, -18.0)	HOP79 (2/21)
05/05 10:14:30 - 05/05 10:21:24	Fixed ( -499.8, -18.0)	HOP79 (3/21)
05/05 10:24:30 - 05/05 10:31:24	Fixed ( -300.8, -18.0)	HOP79 (4/21)
05/05 10:34:30 - 05/05 10:41:24	Fixed ( -101.7, -18.0)	HOP79 (5/21)
05/05 10:44:30 - 05/05 10:55:00	Fixed ( 97.4, -18.0)	HOP79 (6/21)
05/05 11:23:00 - 05/05 11:29:54	Fixed ( 296.5, -18.0)	HOP79 (7/21)
05/05 11:33:00 - 05/05 11:39:54	Fixed ( 495.5, -18.0)	HOP79 (8/21)
05/05 11:43:00 - 05/05 11:49:54	Fixed ( 694.6, -18.0)	HOP79 (9/21)
05/05 11:53:00 - 05/05 11:59:54	Fixed ( 893.7, -18.0)	HOP79 (10/21)
05/05 12:03:00 - 05/05 12:09:54	Fixed ( -913.8, 240.6)	HOP79 (11/21)
05/05 12:13:00 - 05/05 12:33:30	Fixed ( -740.7, 267.1)	HOP79 (12/21)
05/05 13:01:30 - 05/05 13:08:24	Fixed ( -369.7, 287.1)	HOP79 (14/21)
05/05 13:11:30 - 05/05 13:18:24	Fixed ( -178.9, 291.0)	HOP79 (15/21)
05/05 13:21:30 - 05/05 13:28:24	Fixed ( 16.2, 292.1)	HOP79 (16/21)
05/05 13:31:30 - 05/05 13:38:24	Fixed ( 152.9, 291.0)	HOP79 (17/21)
05/05 13:41:30 - 05/05 13:48:24	Fixed ( 343.7, 287.1)	HOP79 (18/21)
05/05 13:51:30 - 05/05 14:12:00	Fixed ( 530.8, 279.6)	HOP79 (19/21)
05/05 14:50:30 - 05/05 14:59:54	Fixed ( 900.8, 222.6)	HOP79 (21/21)
05/05 15:03:00 - 05/05 17:29:00	Track ( 177.6, -323.1) @ 05/05 15:00:00	AR11471 Obs.
05/05 18:24:00 - 05/06 05:58:24	Track ( 204.6, -323.7) @ 05/05 18:21:00	#Cont.
05/06 06:11:30 - 05/06 17:55:54	Track ( 297.8, -326.2) @ 05/06 06:08:30	#Cont.
05/06 18:51:28 - 05/07 05:24:00	Track ( 388.4, -329.2) @ 05/06 18:06:00	#Cont.
05/07 06:14:00 - 05/07 18:03:24	Track ( 475.0, -332.9) @ 05/07 06:11:00	#Cont.
05/07 18:16:30 - 05/08 05:34:24	Track ( 555.0, -336.9) @ 05/07 18:13:30	#Cont.
05/08 05:47:30 - 05/08 09:07:30	Track ( 624.8, -341.2) @ 05/08 05:44:30	#Cont.

<b>PROG= 02 15-time(s)</b>													
└─ <b>Subr= 1 45-time(s) 10.0sec</b>													
└─ <b>Seqn= 92 1-time(s) 2.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
└─ <b>Subr= 2 1-time(s) 10.0sec</b>													
└─ <b>Seqn= 54 1-time(s) 2.0sec</b>													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
└─ <b>Seqn= 79 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Active Region Search**

\* \* \* \* \*

NOT USED

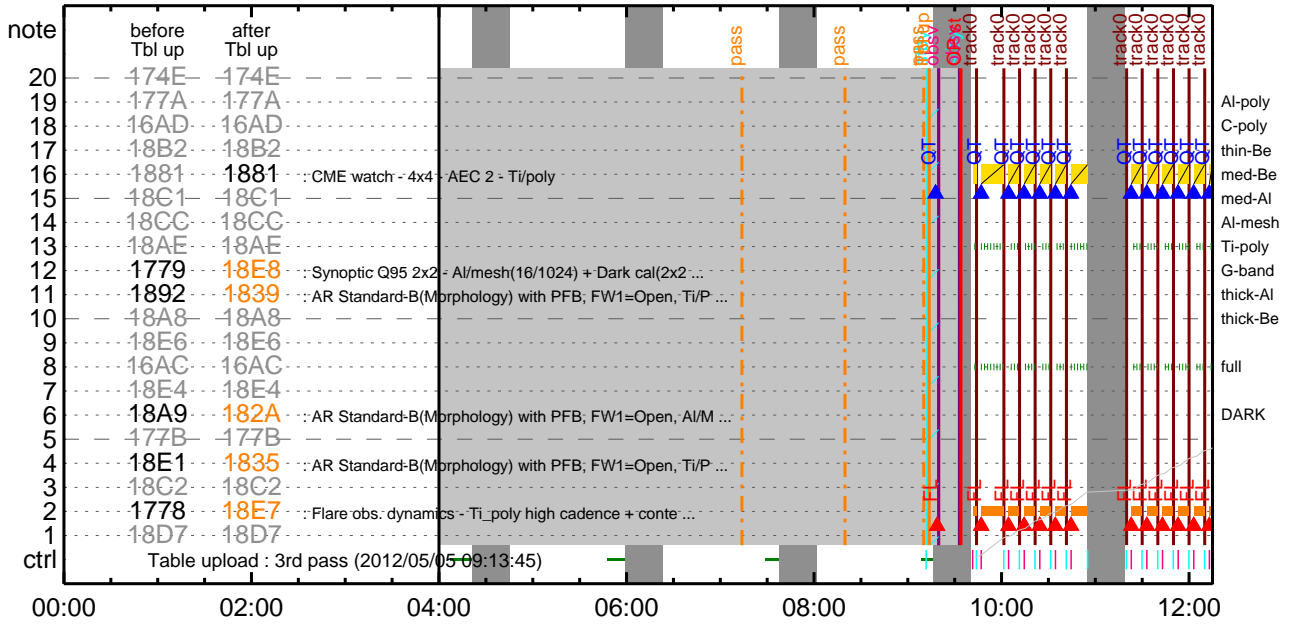
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**Flare Detection**

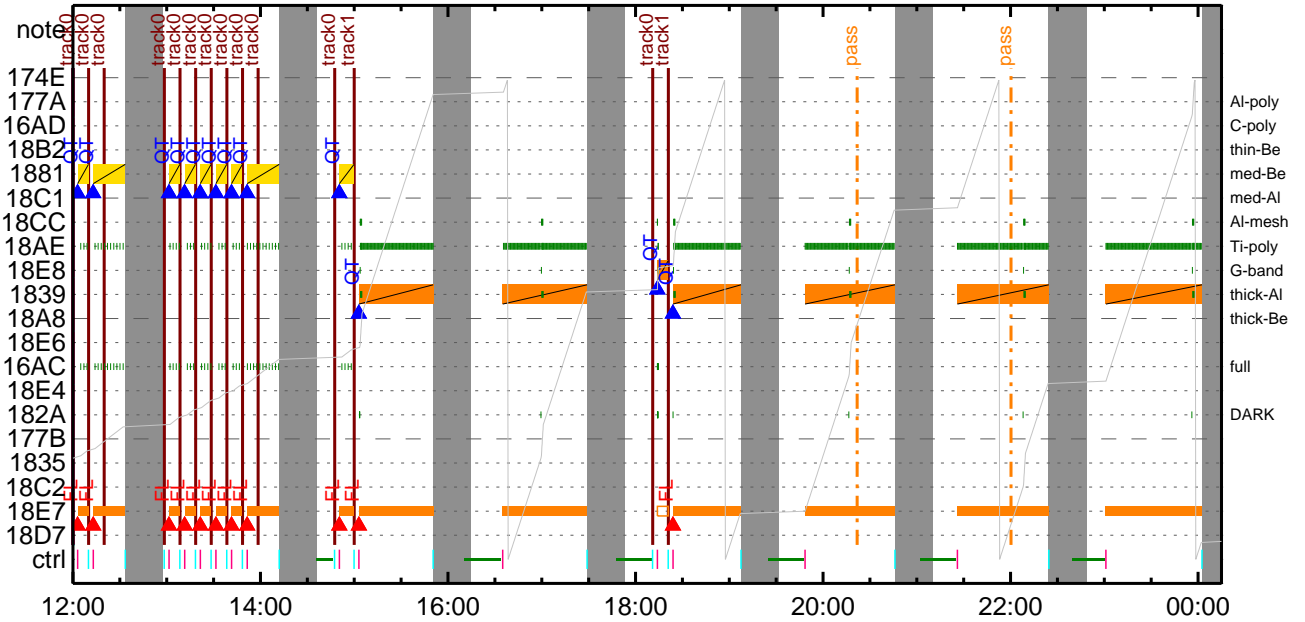
\* \* \* \* \*

<b>FLD Patrol</b>													
Term	Pointing (x, y)								Comment				
05/05 18:23:46 - 05/06 05:58:46	Track ( 204.6, -323.7)										#Cont.		
05/06 06:11:16 - 05/06 17:56:16	Track ( 297.8, -326.2)										#Cont.		
05/06 18:50:32 - 05/07 06:01:16	Track ( 388.4, -329.2)										#Cont.		
05/07 06:13:46 - 05/07 18:03:46	Track ( 475.0, -332.9)										#Cont.		
05/07 18:16:16 - 05/08 05:34:46	Track ( 555.0, -336.9)										#Cont.		
05/08 05:47:16 - 05/10 09:24:00	Track ( 624.8, -341.2)										#Cont.		
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

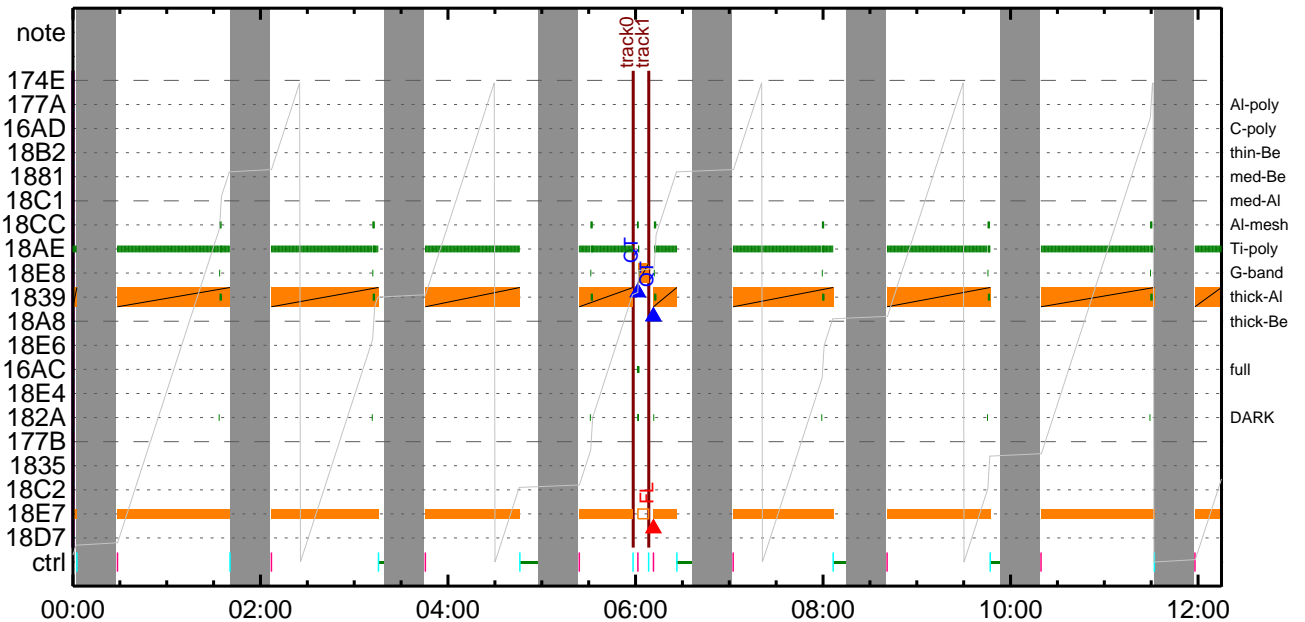
### CMDI #0604 2012/05/05



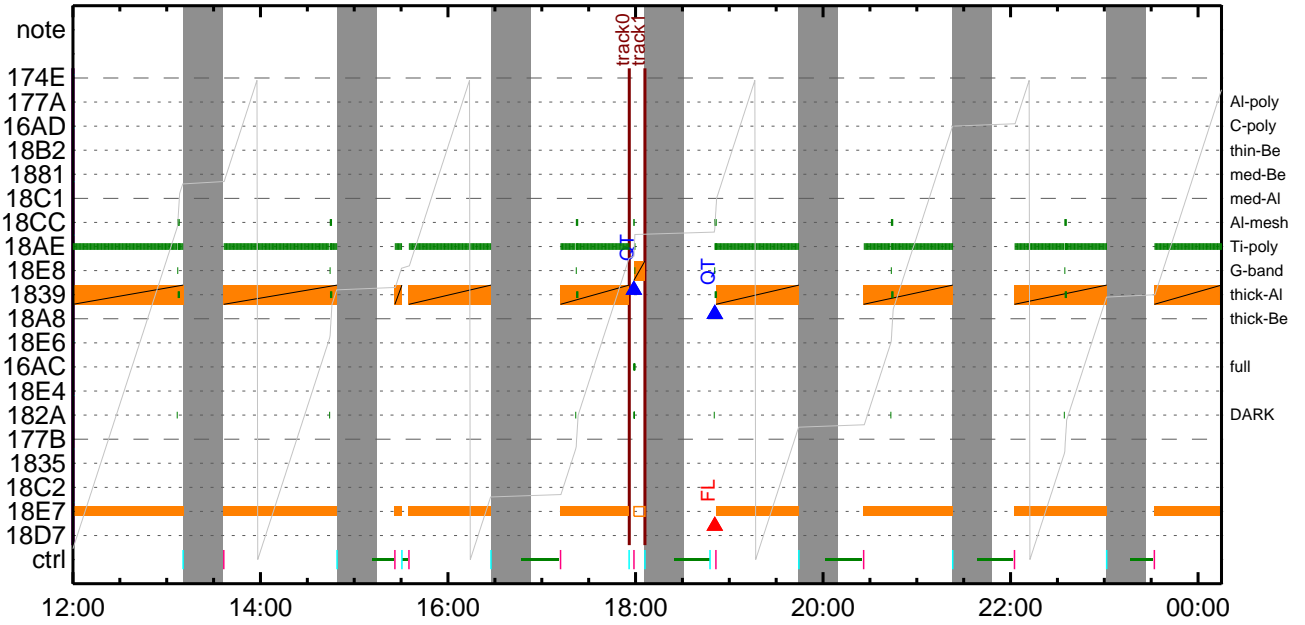
### CMDI #0604 2012/05/05



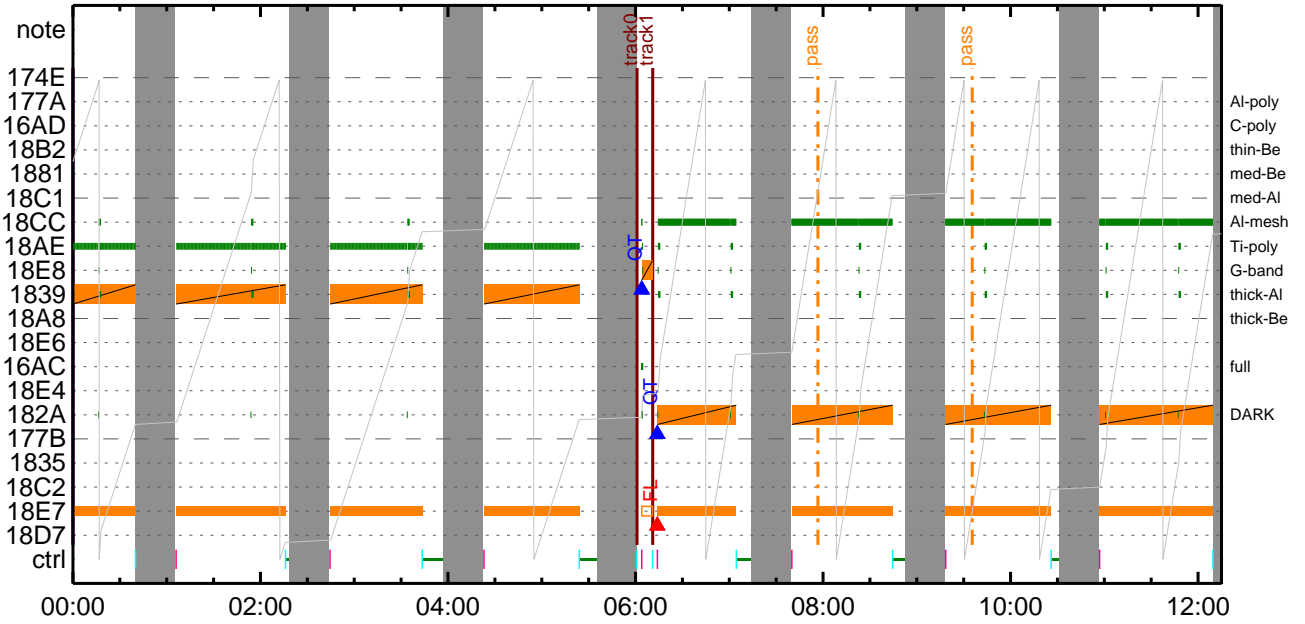
### CMDI #0604 2012/05/06



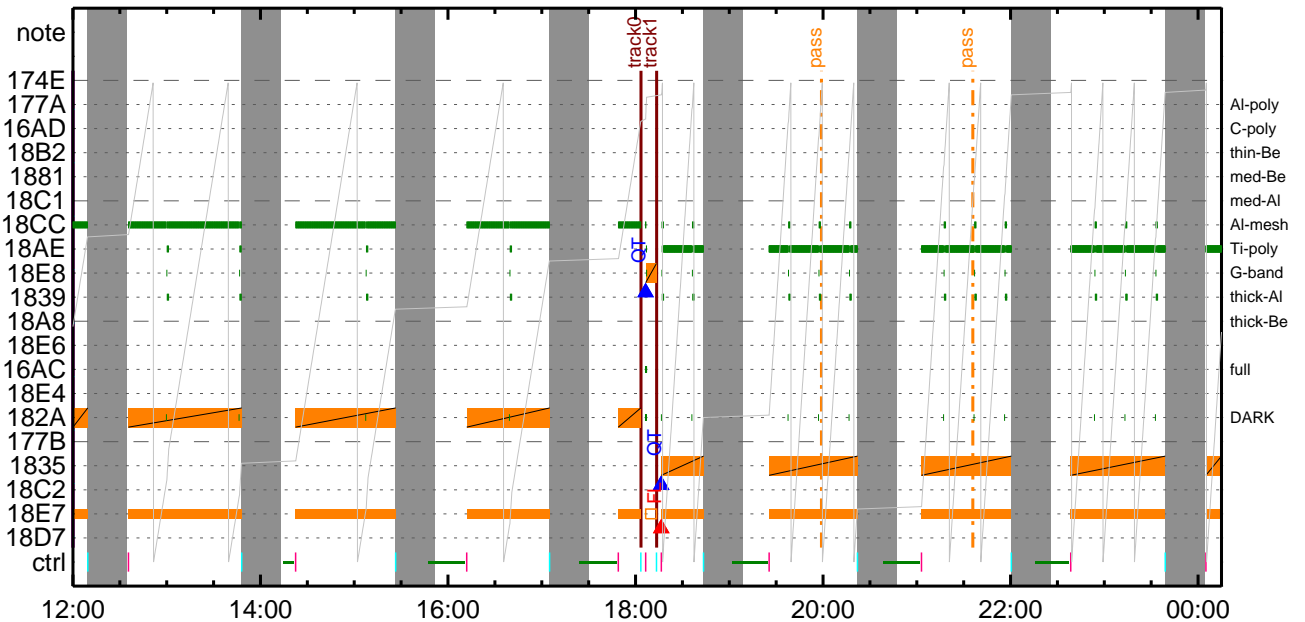
CMDI #0604 2012/05/06



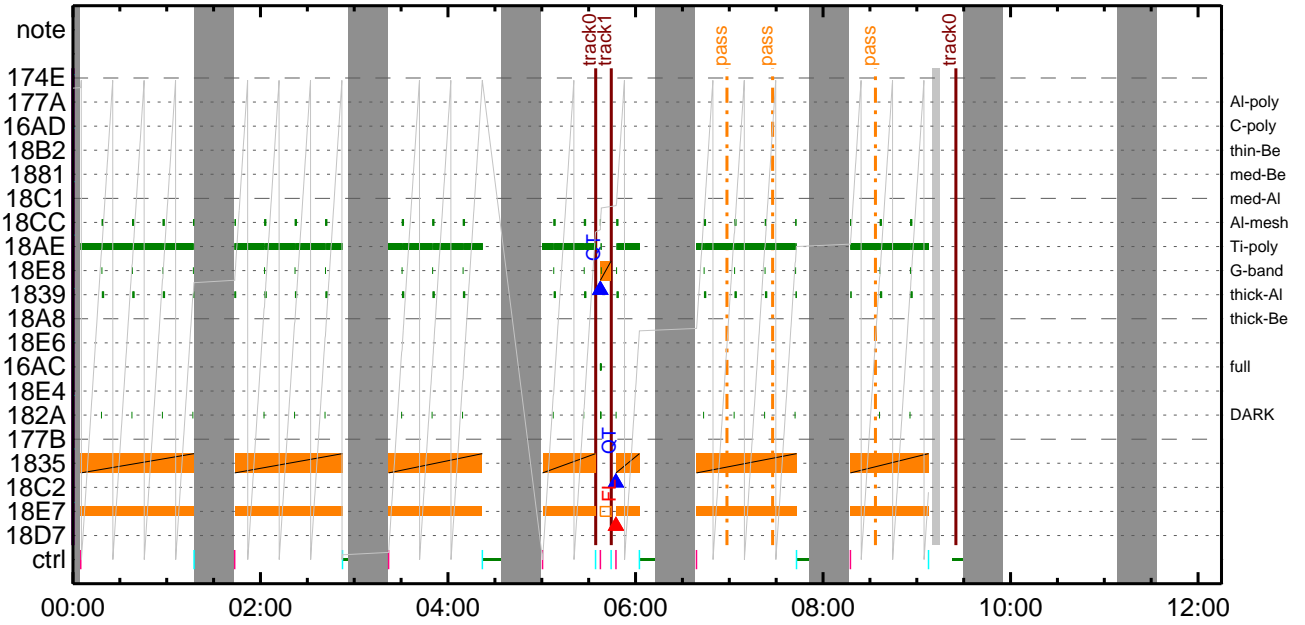
CMDI #0604 2012/05/07



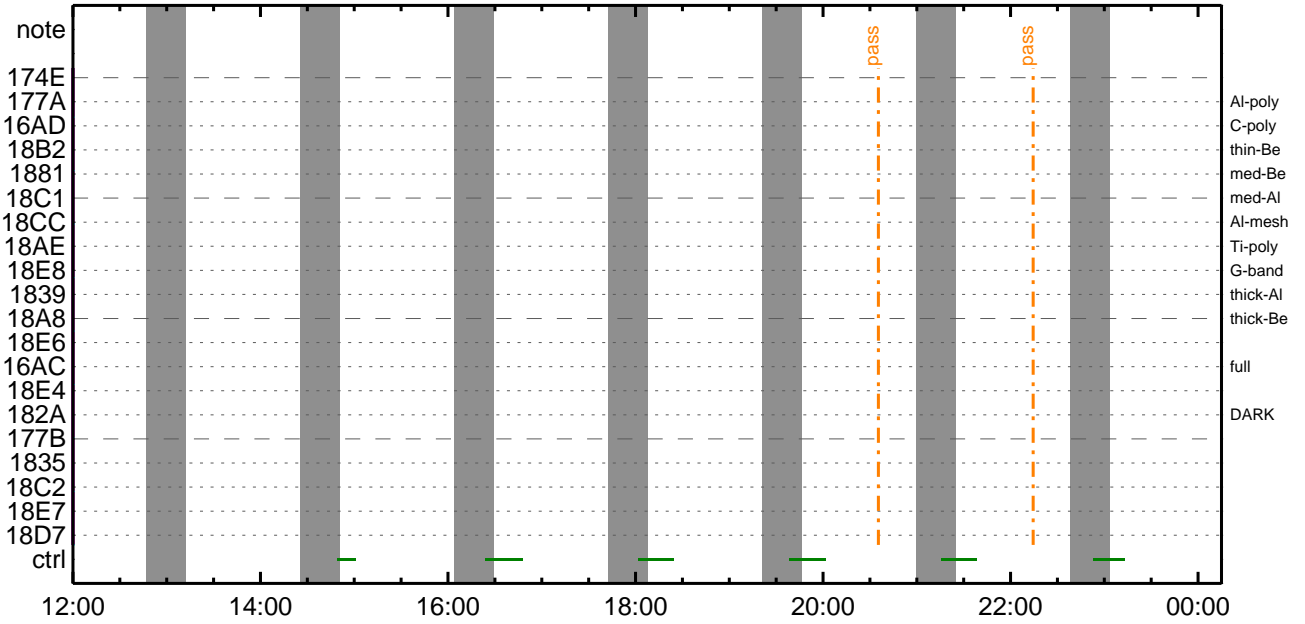
CMDI #0604 2012/05/07



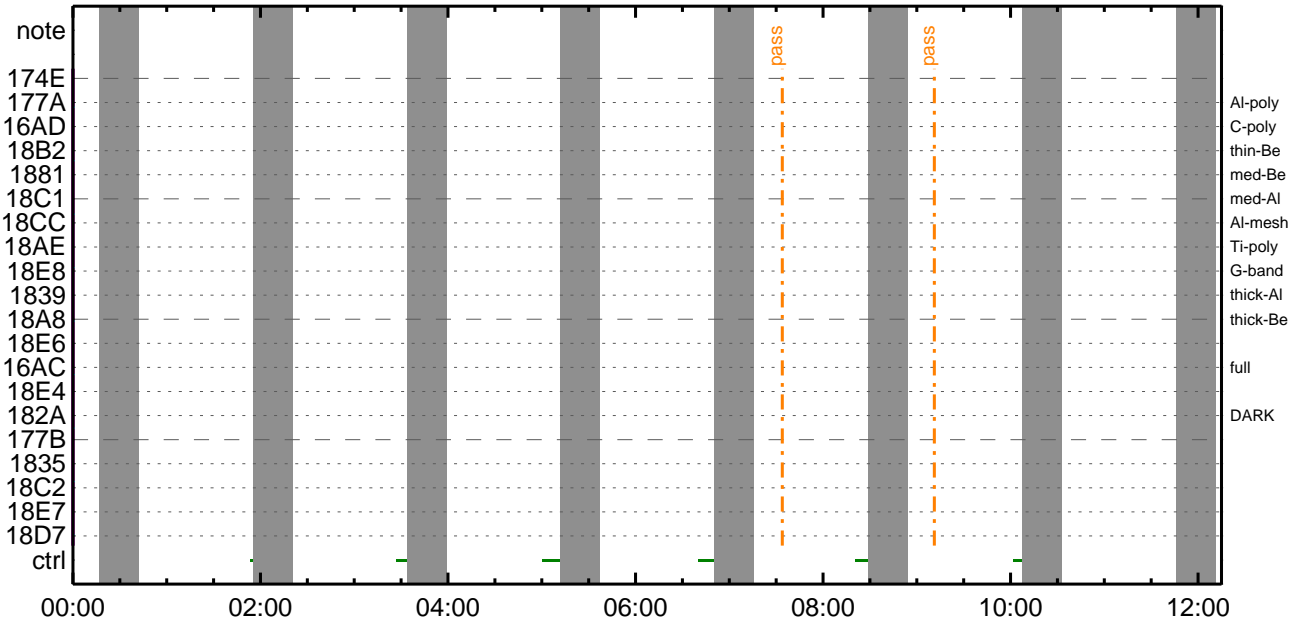
CMDI #0604 2012/05/08



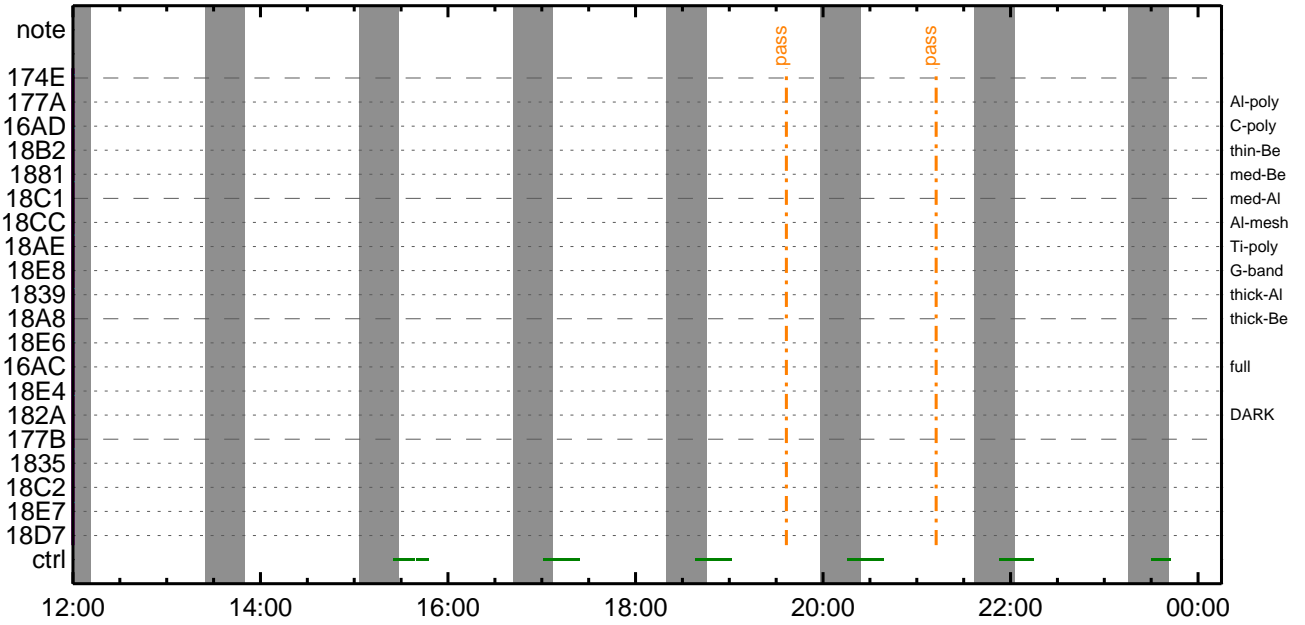
CMDI #0604 2012/05/08



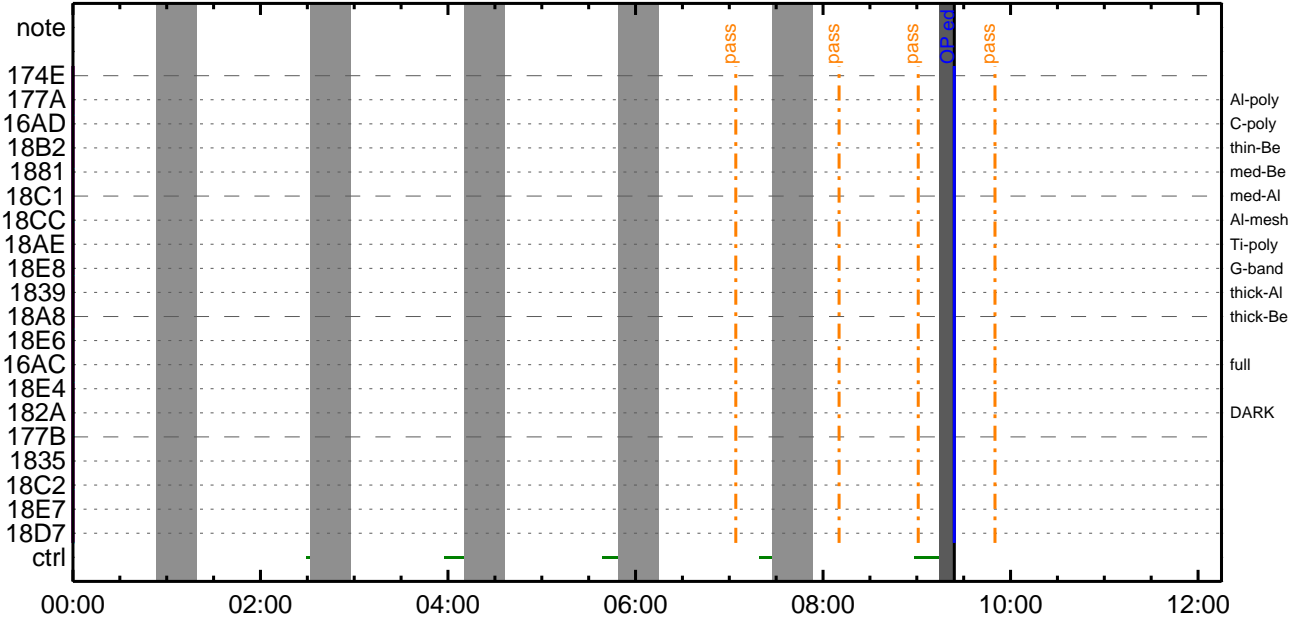
CMDI #0604 2012/05/09



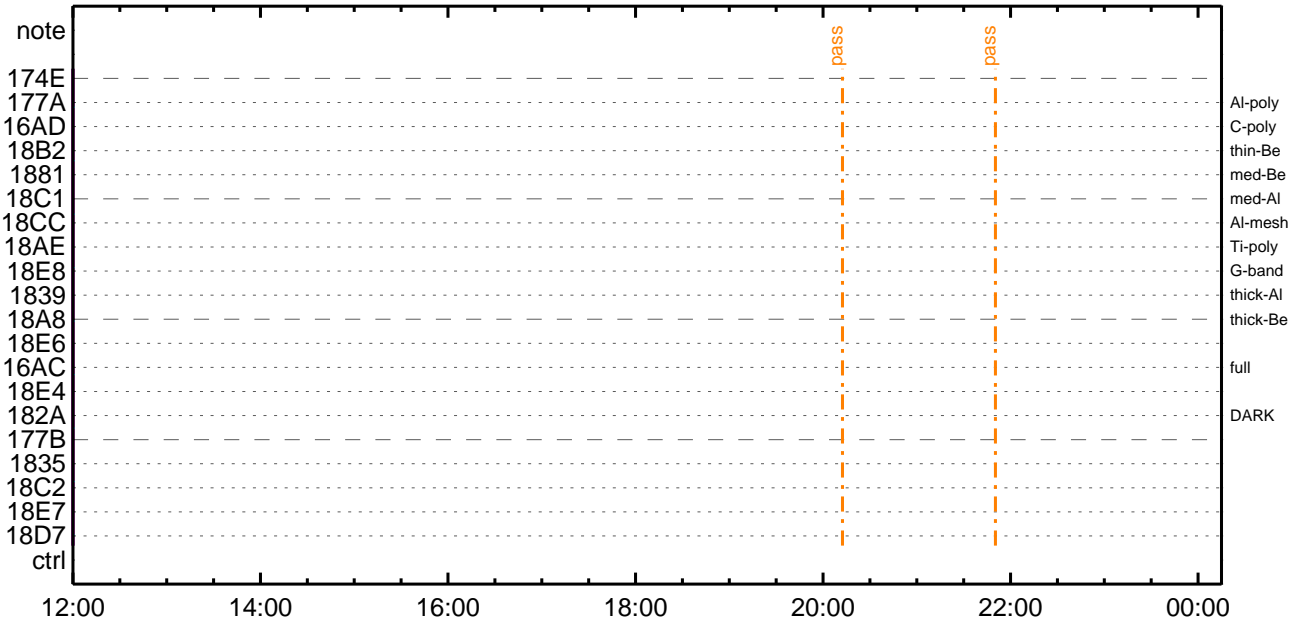
CMDI #0604 2012/05/09



CMDI #0604 2012/05/10



CMDI #0604 2012/05/10







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0096 C.                SET EDUMP I±°iYÑY¹aÇ¹Òa|a³aE;f
0097 C.
0098 C. TIY³YF¥ÖYÉaòdÁDİ¿(UT)
0099 +. TI 2012-05-05 09:29:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0102 C.
0103 +. TI 2012-05-05 09:29:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0106 C.
0107 +. TI 2012-05-05 09:29:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0110 C.
0111 +. TI 2012-05-05 09:33:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0114 C.
0115 C. °E²¼aİÄè%îÍÑaİYÁY§YÁY-¹àìÛ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0117 C.                çç[HK1_TI_CMD_NUM]         EQ      4
0118 C.                çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0120 C.
0121 C. *****
0122 C. TIİİ°èYÀYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]       EQ      07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]       EQ      2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]        EQ      3
0131 C.                çç[HK1_DMP_REPEAT_NUM]       EQ      0
0132 C.                çç[HK1_DMA_DMP_PIM]         EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]          EQ      7
0136 C.                çç[HK1_PKT_GEN_TIME]         EQ      0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]       EQ      32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]      EQ      4M
0139 C.                çç[HK1_DMP_CHK_FLG]         EQ      EXEC
0140 C.
0141 C. YÀYÖY×½ªİ»òð³İÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]         EQ      NON
0143 C.
0144 C. RAM ID=TI_TBLaİ%È¹Ç•è²İOKòð³İÇ§
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y½,¥ì;¼YÈ;Èòðİá¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]          EQ      2
0150 C.                çç[HK1_PKT_GEN_TIME]         EQ      0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]       EQ      32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]      EQ      4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2012-05-05 09:33:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2012-05-05 09:33:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 C.                [ ] [HK1_TI_CMD_NUM]         EQ      2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2012-05-05 09:33:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC (c3)
0176 C.                [ ] [HK1_TI_CMD_NUM]         EQ      1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP ´úÄîaİ»ö¼YªEÄa¹aèDCBC•x²è *****
0181 C. (%á°îYÖYÁYÉY¥YÉYÁYÇYèªE¼aª¼Á»Û¹aè)
0182 C. S. DC-BC dcbc-402:DCBC
0183 C. (MDP_known_event)
0184 C.
0185 C.
0186 C. ***** YD¥¹•İ Daily±¿İÑaÈ¹Ø¹aèDCBC•x²è *****
0187 C. S. DC-BC dcbc-153:DCBC
0188 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 C. ;ãLOS¥ÁY§YÁY-¼Á»Û;ä
0192 C.
0193 C. ***** LOS *****

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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 54s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2012-05-05 09:33:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'ûÀîðî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0153 C. (¼á°îÿÓÿÀÿËÿPÿËÿÁÿçÿè¼¾¼¼Ä»Û¹ñé)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ÿÐÿ¹•î Daily±çîÑñË´Ø¹ñèDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSÿÁÿSÿËÿÿ-¼Ä»Û;ä
0164 C.
0165 . C. ***** LOS *****
0166 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-856 2012-05-05 11:36:08 94 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÄY~¼Á»Û;ã
0005 C.
0006 C. YÄY$;¼Y³YFYÖYÉÁ+¿@
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁíÈø¿øÄø•μ°È×ÁÇøíYøY×Yí;¼YÉjÈÈè%μ•ííÈjÈøÈ¼°ÇÓø•ø¿¼í¹çøí;çÁ@,ùø¹øÈøÈøÇÁ+¿@ø•øÈøøøøøøÈj;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 80 80 20 20)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 06 06)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0f 80 80 06 06)
0052 + DC 07-F0 MDP_XRT_FLD_ENA
0053 BC (d8)
0054 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0055 BC (c8)
0056 + DC 07-F0 MDP_XRT_AEC_RESET
0057 BC (d0)
0058 + DC 07-F0 MDP_XRT_ARS_DIS
0059 BC (d5)
0060 + DC 07-F0 MDP_XRT_FLD_RESET
0061 BC (da)
0062 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0063 BC (c4 10)
0064 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0065 BC (c5 02)
0066 . C. ----- Success Verify ? OK / NG ____
0067 C.
0068 C.
0069 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0070 C.
0071 +. DC 07-F0 MDP_XRT_MODE_OBSV
0072 BC (c2)
0073 +. TI 2012-05-05 09:33:02.0
0074 DC 07-F0 MDP_XRT_MODE_OBSV
0075 BC (c2)
0076 . C. ----- Success Verify ? OK / NG ____
0077 C.
0078 C. ***** XRT END *****
0079 C.
0080 . C. ***** MDP `úÁíøí»ò¼YøÈÄø¹øÈDCBC•x²è *****
0081 C. (¼á°íYÖYÁYÈYbYÈYáYçYèøÈ¼øø¼Á»Ûø¹øè)
0082 . S. DC-BC dcbc-402:DCBC
0083 (MDP_known_event)
0084 C.
0085 C.
0086 . C. ***** YDÿ!•Ī Daily±¿ĪñøÈ´øø¹øÈDCBC•x²è *****
0087 . S. DC-BC dcbc-153:DCBC
0088 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089 C.
0090 C.
0091 . C. ;ãLOSÁY$YÄY~¼Á»Û;ã
0092 C.
0093 . C. ***** LOS *****
0094 C.

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\*\*\* OP Sequence for XRT \*\*\*

2012/05/05	09:40:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/05	09:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
2012/05/05	09:43:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/05/05	09:44:00.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/05/05	09:46:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 01 99 4f ce			
2012/05/05	09:46:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/05/05	09:46:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/05/05	09:46:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/05/05	09:46:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/05/05	09:46:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/05/05	09:46:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/05/05	09:46:58.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10			
2012/05/05	09:47:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2012/05/05	10:01:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/05/05	10:01:30.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/05/05	10:03:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 01 99 3e 1c			
2012/05/05	10:04:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/05/05	10:04:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/05/05	10:04:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/05/05	10:04:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/05/05	10:04:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/05/05	10:04:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/05/05	10:04:28.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10			
2012/05/05	10:04:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2012/05/05	10:11:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/05/05	10:11:30.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/05/05	10:13:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 01 99 2c 6a			
2012/05/05	10:14:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/05/05	10:14:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/05/05	10:14:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/05/05	10:14:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/05/05	10:14:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/05/05	10:14:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/05/05	10:14:28.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10			
2012/05/05	10:14:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2012/05/05	10:21:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/05/05	10:21:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/05/05	10:23:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 01 99 1a b8			
2012/05/05	10:24:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/05/05	10:24:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/05/05	10:24:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/05/05	10:24:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/05/05	10:24:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/05/05	10:24:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/05/05	10:24:28.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10			
2012/05/05	10:24:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2012/05/05			MDP_XRT_CTRL_AUTO	1	07-F0	c0			

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2012/05/05	10:31:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	10:31:30.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 01 99 09 06	
2012/05/05	10:33:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/05	10:34:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/05	10:34:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/05	10:34:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/05	10:34:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/05	10:34:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	10:34:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/05	10:34:28.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2012/05/05	10:34:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	10:41:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	10:41:30.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 01 99 f7 5d	
2012/05/05	10:43:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/05	10:44:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/05	10:44:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/05	10:44:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/05	10:44:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/05	10:44:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	10:44:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/05	10:44:28.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2012/05/05	10:44:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	10:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	10:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	10:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/05	10:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/05	11:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	11:20:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 01 99 e5 aa	
2012/05/05	11:22:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/05	11:22:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/05	11:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/05	11:22:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/05	11:22:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/05	11:22:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	11:22:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/05	11:22:58.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2012/05/05	11:23:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	11:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	11:30:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 01 99 d3 f8	
2012/05/05	11:32:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/05	11:32:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/05	11:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/05	11:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/05	11:32:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/05	11:32:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	11:32:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/05	11:32:58.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	

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2012/05/05	11:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	11:39:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	11:40:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 01 99	c2 46
2012/05/05	11:42:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/05/05	11:42:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/05	11:42:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/05	11:42:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/05	11:42:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/05	11:42:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	11:42:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10
2012/05/05	11:42:58.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2012/05/05	11:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	11:49:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	11:50:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00 01 99	b0 94
2012/05/05	11:52:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/05/05	11:52:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/05	11:52:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/05	11:52:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/05	11:52:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/05	11:52:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	11:52:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10
2012/05/05	11:52:58.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2012/05/05	11:53:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	11:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	12:00:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00 ea a0	51 37
2012/05/05	12:02:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/05/05	12:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/05	12:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/05	12:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/05	12:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/05	12:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	12:02:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10
2012/05/05	12:02:58.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2012/05/05	12:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	12:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	12:10:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 e8 42	41 db
2012/05/05	12:12:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/05/05	12:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/05	12:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/05	12:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/05	12:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/05	12:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	12:12:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10
2012/05/05	12:12:58.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2012/05/05	12:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	12:20:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00 e7 23	31 81



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2012/05/05	12:33:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/05	12:33:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/05	12:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/05	12:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/05	12:58:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/05	12:58:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00 e6 7f 20 dd				
2012/05/05	13:00:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/05	13:01:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/05	13:01:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/05	13:01:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/05	13:01:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/05	13:01:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/05	13:01:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2012/05/05	13:01:28.0	XRT_FL_PROG_SET_404_OG [0x194]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2012/05/05	13:01:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/05	13:08:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/05	13:08:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 e6 25 0f e7				
2012/05/05	13:10:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/05	13:11:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/05	13:11:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/05	13:11:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/05	13:11:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/05	13:11:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/05	13:11:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2012/05/05	13:11:28.0	XRT_FL_PROG_SET_404_OG [0x194]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2012/05/05	13:11:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/05	13:18:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/05	13:18:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00 e6 0d fe 90				
2012/05/05	13:20:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/05	13:21:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/05	13:21:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/05	13:21:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/05	13:21:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/05	13:21:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/05	13:21:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2012/05/05	13:21:28.0	XRT_FL_PROG_SET_404_OG [0x194]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2012/05/05	13:21:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/05	13:28:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/05	13:28:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00 e6 25 f2 67				
2012/05/05	13:30:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/05	13:31:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/05	13:31:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/05	13:31:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/05	13:31:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/05	13:31:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/05	13:31:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2012/05/05	13:31:28.0	XRT_FL_PROG_SET_404_OG [0x194]							

2012/05/05	13:31:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	13:38:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	13:38:30.0	AOCS_Ore-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 e6 7f e1 71	
2012/05/05	13:40:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/05	13:41:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/05	13:41:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/05	13:41:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/05	13:41:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/05	13:41:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	13:41:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/05	13:41:28.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2012/05/05	13:41:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	13:48:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	13:48:30.0	AOCS_Ore-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00 e7 23 d0 d5	
2012/05/05	13:50:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/05	13:51:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/05	13:51:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/05	13:51:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/05	13:51:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/05	13:51:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	13:51:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/05	13:51:28.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2012/05/05	13:51:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	13:58:30.0	AOCS_Ore-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00 e8 42 c0 7b	
2012/05/05	14:12:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	14:12:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	14:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/05	14:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/05	14:47:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	14:47:30.0	AOCS_Ore-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 ec 3a af f0	
2012/05/05	14:49:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/05	14:50:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/05	14:50:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/05	14:50:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/05	14:50:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/05	14:50:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	14:50:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/05	14:50:28.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2012/05/05	14:50:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	14:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	15:00:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	01 00 00 00 00	
2012/05/05	15:02:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/05	15:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/05	15:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/05	15:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/05	15:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	

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2012/05/05	15:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	15:02:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2012/05/05	15:02:58.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2012/05/05	15:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	15:50:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	15:50:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	15:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/05	15:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/05	16:34:00.0	XRT_Custom_418_OG [0x1a2]					
2012/05/05	16:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	17:29:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	17:29:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	17:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/05	17:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/05	18:10:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	18:10:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2012/05/05	18:11:00.5	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00	00 00 00 00
2012/05/05	18:11:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/05/05	18:11:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/05/05	18:11:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/05	18:13:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2012/05/05	18:14:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	18:20:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	18:21:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	01	00 00 00 00
2012/05/05	18:23:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2012/05/05	18:23:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/05	18:23:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/05	18:23:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/05	18:23:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/05	18:23:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	18:23:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2012/05/05	18:23:58.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2012/05/05	18:24:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	19:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	19:07:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	19:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/05	19:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/05	19:47:30.0	XRT_Custom_418_OG [0x1a2]					
2012/05/05	19:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	20:46:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	20:46:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	20:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/05	20:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/05	21:25:00.0	XRT_Custom_418_OG [0x1a2]					
2012/05/05	21:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/05	22:24:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/05	22:24:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/05	22:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2012/05/05	22:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/05	23:00:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/05	23:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	00:02:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	00:02:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/06	00:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/06	00:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/06	00:27:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/06	00:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	01:40:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	01:40:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/06	01:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/06	01:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/06	02:06:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/06	02:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	03:15:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	03:15:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/06	03:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/06	03:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/06	03:44:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/06	03:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	04:46:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	04:46:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/06	04:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/06	04:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/06	05:23:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/06	05:24:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	05:58:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	05:58:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/06	05:58:30.0	AOCS_Orе-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/05/06	05:58:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/05/06	05:58:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/05/06	05:58:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/06	06:01:28.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2012/05/06	06:01:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	06:08:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	06:08:30.0	AOCS_Orе-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/05/06	06:10:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/06	06:11:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/06	06:11:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/06	06:11:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/06	06:11:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/06	06:11:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/06	06:11:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2012/05/06	06:11:28.0	XRT_FL_PROG_SET_404_OG [0x194]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2012/05/06	06:11:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	06:26:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	06:26:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/06	06:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2012/05/06	06:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/06	07:01:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/06	07:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	08:06:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	08:06:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/06	08:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/06	08:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/06	08:40:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/06	08:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	09:47:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	09:47:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/06	09:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/06	09:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/06	10:18:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/06	10:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	11:32:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	11:32:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/06	11:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/06	11:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/06	11:57:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/06	11:58:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	13:10:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	13:10:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/06	13:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/06	13:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/06	13:35:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/06	13:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	14:49:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	14:49:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/06	14:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/06	14:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/06	15:25:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/06	15:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	15:30:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	15:30:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/06	15:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/06	15:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/06	15:34:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/06	15:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	16:27:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	16:27:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/06	16:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/06	16:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/06	17:11:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/06	17:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/06	17:55:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/06	17:55:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/06	17:56:00.0	AOCS_Or-e-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/05/06	17:56:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/05/06	17:56:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/05/06	17:56:20.0	XRT_ARS_DIS_438_OG [0x1b6]							

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2012/05/06	17:58:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/05/06	17:59:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2012/05/06	18:06:00.0	AOCs_Or-point_Start_22_OG [0x0ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/06	18:06:00.5	XRT_CTRL_MANU_408_OG [0x198]	AOCU_NM	5	02-76	01 00 00 00 00
2012/05/06	18:06:02.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/06	18:06:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/06	18:09:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/06	18:47:40.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/06	18:50:12.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/06	18:50:32.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/05/06	18:50:34.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/05/06	18:50:36.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/05/06	18:50:38.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/05/06	18:50:40.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/05/06	18:50:42.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/06	18:50:44.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2012/05/06	18:51:28.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2012/05/06	19:44:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/06	19:44:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/06	19:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/06	19:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/06	20:25:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/06	20:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2012/05/06	21:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2012/05/06	21:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/06	21:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/06	21:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/06	22:01:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/06	22:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/06	23:01:30.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_Custom_418_OG [0x1a2]			
2012/05/06	23:01:32.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2012/05/06	23:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/06	23:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/06	23:31:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/06	23:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/07	00:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/07	00:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_Custom_418_OG [0x1a2]			
2012/05/07	00:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2012/05/07	00:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/07	01:05:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/07	01:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/07	02:16:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/07	02:16:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/07	02:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	XRT_Custom_418_OG [0x1a2]			
2012/05/07	02:16:04.0	XRT_PREFLR_STOP_424_OG [0x1a8]	XRT_CTRL_AUTO_419_OG [0x1a3]			
2012/05/07	02:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/07	02:43:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/07	02:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/07	03:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/07	03:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2012/05/07	03:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/07	03:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/07	04:22:00.0	XRT_Custom_418_OG [0x1a2]								
2012/05/07	04:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	05:24:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	05:24:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	05:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/07	05:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/07	06:00:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	06:00:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/05/07	06:01:00.5	AOCS_Or-e-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00	00	00	00	00
2012/05/07	06:01:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/05/07	06:01:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/05/07	06:01:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/07	06:03:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2012/05/07	06:04:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	06:10:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	06:11:00.0	AOCS_Or-e-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	01	00	00	00	00
2012/05/07	06:13:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/05/07	06:13:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/07	06:13:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/07	06:13:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/07	06:13:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/07	06:13:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	06:13:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2012/05/07	06:13:58.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2012/05/07	06:14:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	07:04:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	07:04:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	07:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/07	07:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/07	07:39:00.0	XRT_Custom_418_OG [0x1a2]								
2012/05/07	07:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	08:44:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	08:44:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	08:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/07	08:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/07	09:17:30.5	XRT_Custom_418_OG [0x1a2]								
2012/05/07	09:18:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	10:26:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	10:26:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	10:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/07	10:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/07	10:56:00.5	XRT_Custom_418_OG [0x1a2]								
2012/05/07	10:57:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	12:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	12:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	12:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2012/05/07	12:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/07	12:34:30.5	XRT_Custom_418_OG [0x1a2]							
2012/05/07	12:35:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	13:48:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	13:48:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	13:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/07	13:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/07	14:21:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/07	14:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	15:26:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	15:26:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	15:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/07	15:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/07	16:11:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/07	16:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	17:05:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	17:05:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	17:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/07	17:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/07	17:48:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/07	17:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	18:03:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	18:03:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/07	18:03:30.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/05/07	18:03:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/05/07	18:03:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/05/07	18:03:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/07	18:06:28.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2012/05/07	18:06:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	18:13:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	18:13:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/05/07	18:15:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/07	18:16:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/07	18:16:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/07	18:16:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/07	18:16:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/07	18:16:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	18:16:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2012/05/07	18:16:28.0	XRT_FL_PROG_SET_404_OG [0x194]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2012/05/07	18:16:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	18:43:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	18:43:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	18:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/07	18:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/07	19:24:30.5	XRT_Custom_418_OG [0x1a2]							
2012/05/07	19:25:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	20:22:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	20:22:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	20:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				



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2012/05/07	20:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/07	21:02:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/07	21:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	22:00:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	22:00:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	22:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/07	22:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/07	22:37:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/07	22:38:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/07	23:39:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/07	23:39:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/07	23:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/07	23:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/08	00:04:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/08	00:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/08	01:17:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/08	01:17:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/08	01:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/08	01:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/08	01:42:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/08	01:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/08	02:52:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/08	02:52:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/08	02:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/08	02:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/08	03:21:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/08	03:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/08	04:22:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/08	04:22:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/08	04:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/08	04:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/08	04:59:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/08	05:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/08	05:34:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/08	05:34:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/08	05:34:30.0	AOCs_OrE-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/05/08	05:34:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/05/08	05:34:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/05/08	05:34:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/08	05:37:28.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2012/05/08	05:37:30.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/08	05:44:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/08	05:44:30.0	AOCs_OrE-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/05/08	05:46:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/08	05:47:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/08	05:47:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/08	05:47:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/08	05:47:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/08	05:47:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/08	05:47:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]							

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2012/05/08	05:47:28.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2012/05/08	05:47:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/08	06:02:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/08	06:02:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/08	06:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/08	06:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/08	06:38:00.0	XRT_Custom_418_OG [0x1a2]					
2012/05/08	06:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/08	07:43:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/08	07:43:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/08	07:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/08	07:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/08	08:16:30.0	XRT_Custom_418_OG [0x1a2]					
2012/05/08	08:17:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/08	09:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/08	09:25:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00	00 00 00 00 00