XRT Timeline to be uploaded on 2012/05/08

Period: 2012/05/08 09:15:00 - 2012/05/12 10:39:00

* * * * * **Normal mode** * * * * *

XOB #183E: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad

Term	Pointing (x, y)	Comment
05/08 09:59:06 - 05/08 17:42:30	Track (-652.6, 206.3) @ 05/08 09:25:00	# OP start + 10min, AR11476
05/08 18:37:30 - 05/09 05:00:00	Track (-590.6, 209.1) @ 05/08 18:34:30	# AR11476
05/09 06:09:30 - 05/09 17:59:54	Track (-506.0, 211.9) @ 05/09 06:06:30	# AR11476

PROG= 19 Inf.-time(s)

Subr= 2 1-time(s) 2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 41 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 1 1-time(s) 2.0sec

Seqn= 16 30-time(s) 80.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	20.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	20.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	20.0sec

XOB #18E8: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)

Term	Pointing (x, y)	Comment
05/08 18:31:30 - 05/08 18:34:24	Fixed (0.0, 0.0)	synoptic, shifted 24.5 min
05/09 05:59:30 - 05/09 06:06:24	Fixed (0.0, 0.0)	synoptic, shifted -3.5 min
05/09 18:03:00 - 05/09 18:09:54	Fixed (0.0, 0.0)	synoptic
05/10 06:20:30 - 05/10 06:27:00	Fixed (0.0, 0.0)	synoptic, shifted 17.5 min

PROG= 12 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 7 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 8 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 4 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1835: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 40sec-cad

Term	Pointing (x, y)	Comment
05/09 18:13:00 - 05/10 02:16:54	Track (-410.8, 214.2) @ 05/09 18:10:00	# AR11476
05/10 07:00:30 - 05/10 08:58:30	Track (-303.7, 215.8) @ 05/10 06:57:30	# AR11476

PROG= 04 Inf.-time(s)

Subr= 2 1-time(s) 2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 41 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 1 1-time(s) 2.0sec

Seqn= 52 45-time(s) 24.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec

XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
05/10 02:32:00 - 05/10 04:16:54	Fixed (0.0, 945.0)	Co-alignment N pole

PROG= 08 1-time(s)

└ **Subr= 1 1-time(s) 360.0sec**
└ **Seqn= 21 24-time(s) 300.0sec**

Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
05/10 04:32:00 - 05/10 06:17:24	Fixed (-945.0, 0.0)	Co-alignment E limb

PROG= 18 1-time(s)
└ **Subr= 1 1-time(s) 360.0sec**
└ **Seqn= 22 15-time(s) 480.0sec**

Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly

Term	Pointing (x, y)	Comment
05/10 06:30:06 - 05/10 06:57:24	Fixed (0.0, 0.0)	synoptic, shifted 17.5 min

PROG= 16 Inf.-time(s)
└ **Subr= 1 1-time(s) 120.0sec**
└ **Seqn= 57 1-time(s) 4.0sec**

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * Flare mode * * * * *

XOB #18E7: Flare obs. dynamics - Ti_poly high cadence + context (thick-Al-384x384)-15 loops

Term	Pointing (x, y)	Comment
05/08 09:59:06 - 05/08 17:42:30	Track (-652.6, 206.3) @ 05/08 09:25:00	# OP start + 10min, AR11476
05/08 18:37:30 - 05/09 05:00:00	Track (-590.6, 209.1) @ 05/08 18:34:30	# AR11476
05/09 06:09:30 - 05/09 17:59:54	Track (-506.0, 211.9) @ 05/09 06:06:30	# AR11476
05/09 18:13:00 - 05/10 02:16:54	Track (-410.8, 214.2) @ 05/09 18:10:00	# AR11476
05/10 06:30:06 - 05/10 06:57:24	Fixed (0.0, 0.0)	synoptic, shifted 17.5 min
05/10 07:00:30 - 05/10 08:58:30	Track (-303.7, 215.8) @ 05/10 06:57:30	# AR11476

PROG= 02 15-time(s)
├ **Subr= 1 45-time(s) 10.0sec**
└ **Seqn= 92 1-time(s) 2.0sec**

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec

└ **Subr= 2 1-time(s) 10.0sec**
├ **Seqn= 54 1-time(s) 2.0sec**

Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec

└ **Seqn= 79 1-time(s) 2.0sec**

Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * Active Region Search * * * * *

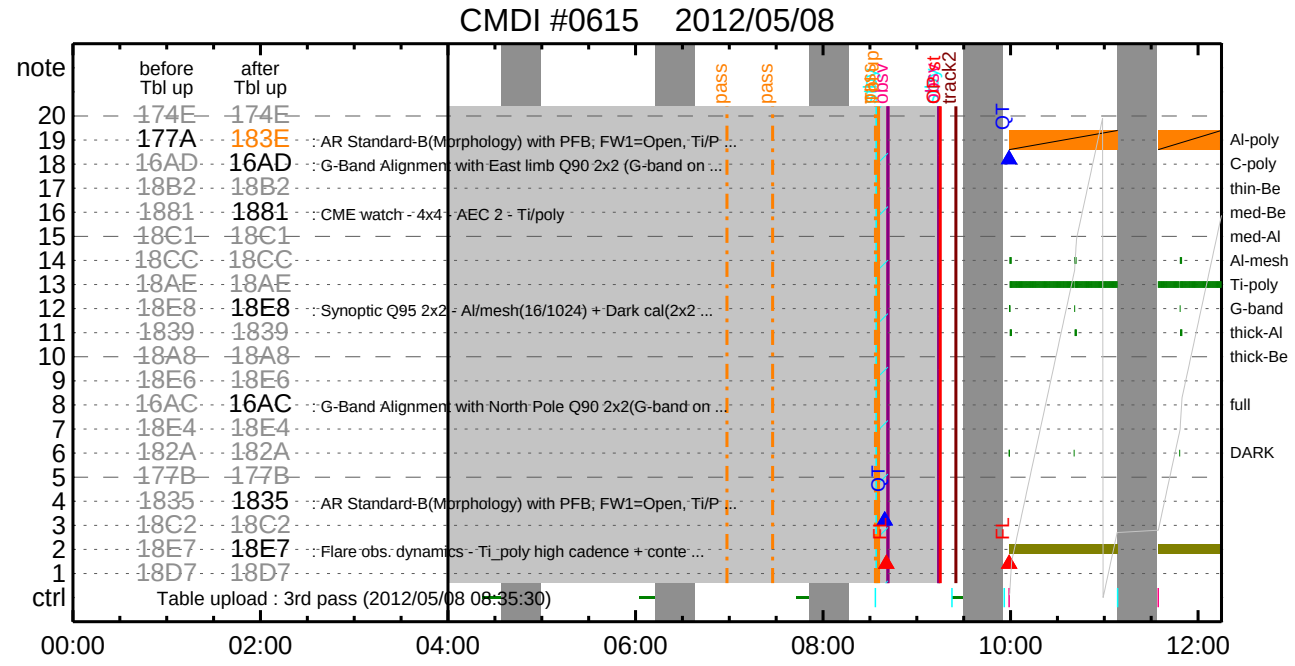
NOT USED

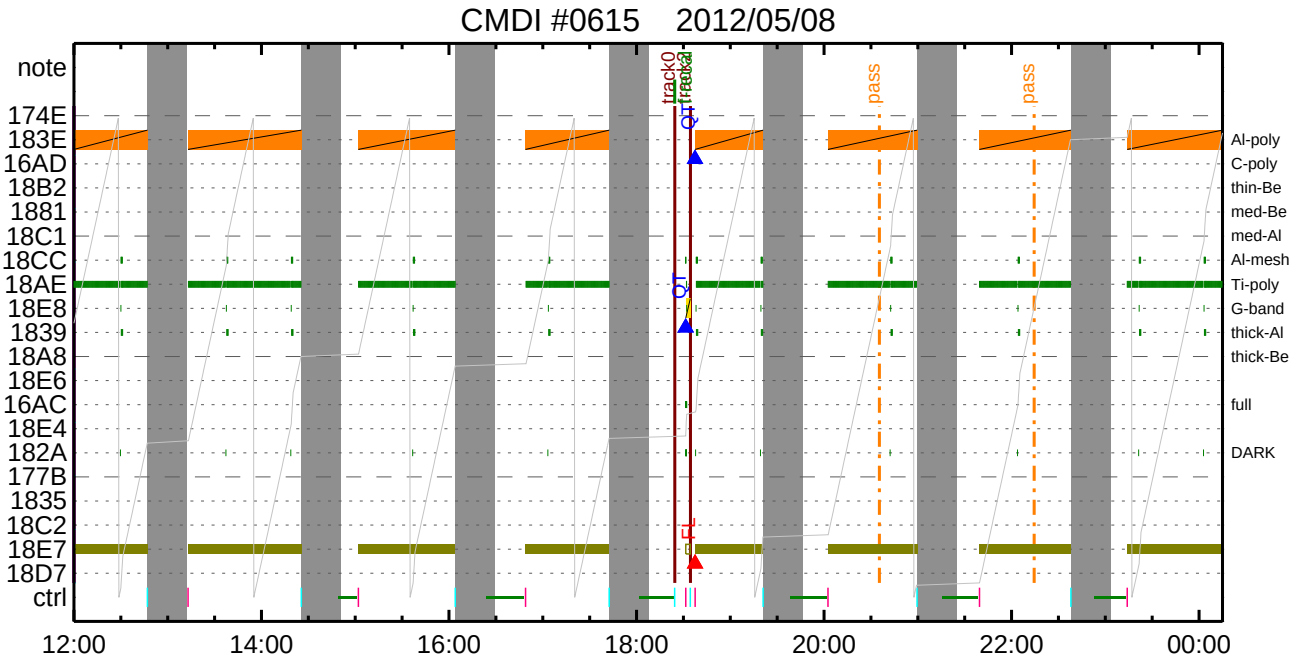
* * * * * Flare Detection * * * * *

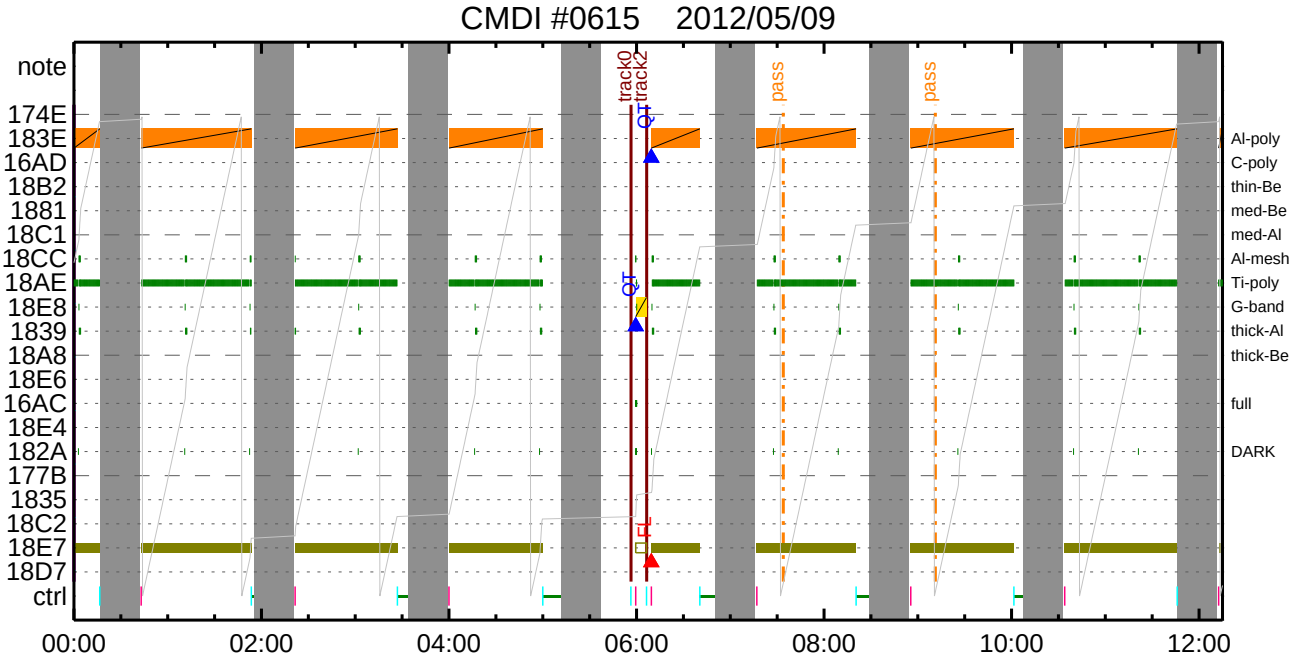
FLD Patrol

Term	Pointing (x, y)	Comment
05/08 18:37:16 - 05/09 05:56:46	Track (-590.6, 209.1) @ 05/08 18:34:30	# AR11476
05/09 06:09:16 - 05/09 18:00:16	Track (-506.0, 211.9) @ 05/09 06:06:30	# AR11476
05/09 18:12:46 - 05/10 02:17:18	Track (-410.8, 214.2) @ 05/09 18:10:00	# AR11476
05/10 06:29:52 - 05/12 10:39:00	Fixed (0.0, 0.0)	synoptic, shifted 17.5 min

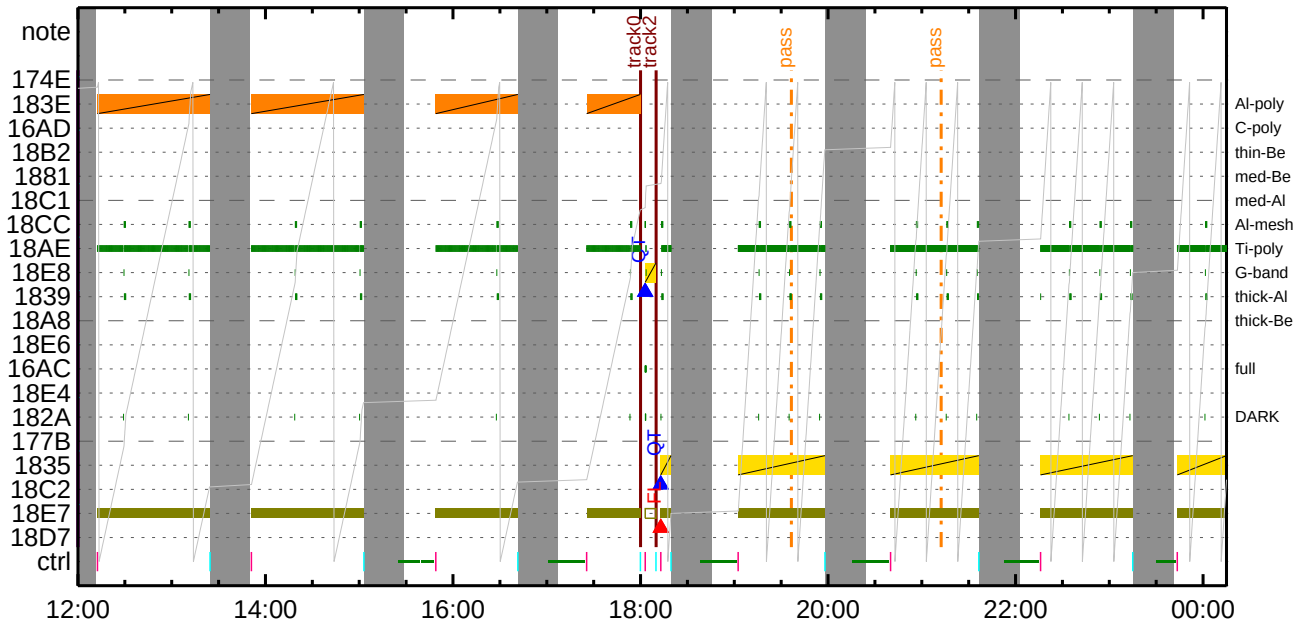
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval


CMDI #0615 2012/05/08
note
before Tbl up
after Tbl up
174E 174E
177A 183E : AR Standard-B(Morphology) with PFB, FW1=Open, Ti/P ...
16AD 16AD : G-Band Alignment with East limb Q90 2x2 (G-band on ...
18B2 18B2
1881 1881 : CME watch - 4x4 - AEC 2 - Ti/poly
18C1 18C1
18CC 18CC
18AE 18AE
18E8 18E8 : Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 ...
1839 1839
18A8 18A8
18E6 18E6
16AC 16AC : G-Band Alignment with North Pole Q90 2x2(G-band on ...
18E4 18E4
182A 182A
177B 177B
1835 1835 : AR Standard-B(Morphology) with PFB, FW1=Open, Ti/P ...
18C2 18C2
18E7 18E7 : Flare obs. dynamics - Ti_poly high cadence + conte ...
18D7 18D7
ctrl
Table upload : 3rd pass (2012/05/08 08:35:30)
pass
pass
track2
QT
QT
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

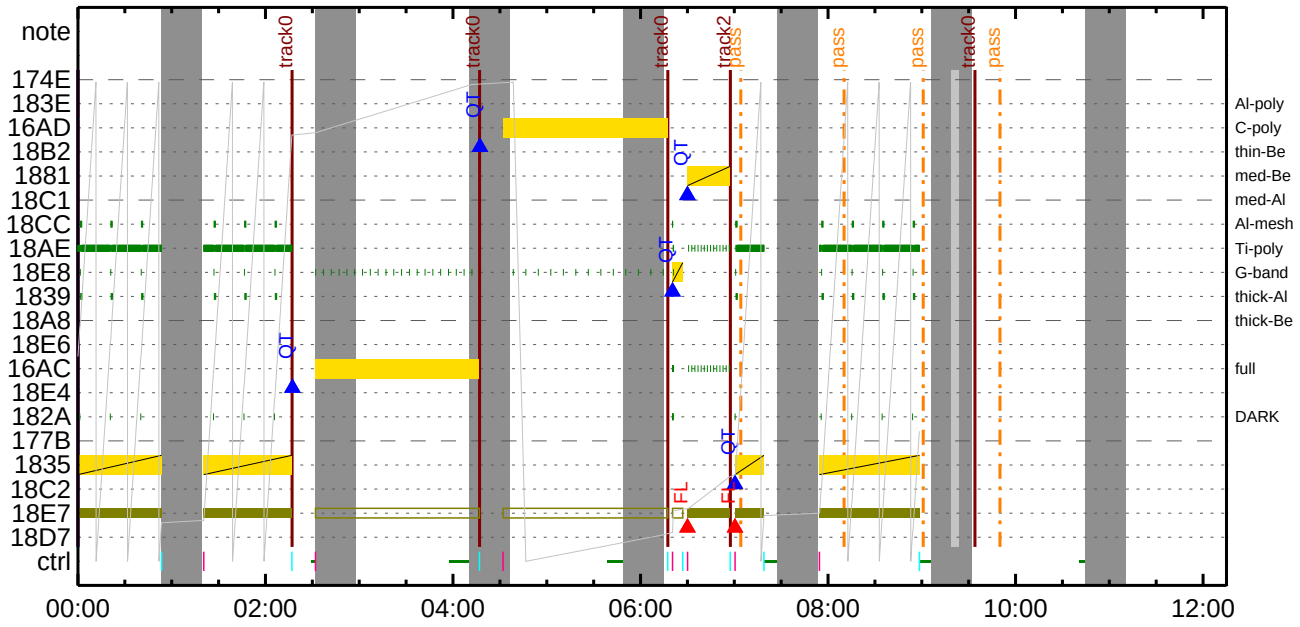

CMDI #0615 2012/05/08
note
174E
183E
16AD
18B2
1881
18C1
18CC
18AE
18E8
1839
18A8
18E6
16AC
18E4
182A
177B
1835
18C2
18E7
18D7
ctrl
track0
track2
QT
QT
FL
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0615 2012/05/09
note
174E
183E
16AD
18B2
1881
18C1
18CC
18AE
18E8
1839
18A8
18E6
16AC
18E4
182A
177B
1835
18C2
18E7
18D7
ctrl
track0
track2
QT
QT
FL
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

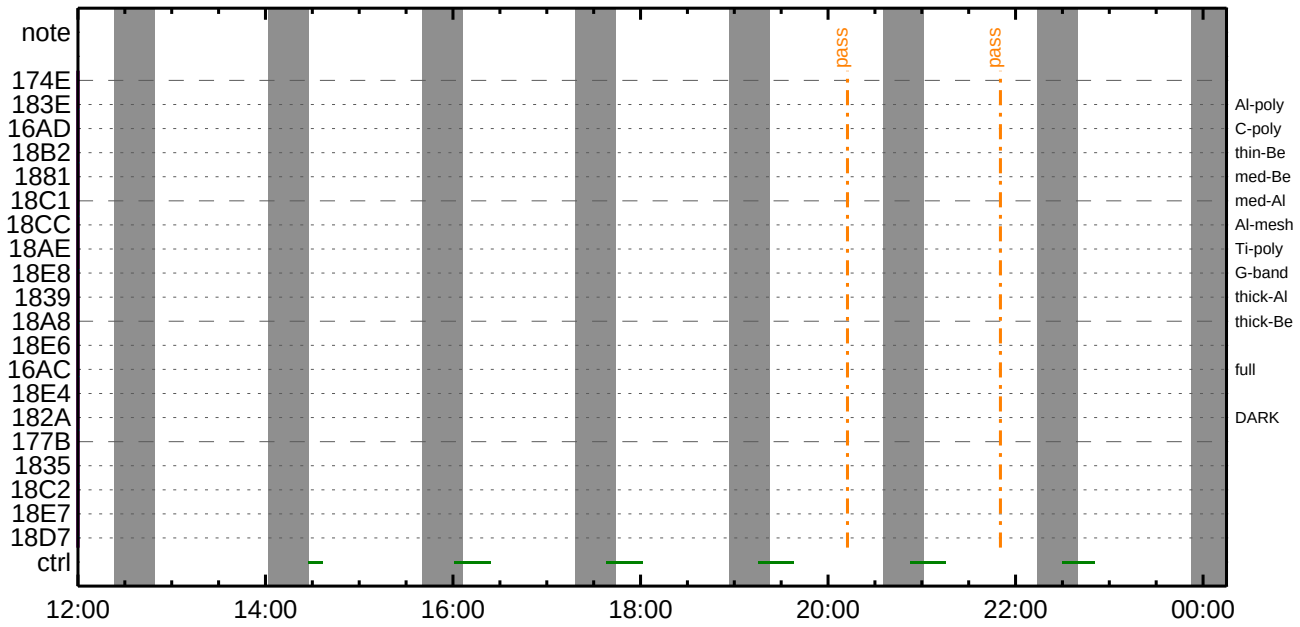
CMDI #0615 2012/05/09

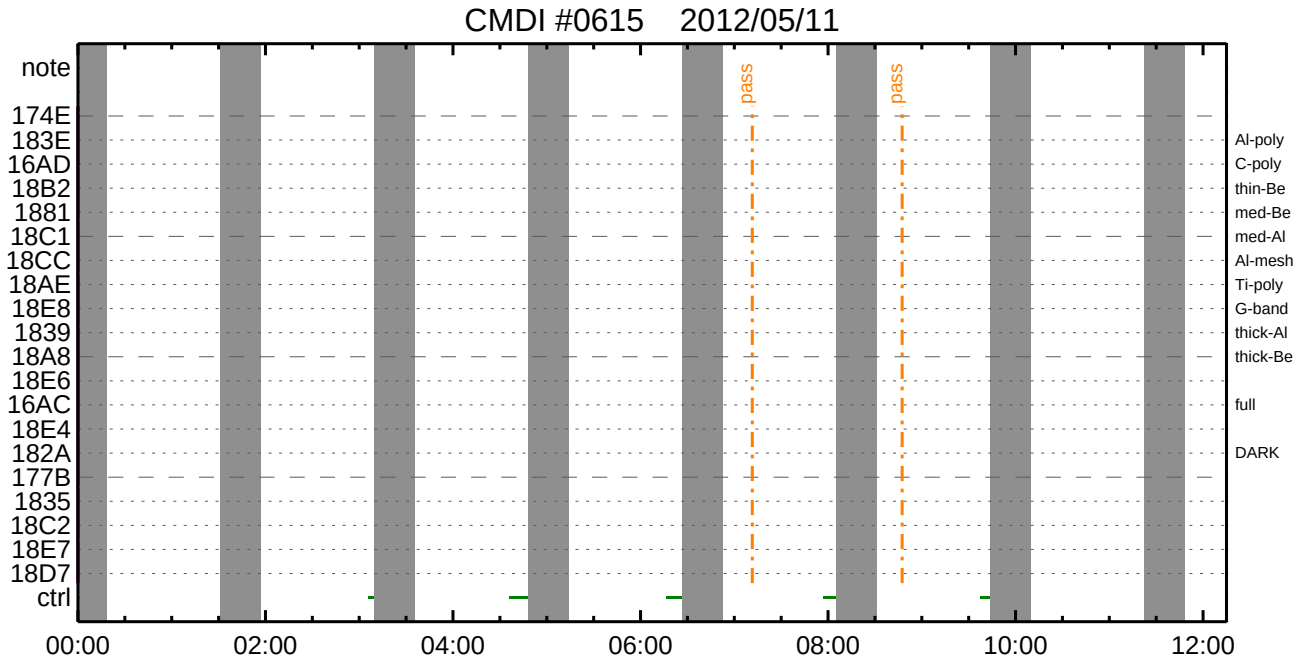


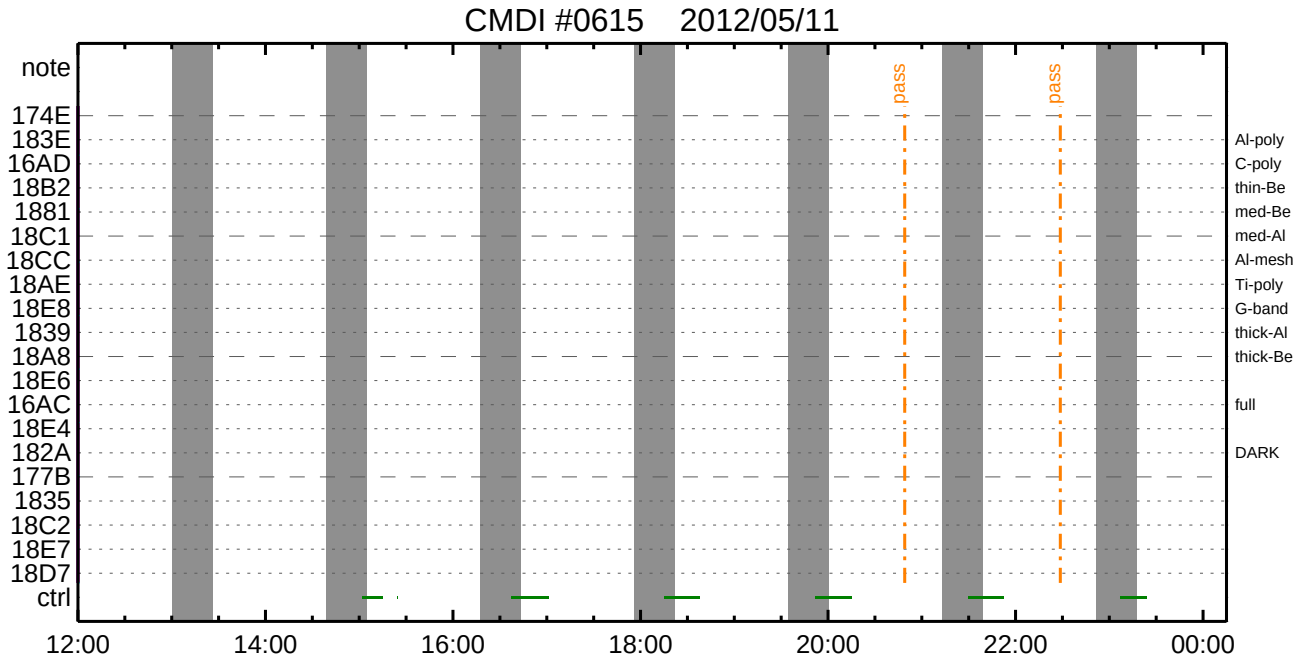
CMDI #0615 2012/05/10

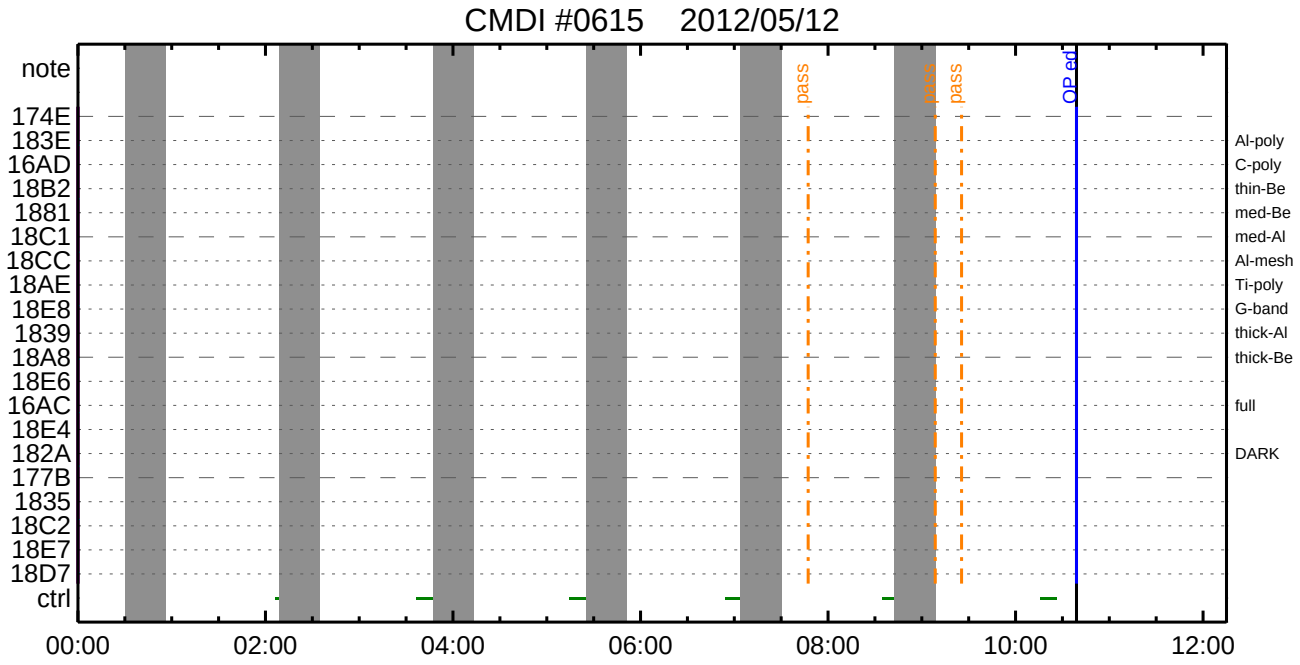


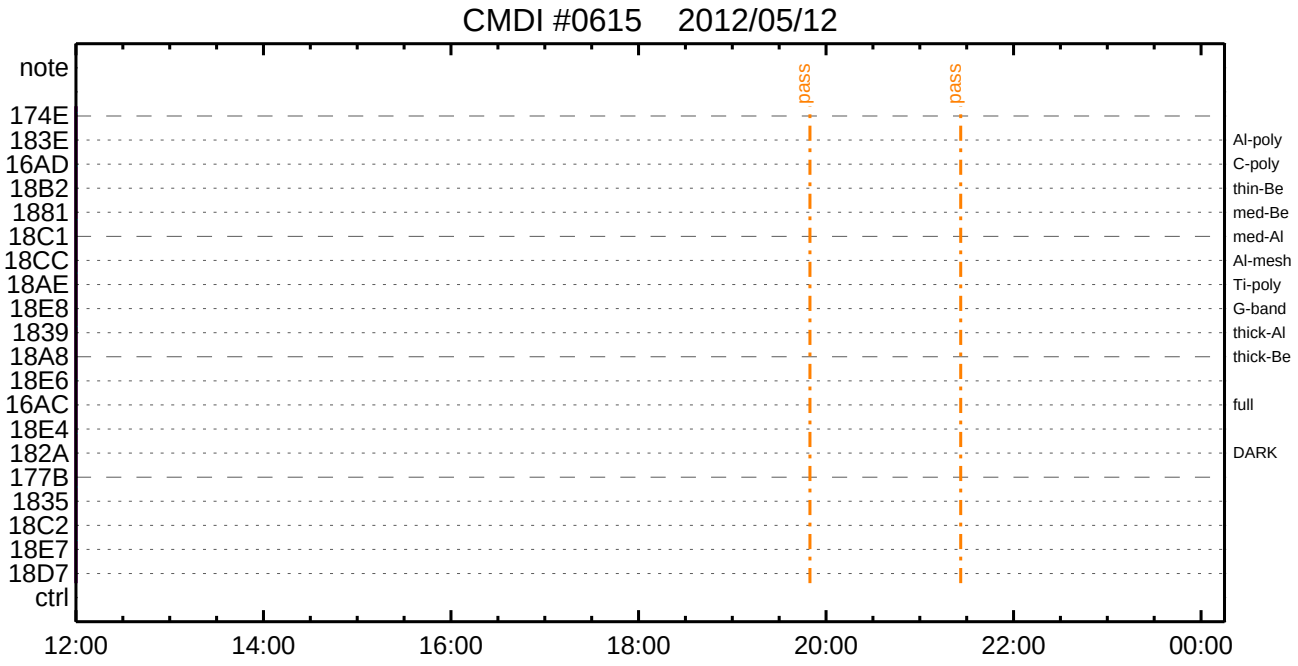
CMDI #0615 2012/05/10




CMDI #0615 2012/05/11
note
174E
183E
16AD
18B2
1881
18C1
18CC
18AE
18E8
1839
18A8
18E6
16AC
18E4
182A
177B
1835
18C2
18E7
18D7
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0615 2012/05/11
note
174E
183E
16AD
18B2
1881
18C1
18CC
18AE
18E8
1839
18A8
18E6
16AC
18E4
182A
177B
1835
18C2
18E7
18D7
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0615 2012/05/12
note
174E
183E
16AD
18B2
1881
18C1
18CC
18AE
18E8
1839
18A8
18E6
16AC
18E4
182A
177B
1835
18C2
18E7
18D7
ctrl
pass
pass
pass
OP end
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0615 2012/05/12
note
174E
183E
16AD
18B2
1881
18C1
18CC
18AE
18E8
1839
18A8
18E6
16AC
18E4
182A
177B
1835
18C2
18E7
18D7
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-860 2012-05-08 14:39:15 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
    C.
    C.
  . C. ****************************
    C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
    C. ****************************
    C.
  . C. ¡ãOP/OG¥í¡¼¥É¡ä
  . S. OP    op-860:OP
    ()
  . S. OG    og-860:OG
    ()
    C.
  . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
    C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
 +. DC 01-23 DHU_DMA_DMP_PRM_SET
    BC       (20 00 7f 01 02)
    C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
    C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
    C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
    C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
    C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
 +. DC 01-22 DHU_MODE_CHNG
    BC       (07 0b f8)
    C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
    C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
    C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
    C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
  . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
    C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
  . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
    C.
    C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
 +. DC 01-23 DHU_DMA_DMP_PRM_SET
    BC       (20 80 7f 01 02)
    C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
    C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
    C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
    C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
    C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
 +. DC 01-22 DHU_MODE_CHNG
    BC       (07 0b f8)
    C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
    C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
    C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
    C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
  . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
    C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
  . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
    C.
    C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
 +. DC 01-23 DHU_DMA_DMP_PRM_SET
    BC       (21 00 41 01 02)
    C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
    C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
    C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
    C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
    C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
 +. DC 01-22 DHU_MODE_CHNG
    BC       (07 0b f8)
    C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
    C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
    C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
    C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
  . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
    C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
  . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
    C.
  . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
    C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤ÎÌá¤¹
 +. DC 01-22 DHU_MODE_CHNG
    BC       (02 0a f8)
    C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     2
    C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
    C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
    C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.
  . C. *********************************
    C. TI-CMD SET (OPOG STOP/COPY/START)
    C. *********************************
    C.
  . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
    C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
    C.
  . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
 +. TI 2012-05-08 09:10:00.0
    DC 01-B3 DHU_OP_STOP
    C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
    C.
 +. TI 2012-05-08 09:10:01.0
    DC 01-B4 DHU_OP_COPY
    C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
    C.
 +. TI 2012-05-08 09:10:01.0
    DC 01-B5 DHU_OPOG_COPY
    C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2012-05-08 09:14:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242   +. TI 2012-05-08 09:14:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245    . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249    . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255   +. TI 2012-05-08 09:14:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258   +. TI 2012-05-08 09:14:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ***********
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268   +. TI 2012-05-08 09:14:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-861 2012-05-08 14:39:15 91 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. ***** AOCS Commands (Orbital Element Update) *****
0046     C. Update the orbital element
0047   +. DC 02-50 AOCU_ORB_PRPGT_START
0048     BC       (16)
0049   +  DC 02-8E AOCU_ORB_UPD
0050     C.
0051     C.       <A_ORB>[ORBIT] EPC =  975557.8 +- 1.0 (s) [ ]
0052     C.
0053   . C.
0054   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055   +. DC 07-FC EIS_MODE_MANU
0056     BC       (21 02)
0057   . C. Verify EIS in MANUAL mode
0058   . C. Estimated OBSTBL upload time is 18s
0059     C. *********************************
0060     C. EIS START OBSTBL LOAD
0061     C. *********************************
0062   . S. RAM   ram-820:EIS_OBSTBL
0063     ()
0064   +. DC 07-FC EIS_DUMP_OBSTBL
0065     BC       (07 07 07 00 00 70 00)
0066     C.
0067     C. Execute, after the success of OBSTBL upload.
0068     C. Set EIS TI-commands
0069   +. TI 2012-05-08 09:14:50.0
0070     DC 07-FC EIS_MODE_CHG_ENA
0071     BC       (20)
0072   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0073     C. *********************************
0074     C. EIS END OBSTBL LOAD
0075     C. *********************************
0076     C.
0077   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0078     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0079   . S. DC-BC dcbc-402:DCBC
0080     (MDP_known_event)
0081     C.
0082     C.
0083   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0084   . S. DC-BC dcbc-153:DCBC
0085     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086     C.
0087     C.
0088   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0089     C.
0090   . C. *********** LOS ***********
0091     C.
```

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(a) Spacecraft Operation Procedure (real-commands)

main-862 2012-05-08 14:39:15 238 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098     C.
0099     C. ************ XRT START ************
0100     C.
0101  +. DC 07-F0 MDP_XRT_CTRL_MANU
0102     BC       (c1)
0103  +  DC 07-F0 MDP_XRT_MODE_STBY
0104     BC       (c3)
0105   . C. ----------- Success Verify ?              OK / NG____
0106     C.
0107     C. XRT Obs. Table Upload
0108   . S. RAM   ram-291:MDP_OBS_X
0109     ()
0110     C.
0111  +. DC 07-F0 MDP_DUMP_XRTTBL
0112     BC       (84 07 00 00 00 3a d4)
0113   . C. ----------- Comparison Check ?                      OK / ERR ____
0114     C.
0115     C.
0116  +. DC 07-F0 MDP_XRT_ROI_SET
0117     BC       (cd 01 b1 b1 04 04)
0118  +  DC 07-F0 MDP_XRT_ROI_SET
0119     BC       (cd 02 b1 b1 08 08)
0120  +  DC 07-F0 MDP_XRT_ROI_SET
0121     BC       (cd 03 b1 b1 08 08)
0122  +  DC 07-F0 MDP_XRT_ROI_SET
0123     BC       (cd 04 b1 b1 06 06)
0124  +  DC 07-F0 MDP_XRT_ROI_SET
0125     BC       (cd 05 85 83 06 06)
0126  +  DC 07-F0 MDP_XRT_ROI_SET
0127     BC       (cd 06 85 83 06 06)
0128  +  DC 07-F0 MDP_XRT_ROI_SET
0129     BC       (cd 07 80 80 20 20)
0130  +  DC 07-F0 MDP_XRT_ROI_SET
0131     BC       (cd 08 80 80 20 08)
0132  +  DC 07-F0 MDP_XRT_ROI_SET
0133     BC       (cd 09 80 80 08 20)
0134  +  DC 07-F0 MDP_XRT_ROI_SET
0135     BC       (cd 0a 80 60 20 18)
0136  +  DC 07-F0 MDP_XRT_ROI_SET
0137     BC       (cd 0b a0 80 18 20)
0138  +  DC 07-F0 MDP_XRT_ROI_SET
0139     BC       (cd 0f 80 80 06 06)
0140  +  DC 07-F0 MDP_XRT_FLD_ENA
0141     BC       (d8)
0142  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143     BC       (c8)
0144  +  DC 07-F0 MDP_XRT_AEC_RESET
0145     BC       (d0)
0146  +  DC 07-F0 MDP_XRT_ARS_DIS
0147     BC       (d5)
0148  +  DC 07-F0 MDP_XRT_FLD_RESET
0149     BC       (da)
0150  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151     BC       (c4 04)
0152  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153     BC       (c5 02)
0154   . C. ---------- Success Verify ?           OK / NG ____
0155     C.
0156     C.
0157   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0158     C.
0159  +. DC 07-F0 MDP_XRT_MODE_OBSV
0160     BC       (c2)
0161  +. TI 2012-05-08 09:14:02.0
0162     DC 07-F0 MDP_XRT_MODE_OBSV
0163     BC       (c2)
0164   . C. ---------- Success Verify ?           OK / NG ____
0165     C.
0166     C. ************ XRT END ************
0167   . C. ********************************
0168     C.  SOT table upload
0169     C. ********************************
0170   . C. < Stop FG table >
0171  +. DC 07-F0 MDP_FG_CTRL_MANU
0172     BC       (51)
0173   . C. ------------------------------------
0174     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0175     C. ------------------------------------
0176     C.
0177   . C. <Upload FG Observation Table>
0178   . S. RAM   ram-266:MDP_OBS_F
0179     ()
0180     C.
0181   . C. < Dump RAMID=MDP_OBS_F >
0182  +. DC 07-F0 MDP_DUMP_FGTBL
0183     BC       (82 07 00 00 00 38 b8)
0184     C. ------------------------------------
0185     C.  MDP_OBS_F verify = OK/NG        [ ]
0186     C. ------------------------------------
0187     C.
0188   . C. < Upload DPL table >
0189     C.
0190     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0191     C.
0192   . S. RAM   ram-271:MDP_DPL
0193     ()
```

```
0194     C.
0195    . C. < Dump RAMID=MDP_DPL >
0196   +. DC 07-F0 MDP_DUMP_FGTBL
0197     BC       (82 07 00 38 b8 00 40)
0198     C. ------------------------------------
0199     C.  MDP_DPL verify          = OK    [ ]
0200     C. ------------------------------------
0201     C.
0202     C. STS_CHK¤òON¤Ë¤¹¤ë
0203     C.
0204    . C. < Update MDP DSC PAR1 >
0205   +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0206     BC       (4c)
0207     C.  MDP_CMD_CODE          = F04C0700[ ]
0208     C.  MDP_CMD_CNT        (count-up 1) [ ]
0209     C. ------------------------------------
0210     C.
0211    . C.
0212     C. ********************************
0213     C.  SOT TI command set
0214     C. ********************************
0215     C. Execute, after the success of TBL upload.
0216   +. TI 2012-05-08 09:14:18.0
0217     DC 07-F0 MDP_SOT_MODE_OBSV
0218     BC       (40)
0219    . C. ------------------------------------
0220     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0221     C. ------------------------------------
0222     C.
0223     C.
0224    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0225     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0226    . S. DC-BC dcbc-402:DCBC
0227     (MDP_known_event)
0228     C.
0229     C.
0230    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0231    . S. DC-BC dcbc-153:DCBC
0232     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0233     C.
0234     C.
0235    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0236     C.
0237    . C. ********** LOS **********
0238     C.
```

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*** OP Sequence for XRT ***

2012/05/08  09:22:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/08  09:22:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/08  09:22:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/08  09:25:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 02 00 00 00 00
2012/05/08  09:25:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/08  09:56:00.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/08  09:58:32.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/05/08  09:58:52.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2012/05/08  09:58:54.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/05/08  09:58:56.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2012/05/08  09:58:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/05/08  09:59:00.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/08  09:59:02.0  XRT_QT_PROG_SET_415_OG [0x19f]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2012/05/08  09:59:04.0  XRT_FL_PROG_SET_404_OG [0x194]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 02
2012/05/08  09:59:06.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/08  11:08:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/08  11:08:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/08  11:08:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/08  11:11:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/08  11:33:30.0  XRT_Custom_418_OG [0x1a2]
2012/05/08  11:34:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/08  12:47:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/08  12:47:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/08  12:47:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/08  12:50:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/08  13:12:00.0  XRT_Custom_418_OG [0x1a2]
2012/05/08  13:13:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/08  14:25:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/08  14:25:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/08  14:25:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/08  14:28:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/08  15:01:00.5  XRT_Custom_418_OG [0x1a2]
2012/05/08  15:02:00.5  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/08  16:04:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/08  16:04:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/08  16:04:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/08  16:07:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/08  16:48:00.0  XRT_Custom_418_OG [0x1a2]
2012/05/08  16:49:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/08  17:42:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/08  17:42:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/08  17:42:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/08  17:45:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/08  18:24:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/08  18:24:26.0  XRT_FOCUS_RECALIBRATE_428_OG [0x1ac]
                                XRT_FOCUS_RECAL      2 07-F8 78 00
2012/05/08  18:24:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2012/05/08  18:28:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/05/08  18:28:46.0  XRT_FLD_DIS_402_OG [0x192]
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                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2012/05/08  18:28:48.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/05/08  18:28:50.0  XRT_ARS_DIS_438_OG [0x1b6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/05/08  18:31:28.0  XRT_QT_PROG_SET_407_OG [0x197]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2012/05/08  18:31:30.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/08  18:34:24.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/08  18:34:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 02 00 00 00 00
2012/05/08  18:36:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/05/08  18:37:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/05/08  18:37:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/05/08  18:37:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/05/08  18:37:22.0  XRT_ARS_DIS_431_OG [0x1af]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/05/08  18:37:24.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/08  18:37:26.0  XRT_QT_PROG_SET_415_OG [0x19f]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2012/05/08  18:37:28.0  XRT_FL_PROG_SET_404_OG [0x194]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 02
2012/05/08  18:37:30.5  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/08  19:21:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/08  19:21:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/08  19:21:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/08  19:24:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/08  20:01:30.0  XRT_Custom_418_OG [0x1a2]
2012/05/08  20:02:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/08  20:59:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/08  20:59:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/08  20:59:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/08  21:02:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/08  21:38:30.0  XRT_Custom_418_OG [0x1a2]
2012/05/08  21:39:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/08  22:38:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/08  22:38:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/08  22:38:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/08  22:41:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/08  23:13:00.0  XRT_Custom_418_OG [0x1a2]
2012/05/08  23:14:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  00:16:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  00:16:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  00:16:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  00:19:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  00:42:00.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  00:43:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  01:53:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  01:53:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  01:53:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  01:56:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  02:20:30.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  02:21:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  03:27:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  03:27:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  03:27:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  03:30:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
```

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                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  03:59:00.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  04:00:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  05:00:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  05:00:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  05:00:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  05:03:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  05:56:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  05:56:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/05/09  05:56:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2012/05/09  05:56:46.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2012/05/09  05:56:48.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/05/09  05:56:50.0  XRT_ARS_DIS_438_OG [0x1b6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/05/09  05:59:28.0  XRT_QT_PROG_SET_407_OG [0x197]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2012/05/09  05:59:30.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  06:06:24.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  06:06:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 02 00 00 00 00
2012/05/09  06:08:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/05/09  06:09:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2012/05/09  06:09:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/05/09  06:09:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2012/05/09  06:09:22.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/05/09  06:09:24.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  06:09:26.0  XRT_QT_PROG_SET_415_OG [0x19f]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2012/05/09  06:09:28.0  XRT_FL_PROG_SET_404_OG [0x194]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 02
2012/05/09  06:09:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  06:40:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  06:40:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  06:40:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  06:43:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  07:16:00.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  07:17:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  08:20:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  08:20:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  08:20:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  08:23:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  08:54:30.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  08:55:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  10:01:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  10:01:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  10:01:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  10:04:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  10:33:00.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  10:34:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  11:46:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  11:46:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  11:46:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  11:49:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  12:11:30.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  12:12:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
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                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  13:24:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  13:24:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  13:24:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  13:27:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  13:50:00.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  13:51:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  15:03:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  15:03:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  15:03:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  15:06:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  15:48:00.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  15:49:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  16:41:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  16:41:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  16:41:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  16:44:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  17:24:30.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  17:25:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  17:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  17:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/05/09  18:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2012/05/09  18:00:16.0  XRT_FLD_DIS_402_OG [0x192]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2012/05/09  18:00:18.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/05/09  18:00:20.0  XRT_ARS_DIS_438_OG [0x1b6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/05/09  18:02:58.0  XRT_QT_PROG_SET_407_OG [0x197]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2012/05/09  18:03:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  18:09:54.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  18:10:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 02 00 00 00 00
2012/05/09  18:12:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/05/09  18:12:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/05/09  18:12:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/05/09  18:12:50.0  XRT_AEC_RESET_443_OG [0x1bb]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/05/09  18:12:52.0  XRT_ARS_DIS_431_OG [0x1af]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/05/09  18:12:54.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  18:12:56.0  XRT_QT_PROG_SET_434_OG [0x1b2]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2012/05/09  18:12:58.0  XRT_FL_PROG_SET_404_OG [0x194]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 02
2012/05/09  18:13:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  18:19:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  18:19:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  18:19:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  18:22:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  19:01:30.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  19:02:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  19:58:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  19:58:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  19:58:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  20:01:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  20:39:00.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  20:40:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
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                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  21:36:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  21:36:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  21:36:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  21:39:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  22:15:00.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  22:16:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/09  23:15:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/09  23:15:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/09  23:15:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/09  23:18:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/09  23:42:30.0  XRT_Custom_418_OG [0x1a2]
2012/05/09  23:43:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/10  00:53:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/10  00:53:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/10  00:53:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/10  00:56:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/10  01:19:30.0  XRT_Custom_418_OG [0x1a2]
2012/05/10  01:20:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/10  02:16:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/10  02:16:56.0  XRT_FOCUS_POSITION_447_OG [0x1bf]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/05/10  02:17:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM              5 02-76 00 ac 00 00 00
2012/05/10  02:17:16.0  XRT_QT_PROG_SET_416_OG [0x1a0]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2012/05/10  02:17:18.0  XRT_FLD_DIS_425_OG [0x1a9]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2012/05/10  02:17:20.0  XRT_FLRCTRL_DIS_446_OG [0x1be]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/05/10  02:17:22.0  XRT_ARS_DIS_448_OG [0x1c0]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/05/10  02:32:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/10  04:16:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/10  04:16:56.0  XRT_FOCUS_POSITION_447_OG [0x1bf]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/05/10  04:17:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                    AOCU_NM              5 02-76 00 00 00 54 00
2012/05/10  04:17:16.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2012/05/10  04:17:18.0  XRT_FLD_DIS_425_OG [0x1a9]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2012/05/10  04:17:20.0  XRT_FLRCTRL_DIS_446_OG [0x1be]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/05/10  04:17:22.0  XRT_ARS_DIS_448_OG [0x1c0]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/05/10  04:32:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/10  06:17:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/10  06:17:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/05/10  06:17:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2012/05/10  06:17:46.0  XRT_FLD_DIS_402_OG [0x192]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2012/05/10  06:17:48.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/05/10  06:17:50.0  XRT_ARS_DIS_438_OG [0x1b6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/05/10  06:20:28.0  XRT_QT_PROG_SET_407_OG [0x197]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2012/05/10  06:20:30.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/10  06:27:00.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/10  06:29:32.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/05/10  06:29:52.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2012/05/10  06:29:54.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/05/10  06:29:56.0  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2012/05/10  06:29:58.0  XRT_ARS_DIS_431_OG [0x1af]
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                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/05/10  06:30:00.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/10  06:30:02.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2012/05/10  06:30:04.0  XRT_FL_PROG_SET_404_OG [0x194]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 02
2012/05/10  06:30:06.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/10  06:57:24.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/10  06:57:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 02 00 00 00 00
2012/05/10  06:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/05/10  07:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2012/05/10  07:00:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/05/10  07:00:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2012/05/10  07:00:22.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/05/10  07:00:24.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/10  07:00:26.0  XRT_QT_PROG_SET_434_OG [0x1b2]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2012/05/10  07:00:28.0  XRT_FL_PROG_SET_404_OG [0x194]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 02
2012/05/10  07:00:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/10  07:19:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/10  07:19:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/10  07:19:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/10  07:22:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/10  07:53:30.0  XRT_Custom_418_OG [0x1a2]
2012/05/10  07:54:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/05/10  08:58:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/05/10  08:58:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/05/10  08:58:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/05/10  09:01:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/05/10  09:34:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
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