

Period: 2012/05/12 10:39:00 - 2012/06/17 12:00:00

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Normal mode

XOB #18E8: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly														
Term		Pointing (x, y)					Comment							
05/12 17:58:30 - 05/12 18:05:24		Fixed (0.0, 0.0)					synoptic, shifted -4.5 min							
05/13 18:18:00 - 05/13 18:24:54		Fixed (0.0, 0.0)					synoptic, shifted 15.0 min							
05/14 18:00:00 - 05/14 18:09:30		Fixed (0.0, 0.0)					synoptic, shifted -3.0 min							
PROG= 12 1-time(s)														
└ Subr= 1 1-time(s) 12.0sec														
└ Seqn= 7 1-time(s) 4.0sec														
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Seqn= 5 1-time(s) 2.0sec														
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└ Seqn= 8 1-time(s) 4.0sec														
└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Seqn= 4 1-time(s) 2.0sec														
└ Open/G-band		Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

Flare mode

XOB #18E7: Flare obs. dynamics - Ti_poly high cadence + context (thick-AI-384x384)-15 loops			
Term	Pointing (x, y)		Comment
05/12 10:52:00 - 05/12 17:55:24	Track (	202.9, 181.3) ^{@ 05/12 10:49:00}	# OP start + 10min, Observe AR 11476 (major flare watch)

05/12 18:08:30 - 05/13 17:32:00 Track (267.1, 179.9) @ 05/12 18:05:30 # AR 11476 continued.
05/13 18:28:00 - 05/14 09:29:54 Track (470.4, 173.8) @ 05/13 18:25:00 # Cont.
05/14 09:33:00 - 05/14 17:56:54 Track (583.9, 169.1) @ 05/14 09:30:00 # Re-point to same curve to allow pointing update on Monday.
05/14 18:53:00 - 05/15 08:48:30 Track (643.3, 166.1) @ 05/14 18:07:00 # Cont.

PROG= 02 15-time(s)												
Subr= 1		45-time(s)		10.0sec								
Seqn= 92		1-time(s)		2.0sec								
Open/Ti-poly		Open/thick-Al		close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	2 0 2.0sec
Open/Ti-poly		Open/thick-Al		close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3 0 2.0sec
Subr= 2		1-time(s)		10.0sec								
Seqn= 54		1-time(s)		2.0sec								
Open/thick-Al		Open/thick-Al		close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	2 0 2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	3 0 2.0sec
Seqn= 79		1-time(s)		2.0sec								
Open/G-band		Open/G-band		open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

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Active Region Search

* * * * *

NOT USED

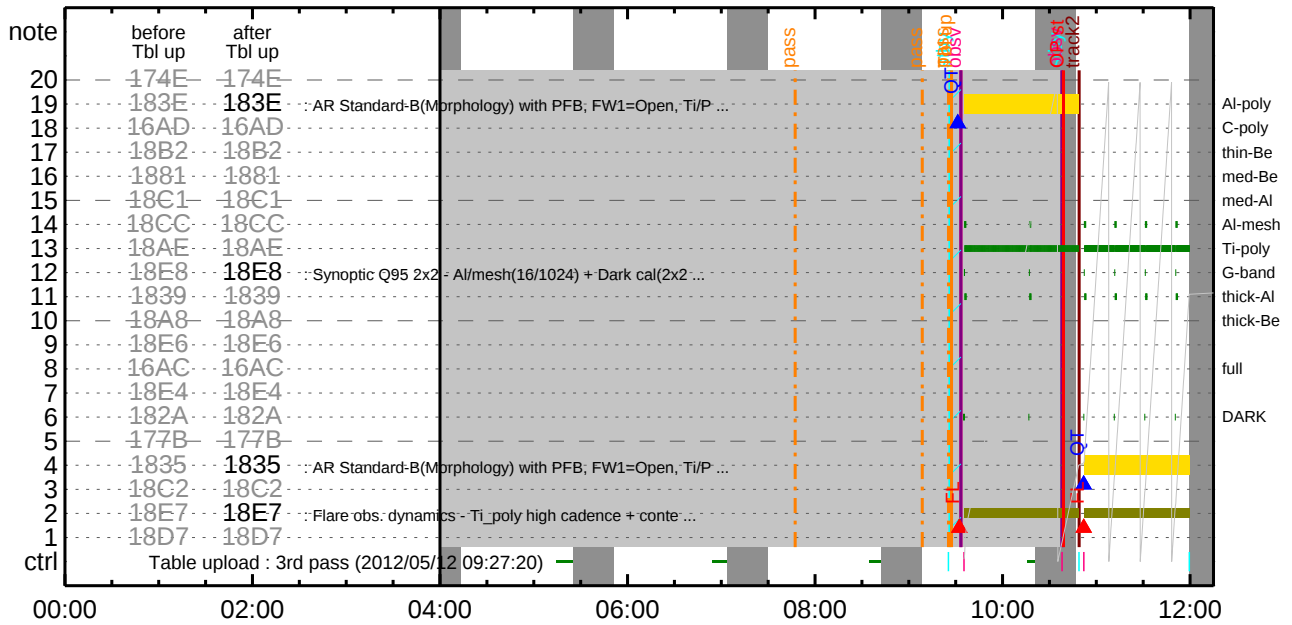
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Flare Detection

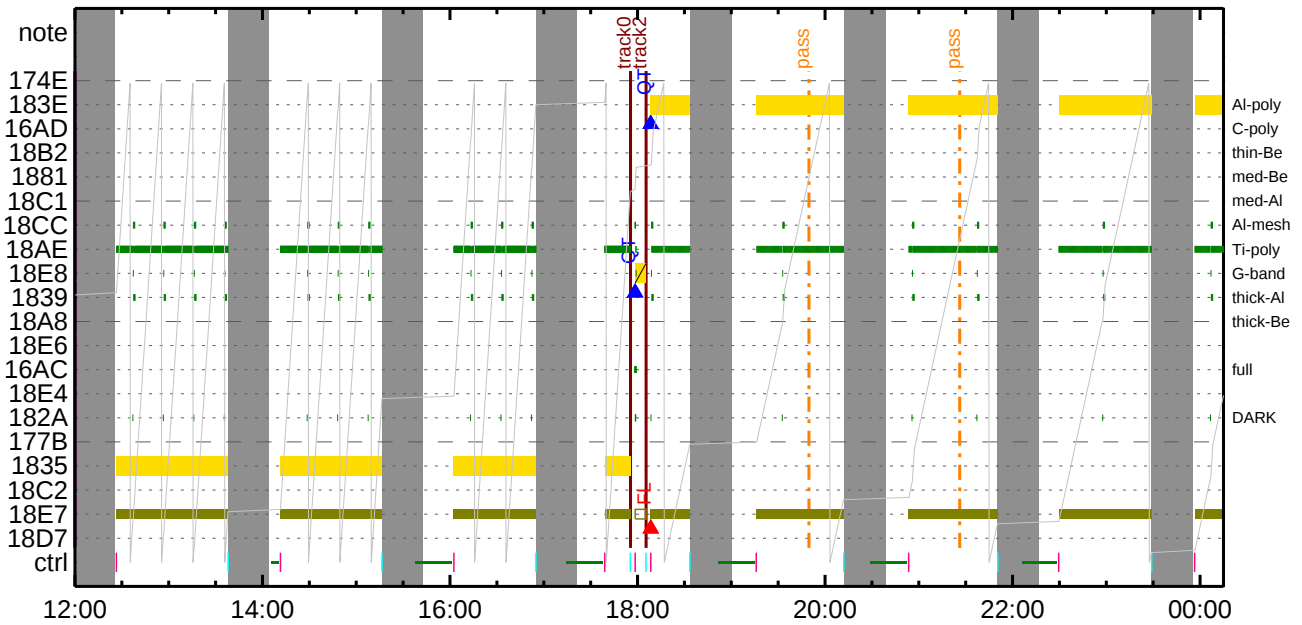
* * * * *

FLD Patrol												
Term		Pointing (x, y)		Comment								
05/12 18:08:16 - 05/13 18:15:16		Track (267.1, 179.9)		@ 05/12 18:05:30 # AR 11476 continued.								
05/13 18:27:46 - 05/14 17:57:16		Track (470.4, 173.8)		@ 05/13 18:25:00 # Cont.								
05/14 18:52:46 - 06/17 12:00:00		Track (643.3, 166.1)		@ 05/14 18:07:00 # Cont.								
Open/Ti-poly		Open/thick-Al		close	Safe	Norm	8ms	Obs	8x8	Q=50 30sec		
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

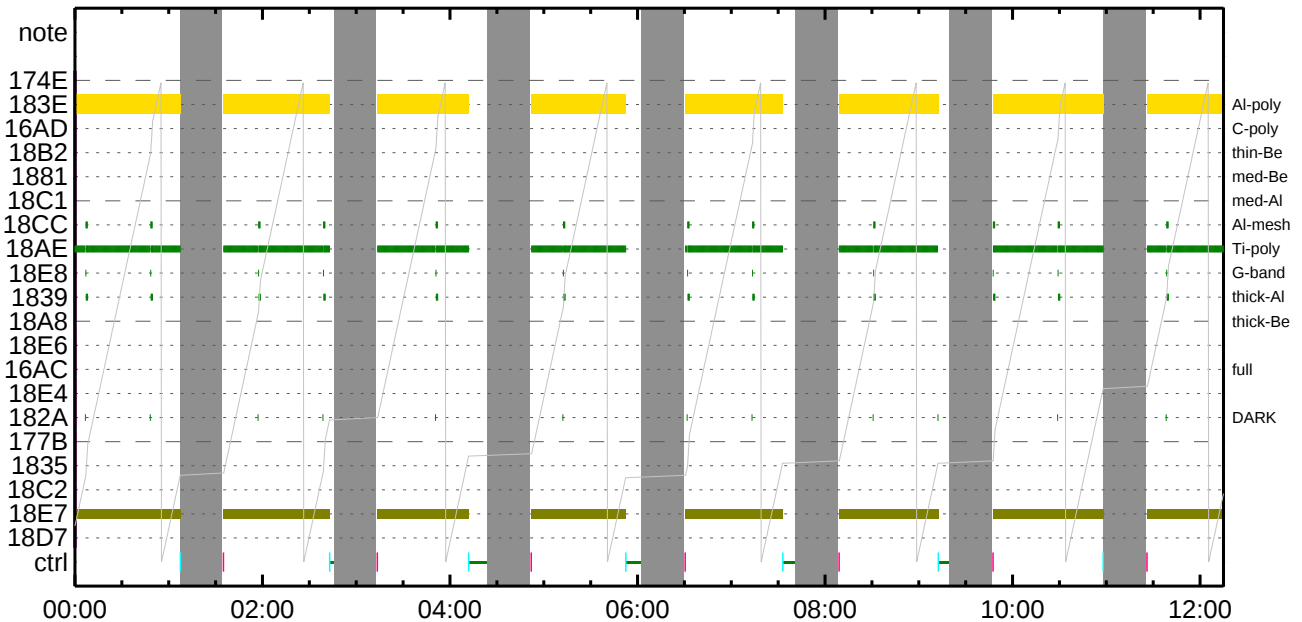
CMDI #0625 2012/05/12



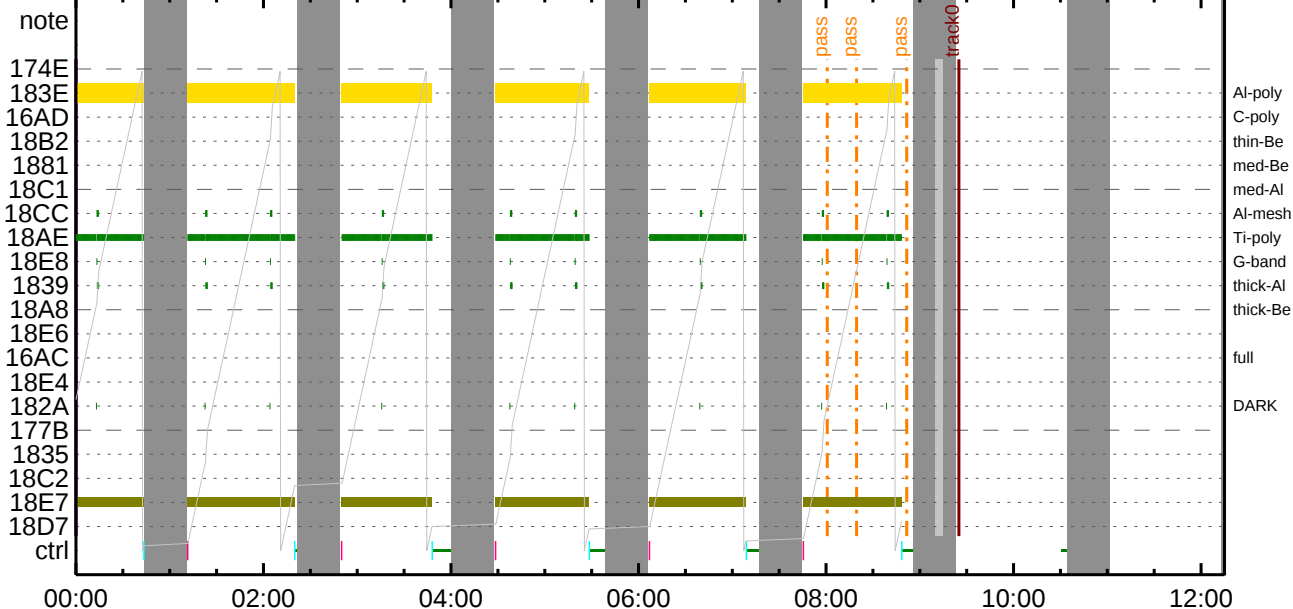
CMDI #0625 2012/05/12



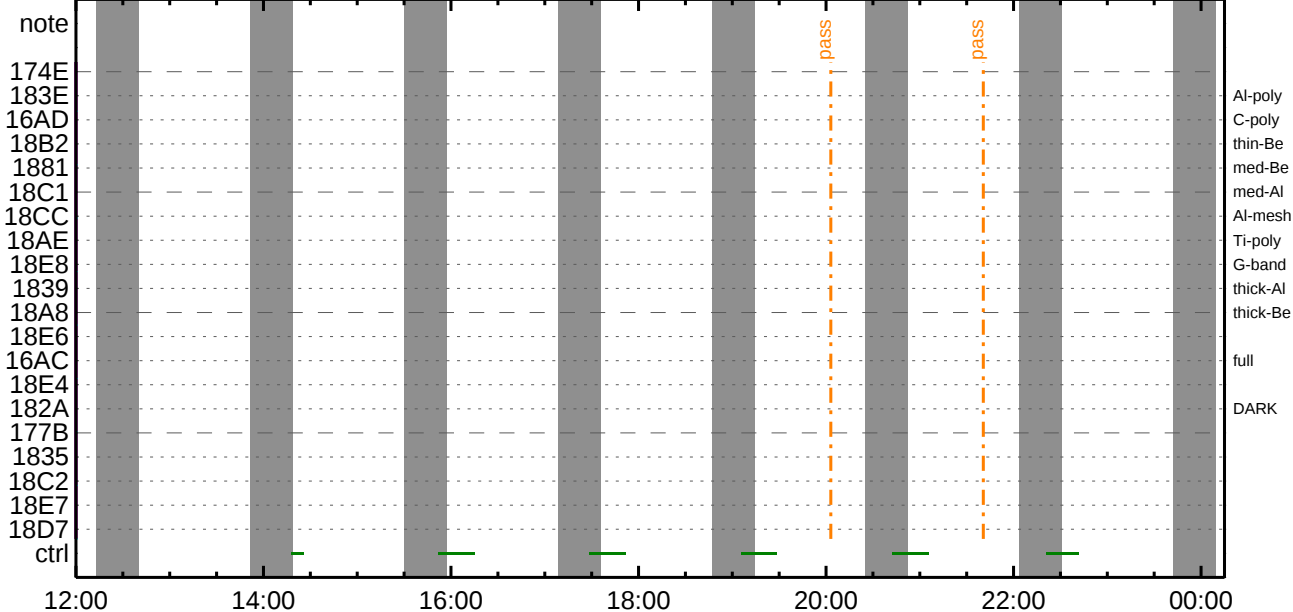
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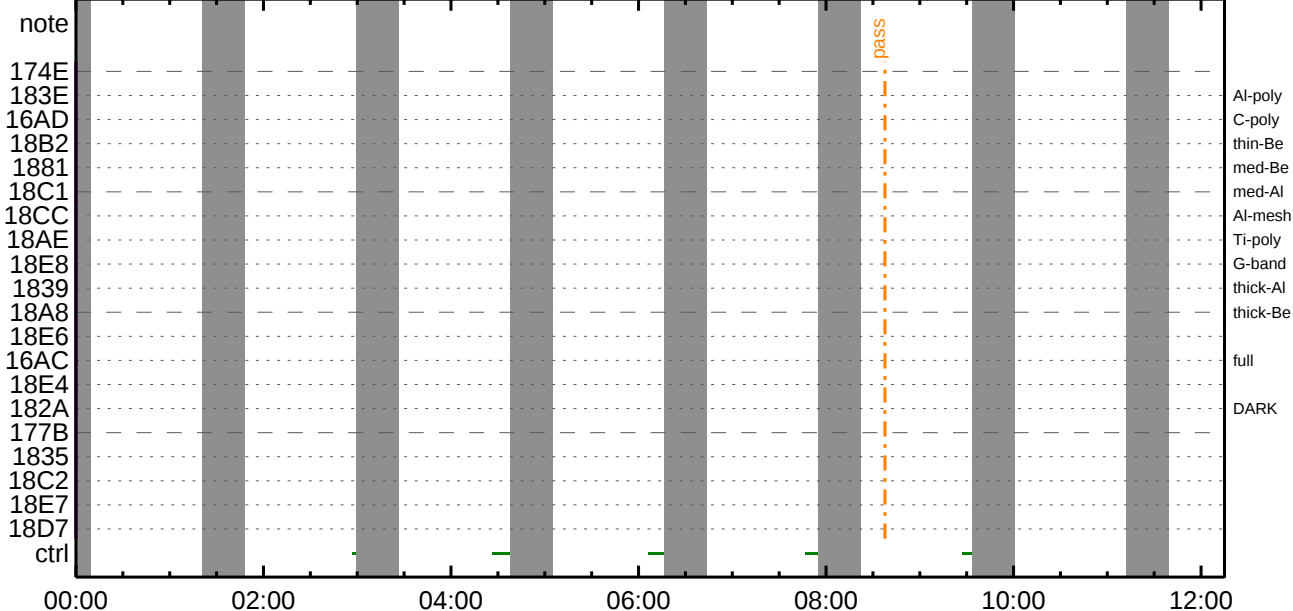
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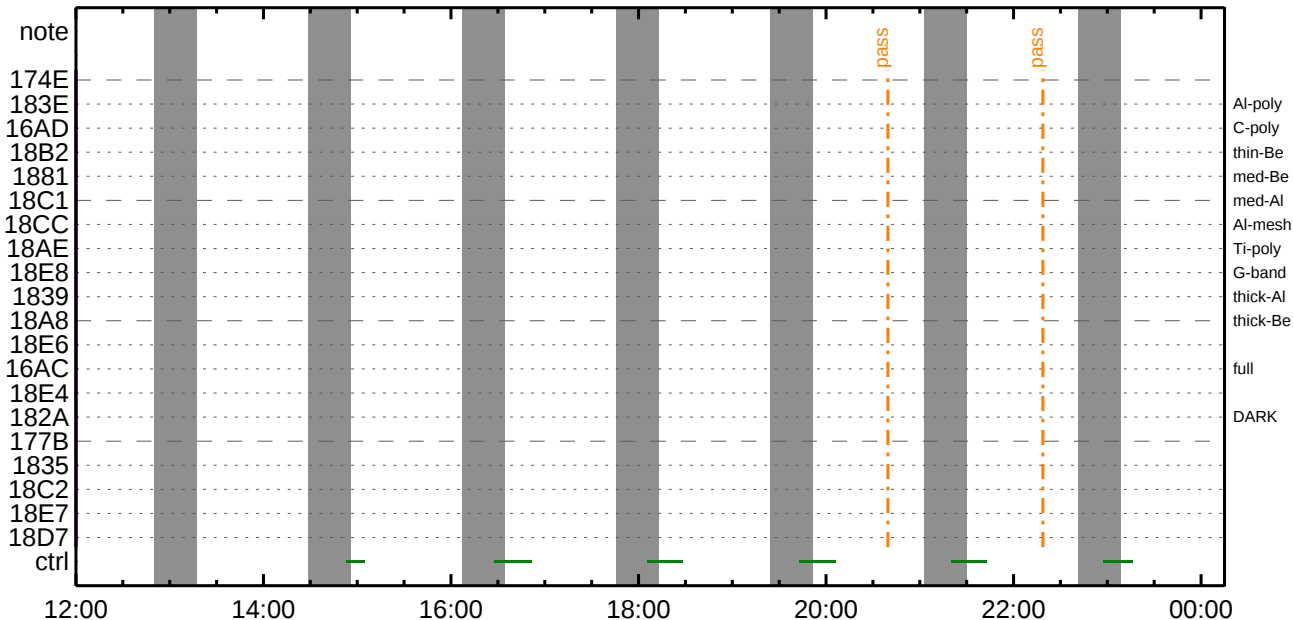
CMDI #0625 2012/05/15



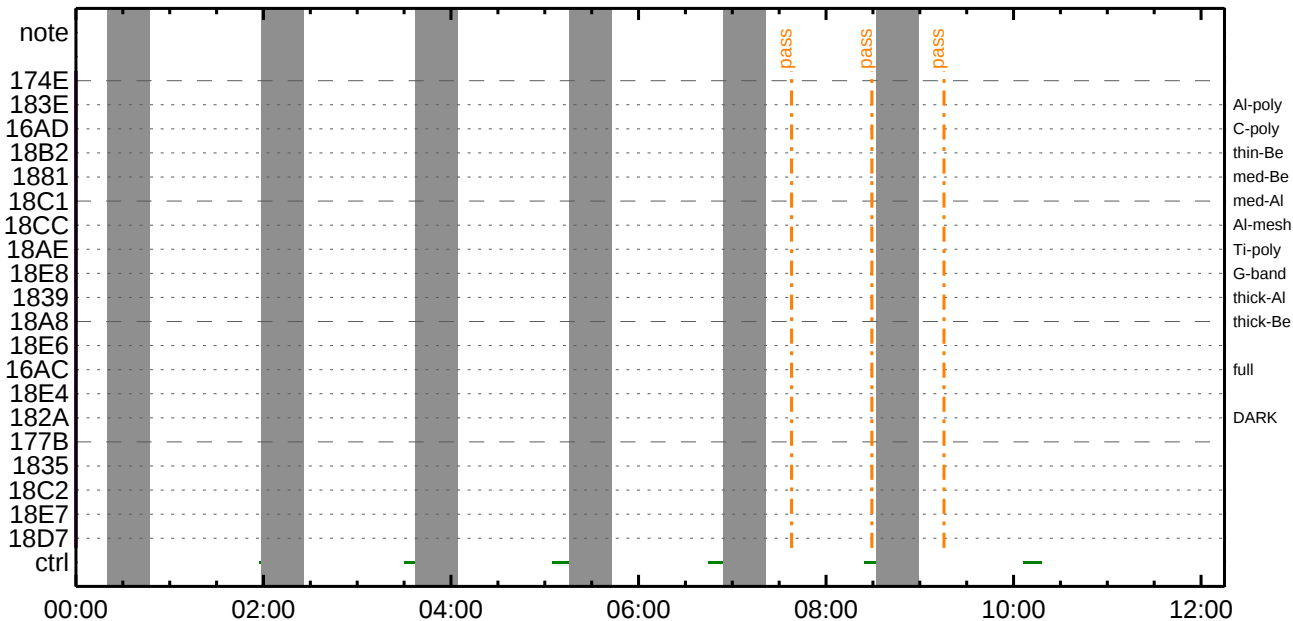
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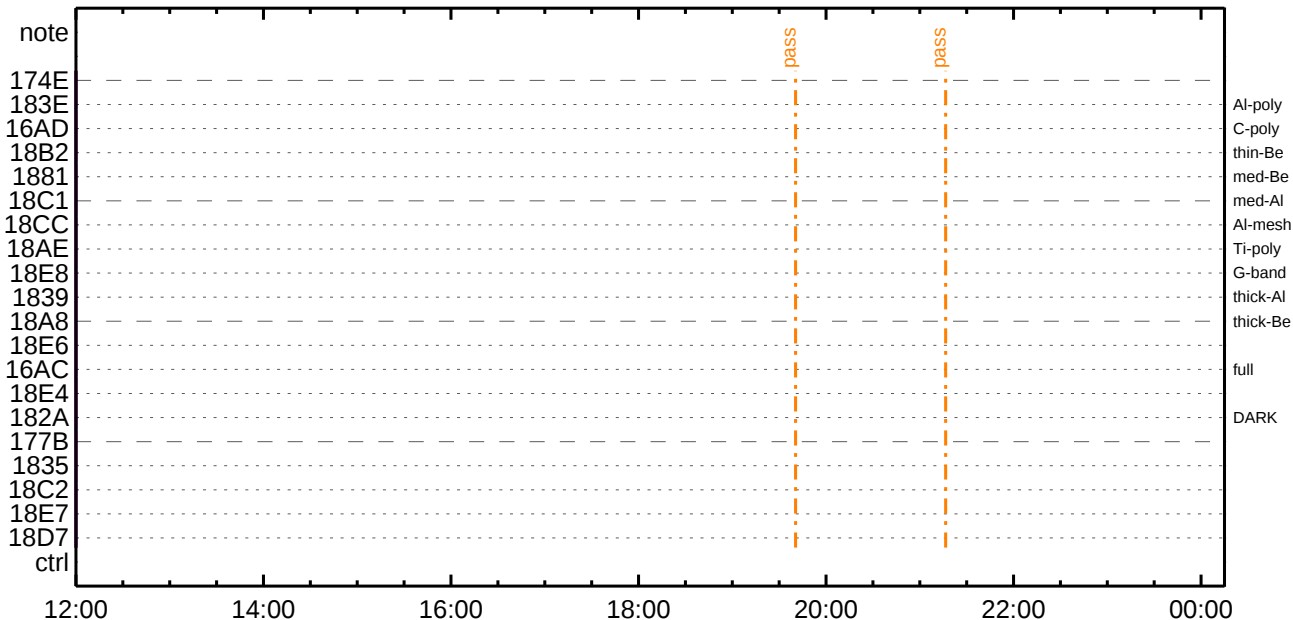
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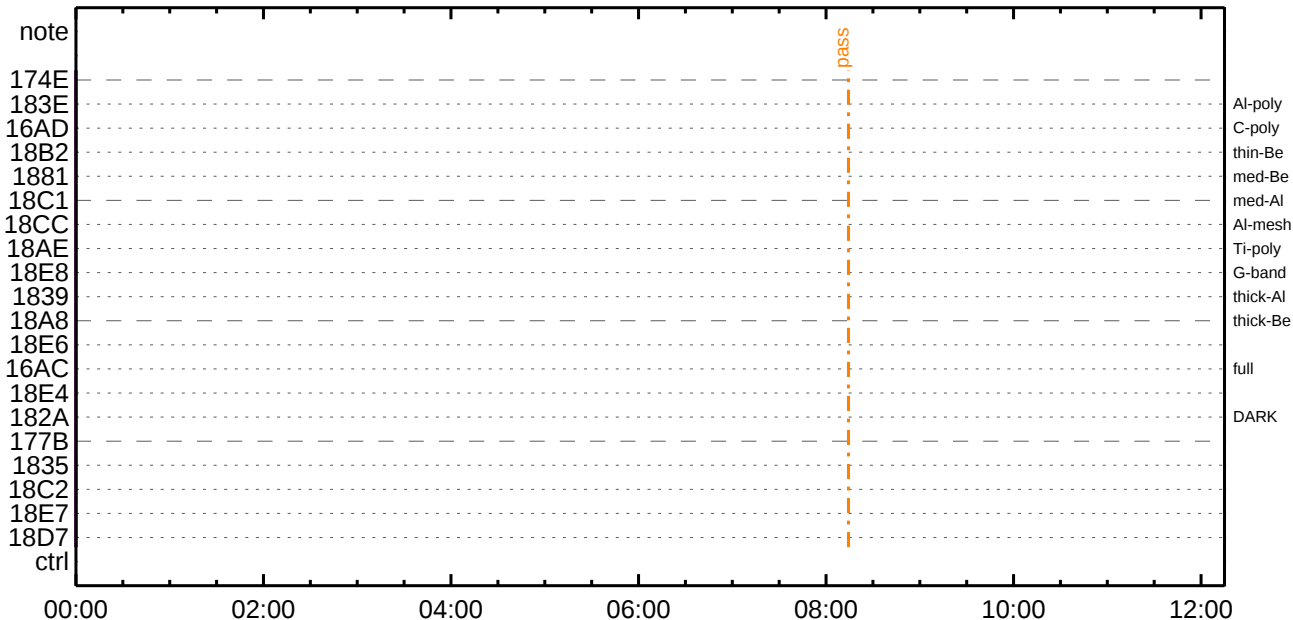
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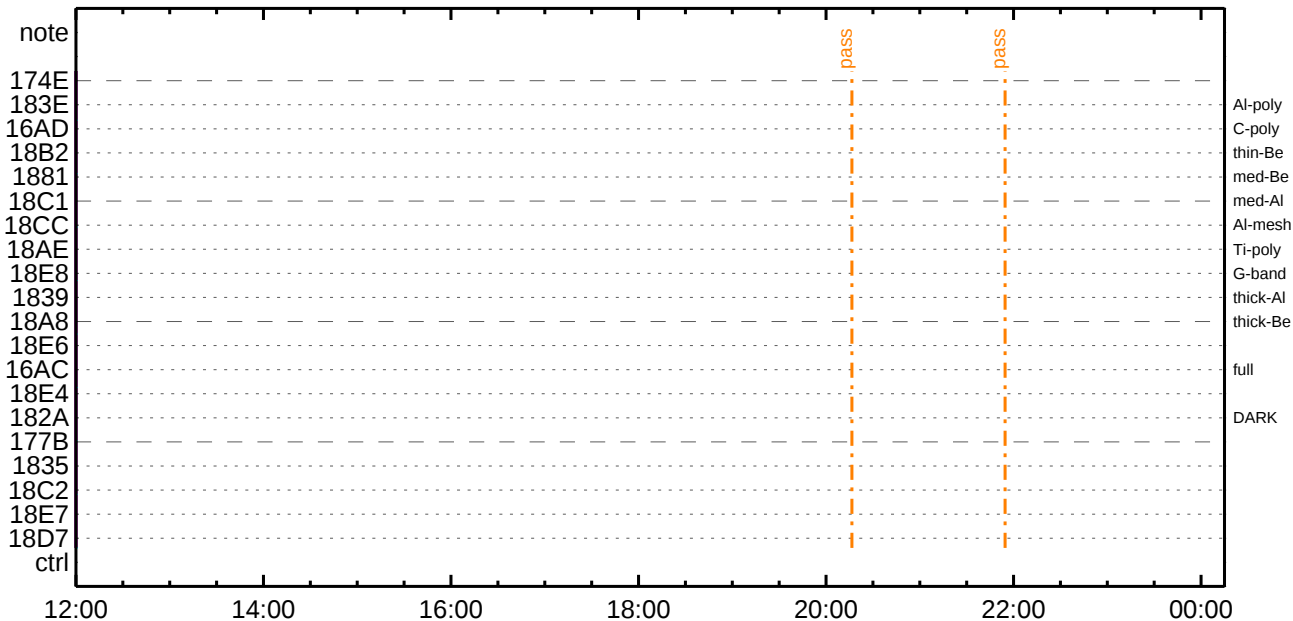
CMDI #0625 2012/05/17



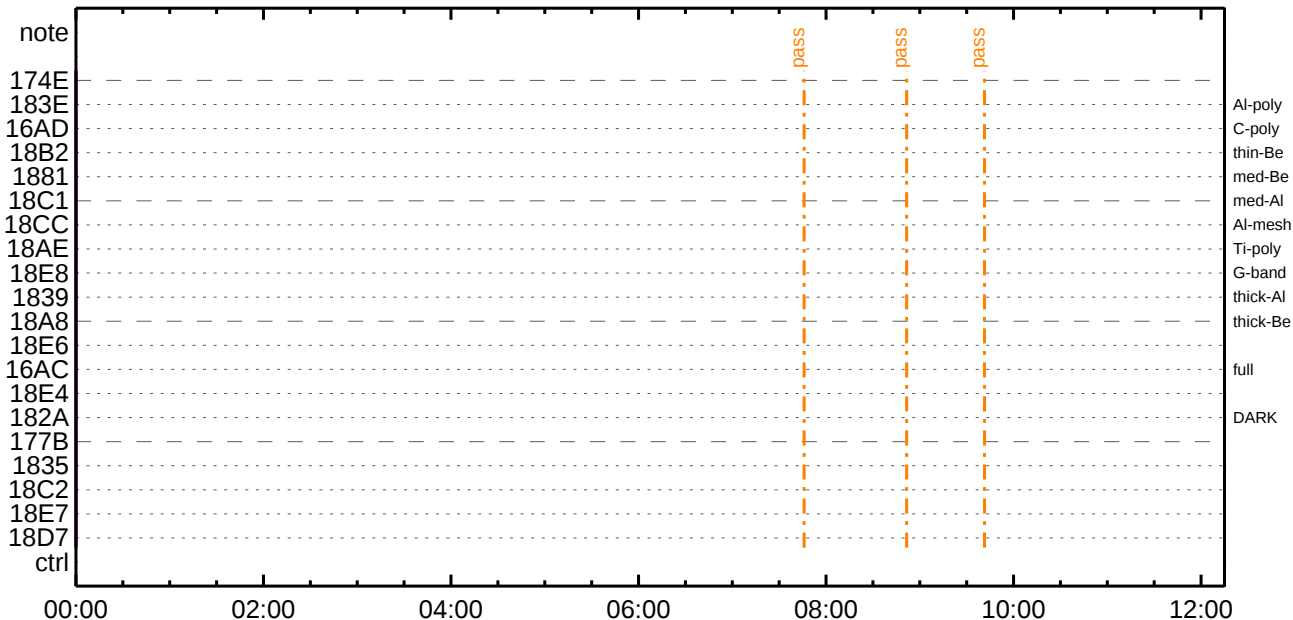
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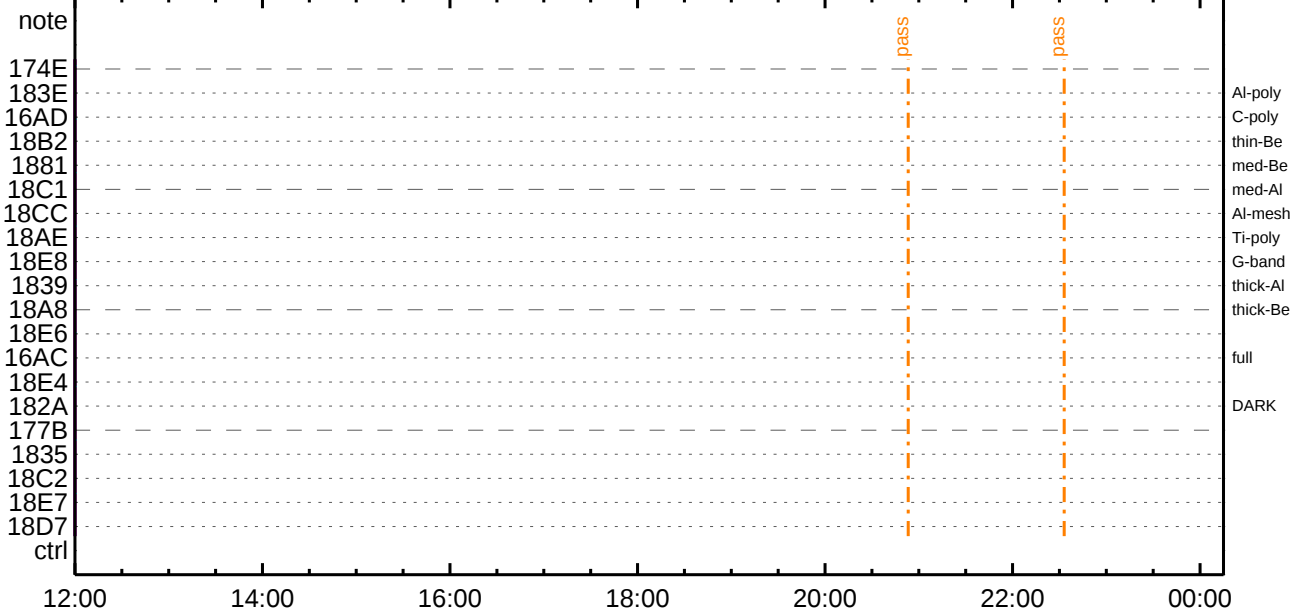
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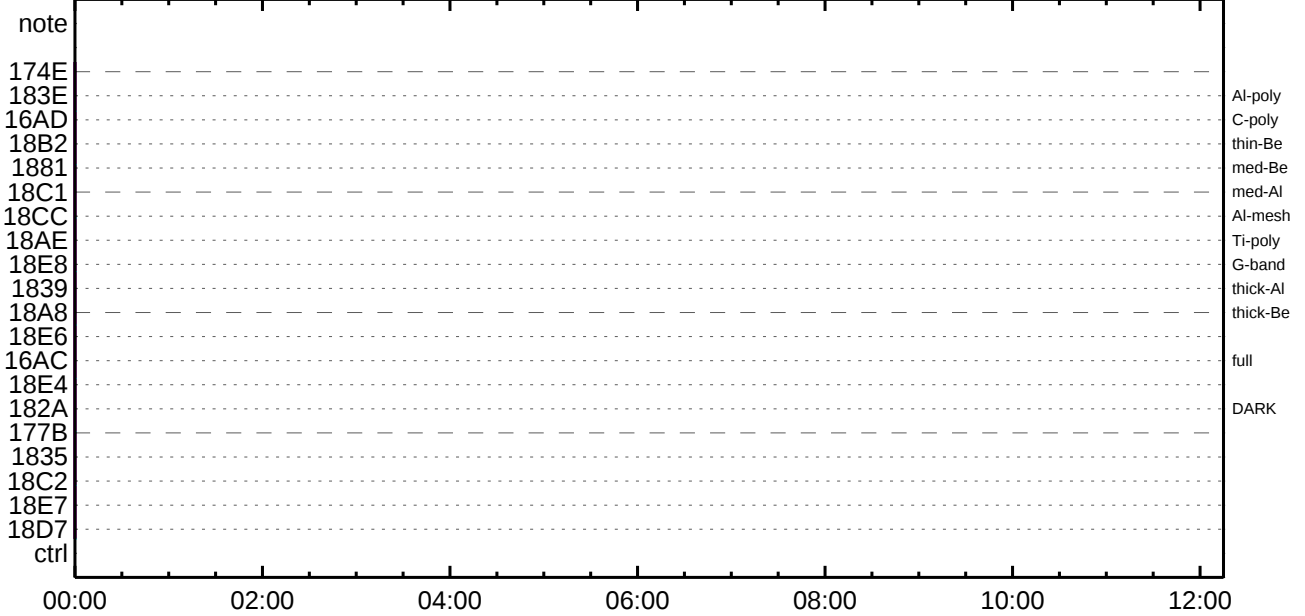
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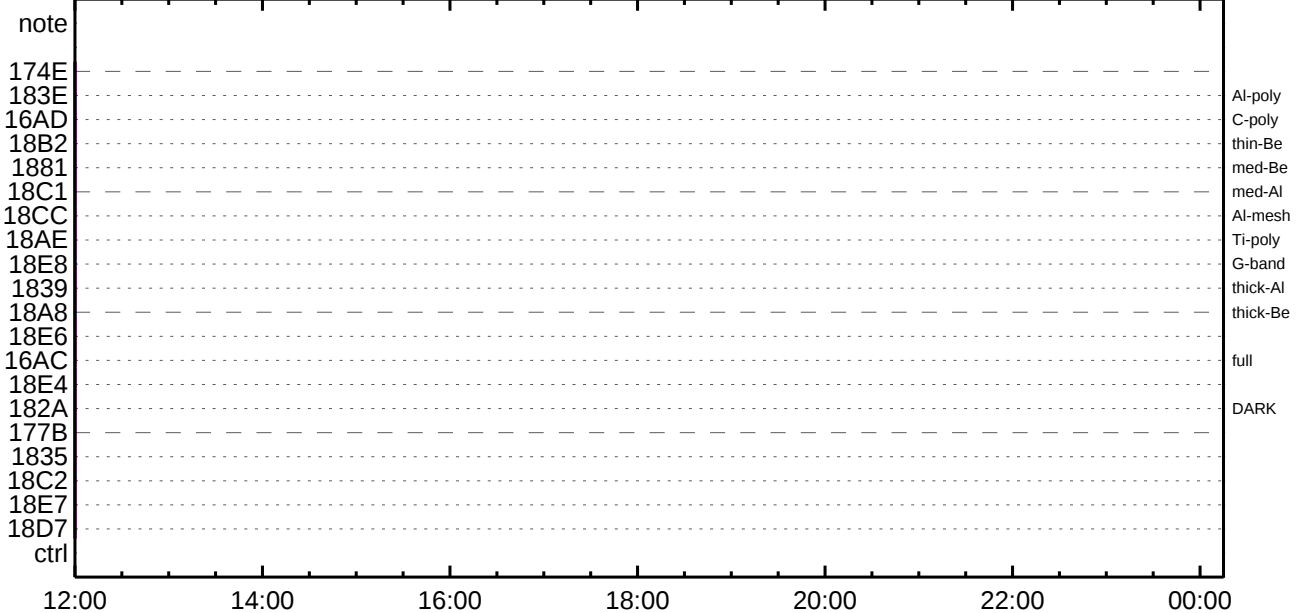
CMDI #0625 2012/05/19



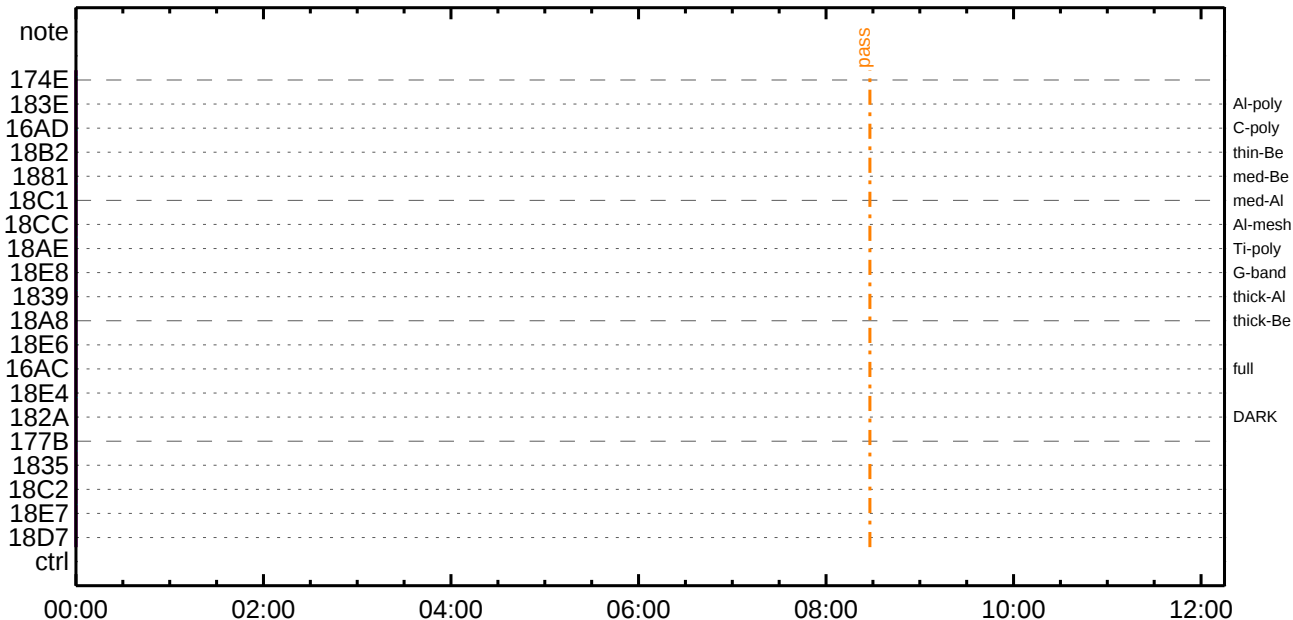
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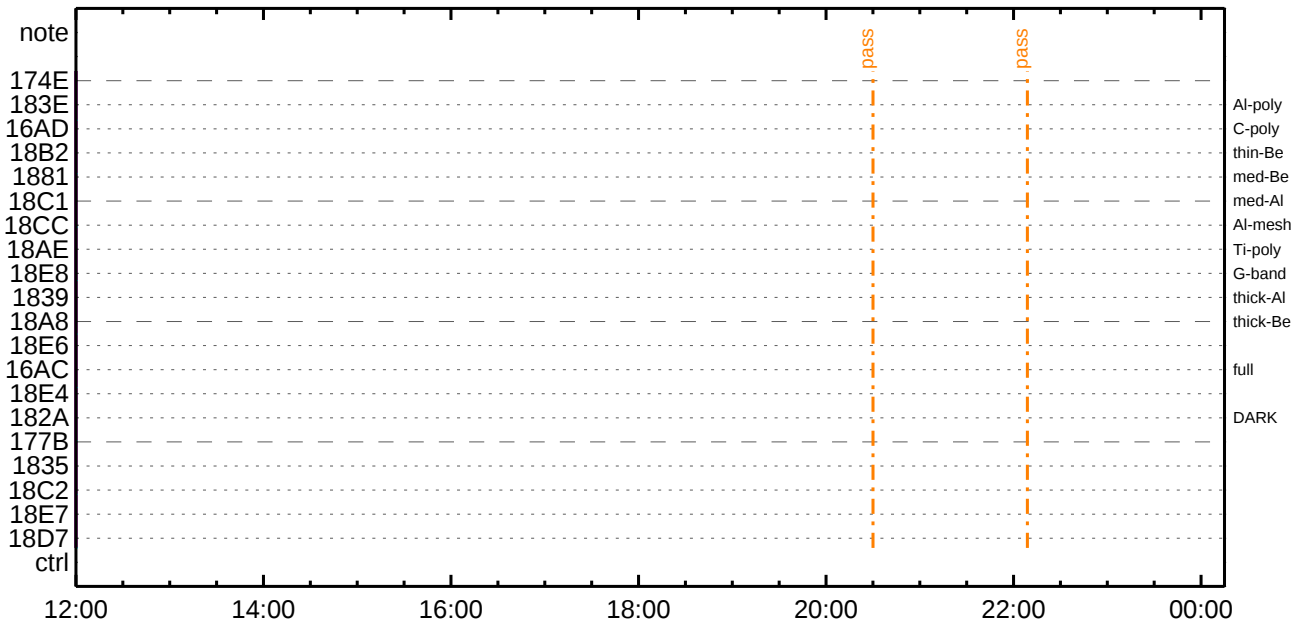
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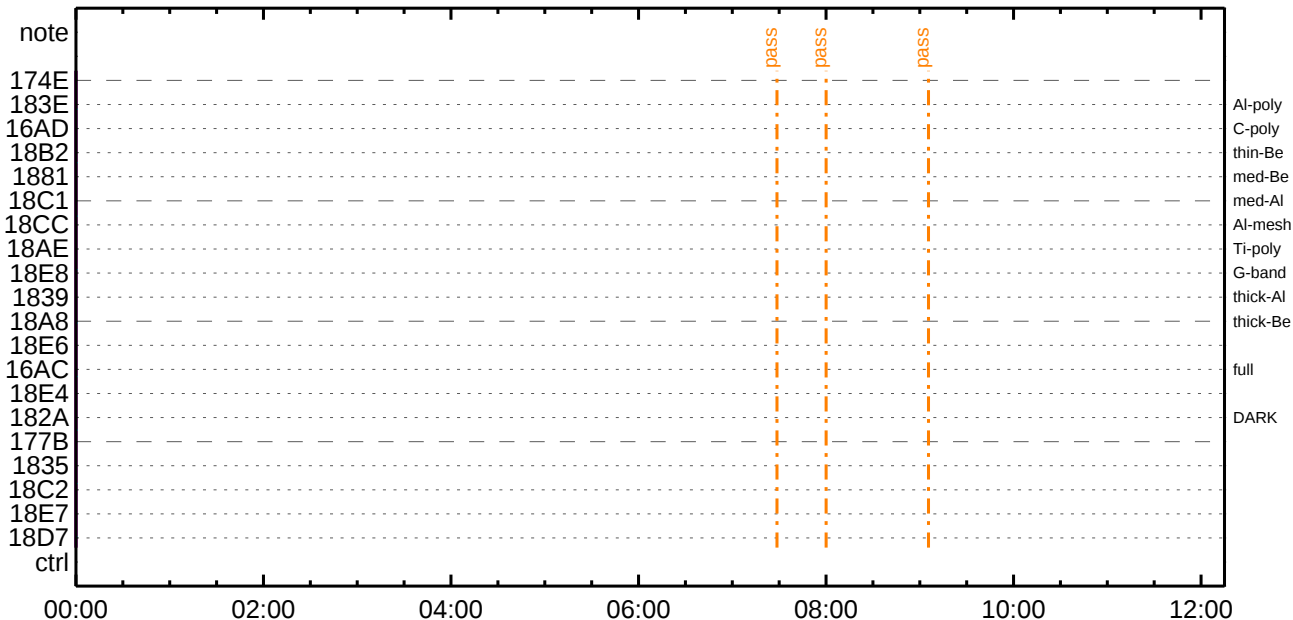
CMDI #0625 2012/05/21



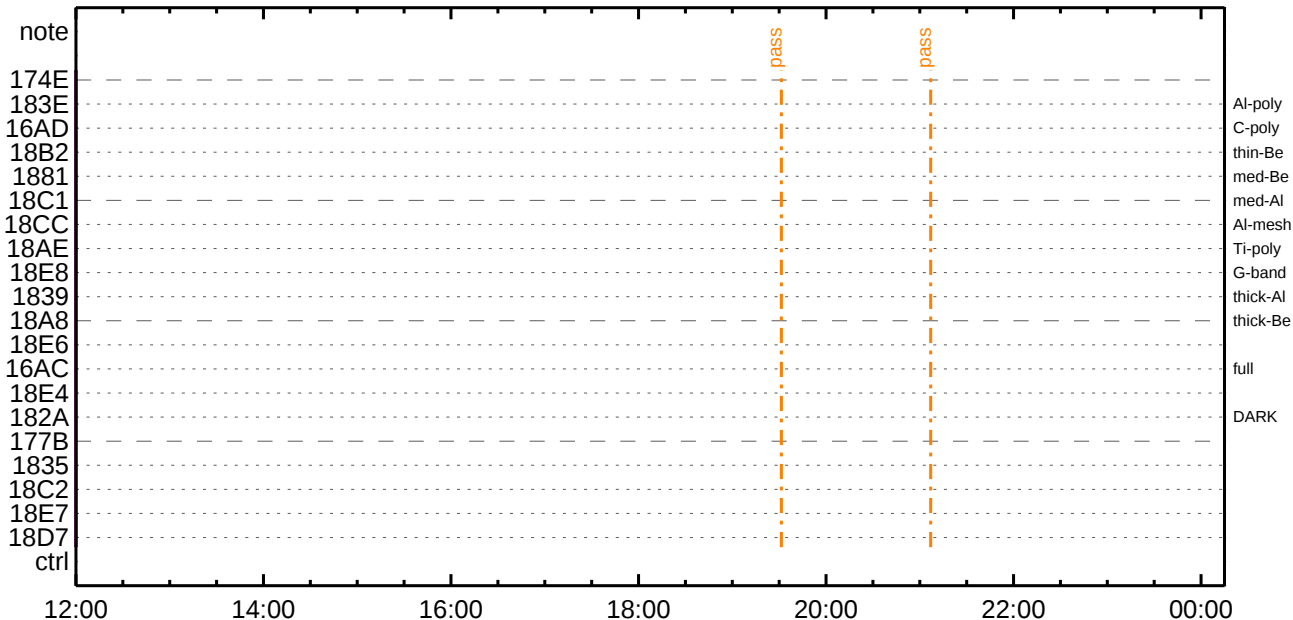
CMDI #0625 2012/05/21



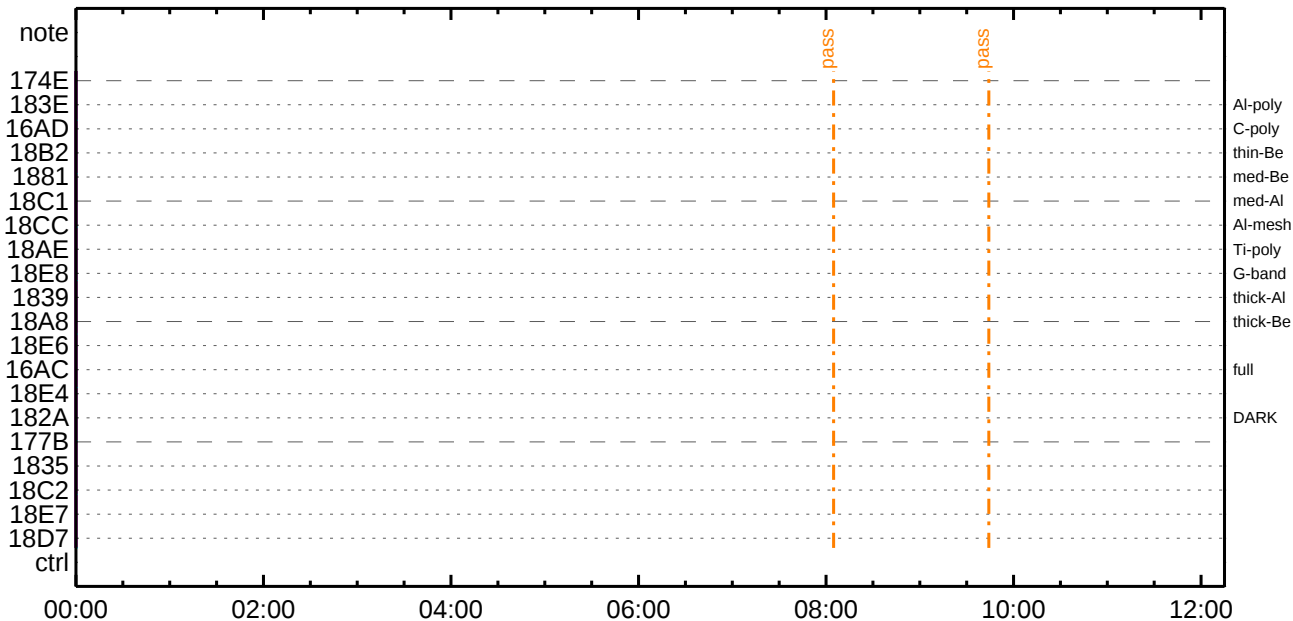
CMDI #0625 2012/05/22



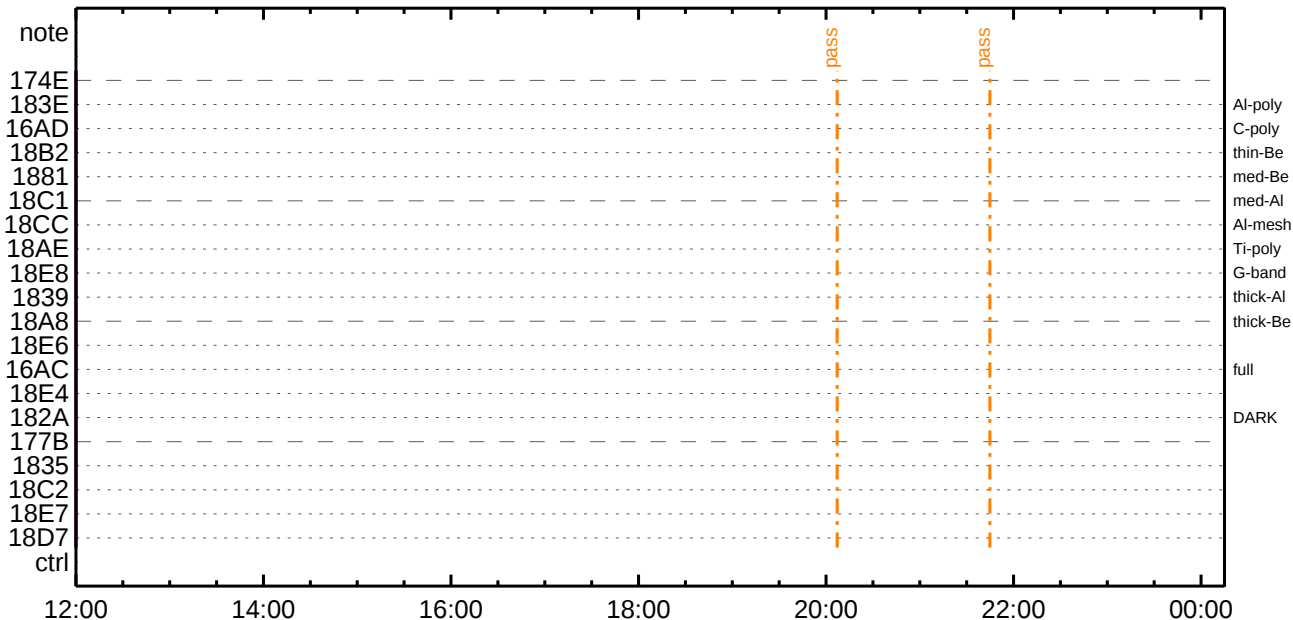
CMDI #0625 2012/05/22



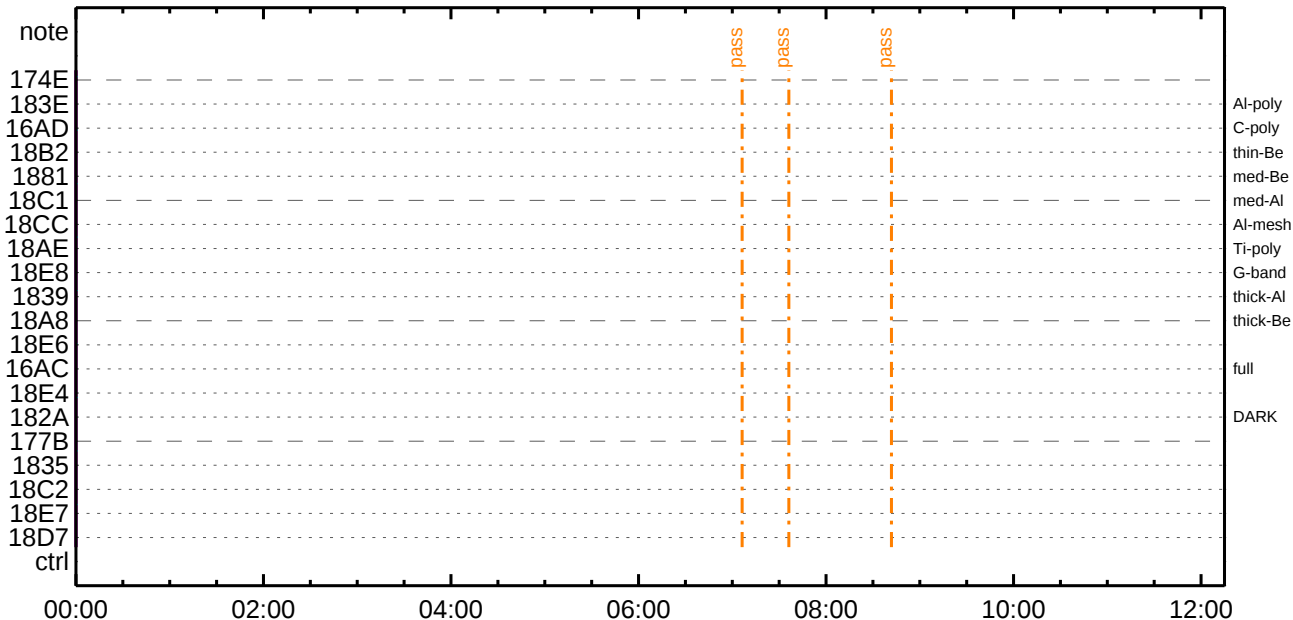
CMDI #0625 2012/05/23



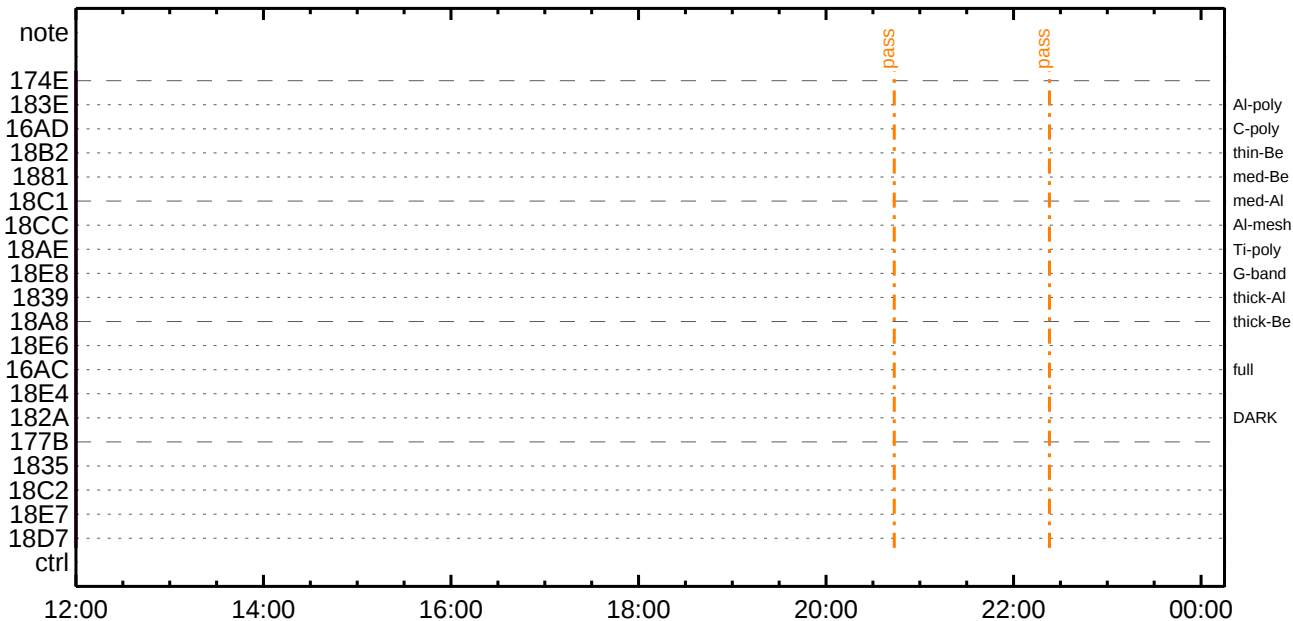
CMDI #0625 2012/05/23



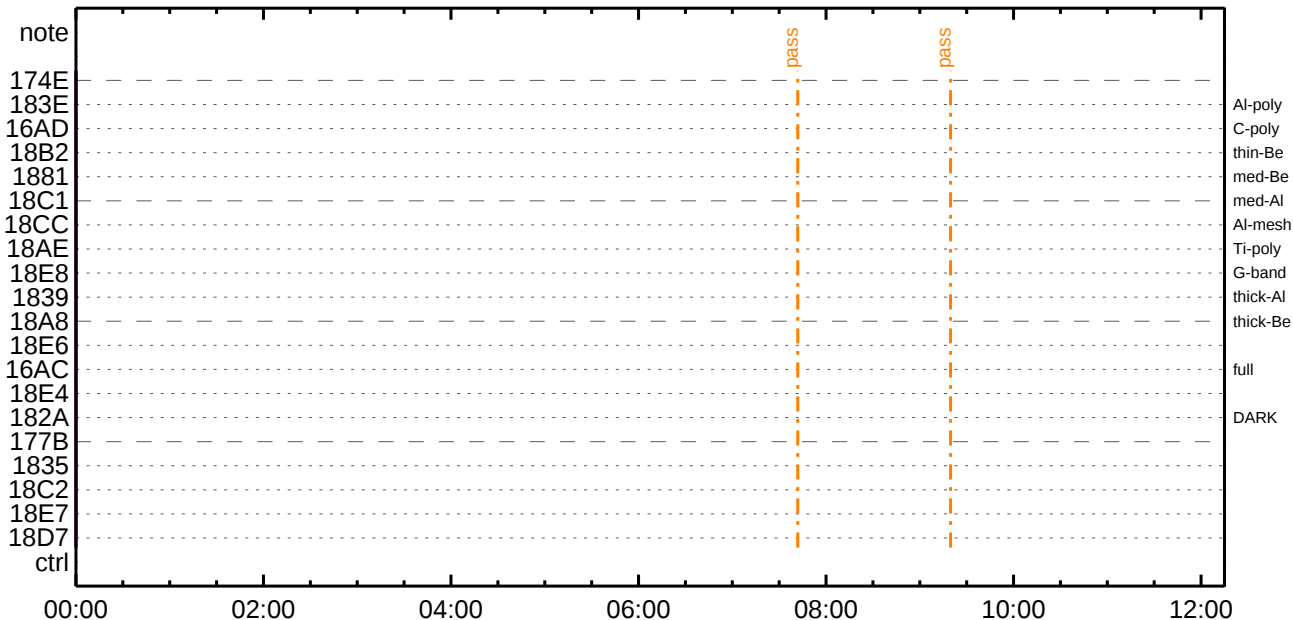
CMDI #0625 2012/05/24



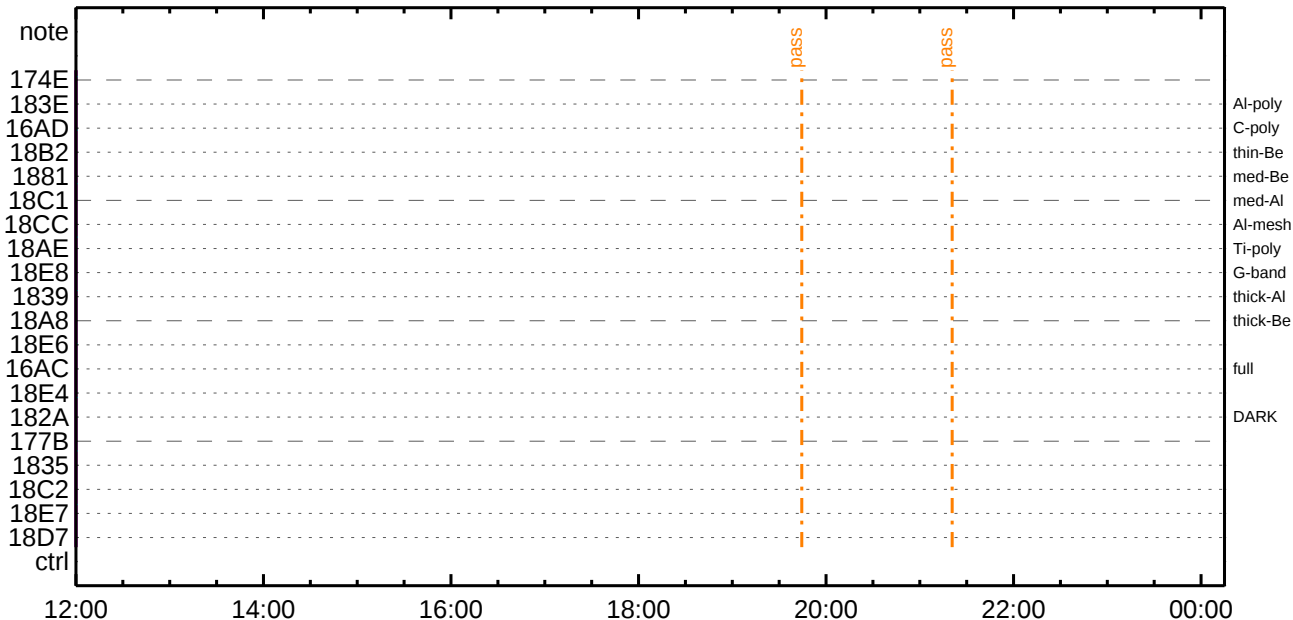
CMDI #0625 2012/05/24



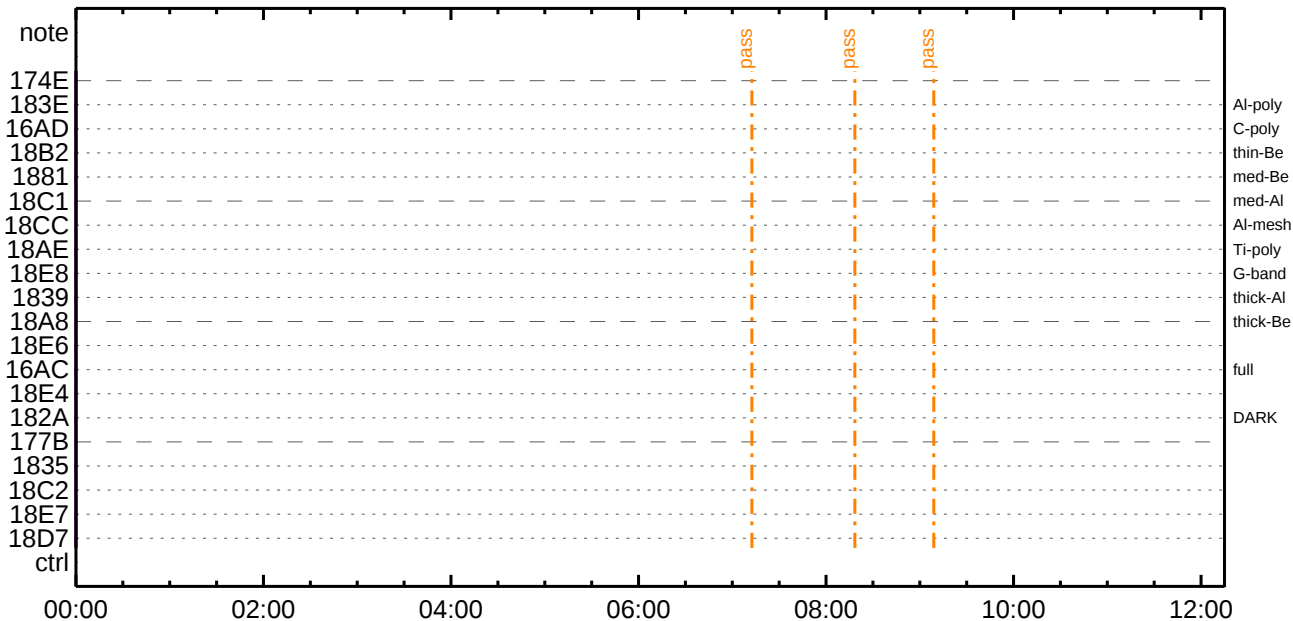
CMDI #0625 2012/05/25



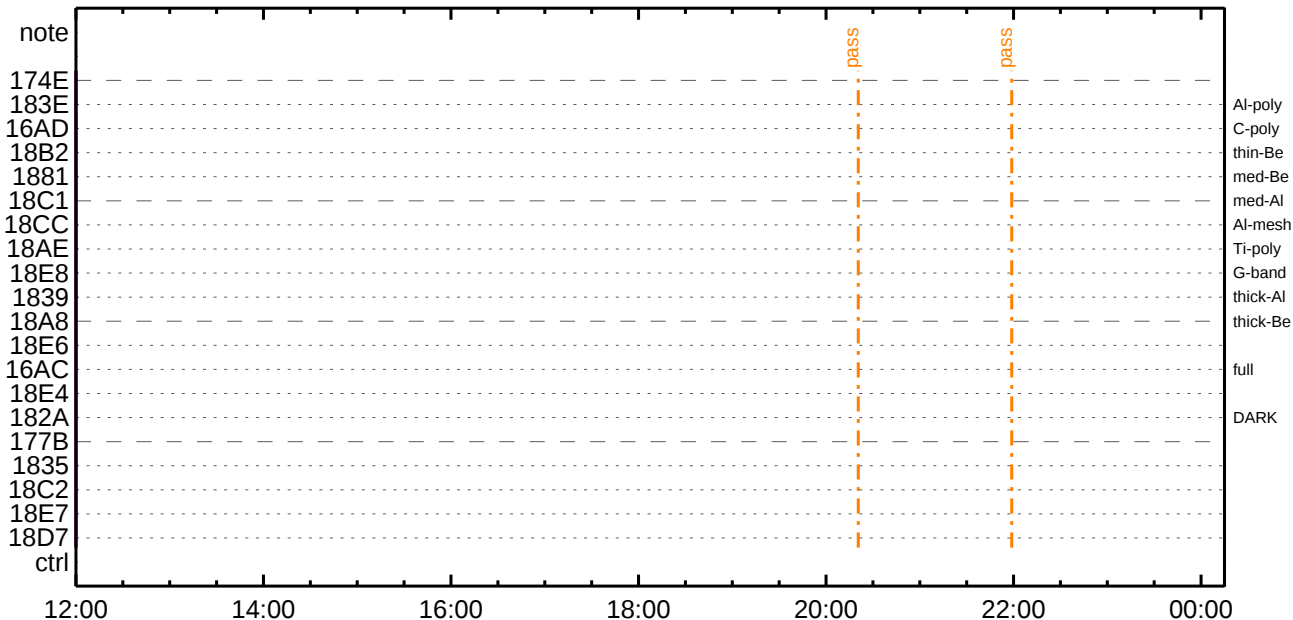
CMDI #0625 2012/05/25



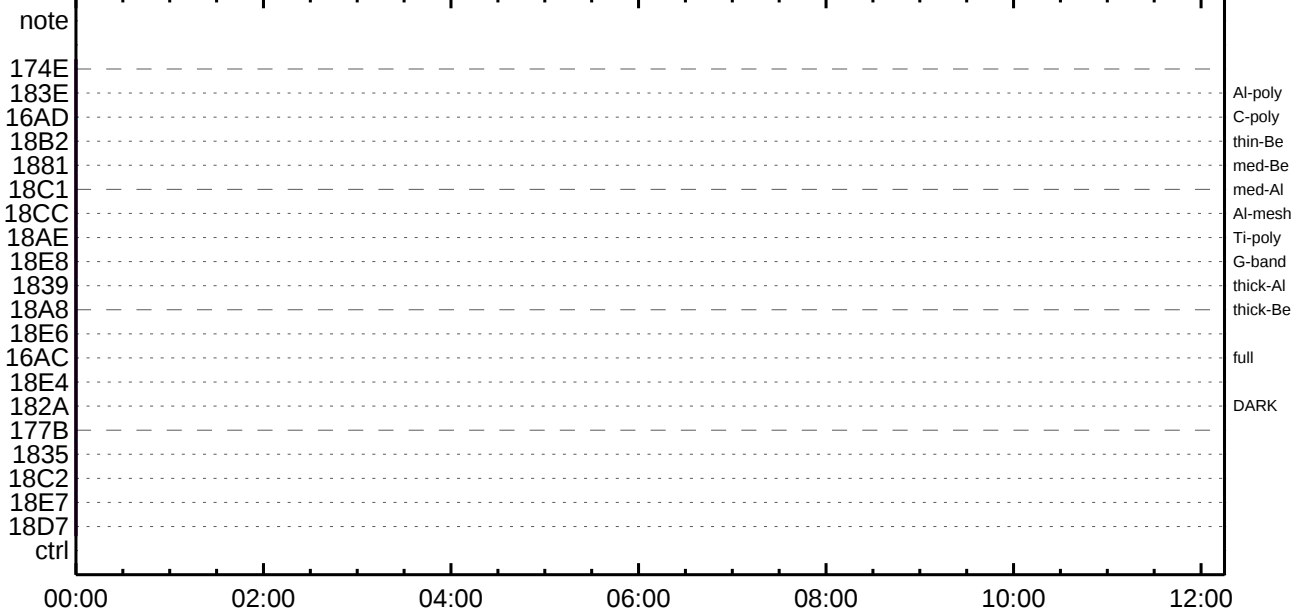
CMDI #0625 2012/05/26



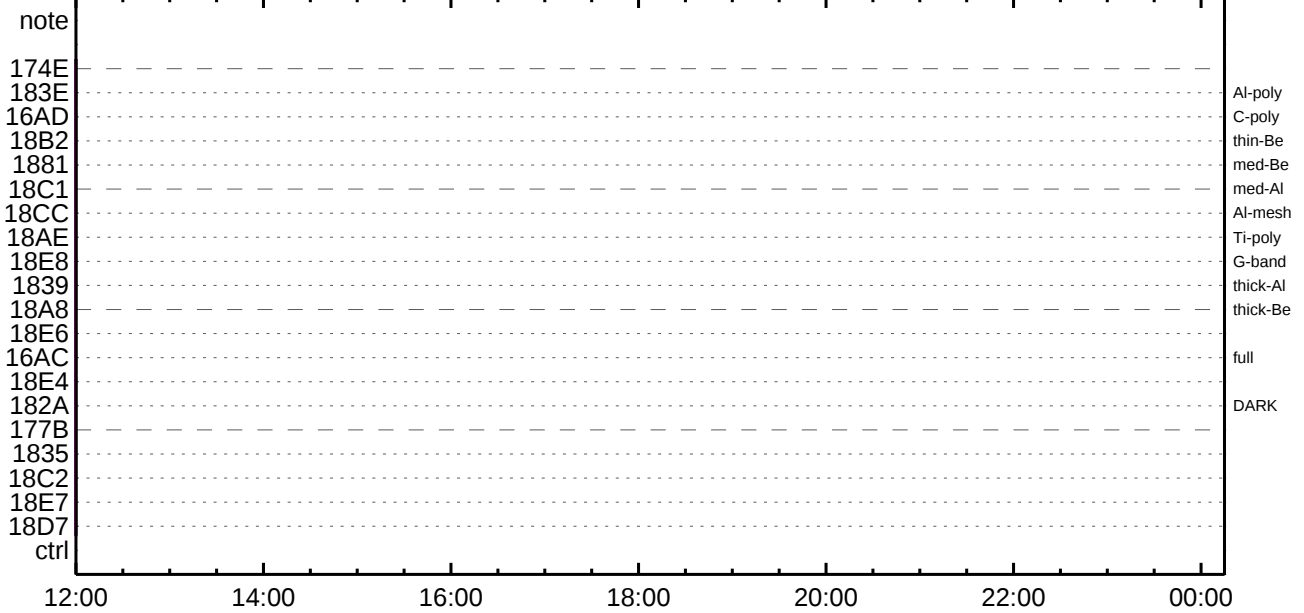
CMDI #0625 2012/05/26



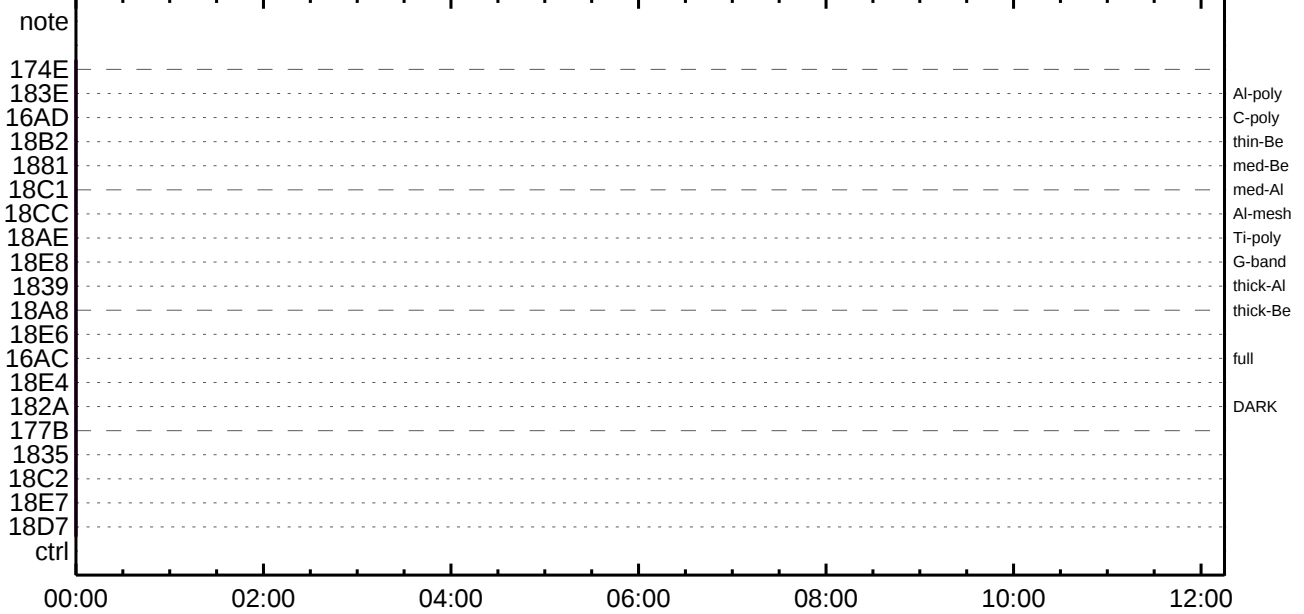
CMDI #0625 2012/05/27



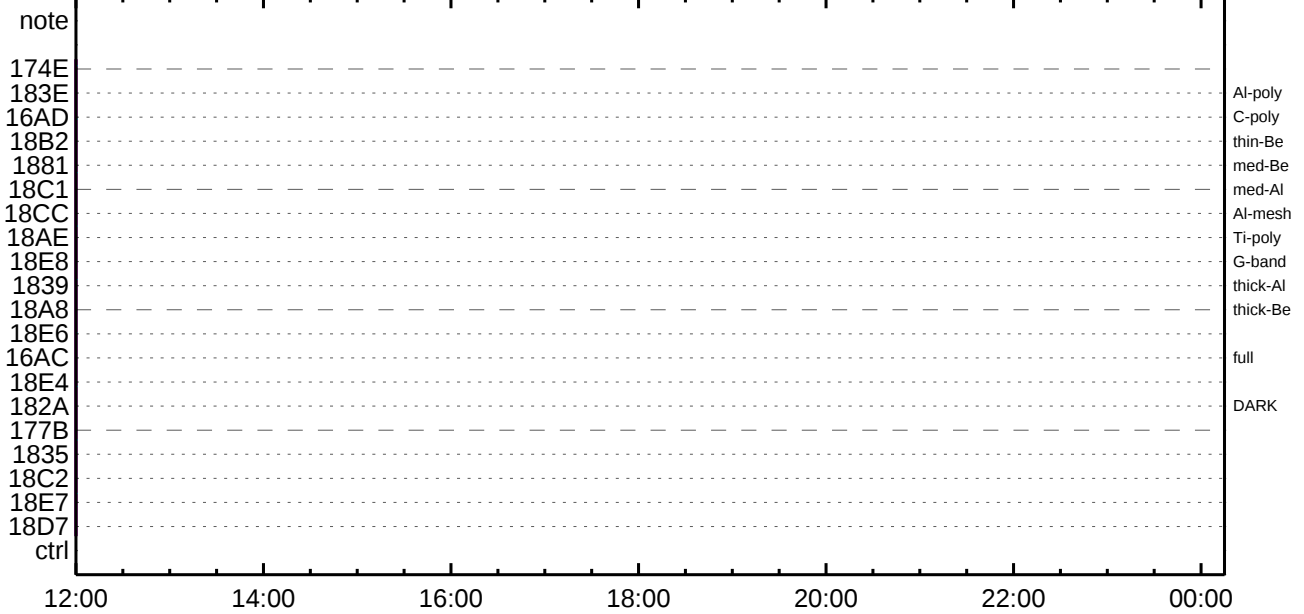
CMDI #0625 2012/05/27



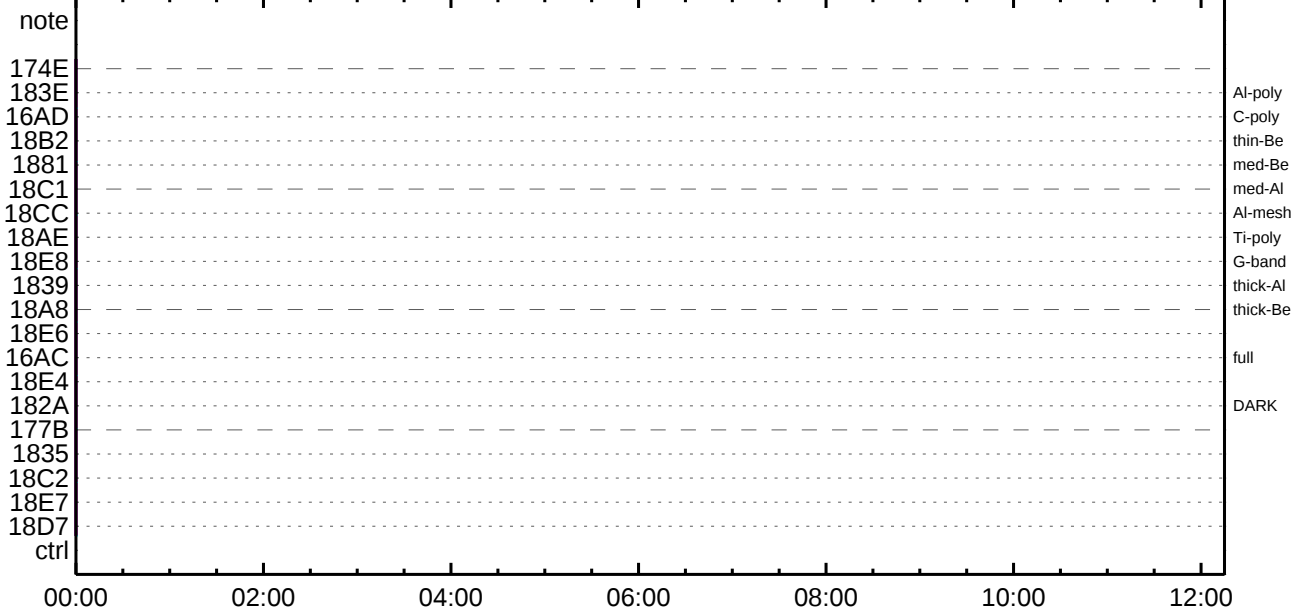
CMDI #0625 2012/05/28



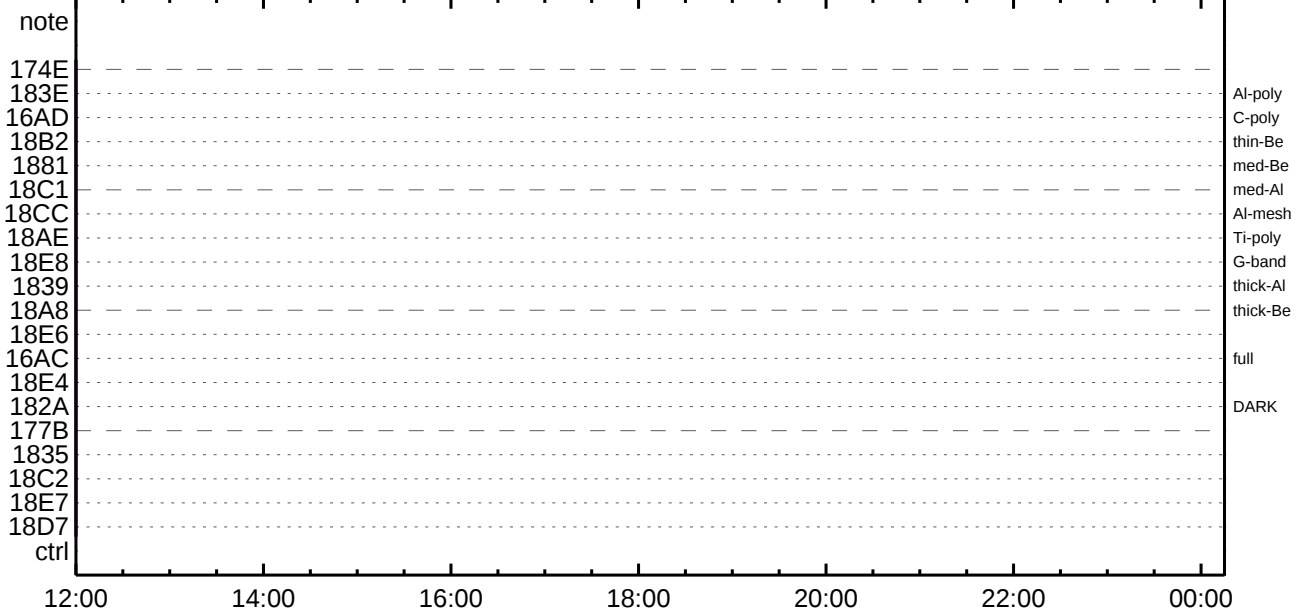
CMDI #0625 2012/05/28



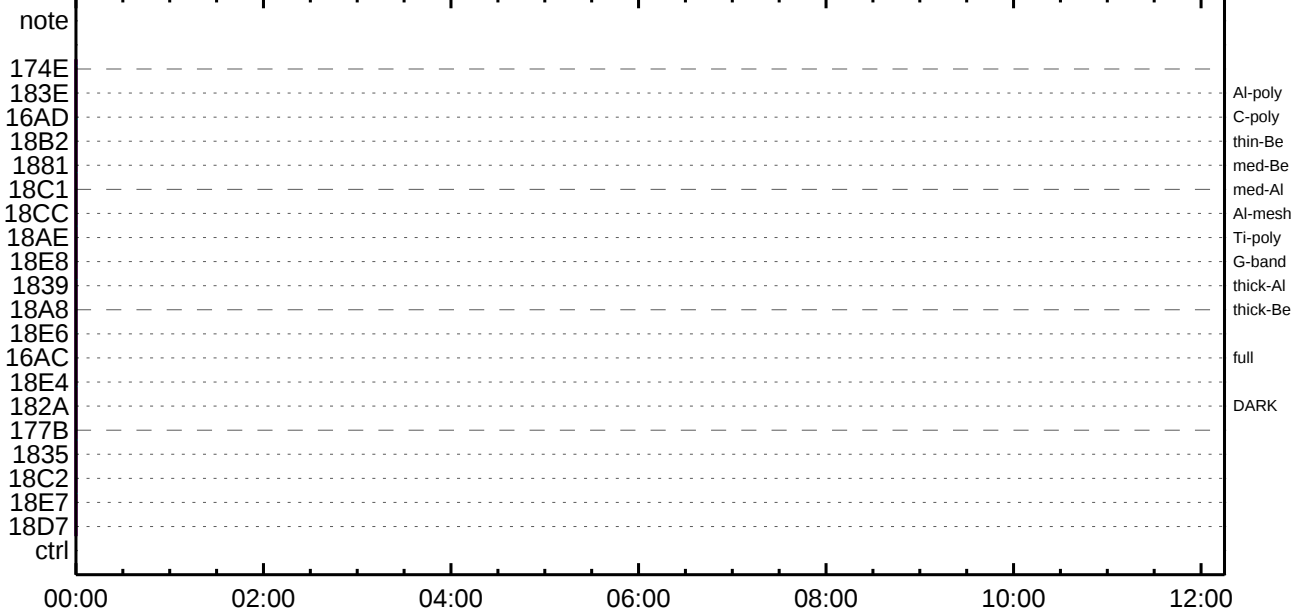
CMDI #0625 2012/05/29



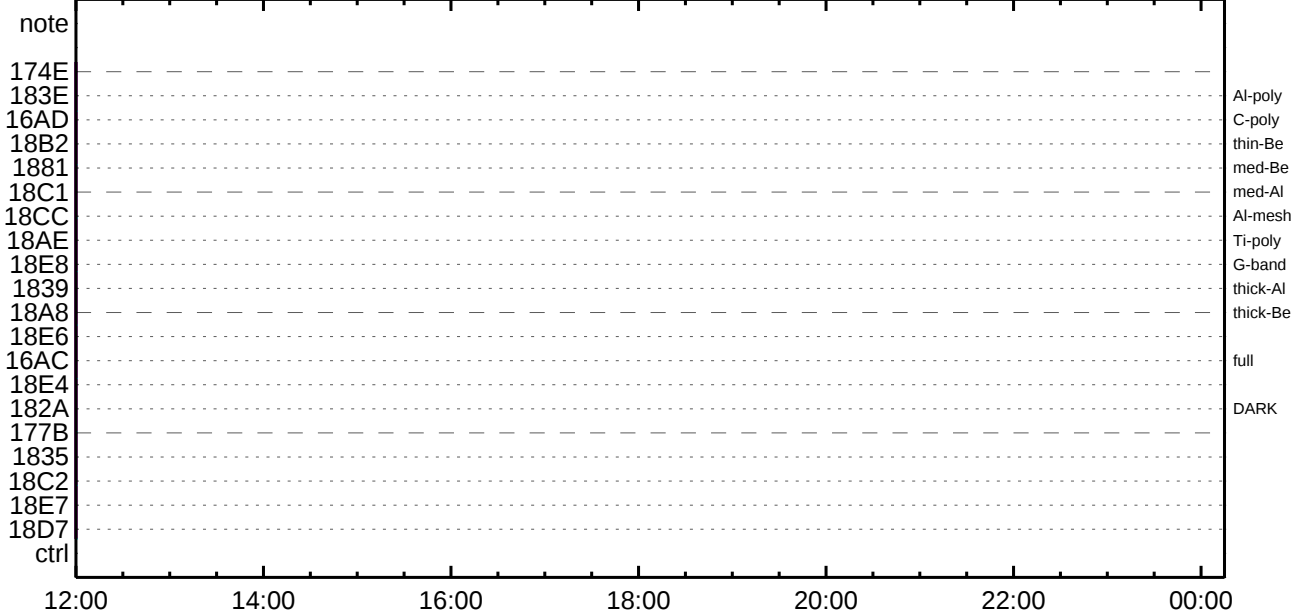
CMDI #0625 2012/05/29



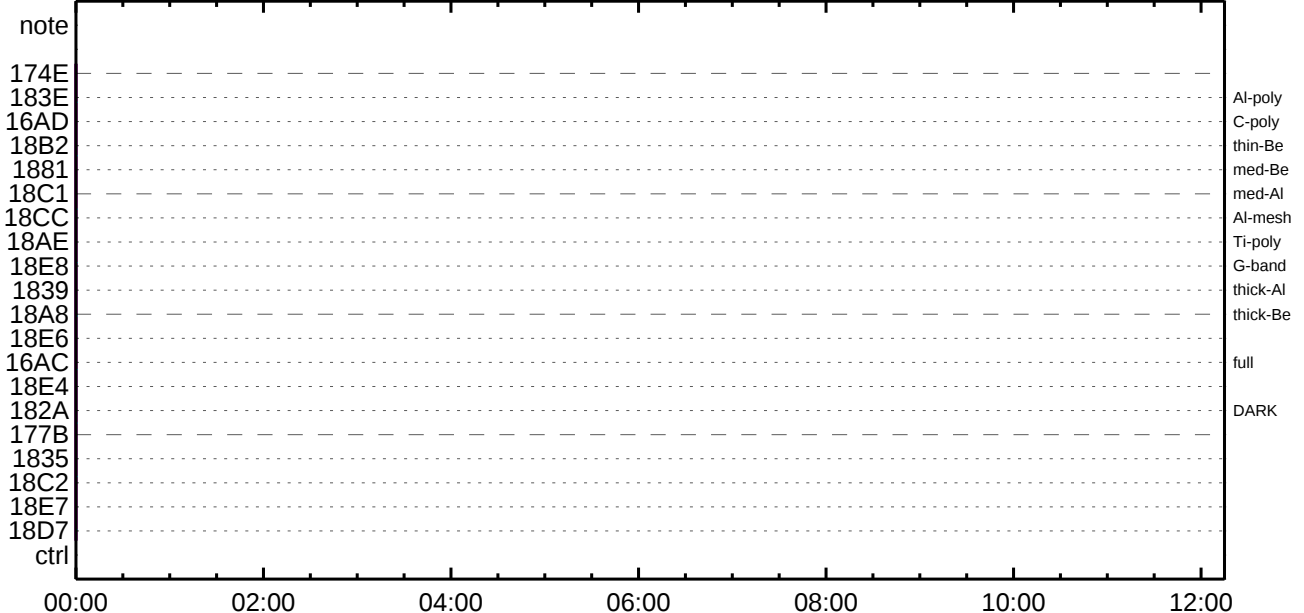
CMDI #0625 2012/05/30



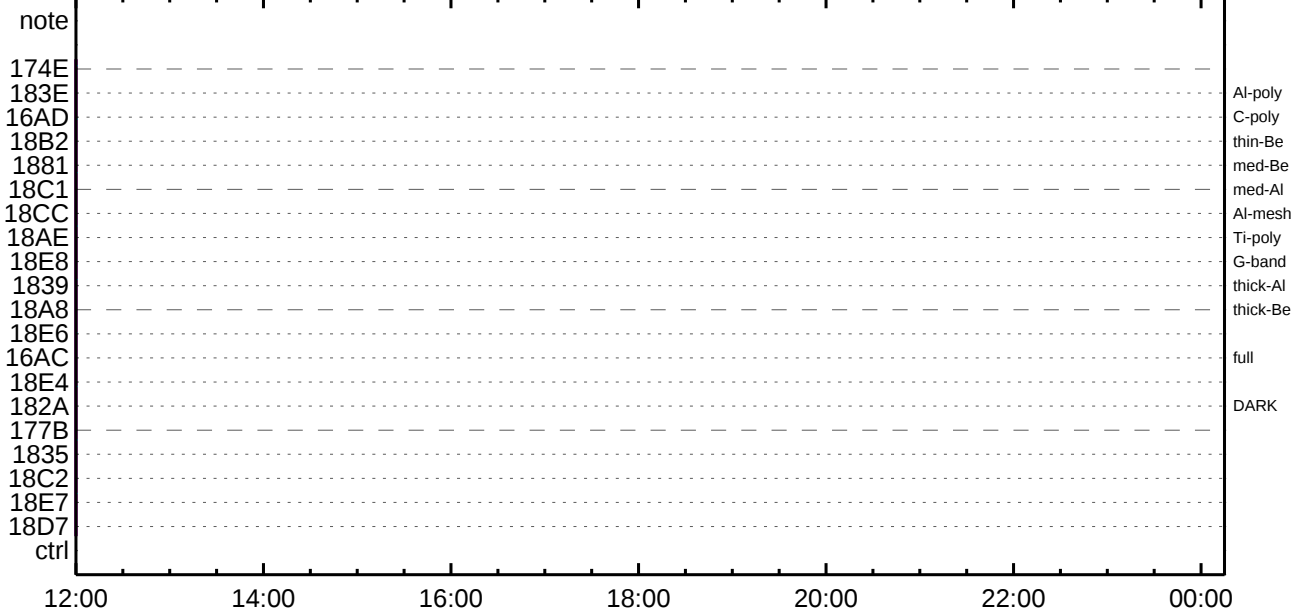
CMDI #0625 2012/05/30



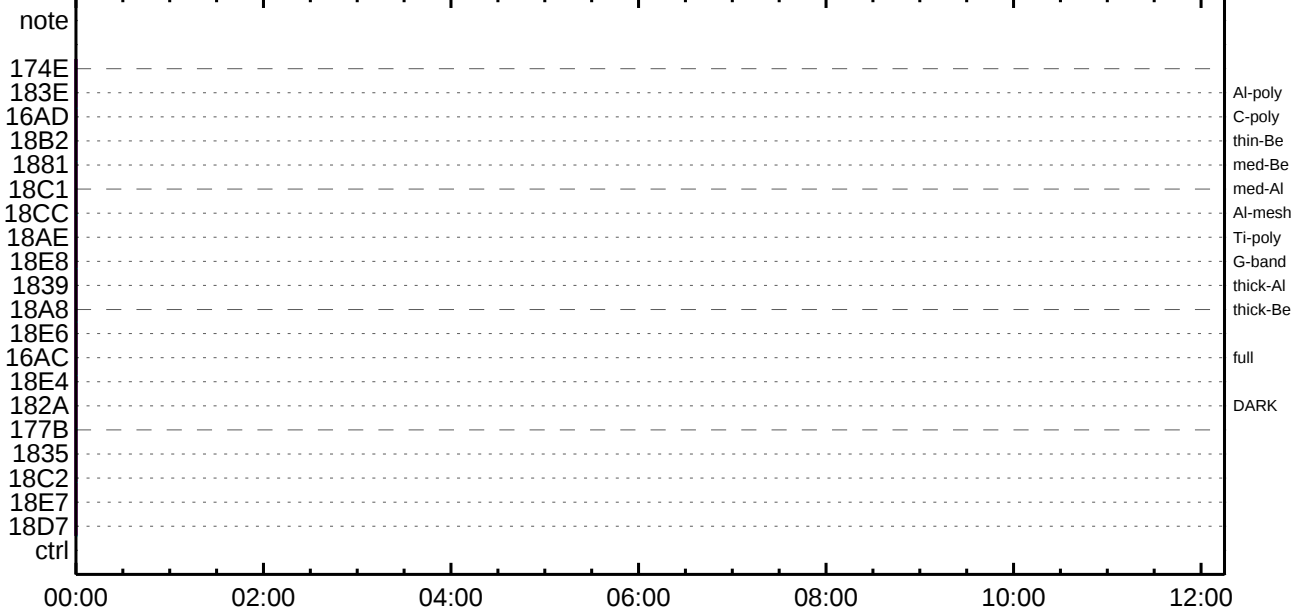
CMDI #0625 2012/05/31



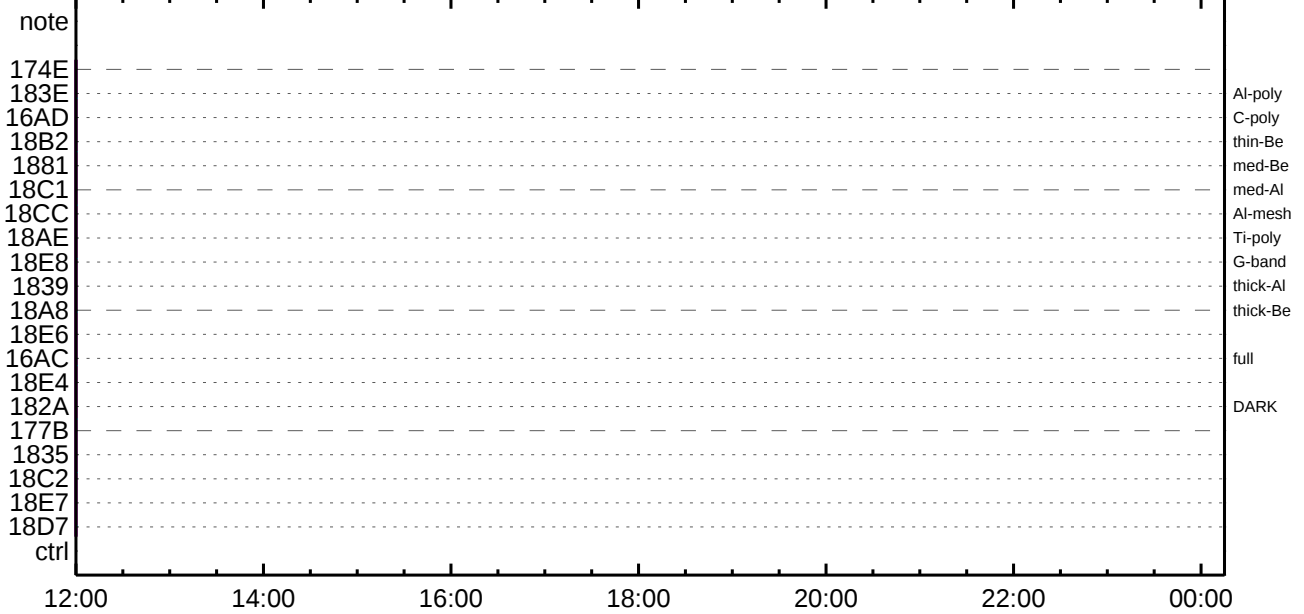
CMDI #0625 2012/05/31



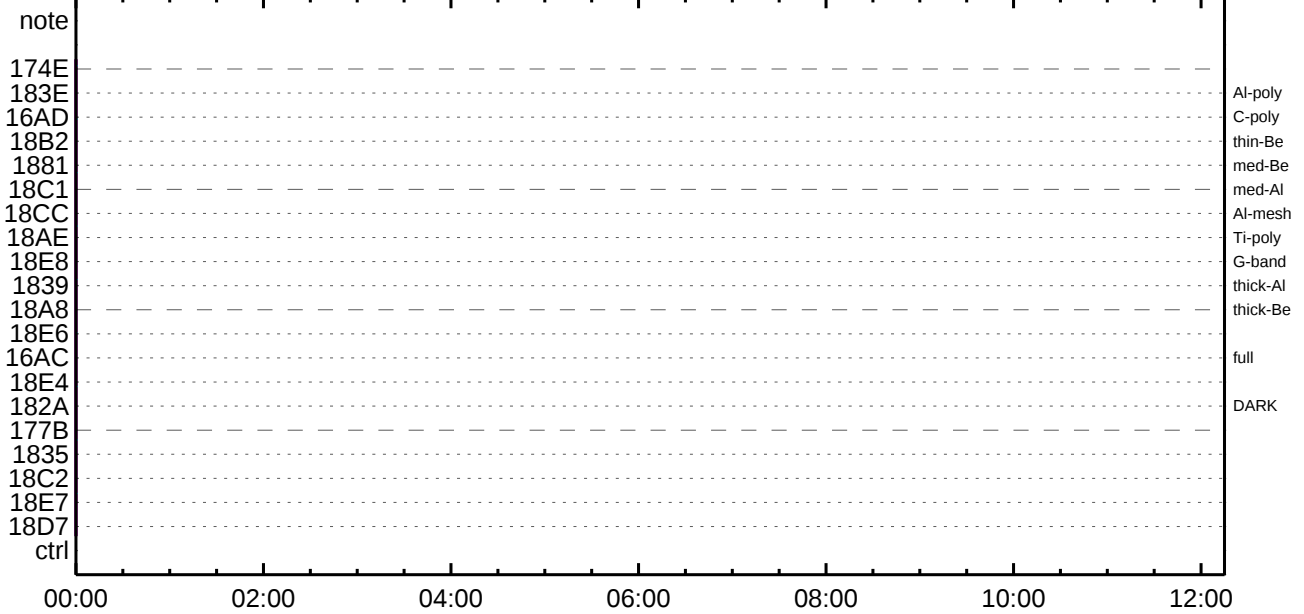
CMDI #0625 2012/06/01



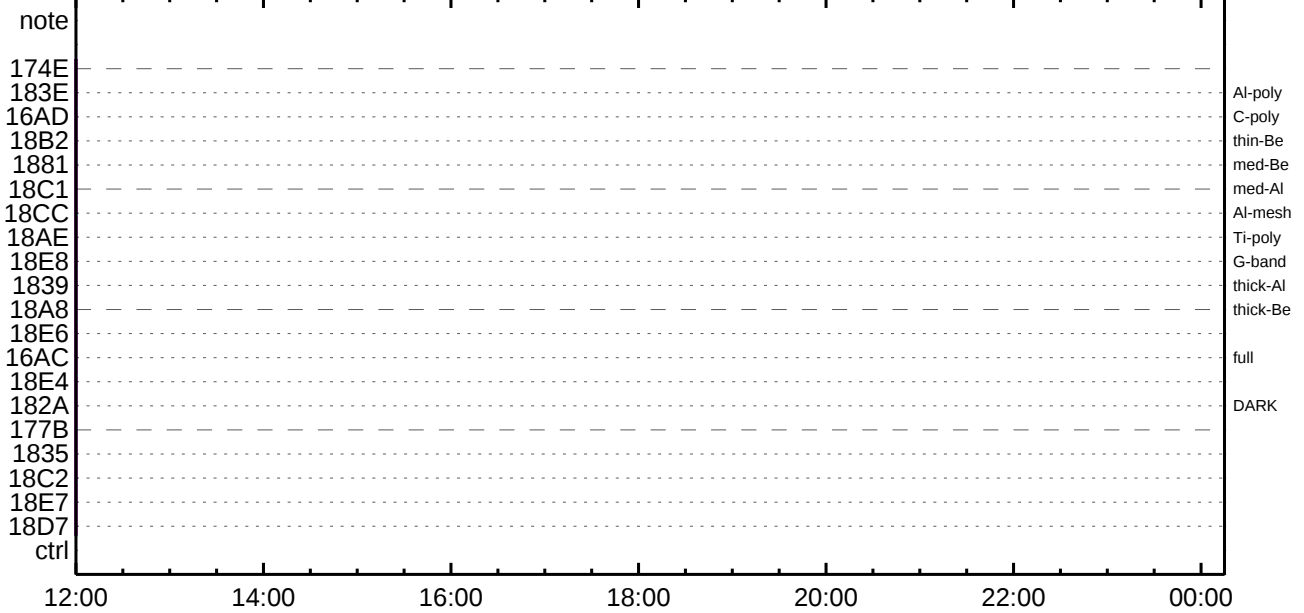
CMDI #0625 2012/06/01



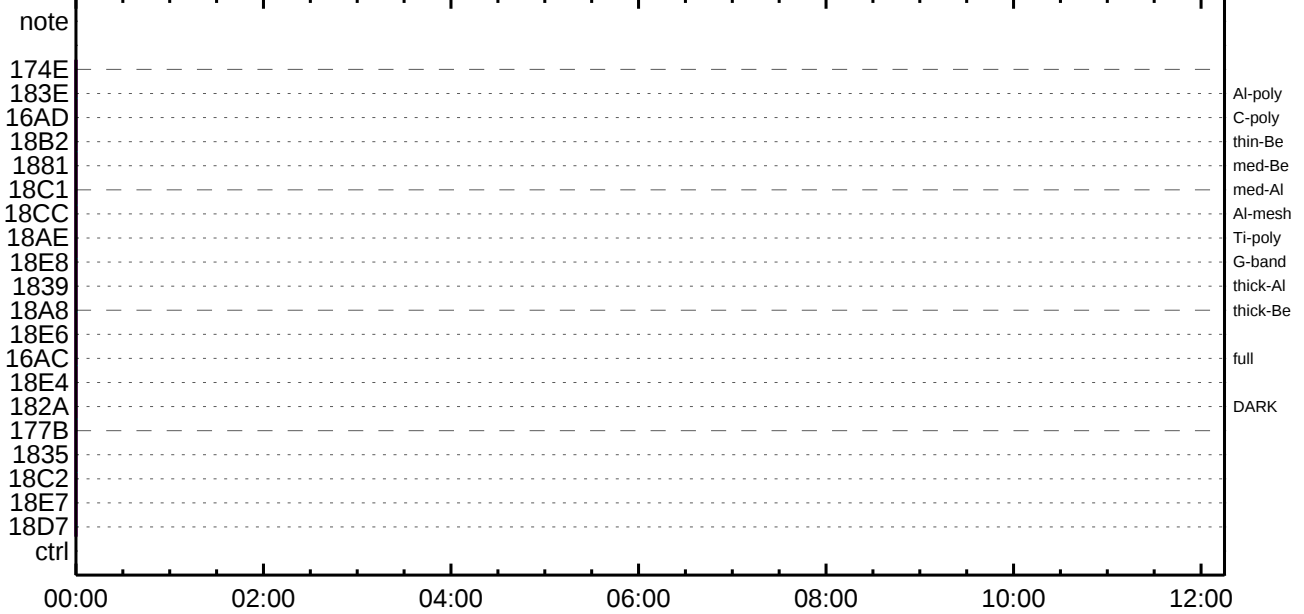
CMDI #0625 2012/06/02



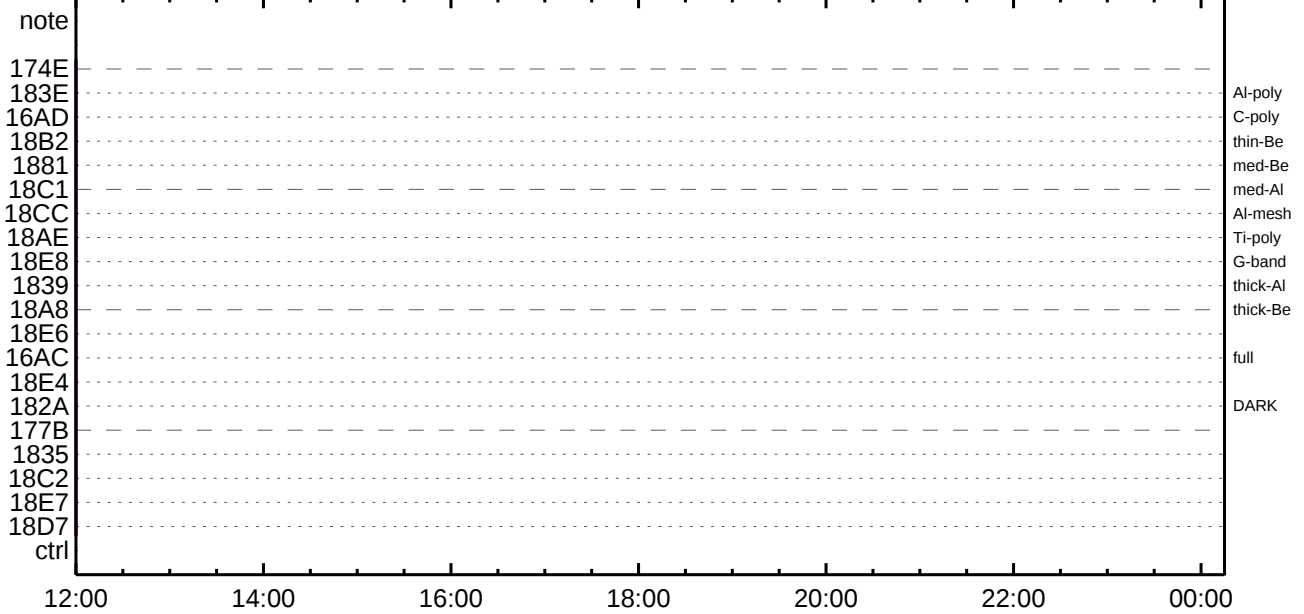
CMDI #0625 2012/06/02



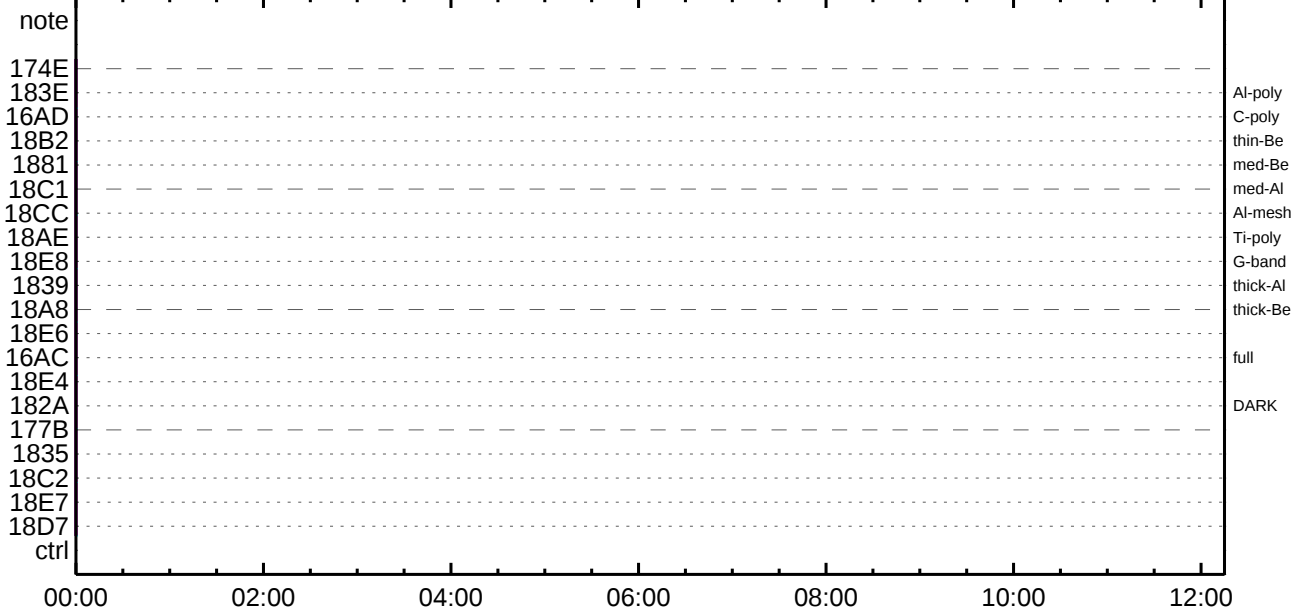
CMDI #0625 2012/06/03



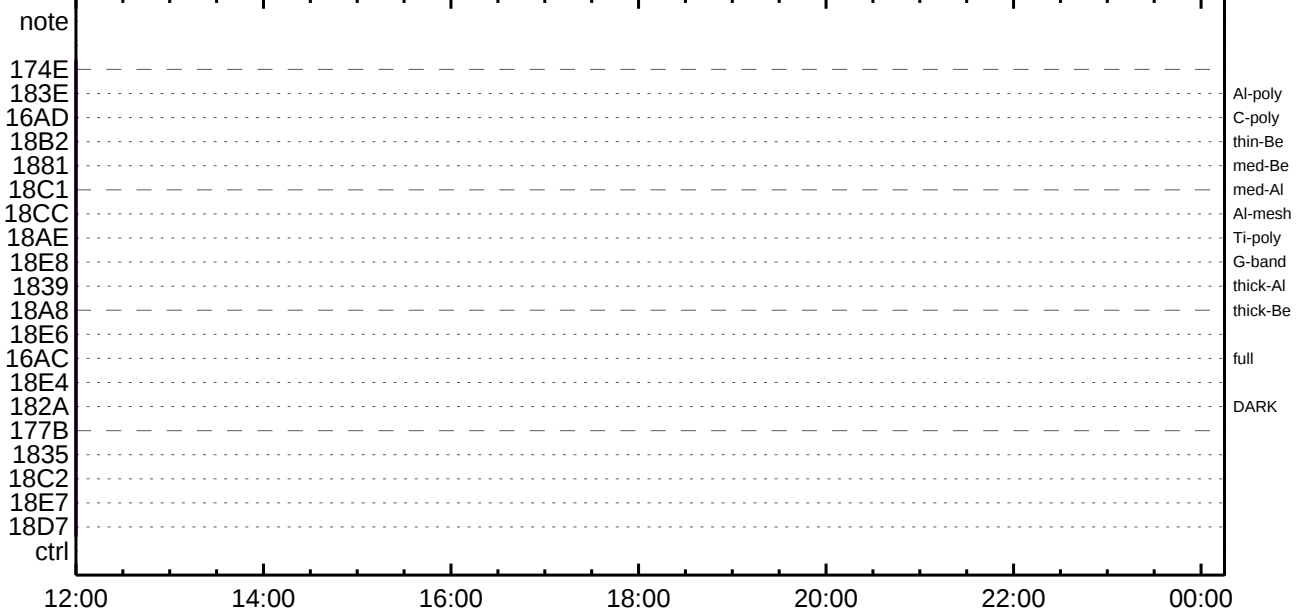
CMDI #0625 2012/06/03



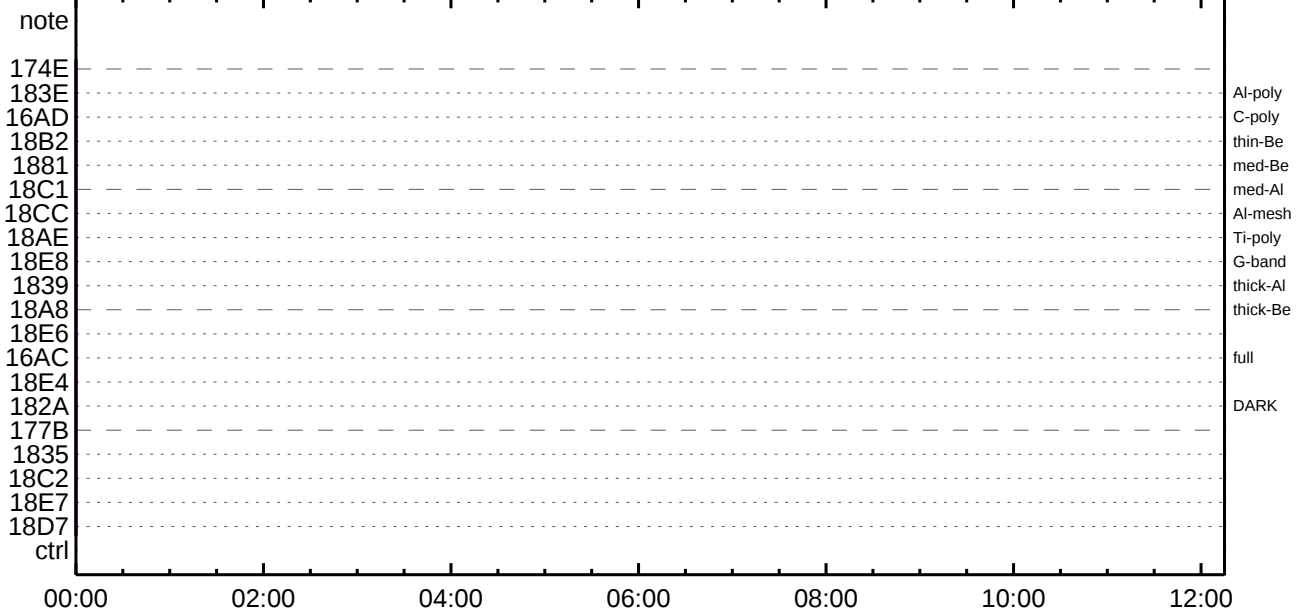
CMDI #0625 2012/06/04



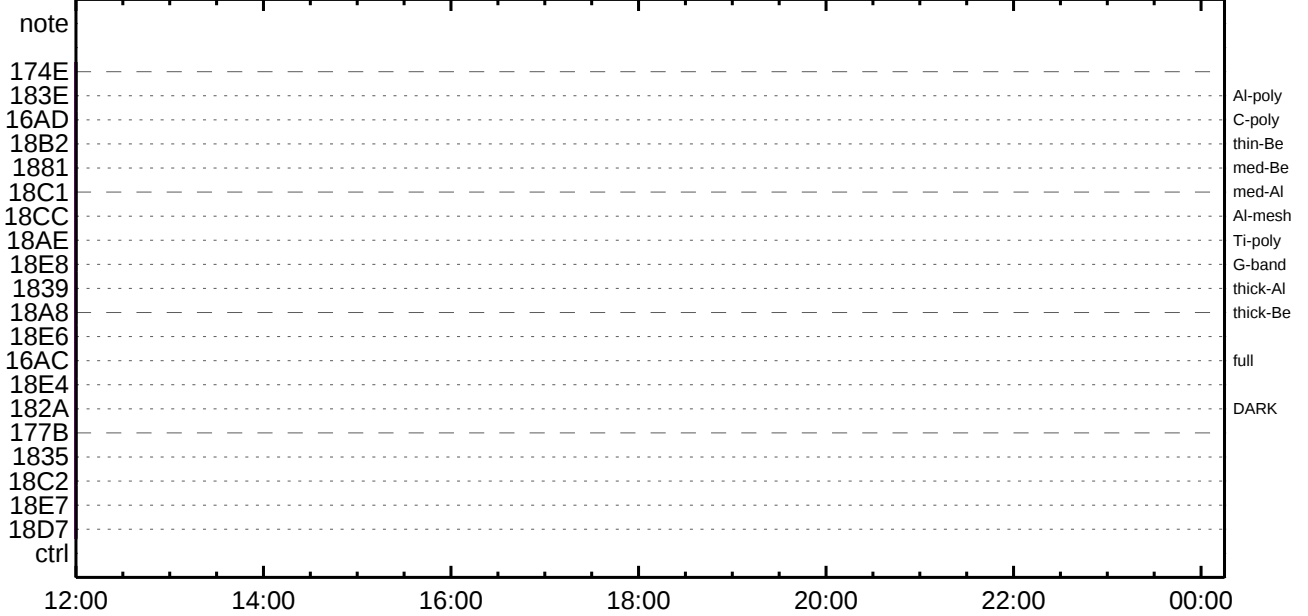
CMDI #0625 2012/06/04



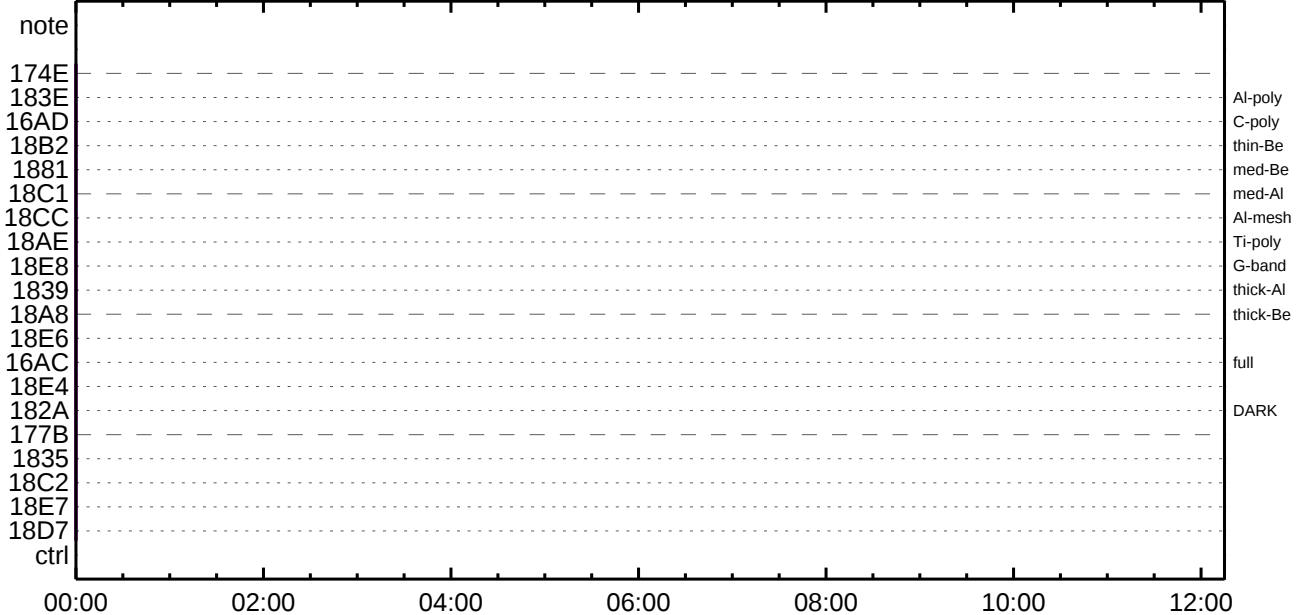
CMDI #0625 2012/06/05



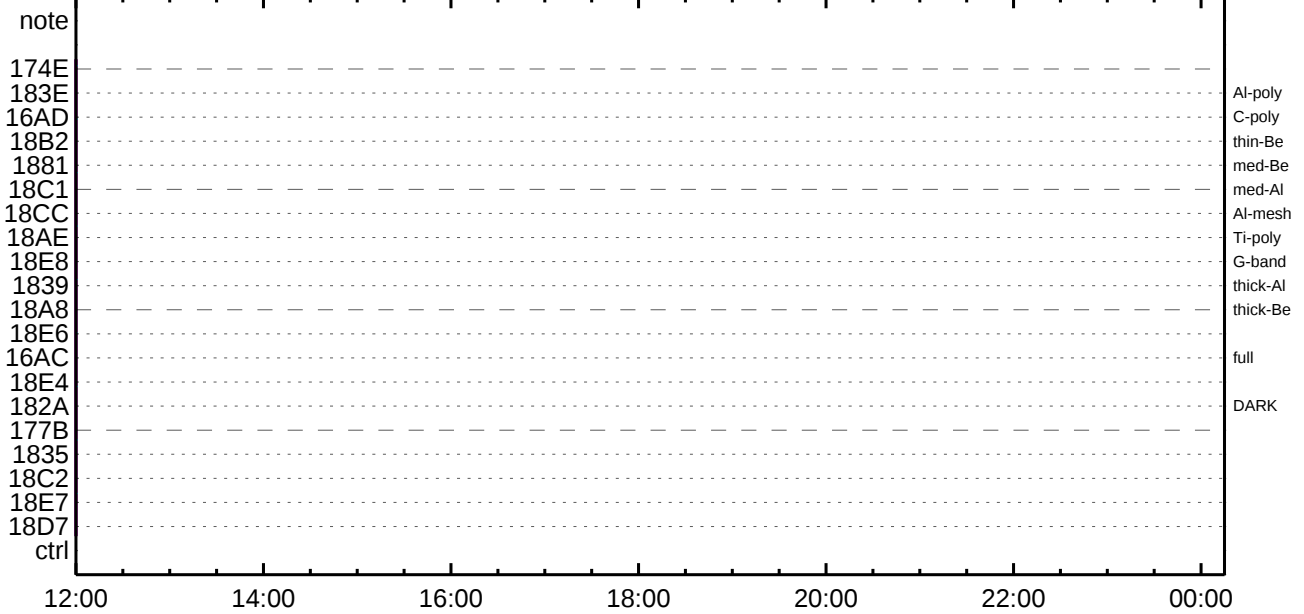
CMDI #0625 2012/06/05



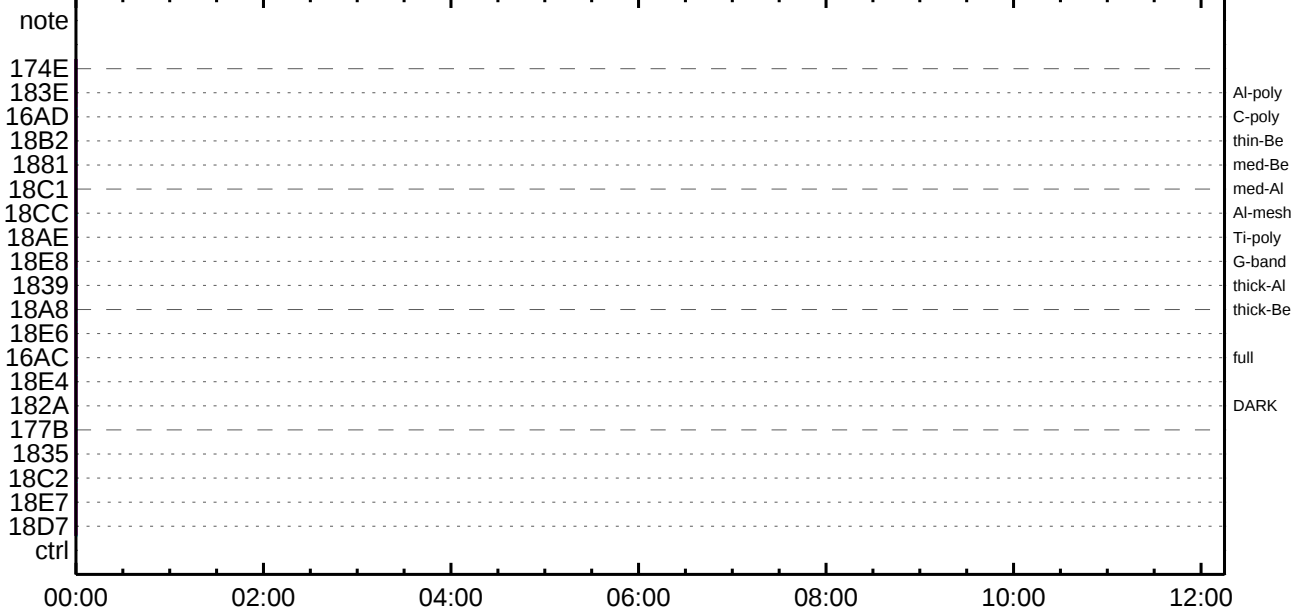
CMDI #0625 2012/06/06



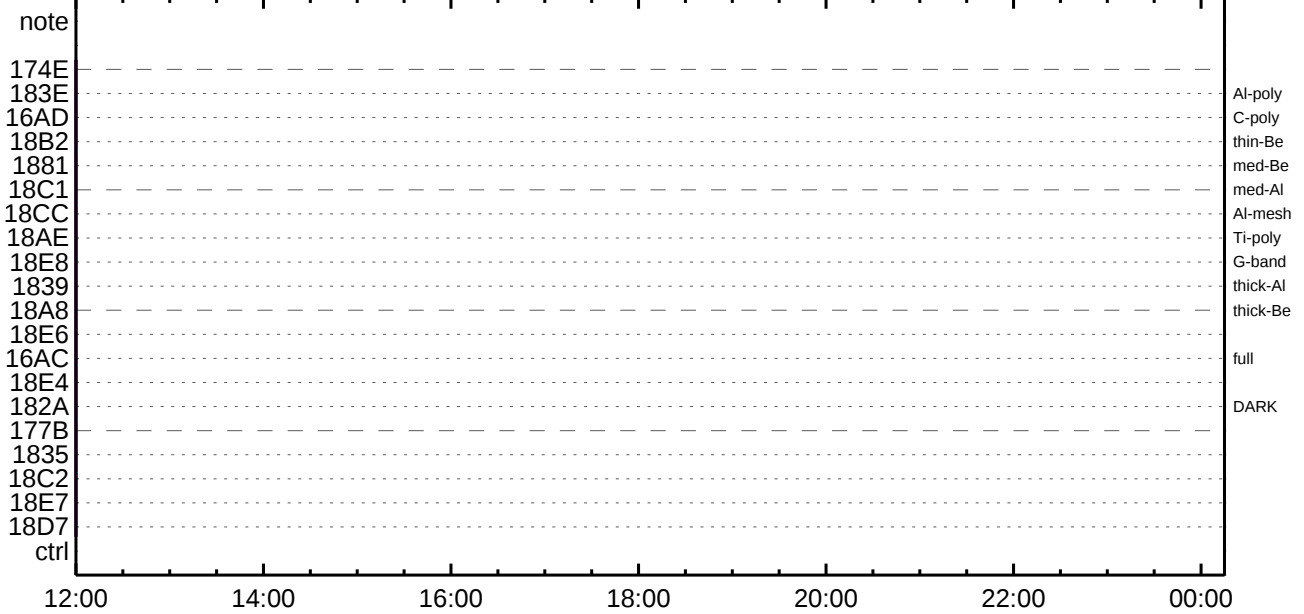
CMDI #0625 2012/06/06



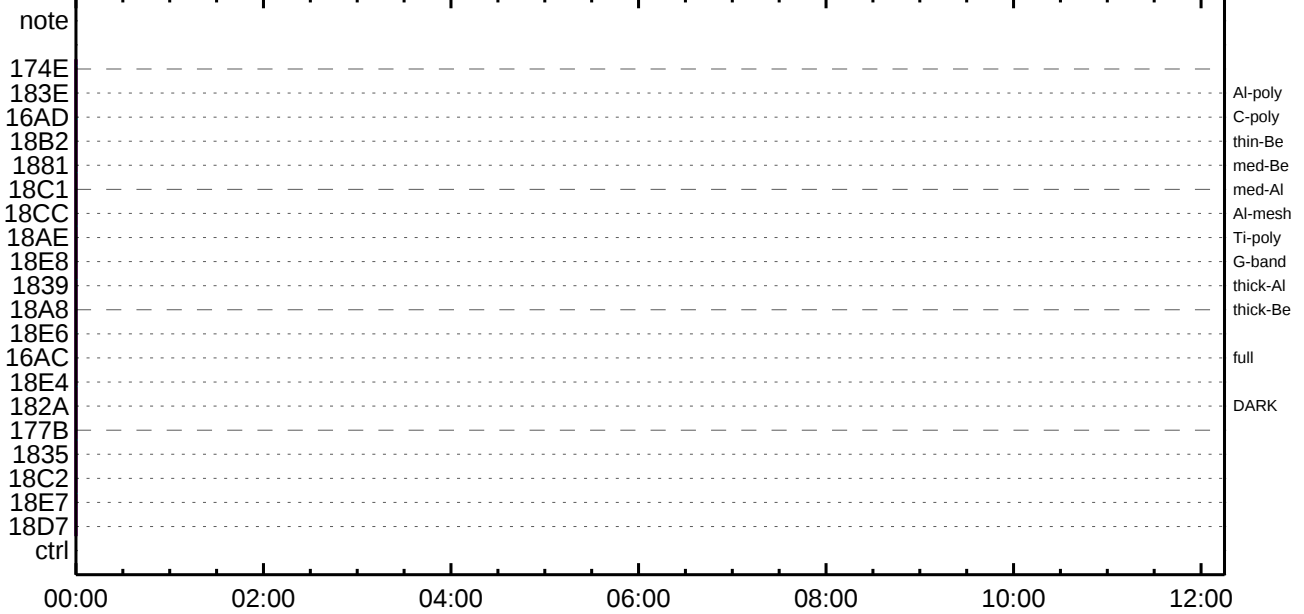
CMDI #0625 2012/06/07



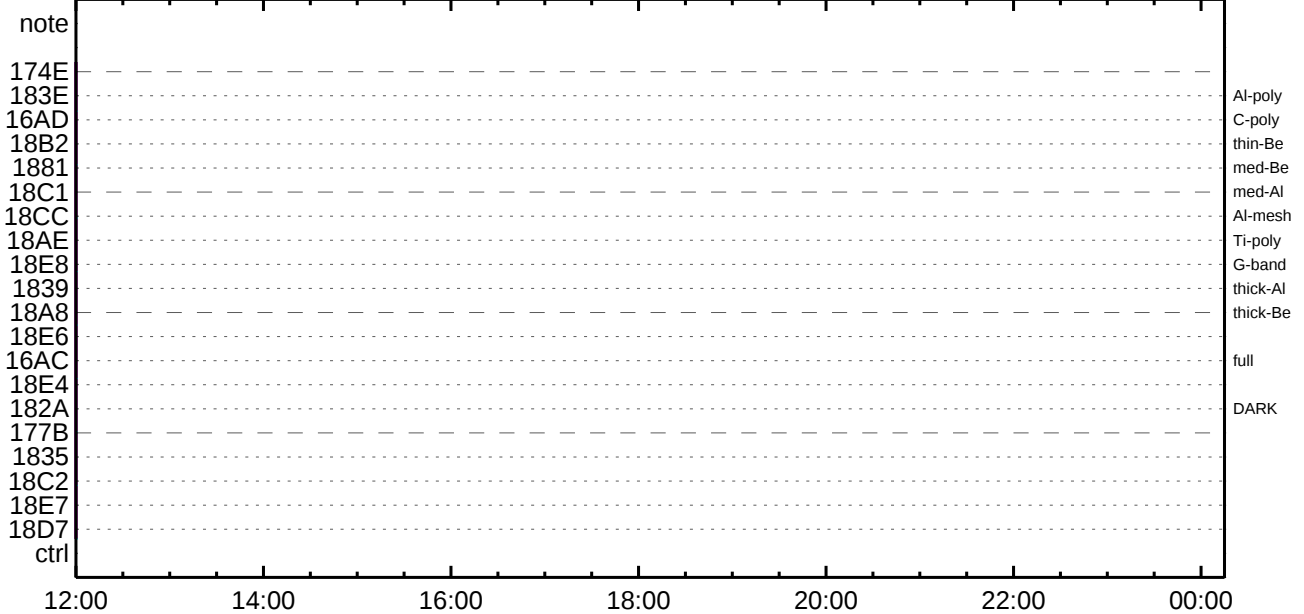
CMDI #0625 2012/06/07



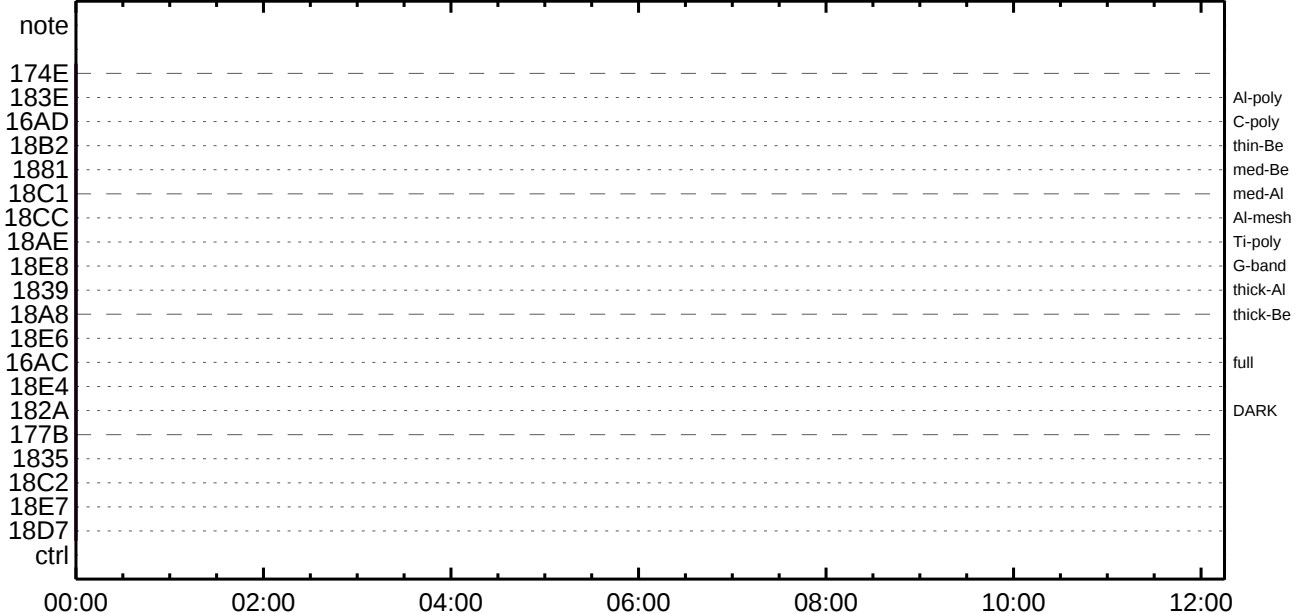
CMDI #0625 2012/06/08



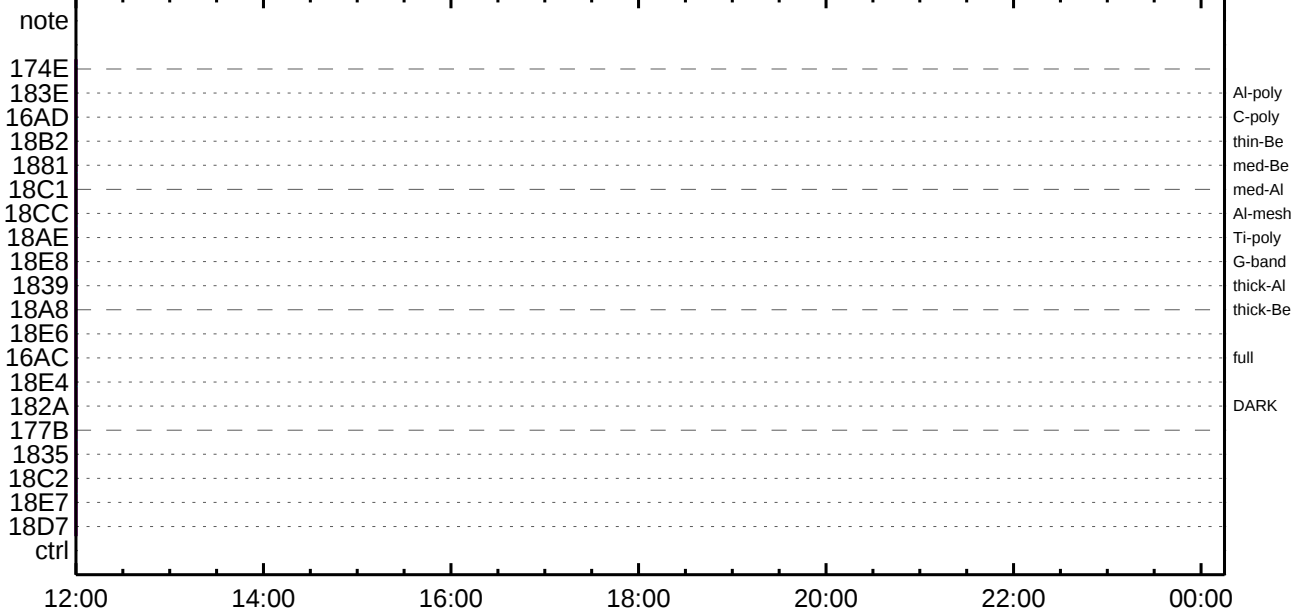
CMDI #0625 2012/06/08



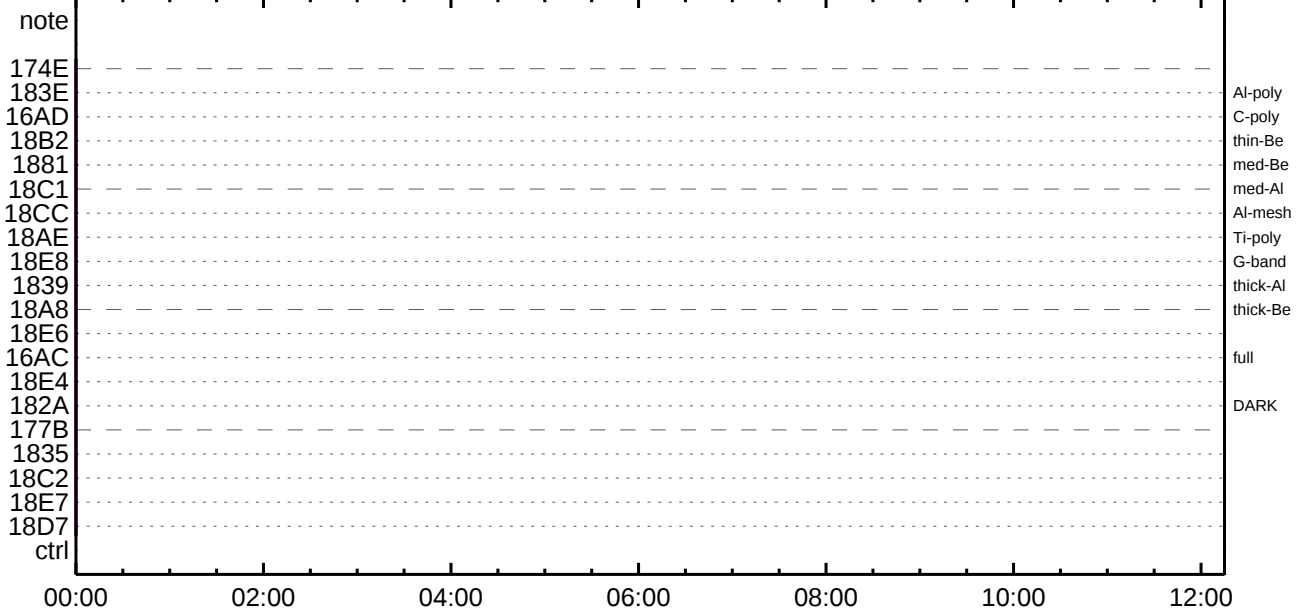
CMDI #0625 2012/06/09



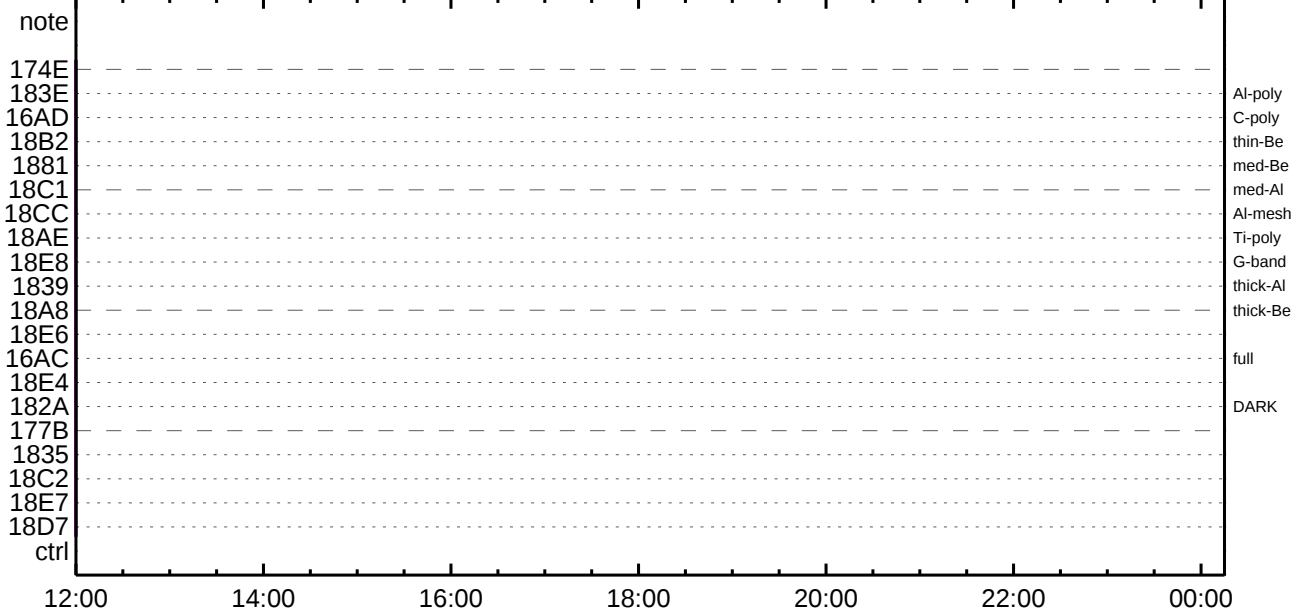
CMDI #0625 2012/06/09



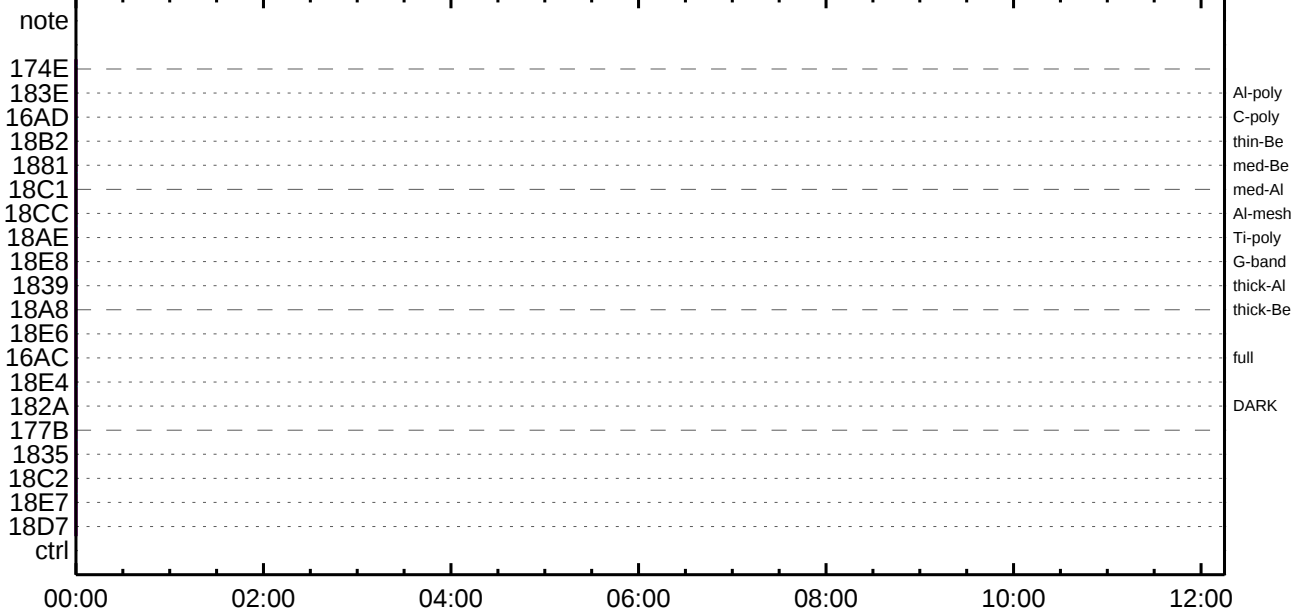
CMDI #0625 2012/06/10



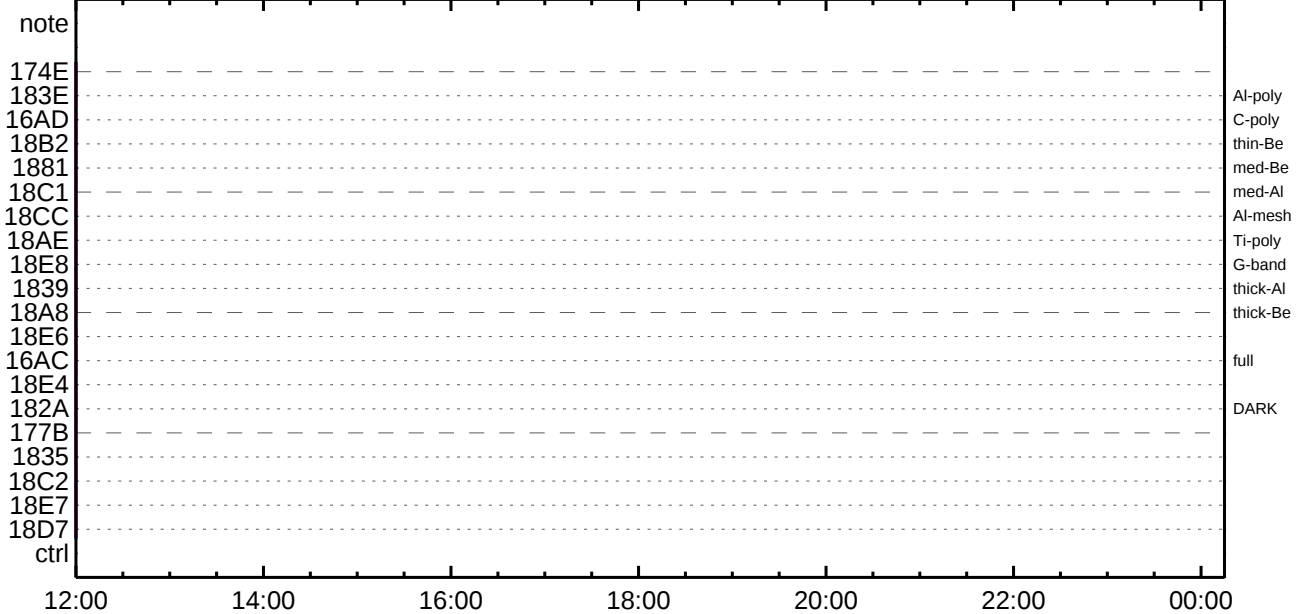
CMDI #0625 2012/06/10



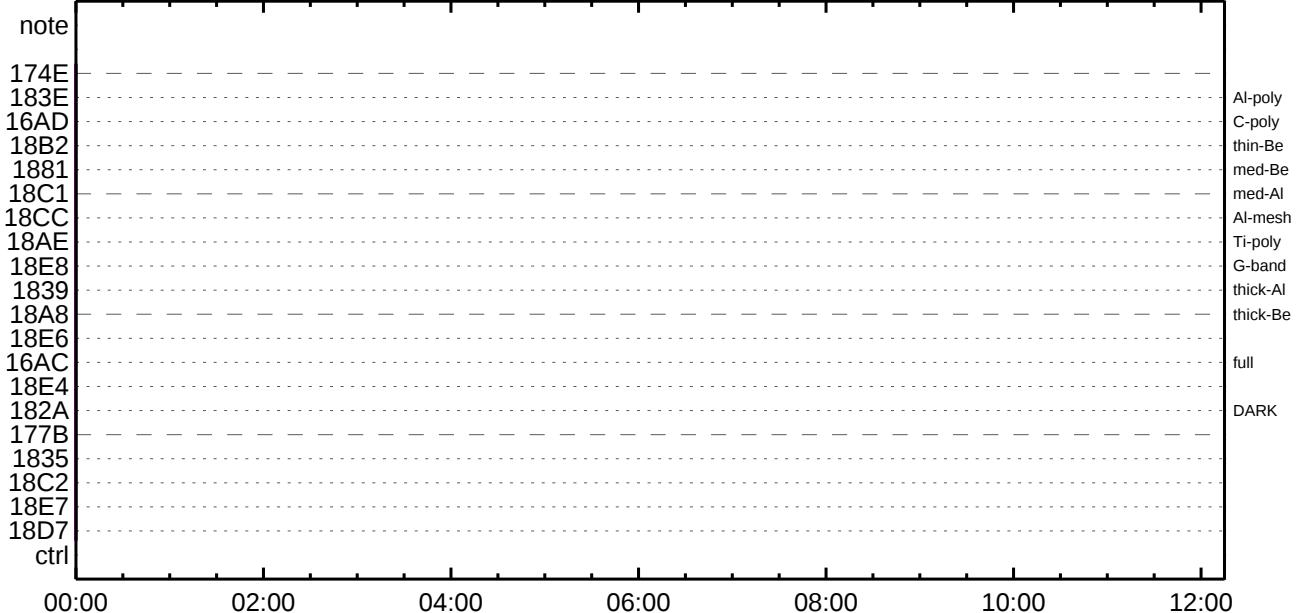
CMDI #0625 2012/06/11



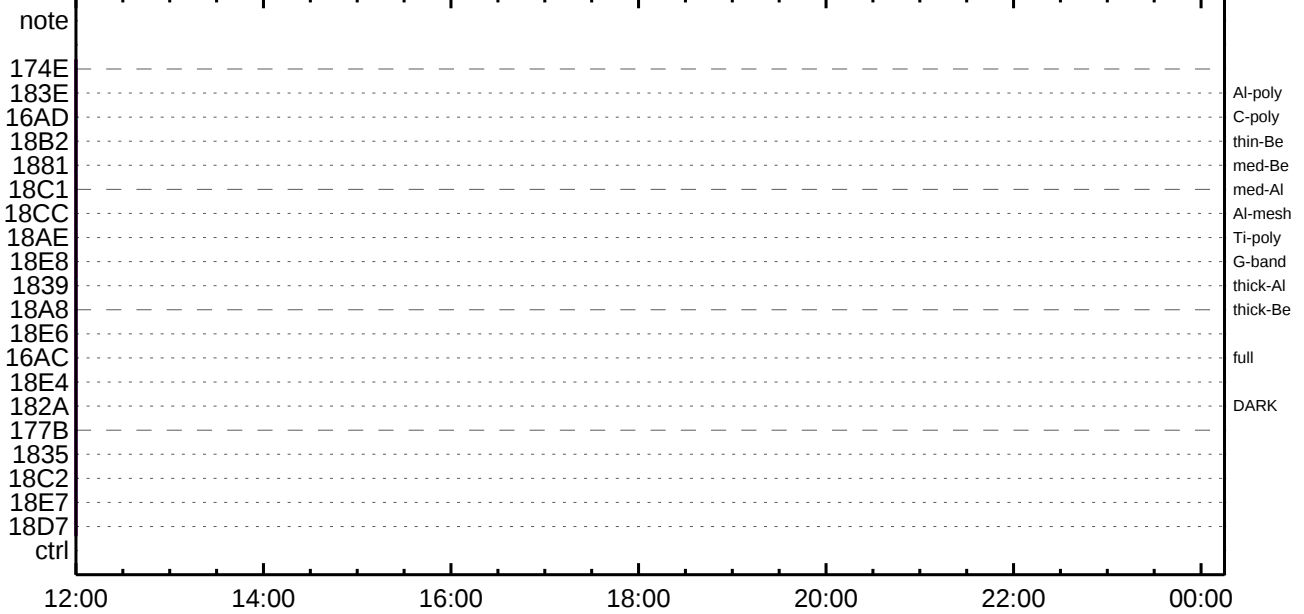
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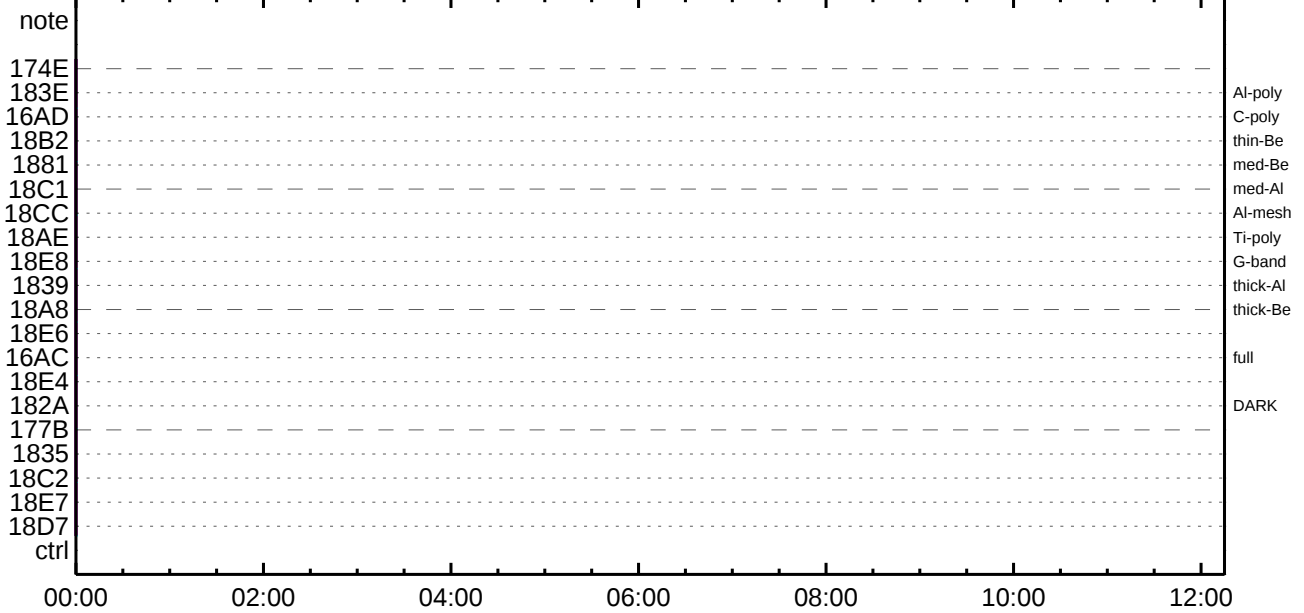
CMDI #0625 2012/06/12



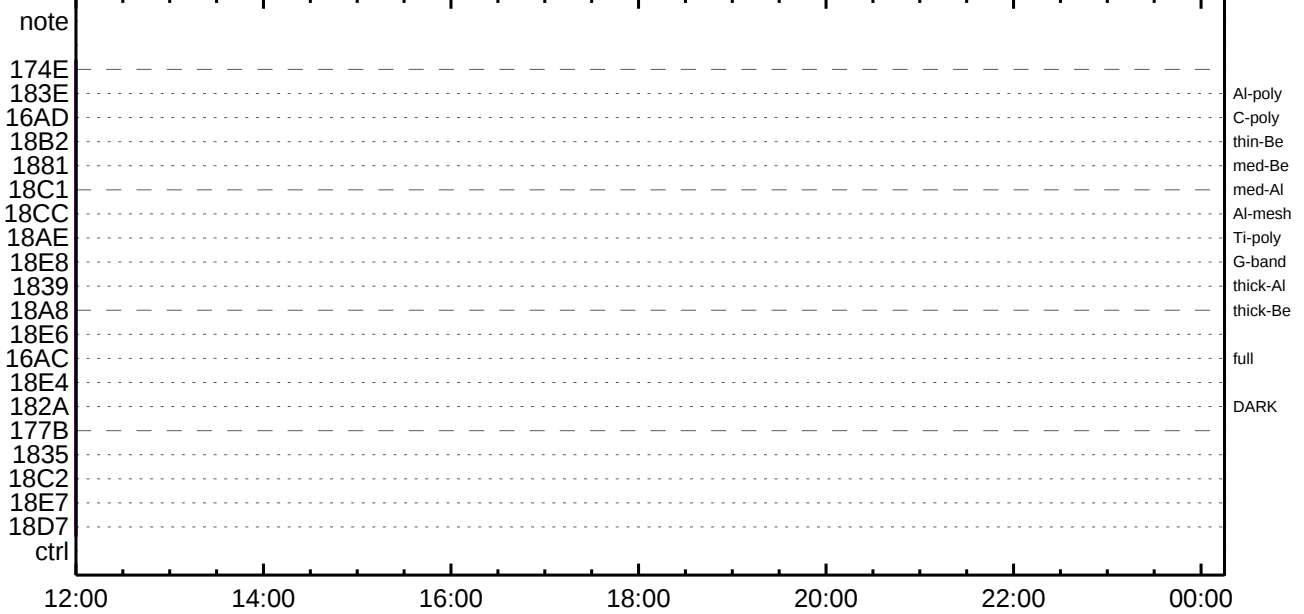
CMDI #0625 2012/06/12



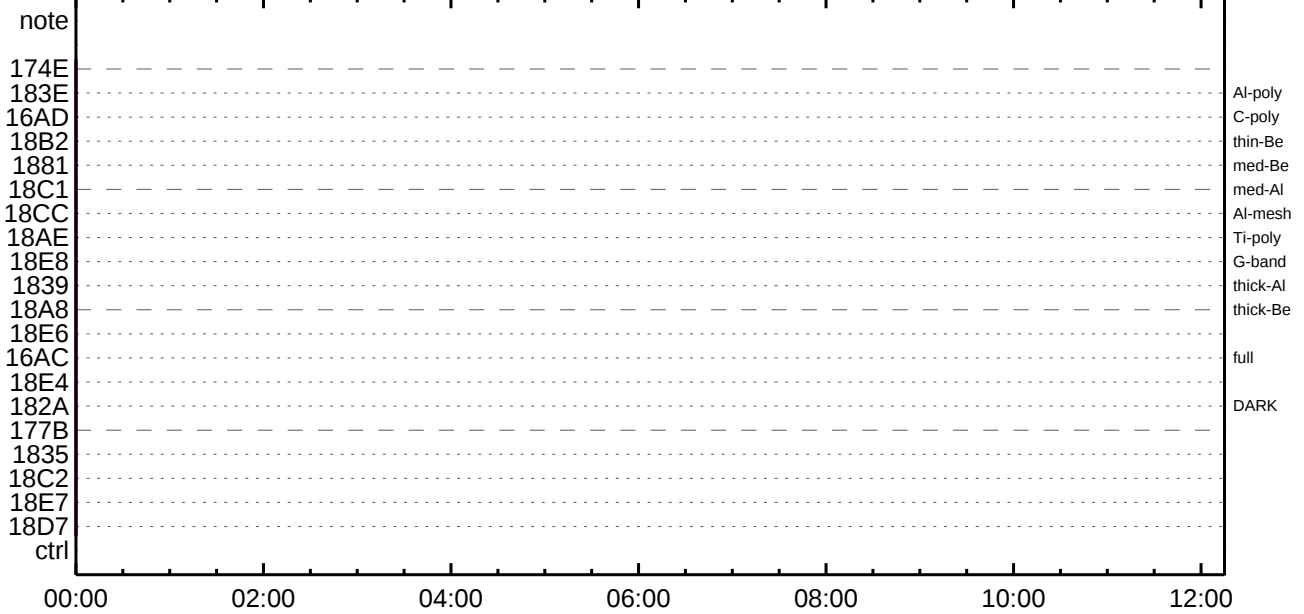
CMDI #0625 2012/06/13



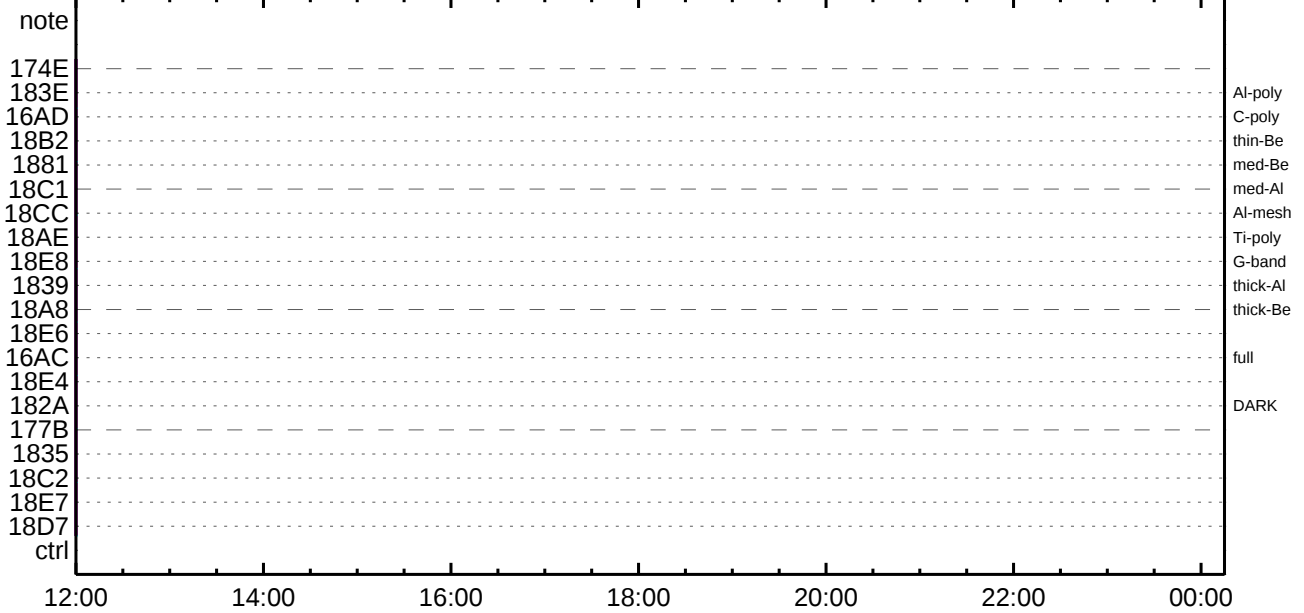
CMDI #0625 2012/06/13



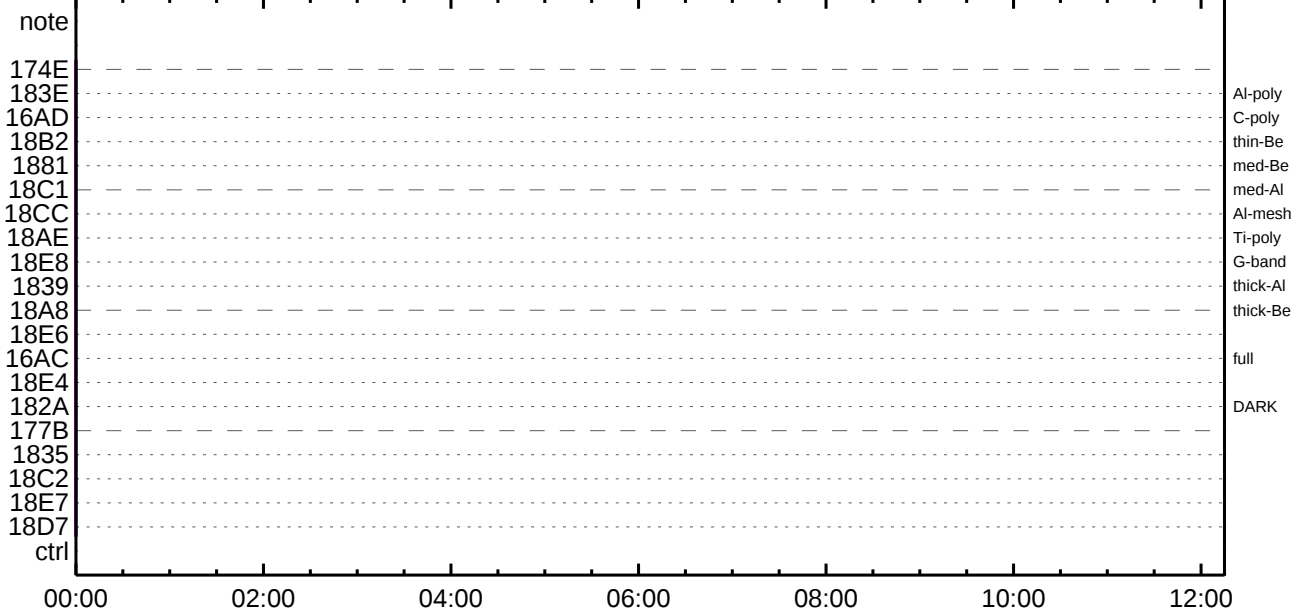
CMDI #0625 2012/06/14



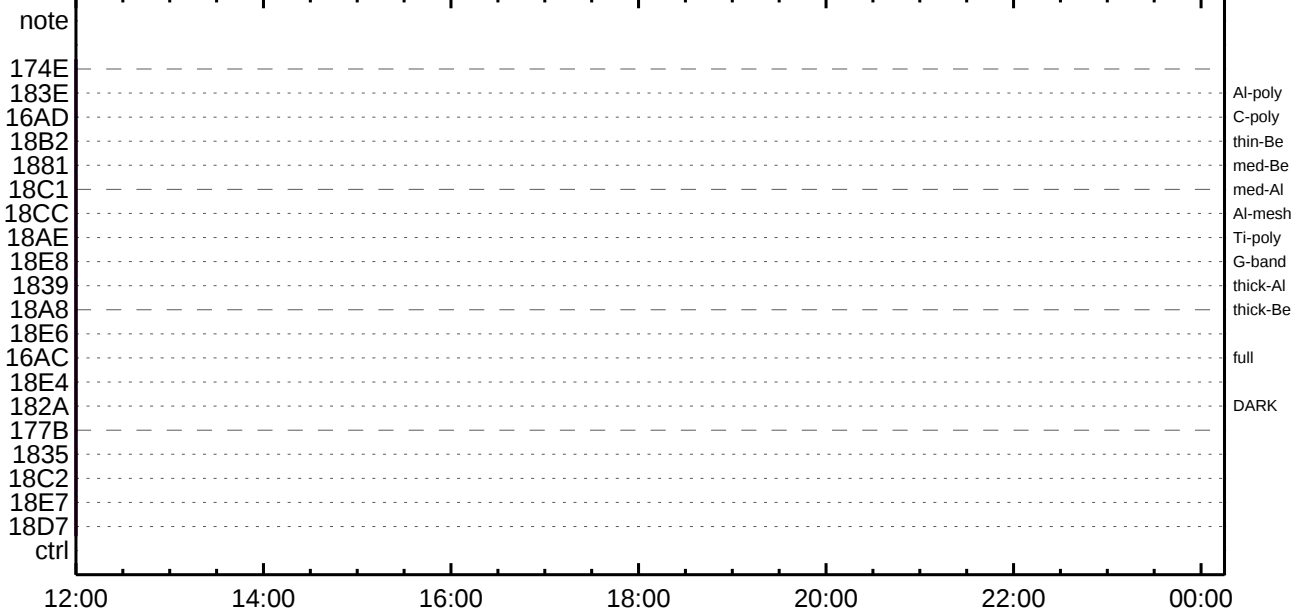
CMDI #0625 2012/06/14



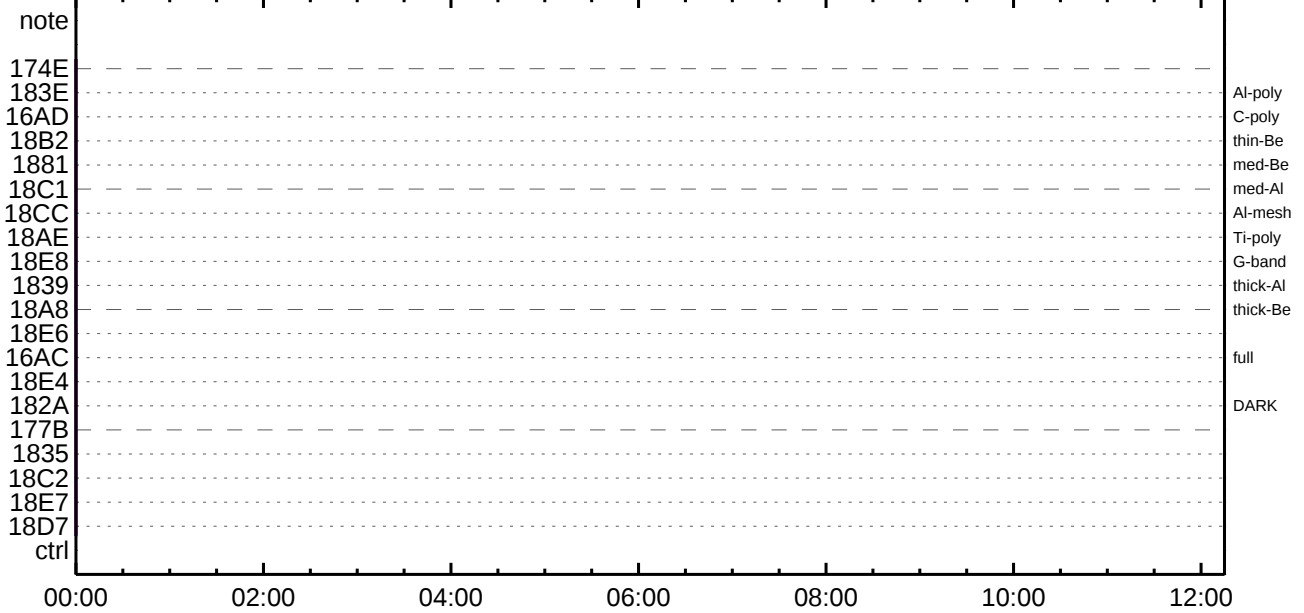
CMDI #0625 2012/06/15



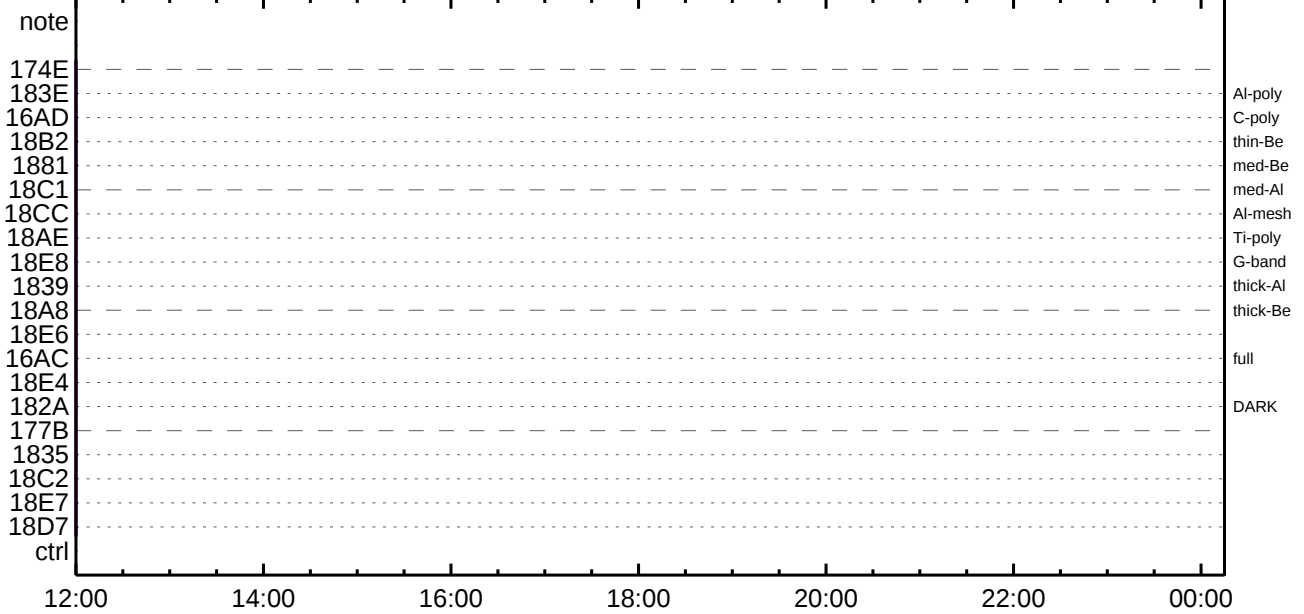
CMDI #0625 2012/06/15



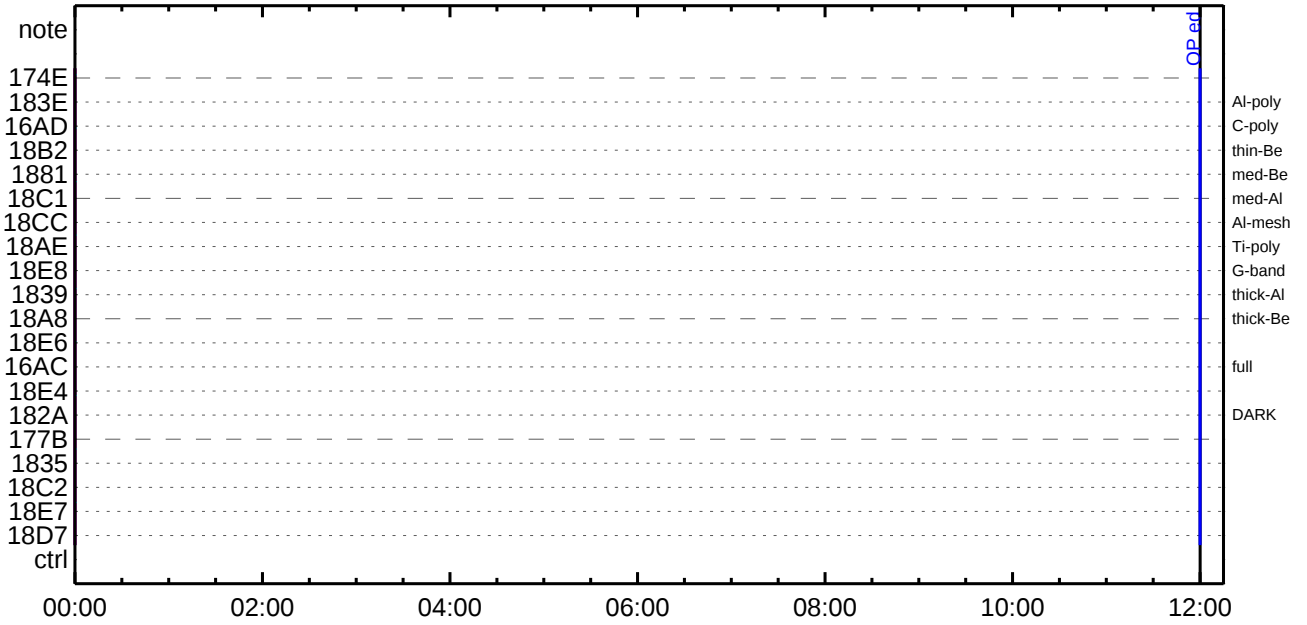
CMDI #0625 2012/06/16



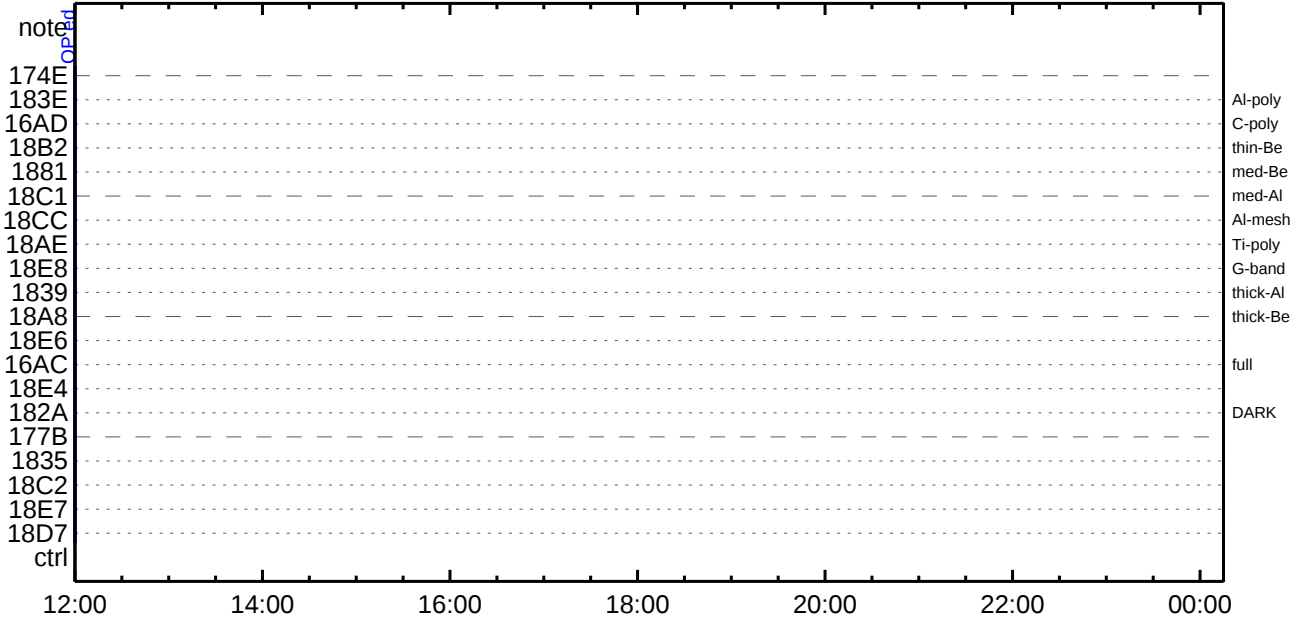
CMDI #0625 2012/06/16



CMDI #0625 2012/06/17



CMDI #0625 2012/06/17



(a) Spacecraft Operation Procedure (real-commands)

```
main-870 2012-05-12 14:23:26 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥P¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¤•µ°Æ»İ×AÇ¤İ¥¢¥Á¥×¥İj¼¥ÉjĒĒ½µ•İĒĒjĒĒ½°Ç¿¤•¤¿¼ı¹Ç¤İjÇÀ®, Ü¤ı¤Ē¤¤¤ÇÁ÷¿®¤•¤Ē¤¤¤¤ĒjĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ÆĀ, İ×ĒY¤¤ŁOS¤¤¤Ç¤İ»p`Ŏ¤Ŏıİİ, ¤•jÇĒŎİ×¤ĒXĀŎON¤İıŎ¤Ēı¤Ē¤¤¤¤ĒjĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ¢¢[HK1_XPA_ON/OFF] EQ ON
0025 C. ¢¢[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ¢¢[HK1_XMOD_ON/OFF] EQ ON
0027 C. ¢¢[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Ó¥É¥İ¥Ã¥¼ŎĀŎ¤¬°ĀĀĒ¤•¤¿¤ĒjÇ°Ē²¼¤İ°ÆĀ, ¼Ē½Ç¤¤Ŏ¼ĀıŎ¤ı¤ĒjĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼ı°ÆĀ
0033 C. *****
0034 C. ¨ ¨ RESTARTjĒPT1jĒ¤•¤¿¤¤¤¼ı¹Ç¤İjÇ°Ē²¼¤İ¼ĀıŎ¤¤¤¤jÇDCBC-150¤Ŏ¿ĒĒ¤¤jĒ
0035 C.
0036 . C. jãPT1°ÆĀ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼ĀıŎ, j¼Ŭ)
0043 C. ¢¢[HK1_REP_STA/STP] EQ START (¼ĀıŎ, j¼Ŭ)
0044 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼ĀıŎ, j¼Ŭ)
0045 C.
0046 . C. jã¥¢¥Ó¥Æ¥ĒĀŬĀŎjĒĀ•Ā²ŎĒŎjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼ĀıŎ, j¼Ŭ)
0050 C. ¢¢[HK1_REP_STA/STP] EQ START (¼ĀıŎ, j¼Ŭ)
0051 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼ĀıŎ, j¼Ŭ)
0052 C.
0053 C.
0054 . C. PT1°ÆĀ, ¤¬¼«Æ°ĀĀ»İ¤•¤¿, ĀjÇ°Ē²¼¤¤Ŏ¼ĀıŎ¤ı¤ĒjĒ
0055 C. ¥¢¥Ó¥Æ¥ĒĀŬĀŎ¤ĀĀ•Ā²ŎĒŎ¤¬ŎĀ¤¤¼ı¹Ç¤İ °İ»¤ı¤Ē¤¤¤ÇĀŎ¤ĀjĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼ı°ÆĀ
0059 C. *****
0060 C. ¨ ¨ RESTARTjĒPT2jĒ¤•¤¿¤¤¤¼ı¹Ç¤İjÇ°Ē²¼¤İ¼ĀıŎ¤¤¤¤jÇDCBC-151¤Ŏ¿ĒĒ¤¤¤jĒ
0061 C.
0062 . C. jãPT2°ÆĀ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼ĀıŎ, j¼Ŭ)
0069 C. ¢¢[HK1_REP_STA/STP] EQ START (¼ĀıŎ, j¼Ŭ)
0070 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼ĀıŎ, j¼Ŭ)
0071 C.
0072 . C. jã¥¢¥Ó¥Æ¥ĒĀŬĀŎjĒĀ•Ā²ŎĒŎjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼ĀıŎ, j¼Ŭ)
0076 C. ¢¢[HK1_REP_STA/STP] EQ START (¼ĀıŎ, j¼Ŭ)
0077 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼ĀıŎ, j¼Ŭ)
0078 C.
0079 . C. *****
0080 C. DR°ÆĀ, Āã»İjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ÆĀ, Āã»İjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ¢¢[HK1_REP_STA/STP] EQ STOP
0087 C. ¢¢[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ¢¢[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ¢¢[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ¢¢[HK1_XPA_ON/OFF] EQ OFF
```

0096	C.				
0097	C.				
0098	.	*****			
0099	C.	OP/OG¶ıı¼¥Éı ¥À¥ó×			
0100	C.	*****			
0101	C.				
0102	C.	iãOP/OG¶ıı¼¥É;ä			
0103	S.	OP op-870:OP			
0104	()				
0105	S.	OG og-870:OG			
0106	()				
0107	C.				
0108	C.	iãnMOG&OPîî°è¥À¥ó×jã			
0109	C.	NMOG(0x200000-0x207FFF;§ 32 kbyte)			
0110	+ DC	01-23 DHU_DMA_DMP_PRM_SET			
0111	BC	(20 00 7f 01 02)			
0112	C.	¢¢[HK1_DMP_TOP_ADRS_1]	EQ	40	
0113	C.	¢¢[HK1_DMP_TOP_ADRS_0]	EQ	0	
0114	C.	¢¢[HK1_DMP_BLOCK_NUM]	EQ	127	
0115	C.	¢¢[HK1_DMP_REPEAT_NUM]	EQ	0	
0116	C.	¢¢[HK1_DMA_DMP_PIM]	EQ	DHU	
0117	+ DC	01-22 DHU_MODE_CHNG			
0118	BC	(07 0b f8)			
0119	C.	¢¢[HK1_PKT_FORM_NO]	EQ	7	
0120	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s	
0121	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k	
0122	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M	
0123	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC	
0124	C.	¥À¥ó¥×½ªİ»ð³İÇŞ			
0125	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	NON	
0126	C.	RAM ID=NMOG¤İ%Ê¹Ç•ë²İOK¤ð³İÇŞ			
0127	C.				
0128	C.	NMOG(0x208000-0x20FFFF;§ 32 kbyte)			
0129	+ DC	01-23 DHU_DMA_DMP_PRM_SET			
0130	BC	(20 80 7f 01 02)			
0131	C.	¢¢[HK1_DMP_TOP_ADRS_1]	EQ	41	
0132	C.	¢¢[HK1_DMP_TOP_ADRS_0]	EQ	0	
0133	C.	¢¢[HK1_DMP_BLOCK_NUM]	EQ	127	
0134	C.	¢¢[HK1_DMP_REPEAT_NUM]	EQ	0	
0135	C.	¢¢[HK1_DMA_DMP_PIM]	EQ	DHU	
0136	+ DC	01-22 DHU_MODE_CHNG			
0137	BC	(07 0b f8)			
0138	C.	¢¢[HK1_PKT_FORM_NO]	EQ	7	
0139	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s	
0140	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k	
0141	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M	
0142	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC	
0143	C.	¥À¥ó¥×½ªİ»ð³İÇŞ			
0144	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	NON	
0145	C.	RAM ID=NMOG¤İ%Ê¹Ç•ë²İOK¤ð³İÇŞ			
0146	C.				
0147	C.	NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)			
0148	+ DC	01-23 DHU_DMA_DMP_PRM_SET			
0149	BC	(21 00 41 01 02)			
0150	C.	¢¢[HK1_DMP_TOP_ADRS_1]	EQ	42	
0151	C.	¢¢[HK1_DMP_TOP_ADRS_0]	EQ	0	
0152	C.	¢¢[HK1_DMP_BLOCK_NUM]	EQ	65	
0153	C.	¢¢[HK1_DMP_REPEAT_NUM]	EQ	0	
0154	C.	¢¢[HK1_DMA_DMP_PIM]	EQ	DHU	
0155	+ DC	01-22 DHU_MODE_CHNG			
0156	BC	(07 0b f8)			
0157	C.	¢¢[HK1_PKT_FORM_NO]	EQ	7	
0158	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s	
0159	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k	
0160	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M	
0161	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC	
0162	C.	¥À¥ó¥×½ªİ»ð³İÇŞ			
0163	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	NON	
0164	C.	RAM ID=NMOG,RAM ID=OP¤İ%Ê¹Ç•ë²İOK¤ð³İÇŞ			
0165	C.				
0166	C.	***** °Ê²¼¤İ%Ä´ŒÁ°¤ËÈ¬¤Ö÷¿® (%âµ-¥À¥ó¥×½ªê½Ç¤ðÃ¤æ¤Ç½¤¤¨¤ë%ıı½Ç¤Ç¤â) *****			
0167	C.	DHUYâı¼¥ÉıĚ¼Y½ ¥ıı¼¥ÉıĚ¤ðİã¤¹			
0168	+ DC	01-22 DHU_MODE_CHNG			
0169	BC	(02 0a f8)			
0170	C.	¢¢[HK1_PKT_FORM_NO]	EQ	2	
0171	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.5S	
0172	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32K	
0173	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M	
0174	C.				
0175	C.	*****			
0176	C.	TI-CMD SET (OPOG STOP/COPY/START)			
0177	C.	*****			
0178	C.				
0179	C.	NOTICE i§ OPOG UPLOAD¤¬÷¿®NG¤İ%ıı½Çı¢°Ê²¼¤İTİ-CMDÁ÷¿®¤İ%Â¹Ô¤¤¤Ê¤¤¤¤¤¤ı£			
018					

0194	C.			
0195	+. TI	2012-05-12 10:38:59.5		
0196	DC	01-B2 DHU_OP_START		
0197	C.	☐☐[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0198	C.			
0199	C.	°Ê²¼☐İÄê%İİŇ☐İ¥Á¥\$¥Ä¥¹äİÜ		
0200	C.	☐☐[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0201	C.	☐☐[HK1_TI_CMD_NUM]	EQ	4
0202	C.	☐☐[HK1_NEXT_EXEC_PIM]	EQ	DHU
0203	C.	☐☐[HK1_NEXT_EXEC_DC]	EQ	0xB3
0204	C.			
0205	C.	*****		
0206	C.	TIİİ°è¥Ä¥0¥×		
0207	C.	*****		
0208	C.			
0209	C.	TI_TBL(0x03AB00-0x03AEFF;S 1024byte)		
0210	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0211	BC	(03 ab 03 01 02)		
0212	C.	☐☐[HK1_DMP_TOP_ADRS_1]	EQ	07
0213	C.	☐☐[HK1_DMP_TOP_ADRS_0]	EQ	2B
0214	C.	☐☐[HK1_DMP_BLOCK_NUM]	EQ	3
0215	C.	☐☐[HK1_DMP_REPEAT_NUM]	EQ	0
0216	C.	☐☐[HK1_DMA_DMP_PIM]	EQ	DHU
0217	+. DC	01-22 DHU_MODE_CHNG		
0218	BC	(07 0b f8)		
0219	C.	☐☐[HK1_PKT_FORM_NO]	EQ	7
0220	C.	☐☐[HK1_PKT_GEN_TIME]	EQ	0.25 s
0221	C.	☐☐[HK1_S_TLM_BIT_RATE]	EQ	32k
0222	C.	☐☐[HK1_X_TLM_BIT_RATE]	EQ	4M
0223	C.	☐☐[HK1_DMP_CHK_FLG]	EQ	EXEC
0224	C.			
0225	C.	¥Ä¥0¥×½ªİ»☐0³İÇ\$		
0226	C.	☐☐[HK1_DMP_CHK_FLG]	EQ	NON
0227	C.			
0228	C.	RAM ID=TI_TBL☐İ¥Ê¹Ç•ê²İOK☐0³İÇ\$		
0229	C.			
0230	C.	DHU¥âİ;¼¥ÉİÊ¼Ý½, ¥İİ;¼¥ÊİÊ☐0İä☐¹		
0231	+. DC	01-22 DHU_MODE_CHNG		
0232	BC	(02 0a f8)		
0233	C.	☐☐[HK1_PKT_FORM_NO]	EQ	2
0234	C.	☐☐[HK1_PKT_GEN_TIME]	EQ	0.5S
0235	C.	☐☐[HK1_S_TLM_BIT_RATE]	EQ	32K
0236	C.	☐☐[HK1_X_TLM_BIT_RATE]	EQ	4M
0237	C.			
0238	C.	*****		
0239	C.	SOT TI command set		
0240	C.	*****		
0241	C.	Execute, after the success of OP upload.		
0242	+. TI	2012-05-12 10:38:16.0		
0243	DC	07-F0 MDP_SOT_MODE_STBY		
0244	BC	(41)		
0245	C.	-----		
0246	C.	HK1_TI_CMD_NUM = 1 CNTUP []		
0247	C.	-----		
0248	C.	***** SOT END *****		
0249	C.	Stop EIS observation and temporarily disable EIS mode changes		
0250	C.			
0251	C.			
0252	C.	***** Start EIS operation (TI set) *****		
0253	C.	Execute, after the success of OP upload.		
0254	C.	Set EIS TI-commands		
0255	+. TI	2012-05-12 10:38:30.0		
0256	DC	07-FC EIS_MODE_MANU		
0257	BC	(21 02)		
0258	+. TI	2012-05-12 10:38:40.0		
0259	DC	07-FC EIS_MODE_CHG_DIS		
0260	BC	(22)		
0261	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0262	C.	***** End EIS operation (TI set) *****		
0263	C.			
0264	C.			
0265	C.			
0266	C.	***** XRT START *****		
0267	C.	Execute, after the success of OP upload.		
0268	+. TI	2012-05-12 10:38:00.0		
0269	DC	07-F0 MDP_XRT_MODE_STBY		
0270	BC	(c3)		
0271	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0272	C.			
0273	C.	***** XRT END *****		
0274	C.			
0275	C.	***** MDP ´ÜÄİİ»0¼Ý☐ĒÄĐ☐¹☐ēDCBC•×²è *****		
0276	C.	(¼Ä°İ¥0¥Ä¥Ē¥Đ¥Ē¥Ç¥ē☐Ē¼☐☐¼Ä»Ü☐¹☐ē)		
0277	S.	DC-BC dcbc-402:DCBC		
0278		(MDP_known_event)		
0279	C.			
0280	C.			
0281	C.	***** ¥Đ¥¹•İ Daily±çİŇ☐Ē´Đ☐¹☐ēDCBC•×²è *****		
0282	S.	DC-BC dcbc-153:DCBC		
0283		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0284	C.			
0285	C.			
0286	C.	İäLOS¥Ä¥\$¥Ä¥¼Ä»Üİä		
0287	C.			
0288	C.	***** LOS *****		
0289	C.			

(a) Spacecraft Operation Procedure (real-commands)

```
main-871 2012-05-12 14:23:26 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Å»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ĀiĲ¿¿A¿•μ°£»İ×AÇ¿İ¥Ç¥A¥×¥i¼¥ÉiĒĒ¿μ•īĒĒiĒ¿°Ç¿¿•¿¿i¹Ç¿¿iÇÀ®, ū¿¹¿Ē¿¿¿¿ÇÁ÷¿®¿•¿Ē¿¿¿¿¿Ē¿¿£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0015 +. DC 07-FC EIS_MODE_MANU
0016 BC (21 02)
0017 . C. Verify EIS in MANUAL mode
0018 . C. Estimated OBSTBL upload time is 25s
0019 C. *****
0020 C. EIS START OBSTBL LOAD
0021 C. *****
0022 . S. RAM ram-820:EIS_OBSTBL
0023 ()
0024 +. DC 07-FC EIS_DUMP_OBSTBL
0025 BC (07 07 07 00 00 70 00)
0026 C.
0027 C. Execute, after the success of OBSTBL upload.
0028 C. Set EIS TI-commands
0029 +. TI 2012-05-12 10:38:50.0
0030 DC 07-FC EIS_MODE_CHG_ENA
0031 BC (20)
0032 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0033 C. *****
0034 C. EIS END OBSTBL LOAD
0035 C. *****
0036 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0037 C. Upload the Orbit Element and the Target Attitude
0038 C. RAM-ID:TARGET_ATT
0039 . S. RAM ram-150:TARGET_ATT
0040 ()
0041 C.
0042 C.
0043 C. Set the dump memory area of TARGET_ATT
0044 +. DC 02-48 AOCU_DUMP_SET
0045 BC (07 00 00 00 18 00)
0046 C.
0047 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0048 C.
0049 C.
0050 C. Change the TLMFormatNo for the AOCS Dump Format
0051 +. DC 01-22 DHU_MODE_CHNG
0052 BC (04 0b f8)
0053 C.
0054 C. Wait for AOCS DUMP to end
0055 C.
0056 . C. Check the dump memory
0057 C.
0058 C. Result = OK [ ]
0059 C.
0060 +. DC 01-22 DHU_MODE_CHNG
0061 BC (02 0a f8)
0062 C.
0063 C. <A_***>[TLM STS] FMT = 2 [ ]
0064 C.
0065 +. DC 02-8E AOCU_ORB_UPD
0066 C.
0067 C.
0068 . C. ***** MDP ´ŪĀİ¿İ»Ō%Ÿ¿ĒĀĐ¿¹¿ĒDCBC•×²Ē *****
0069 C. (%Ā°İ¥Ō¥Ā¥Ē¥D¥Ē¥Ā¥Ç¥Ē¿Ē½¼¿¿¼Ā»Ü¿¹¿Ē)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** ¥Đ¥¹•İ Daily±¿İŅ¿Ē´Ō¿¹¿ĒDCBC•×²Ē *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. iãLOS¥Á¥S¥Ã¥¼Å»Üjã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-872 2012-05-12 14:23:26 215 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¤•µ°Æ»İ×AÇ¤İ¥Ç¥Á¥×¥İj¼¥ÉjĒĒ½µ•İĒĒjĒĒ½°Ç¿¤•¤¿¼ı¹Ç¤İjÇÀ®, Ü¤ı¤Ē¤¤¤ÇÁ÷¿®¤•¤Ē¤¤¤¤ĒjĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ĒĒ, İ×ĒY¤¤ŁOS¤¤¤Ç¤İ»p´Ŏ¤Ŏıİİ, ¤•jÇĒŎİ×¤ĒXĒŎON¤İıŎĒĒı¤Ē¤¤¤¤ĒjĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Ó¥É¥İ¥Ã¥¼ŎĒŎ¤¬°ĒĒĒ¤•¤¿¤ĒjÇ°Ē²¼¤İ°ĒĒ, ¼Ē½Ç¤Ŏ½ĒıŎĒı¤ĒjĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼ı°ĒĒ
0033 C. *****
0034 C. ¨ ¨ RESTARTjĒPT1jĒ¤•¤¿¤¤¤¼ı¹Ç¤İjÇ°Ē²¼¤İ¼ĒıŎ¤¤¤¤jÇDCBC-150¤Ŏ¿ĒĒ¤¤jĒ
0035 C.
0036 . C. jãPT1°ĒĒ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼ĒıŎ, j¼Ŏ)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼ĒıŎ, j¼Ŏ)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼ĒıŎ, j¼Ŏ)
0045 C.
0046 . C. jã¥Ç¥Ó¥Æ¥ĒĒŎĒjĒĒ•Ē°²ŎĒŎjĒ, Ē¤İ°ĒĒ, °Ē³«jã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼ĒıŎ, j¼Ŏ)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼ĒıŎ, j¼Ŏ)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼ĒıŎ, j¼Ŏ)
0052 C.
0053 C.
0054 . C. PT1°ĒĒ, ¤¬¼«°ĒĒ»İ¤•¤¿, ĒjÇ°Ē²¼¤Ŏ½ĒıŎ¤ĒjĒ
0055 C. ¥Ç¥Ó¥Æ¥ĒĒŎĒjĒĒ•Ē°²ŎĒŎ¤¬ĒĒ¤¤¼ı¹Ç¤İ °İ»¤ı¤Ē¤¤¤ÇĒŎĒjĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼ı°ĒĒ
0059 C. *****
0060 C. ¨ ¨ RESTARTjĒPT2jĒ¤•¤¿¤¤¤¼ı¹Ç¤İjÇ°Ē²¼¤İ¼ĒıŎ¤¤¤¤jÇDCBC-151¤Ŏ¿ĒĒ¤¤jĒ
0061 C.
0062 . C. jãPT2°ĒĒ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼ĒıŎ, j¼Ŏ)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼ĒıŎ, j¼Ŏ)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼ĒıŎ, j¼Ŏ)
0071 C.
0072 . C. jã¥Ç¥Ó¥Æ¥ĒĒŎĒjĒĒ•Ē°²ŎĒŎjĒ, Ē¤İ°ĒĒ, °Ē³«jã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼ĒıŎ, j¼Ŏ)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼ĒıŎ, j¼Ŏ)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼ĒıŎ, j¼Ŏ)
0078 C.
0079 . C. *****
0080 C. DR°ĒĒ, Ē¤»İjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ĒĒ, Ē¤»İjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C. *****
0099 C. SOT table upload
0100 C. *****
0101 C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 C. <Upload SP Observation Table>
0109 S. RAM ram-286:MDP_OBS_S
0110 ( )
0111 C.
0112 C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2012-05-12 10:38:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 +. DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 +. DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 +. DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 +. DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 +. DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 +. DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 +. DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 80 80 20 20)
0162 +. DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 08)
0164 +. DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 08 20)
0166 +. DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0f 80 80 06 06)
0168 +. DC 07-F0 MDP_XRT_FLD_ENA
0169 BC (d8)
0170 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0171 BC (c8)
0172 +. DC 07-F0 MDP_XRT_AEC_RESET
0173 BC (d0)
0174 +. DC 07-F0 MDP_XRT_ARS_DIS
0175 BC (d5)
0176 +. DC 07-F0 MDP_XRT_FLD_RESET
0177 BC (da)
0178 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0179 BC (c4 13)
0180 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0181 BC (c5 02)
0182 C. ----- Success Verify ? OK / NG ____
0183 C.
0184 C.
0185 C. All OK? Yes--> Please Proceed. / No --> Stop here.
0186 C.
0187 +. DC 07-F0 MDP_XRT_MODE_OBSV
0188 BC (c2)
0189 +. TI 2012-05-12 10:38:02.0
0190 DC 07-F0 MDP_XRT_MODE_OBSV
0191 BC (c2)
0192 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0193 BC (c0)

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*** OP Sequence for XRT ***

2012/05/12	10:48:54.0	XRT_CTRL_MANU_439_OG [0x1b7]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/05/12	10:49:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 02 00 00 00 00
2012/05/12	10:51:26.0	XRT_FOCUS_POSITION_409_OG [0x199]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/05/12	10:51:46.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2012/05/12	10:51:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2012/05/12	10:51:50.0	XRT_AEC_RESET_443_OG [0x1bb]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2012/05/12	10:51:52.0	XRT_ARS_DIS_431_OG [0x1af]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/05/12	10:51:54.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/05/12	10:51:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 04
2012/05/12	10:51:58.0	XRT_FL_PROG_SET_404_OG [0x194]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 02
2012/05/12	10:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/05/12	11:59:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/05/12	11:59:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/05/12	11:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/05/12	12:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/05/12	12:25:30.0	XRT_Custom_418_OG [0x1a2]		
2012/05/12	12:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/05/12	13:38:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/05/12	13:38:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/05/12	13:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/05/12	13:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/05/12	14:10:30.0	XRT_Custom_418_OG [0x1a2]		
2012/05/12	14:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/05/12	15:16:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/05/12	15:16:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/05/12	15:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/05/12	15:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/05/12	16:01:30.0	XRT_Custom_418_OG [0x1a2]		
2012/05/12	16:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/05/12	16:55:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/05/12	16:55:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/05/12	16:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/05/12	16:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/05/12	17:38:00.0	XRT_Custom_418_OG [0x1a2]		
2012/05/12	17:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/05/12	17:55:24.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/05/12	17:55:26.0	XRT_FOCUS_POSITION_401_OG [0x191]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2012/05/12	17:55:30.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00
2012/05/12	17:55:46.0	XRT_FLD_DIS_402_OG [0x192]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2012/05/12	17:55:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2012/05/12	17:55:50.0	XRT_ARS_DIS_438_OG [0x1b6]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2012/05/12	17:58:28.0	XRT_QT_PROG_SET_407_OG [0x197]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0c
2012/05/12	17:58:30.0	XRT_CTRL_AUTO_406_OG [0x196]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/05/12	18:05:24.0	XRT_CTRL_MANU_439_OG [0x1b7]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/05/12	18:05:30.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 02 00 00 00 00
2012/05/12	18:07:56.0	XRT_FOCUS_POSITION_409_OG [0x199]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2012/05/12	18:08:16.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2012/05/12	18:08:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]		

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2012/05/12	18:08:20.0	XRT_AEC_RESET_443_0G [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2012/05/12	18:08:22.0	XRT_ARS_DIS_431_0G [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/05/12	18:08:24.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/12	18:08:26.0	XRT_QT_PROG_SET_415_0G [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2012/05/12	18:08:28.0	XRT_FL_PROG_SET_404_0G [0x194]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2012/05/12	18:08:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/12	18:33:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/12	18:33:32.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/12	18:33:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/12	18:36:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/12	19:15:00.0	XRT_Custom_418_0G [0x1a2]				
2012/05/12	19:16:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/12	20:12:00.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/12	20:12:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/12	20:12:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/12	20:15:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/12	20:52:30.0	XRT_Custom_418_0G [0x1a2]				
2012/05/12	20:53:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/12	21:50:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/12	21:50:32.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/12	21:50:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/12	21:53:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/12	22:28:30.0	XRT_Custom_418_0G [0x1a2]				
2012/05/12	22:29:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/12	23:29:00.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/12	23:29:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/12	23:29:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/12	23:32:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/12	23:55:30.0	XRT_Custom_418_0G [0x1a2]				
2012/05/12	23:56:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	01:07:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	01:07:32.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	01:07:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	01:10:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	01:34:00.0	XRT_Custom_418_0G [0x1a2]				
2012/05/13	01:35:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	02:43:00.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	02:43:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	02:43:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	02:46:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	03:12:30.0	XRT_Custom_418_0G [0x1a2]				
2012/05/13	03:13:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	04:12:00.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	04:12:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	04:12:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	04:15:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	04:51:00.0	XRT_Custom_418_0G [0x1a2]				
2012/05/13	04:52:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	05:52:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	05:52:32.0	XRT_FLD_RESET_412_0G [0x19c]				

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2012/05/13	05:52:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	05:55:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	06:29:30.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	06:30:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	07:33:00.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	07:33:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	07:33:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	07:36:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	08:08:00.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	08:09:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	09:12:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	09:12:32.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	09:12:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	09:15:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	09:46:30.5	XRT_Custom_418_0G [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	09:47:30.5	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	10:58:00.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	10:58:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	10:58:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	11:01:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	11:25:00.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	11:26:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	12:36:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	12:36:32.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	12:36:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	12:39:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	13:03:30.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	13:04:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	14:15:00.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	14:15:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	14:15:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	14:18:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	14:51:00.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	14:52:00.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	15:53:30.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	15:53:32.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	15:53:34.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	15:56:44.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	16:38:30.0	XRT_Custom_418_0G [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	16:39:30.0	XRT_CTRL_AUTO_419_0G [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	17:32:00.0	XRT_CTRL_MANU_408_0G [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	17:32:02.0	XRT_FLD_RESET_412_0G [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	17:32:04.0	XRT_PREFLR_STRT_422_0G [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	17:35:14.0	XRT_PREFLR_STOP_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	18:14:54.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	18:14:56.0	XRT_FOCUS_POSITION_401_0G [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/05/13	18:15:00.0	AOCs_Ore-point_Start_2_0G [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2012/05/13	18:15:16.0	XRT_FLD_DIS_402_0G [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/05/13	18:15:18.0	XRT_FLRCTRL_DIS_433_0G [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/05/13	18:15:20.0	XRT_ARS_DIS_438_0G [0x1b6]				

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2012/05/13	18:17:58.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/05/13	18:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2012/05/13	18:24:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	18:25:00.0	AOCs_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	18:27:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	02 00 00 00 00
2012/05/13	18:27:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/05/13	18:27:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/05/13	18:27:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/05/13	18:27:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/05/13	18:27:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/05/13	18:27:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	18:27:58.0	XRT_FL_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2012/05/13	18:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2012/05/13	19:10:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	19:10:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	19:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	19:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	19:52:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	19:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2012/05/13	20:49:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	20:49:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	20:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	20:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	21:29:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	21:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2012/05/13	22:27:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/13	22:27:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/13	22:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/13	22:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/13	23:04:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/13	23:05:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2012/05/14	00:06:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	00:06:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	00:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	00:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	00:33:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	00:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2012/05/14	01:44:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	01:44:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	01:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	01:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	02:11:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	02:12:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
2012/05/14	03:20:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	03:20:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	03:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	03:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	03:50:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	03:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2012/05/14	04:50:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	04:50:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	04:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	04:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	05:28:30.0	XRT_Custom_418_OG [0x1a2]			
2012/05/14	05:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	06:30:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	06:30:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	06:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	06:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	07:07:00.0	XRT_Custom_418_OG [0x1a2]			
2012/05/14	07:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	08:10:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	08:10:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	08:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	08:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	08:45:30.0	XRT_Custom_418_OG [0x1a2]			
2012/05/14	08:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	09:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	09:30:00.0	AOCs_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2012/05/14	09:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/05/14	09:32:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/05/14	09:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/05/14	09:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/05/14	09:32:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/05/14	09:32:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	09:32:56.0	XRT_QT_PROG_SET_415_OG [0x19f]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2012/05/14	09:32:58.0	XRT_FL_PROG_SET_404_OG [0x194]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2012/05/14	09:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	09:51:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	09:51:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	09:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	09:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	10:24:00.0	XRT_Custom_418_OG [0x1a2]			
2012/05/14	10:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	11:35:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	11:35:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	11:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	11:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	12:02:30.0	XRT_Custom_418_OG [0x1a2]			
2012/05/14	12:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	13:14:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	13:14:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	13:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	13:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	13:41:00.0	XRT_Custom_418_OG [0x1a2]			
2012/05/14	13:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	14:52:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	14:52:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	14:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			

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2012/05/14	14:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	15:38:30.0	XRT_Custom_418_OG [0x1a2]				
2012/05/14	15:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	16:31:00.0	XRT_CTRL_MANU_408_OG [0x198]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	16:31:02.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	16:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	16:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	17:15:00.0	XRT_Custom_418_OG [0x1a2]				
2012/05/14	17:16:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	17:56:54.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	17:56:56.0	XRT_FOCUS_POSITION_401_OG [0x191]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/05/14	17:57:00.0	AOCS_ORe-point_Start_2_OG [0x098]				
			AOCU_NM	5	02-76	00 00 00 00 00
2012/05/14	17:57:16.0	XRT_FLD_DIS_402_OG [0x192]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2012/05/14	17:57:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/05/14	17:57:20.0	XRT_ARS_DIS_438_OG [0x1b6]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2012/05/14	17:59:58.0	XRT_QT_PROG_SET_407_OG [0x197]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2012/05/14	18:00:00.0	XRT_CTRL_AUTO_406_OG [0x196]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	18:07:00.0	AOCS_ORe-point_Start_1_OG [0x097]				
			AOCU_NM	5	02-76	02 00 00 00 00
2012/05/14	18:09:30.0	XRT_CTRL_MANU_408_OG [0x198]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	18:09:32.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	18:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	18:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	18:52:24.0	XRT_CTRL_MANU_403_OG [0x193]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	18:52:26.0	XRT_FOCUS_POSITION_409_OG [0x199]				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/05/14	18:52:46.0	XRT_FLD_ENA_411_OG [0x19b]				
			MDP_XRT_FLD_ENA	1	07-F0	d8
2012/05/14	18:52:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/05/14	18:52:50.0	XRT_AEC_RESET_443_OG [0x1bb]				
			MDP_XRT_AEC_RESET	1	07-F0	d0
2012/05/14	18:52:52.0	XRT_ARS_DIS_431_OG [0x1af]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2012/05/14	18:52:54.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	18:52:56.0	XRT_QT_PROG_SET_415_OG [0x19f]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2012/05/14	18:52:58.0	XRT_FL_PROG_SET_404_OG [0x194]				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2012/05/14	18:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	19:48:00.0	XRT_CTRL_MANU_408_OG [0x198]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	19:48:02.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	19:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	19:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	20:29:00.0	XRT_Custom_418_OG [0x1a2]				
2012/05/14	20:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	21:26:30.0	XRT_CTRL_MANU_408_OG [0x198]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	21:26:32.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	21:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	21:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/14	22:05:30.0	XRT_Custom_418_OG [0x1a2]				
2012/05/14	22:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/14	23:05:00.0	XRT_CTRL_MANU_408_OG [0x198]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/14	23:05:02.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/14	23:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/14	23:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2012/05/14	23:34:30.5	XRT_Custom_418_OG [0x1a2]		
2012/05/14	23:35:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/05/15	00:43:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/05/15	00:43:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/05/15	00:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/05/15	00:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/05/15	01:10:30.0	XRT_Custom_418_OG [0x1a2]		
2012/05/15	01:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/05/15	02:20:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/05/15	02:20:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/05/15	02:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/05/15	02:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/05/15	02:49:00.0	XRT_Custom_418_OG [0x1a2]		
2012/05/15	02:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/05/15	03:48:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/05/15	03:48:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/05/15	03:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/05/15	03:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/05/15	04:27:30.0	XRT_Custom_418_OG [0x1a2]		
2012/05/15	04:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/05/15	05:28:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/05/15	05:28:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/05/15	05:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/05/15	05:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/05/15	06:06:00.0	XRT_Custom_418_OG [0x1a2]		
2012/05/15	06:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/05/15	07:09:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/05/15	07:09:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/05/15	07:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/05/15	07:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/05/15	07:44:30.0	XRT_Custom_418_OG [0x1a2]		
2012/05/15	07:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/05/15	08:48:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/05/15	08:48:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/05/15	08:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/05/15	08:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/05/15	09:25:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00