

XRT Timeline to be uploaded on 2012/05/22

Period: 2012/05/22 10:19:00 - 2012/06/26 12:00:00

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Normal mode

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XOB #18EA: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-band(16)												
Term	Pointing (x, y)		Comment									
05/22 12:12:24 - 05/22 13:17:30	Fixed (-8.0, -780.0)	(3/20)									
05/22 14:15:00 - 05/22 14:56:00	Fixed (-8.0, -480.0)	(6/20)									
05/22 18:02:30 - 05/22 18:13:00	Fixed (0.0, 0.0)	synoptic, shifted -0.5 min									
05/22 22:38:00 - 05/22 23:08:30	Fixed (-8.0, 310.0)	(14/20)									
05/23 01:19:00 - 05/23 02:24:00	Fixed (-8.0, 610.0)	(17/20)									
05/23 06:18:30 - 05/23 06:25:24	Fixed (0.0, 0.0)	synoptic, shifted 15.5 min									
05/23 18:01:00 - 05/23 18:07:54	Fixed (0.0, 0.0)	synoptic, shifted -2.0 min									
05/24 05:46:00 - 05/24 05:52:54	Fixed (0.0, 0.0)	synoptic, shifted -17.0 min									
PROG= 07 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└ Seqn= 46 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└ Seqn= 69 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1835: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 40sec-cad												
Term	Pointing (x, y)		Comment									
05/23 03:48:00 - 05/23 06:15:24	Track (646.2, 149.1)	[Ⓜ] 05/23 03:45:00	AR NOAA11484								
05/23 11:07:30 - 05/23 17:12:00	Track (692.4, 147.3)	[Ⓜ] 05/23 11:00:00	AR NOAA11484								
05/23 18:11:00 - 05/24 05:42:54	Track (734.5, 145.4)	[Ⓜ] 05/23 18:08:00	(cont.)								
PROG= 04 Inf.-time(s)												
└ Subr= 2 1-time(s) 2.0sec												
└ Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└ Seqn= 41 4-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└ Subr= 1 1-time(s) 2.0sec												
└ Seqn= 52 45-time(s) 24.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18ED: Coronal cavity (2 filters, offset)-DPCM 2x2-768FOV-AEC0-15min cad												
Term	Pointing (x, y)		Comment									
05/23 06:28:30 - 05/23 10:35:00	Fixed (-8.0, -973.0)	HOP200 (with LaPalma 08:30-11)									
05/24 05:56:00 - 05/24 08:40:00	Fixed (-8.0, -973.0)	HOP200 (with LaPalma)									
PROG= 12 Inf.-time(s)												
└ Subr= 1 20-time(s) 900.0sec												
└ Seqn= 85 2-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	768x768 (1536, 1192)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	768x768 (1536, 1192)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term	Pointing (x, y)		Comment									
05/23 03:48:00 - 05/23 06:15:24	Track (646.2, 149.1)	[Ⓜ] 05/23 03:45:00	AR NOAA11484								
05/23 06:28:30 - 05/23 10:35:00	Fixed (-8.0, -973.0)	HOP200 (with LaPalma 08:30-11)									
05/23 11:07:30 - 05/23 17:12:00	Track (692.4, 147.3)	[Ⓜ] 05/23 11:00:00	AR NOAA11484								
05/23 18:11:00 - 05/24 05:42:54	Track (734.5, 145.4)	[Ⓜ] 05/23 18:08:00	(cont.)								
05/24 05:56:00 - 05/24 08:40:00	Fixed (-8.0, -973.0)	HOP200 (with LaPalma)									
PROG= 13 1-time(s)												

Subr= 1		30-time(s)		20.0sec											
└─ Seqn= 88		1-time(s)		2.0sec											
└─	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
└─	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
└─ Seqn=100		1-time(s)		2.0sec											
└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
└─	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
└─ Seqn= 90		1-time(s)		2.0sec											
└─	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
└─	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
└─	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
Subr= 3		30-time(s)		60.0sec											
└─ Seqn= 88		1-time(s)		2.0sec											
└─	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
└─	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
└─ Seqn= 60		1-time(s)		2.0sec											
└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
└─	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
└─ Seqn= 90		1-time(s)		2.0sec											
└─	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
└─	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
└─	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
Subr= 3		30-time(s)		60.0sec											
└─ Seqn= 88		1-time(s)		2.0sec											
└─	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
└─	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
└─ Seqn= 60		1-time(s)		2.0sec											
└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
└─	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
└─ Seqn= 90		1-time(s)		2.0sec											
└─	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
└─	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
└─	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
Subr= 3		30-time(s)		60.0sec											
└─ Seqn= 88		1-time(s)		2.0sec											
└─	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
└─	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
└─ Seqn= 60		1-time(s)		2.0sec											
└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
└─	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Subr= 4		24-time(s)		600.0sec											
└─ Seqn= 89		1-time(s)		2.0sec											
└─	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec		
└─	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec		
└─	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

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Active Region Search

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NOT USED

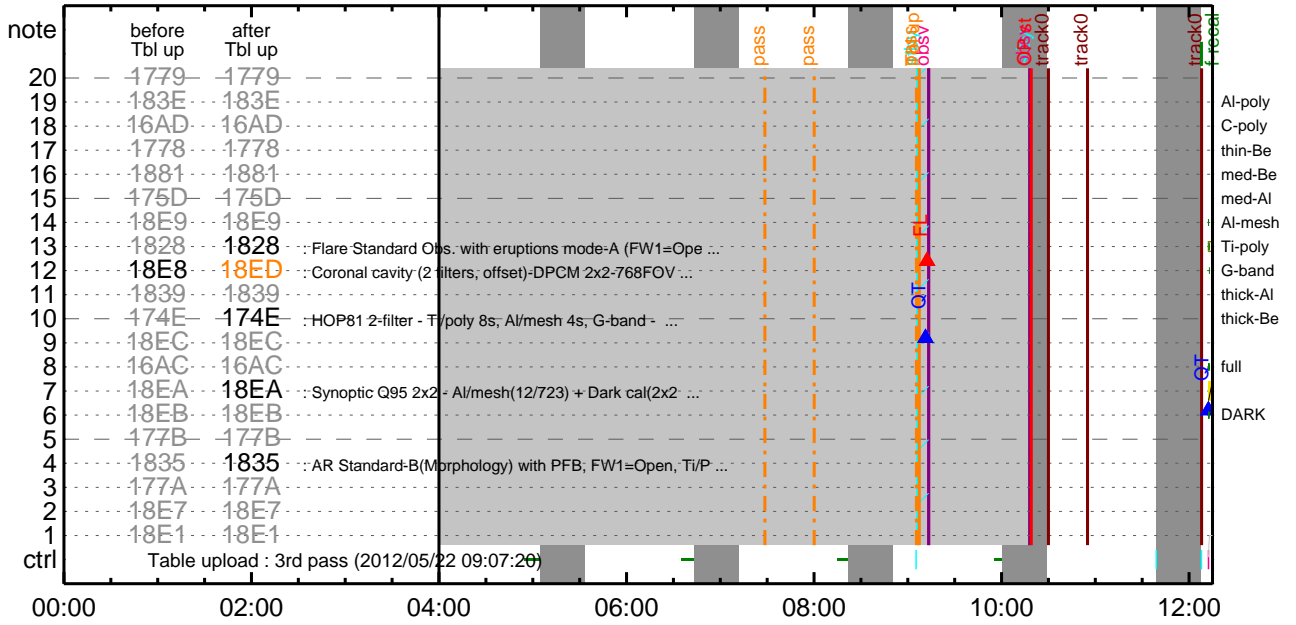
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Flare Detection

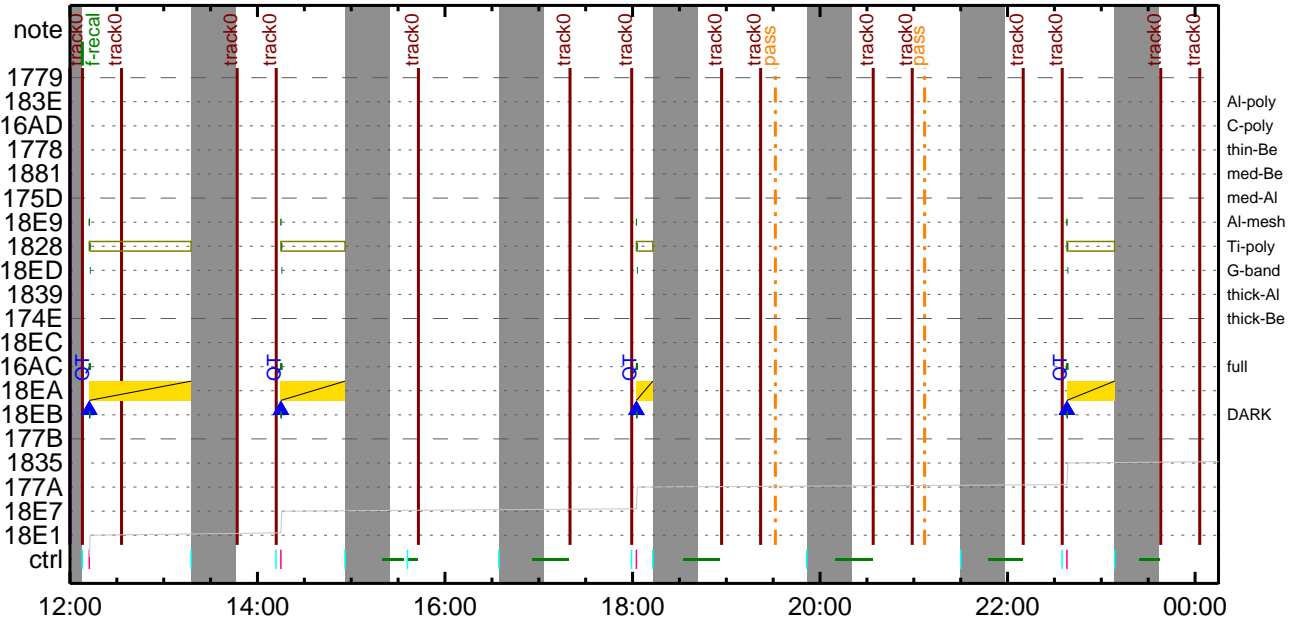
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FLD Patrol													
Term		Pointing (x, y)						Comment					
05/23 03:47:46 - 05/23 06:15:46		Track (646.2, 149.1) ^{® 05/23 03:45:00}						AR NOAA11484					
05/23 06:28:16 - 05/23 17:58:16		Fixed (-8.0, -973.0)						HOP200 (with LaPalma 08:30-11)					
05/23 18:10:46 - 05/24 05:43:16		Track (734.5, 145.4) ^{® 05/23 18:08:00}						(cont.)					
05/24 05:55:46 - 06/26 12:00:00		Fixed (-8.0, -973.0)						HOP200 (with LaPalma)					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec				
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

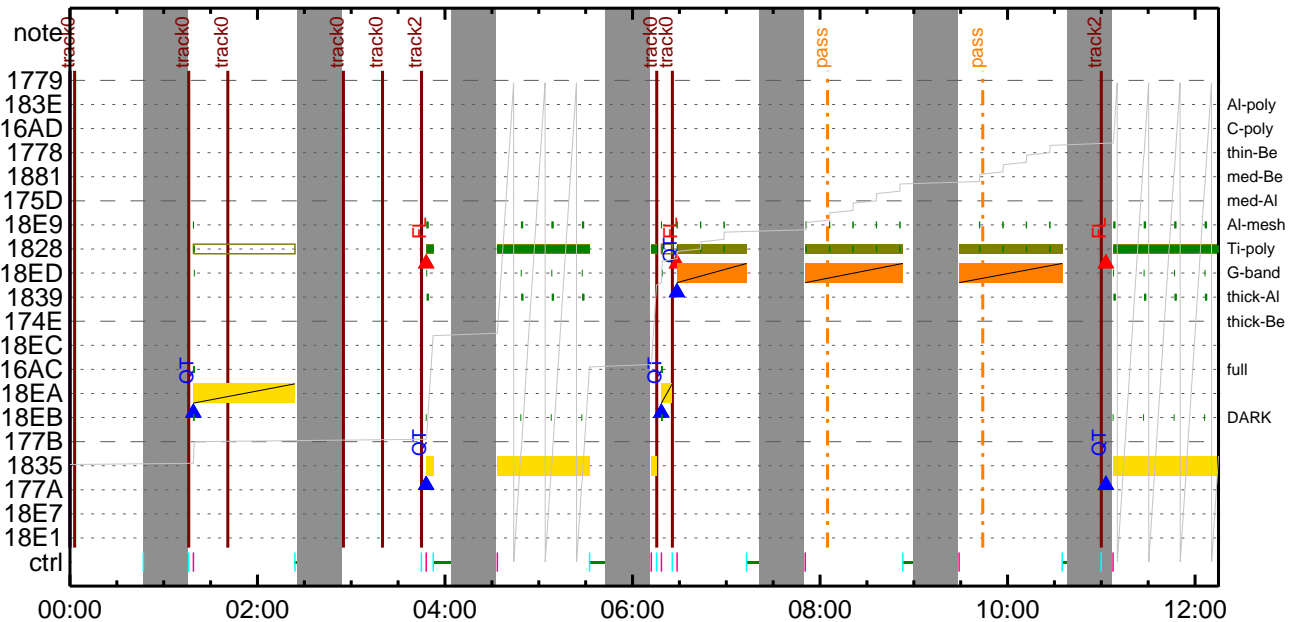
CMDI #0644 2012/05/22



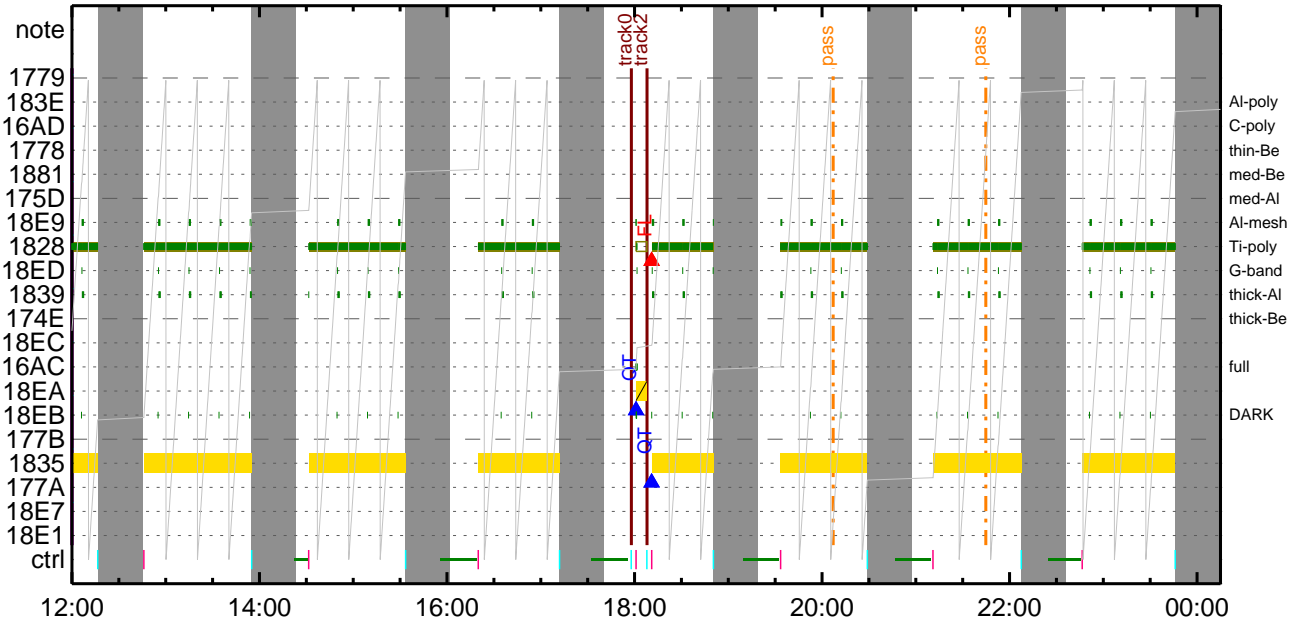
CMDI #0644 2012/05/22



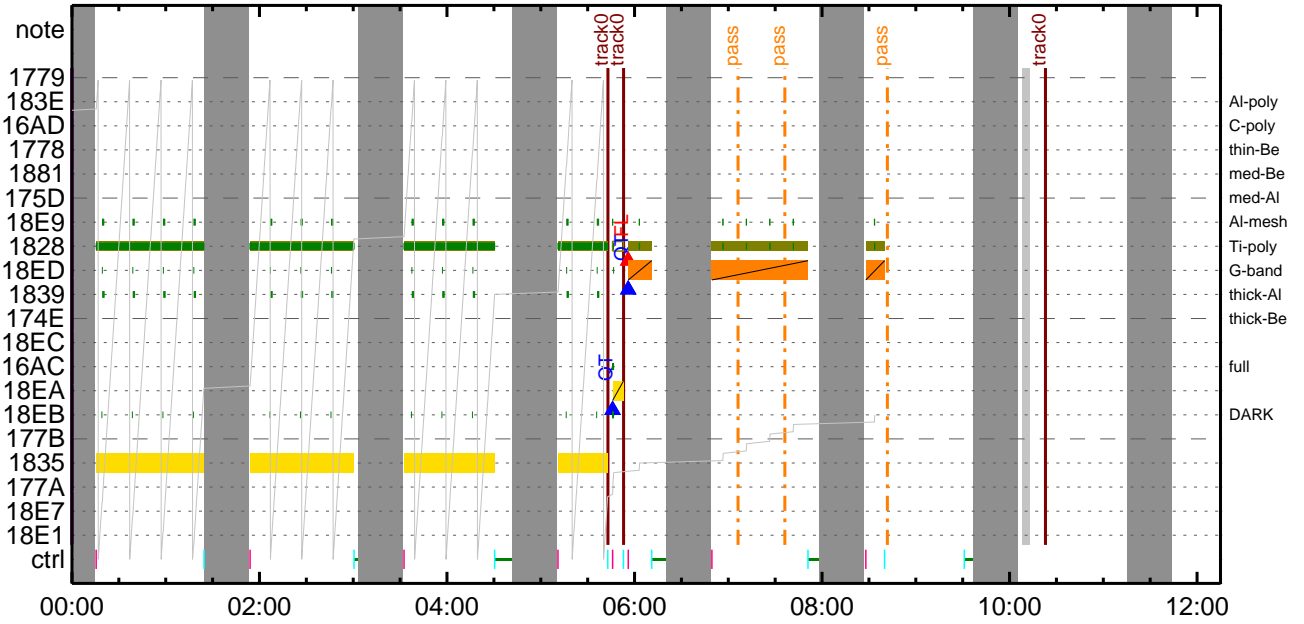
CMDI #0644 2012/05/23



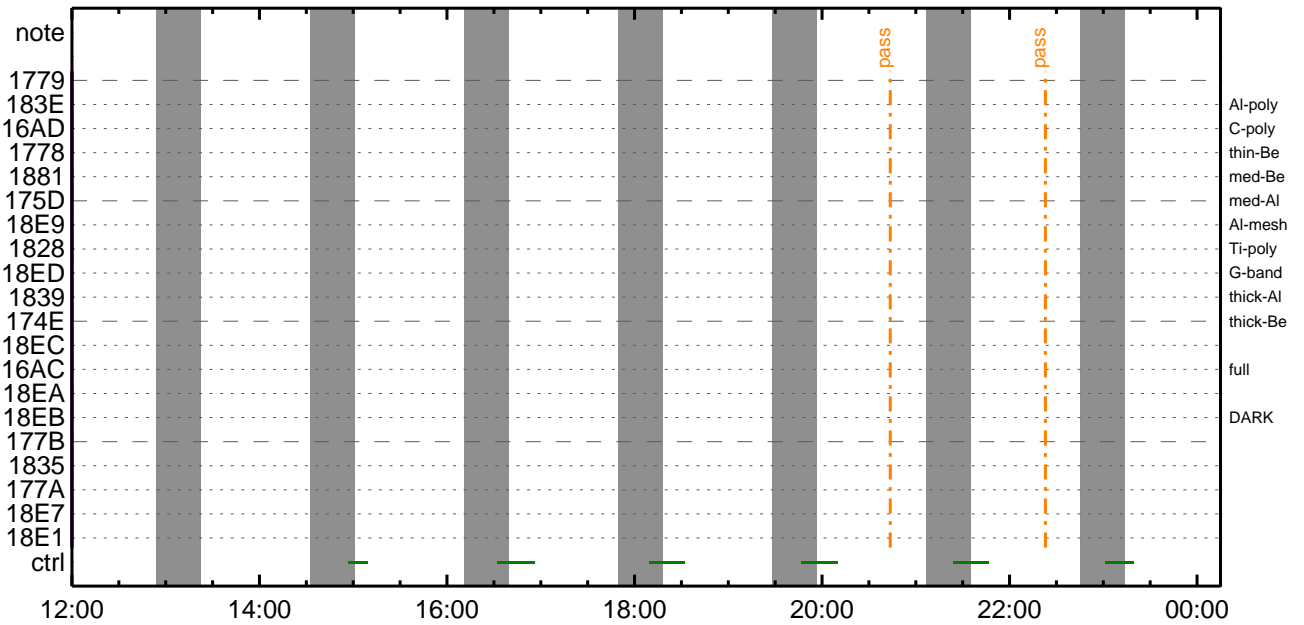
CMDI #0644 2012/05/23



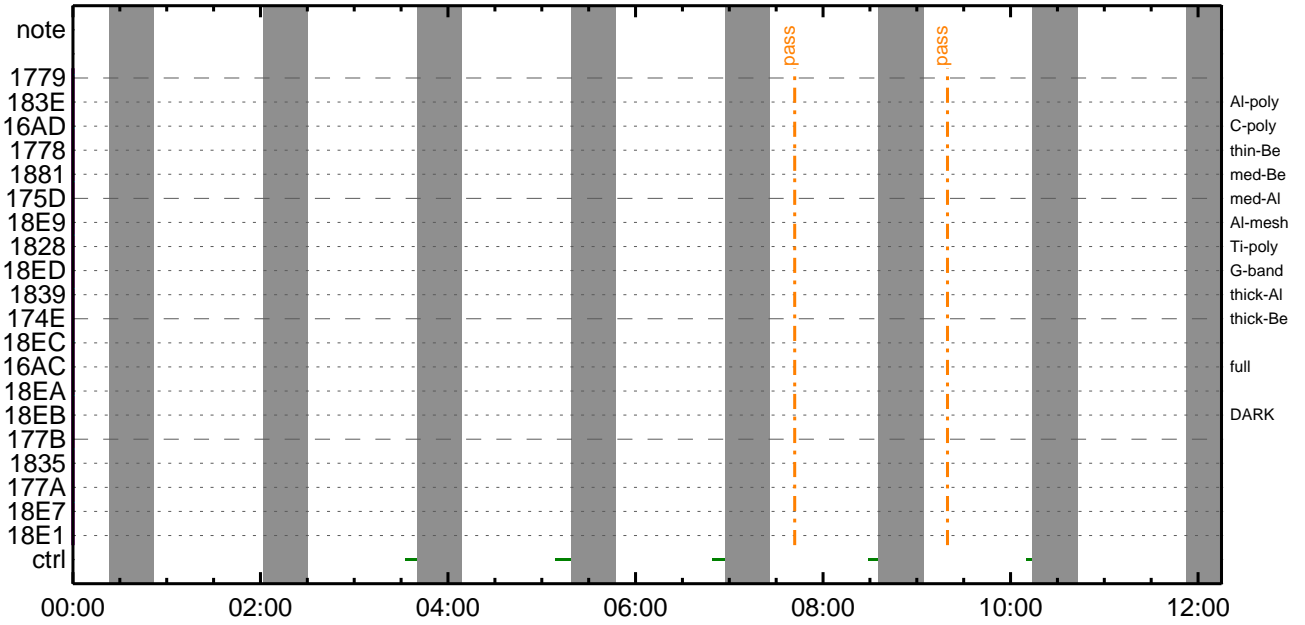
CMDI #0644 2012/05/24



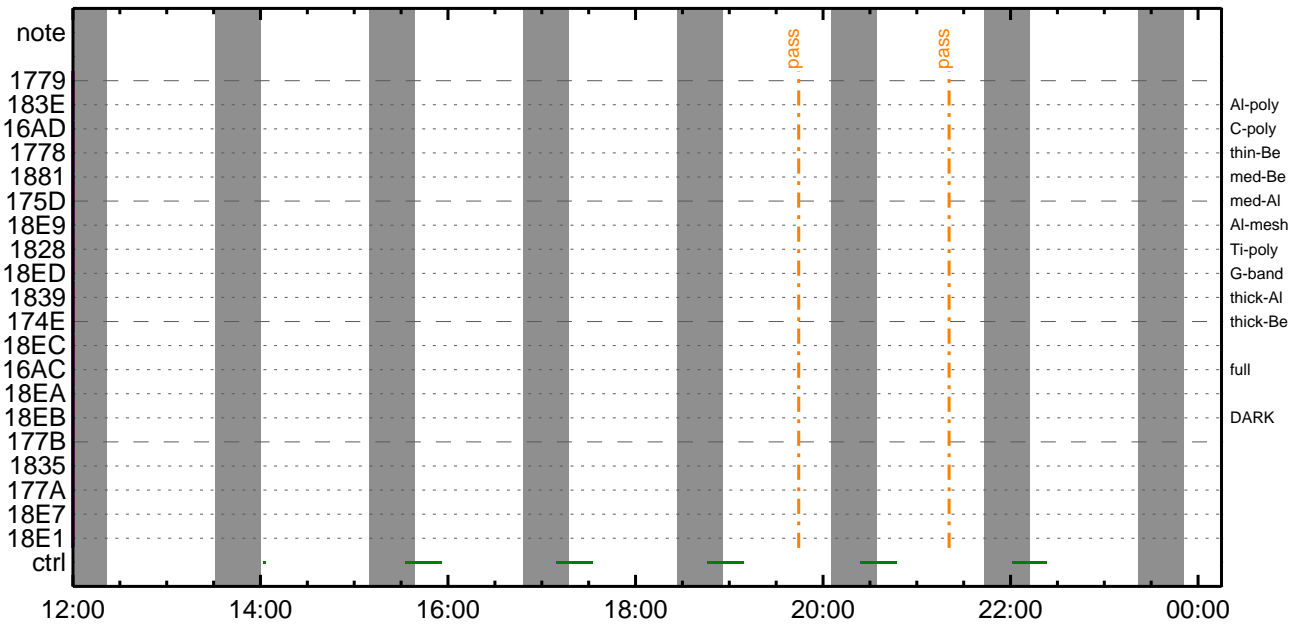
CMDI #0644 2012/05/24



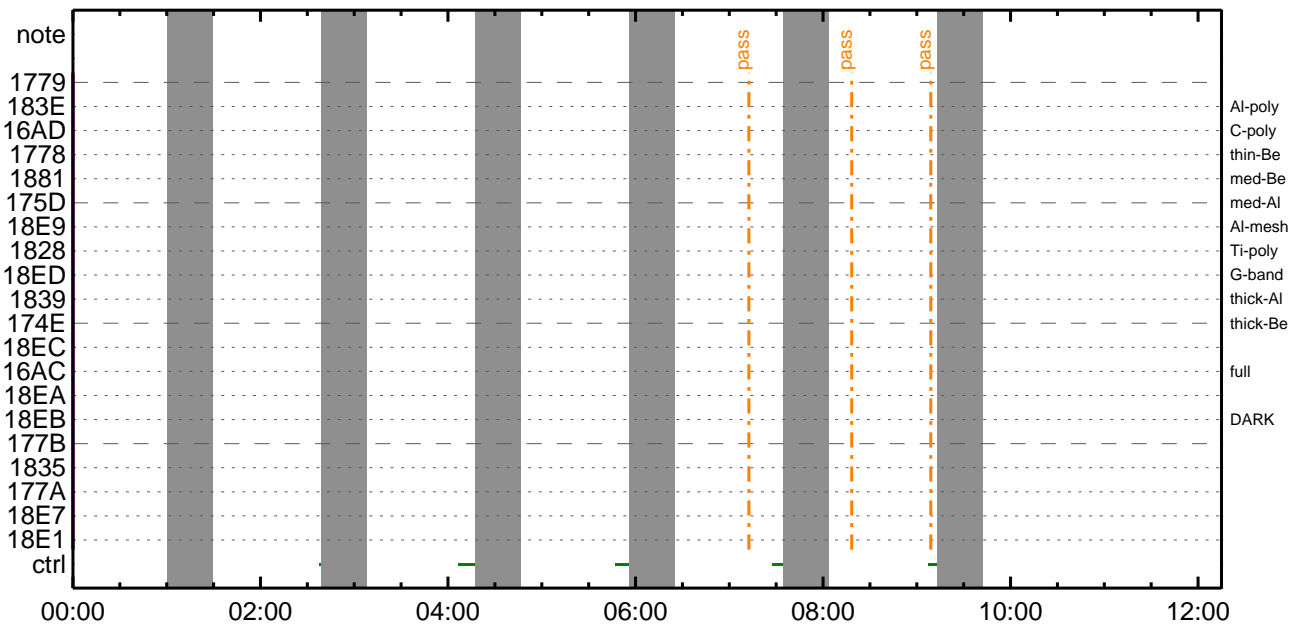
CMDI #0644 2012/05/25



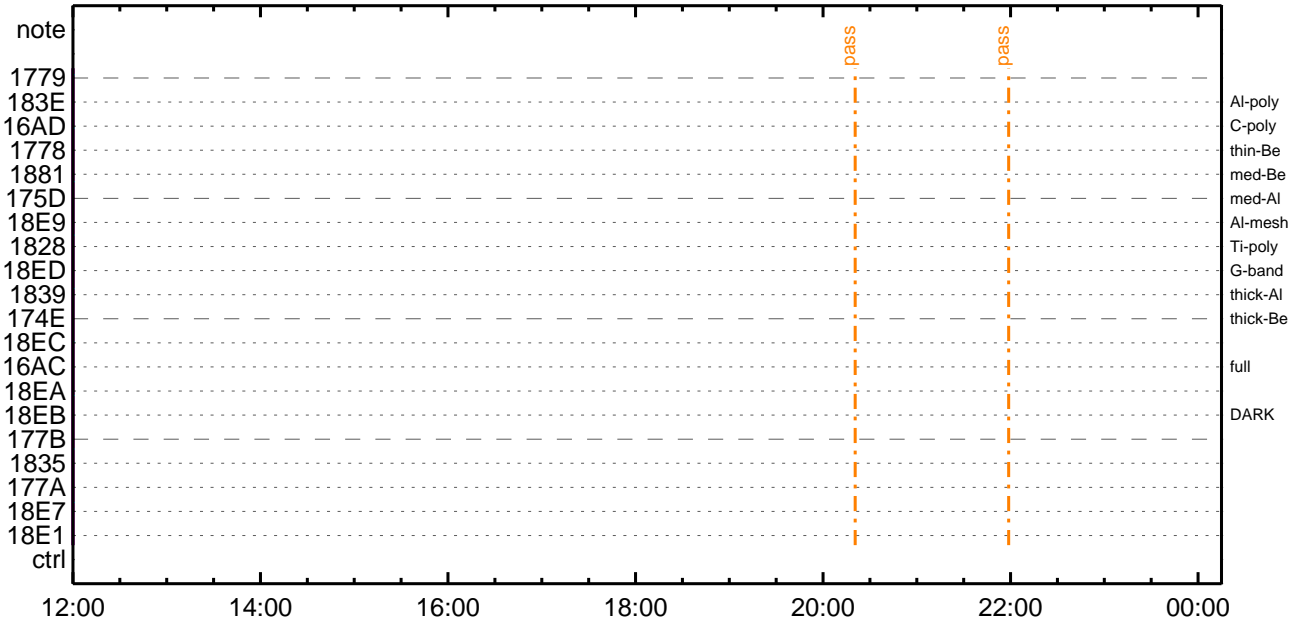
CMDI #0644 2012/05/25



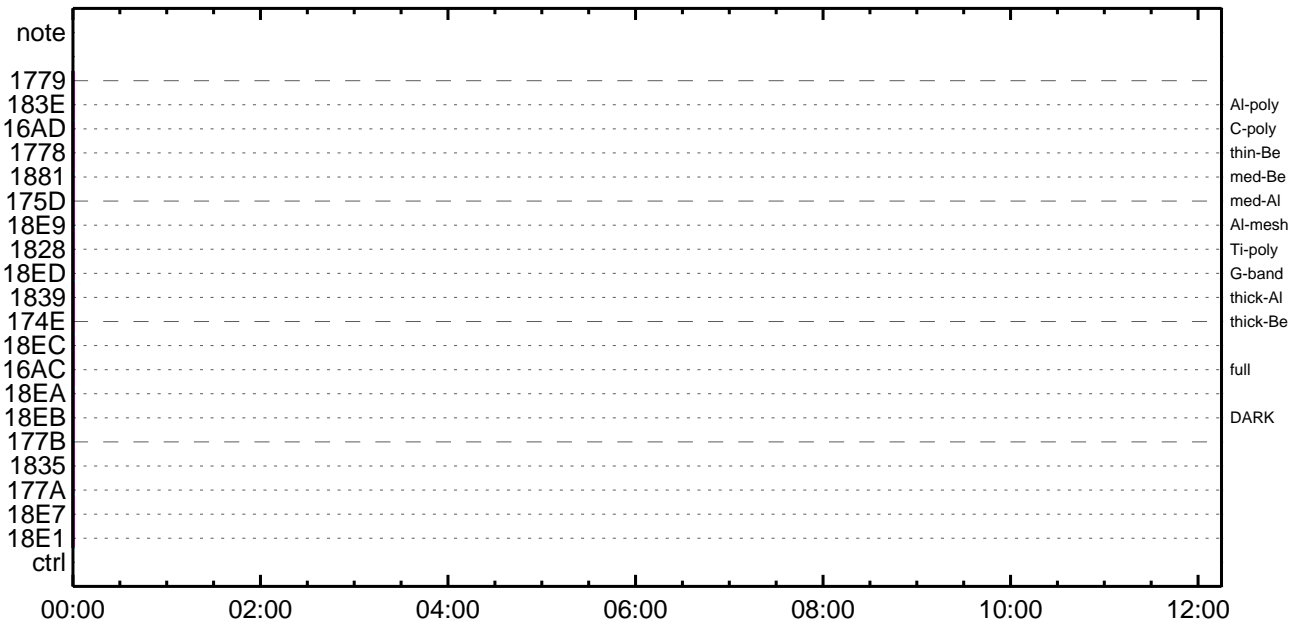
CMDI #0644 2012/05/26



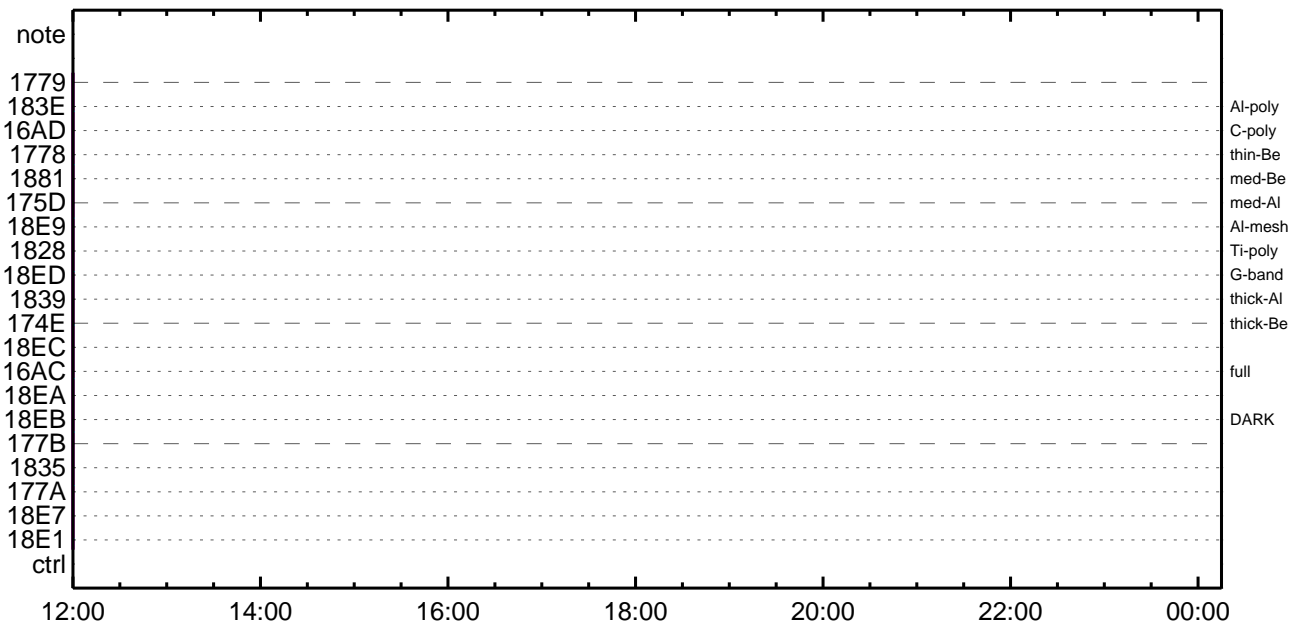
CMDI #0644 2012/05/26



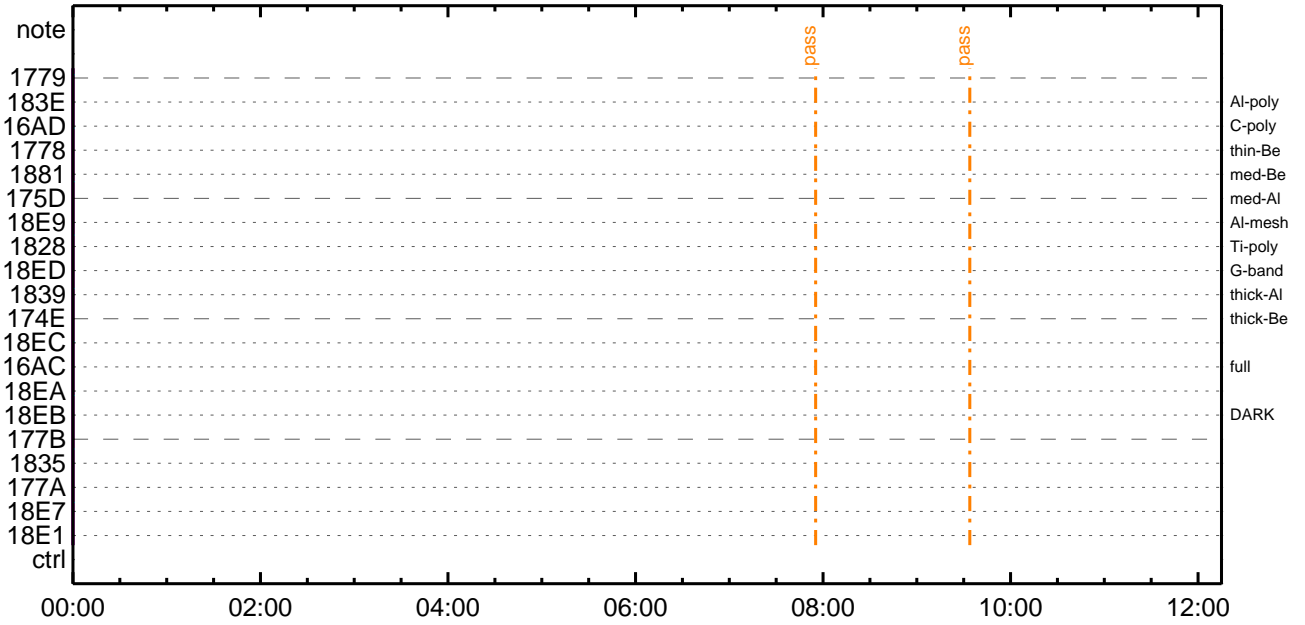
CMDI #0644 2012/05/27



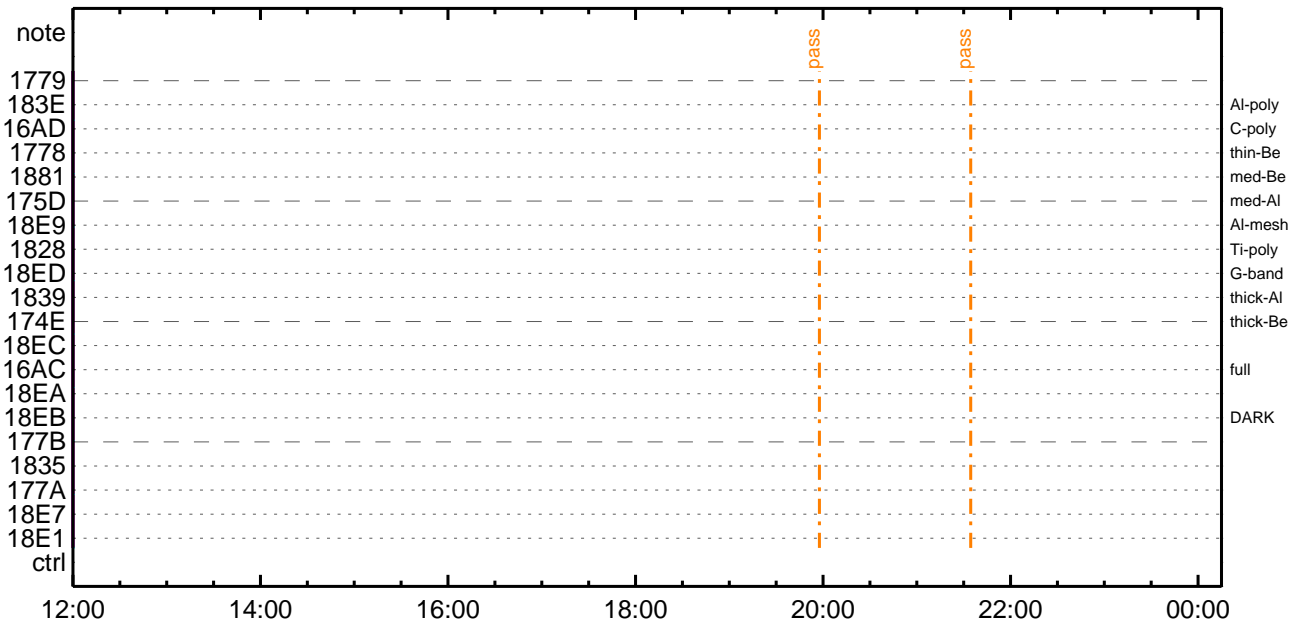
CMDI #0644 2012/05/27



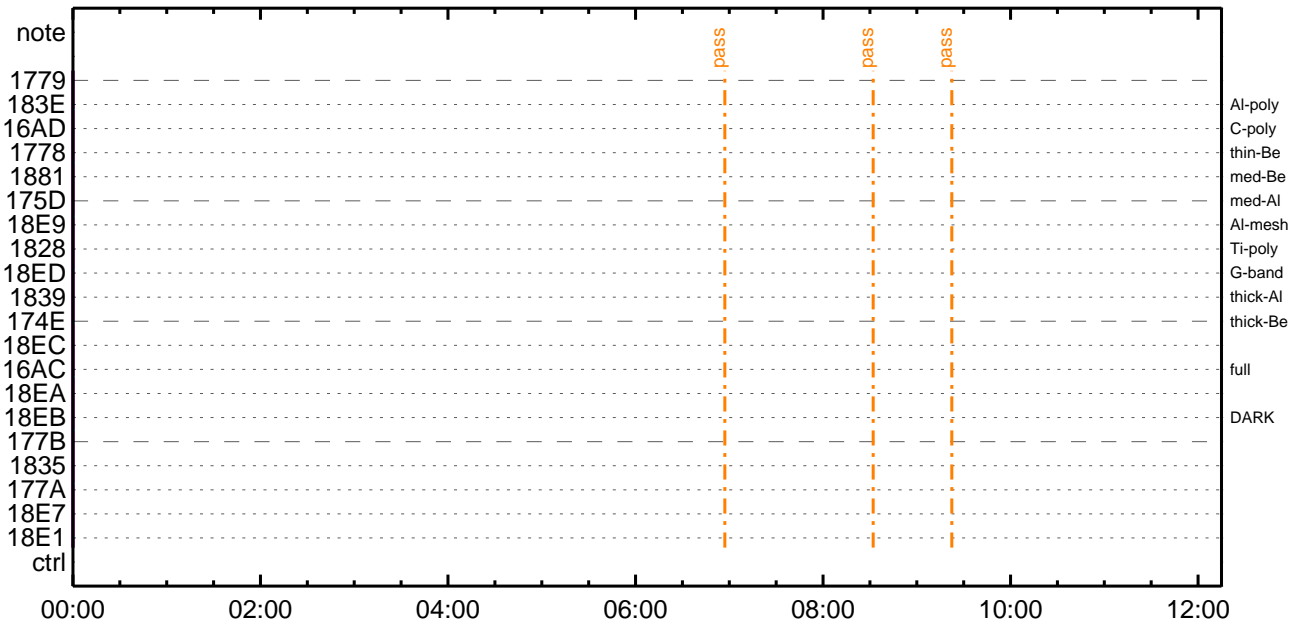
CMDI #0644 2012/05/28



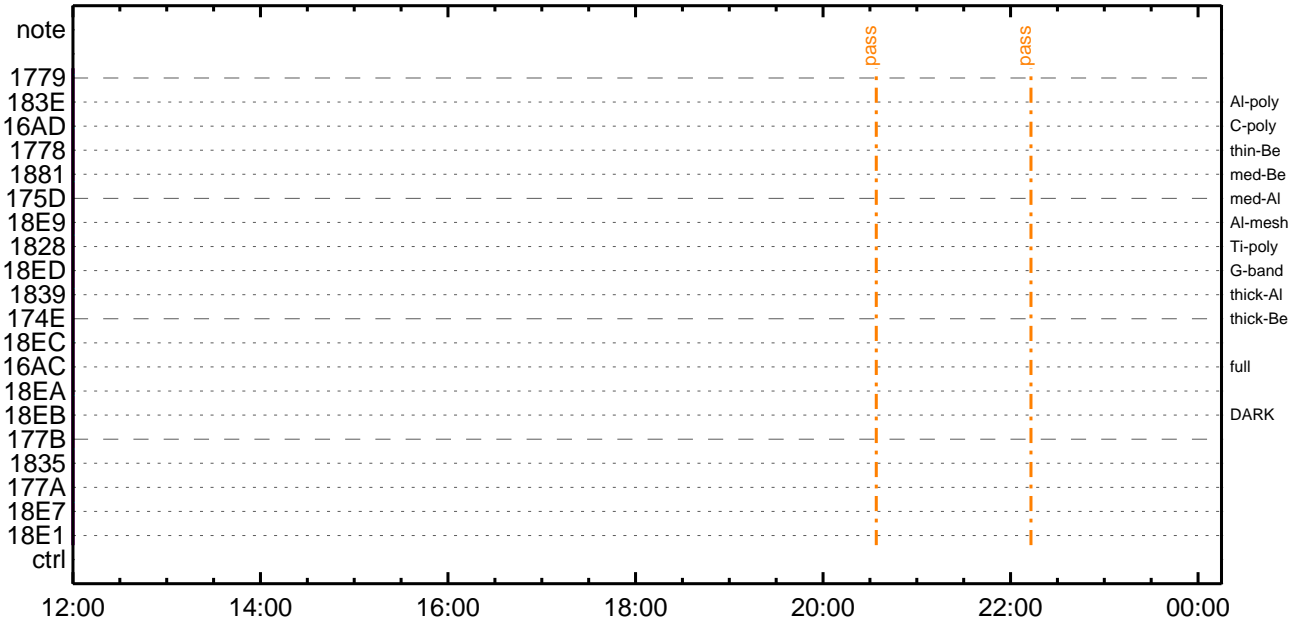
CMDI #0644 2012/05/28



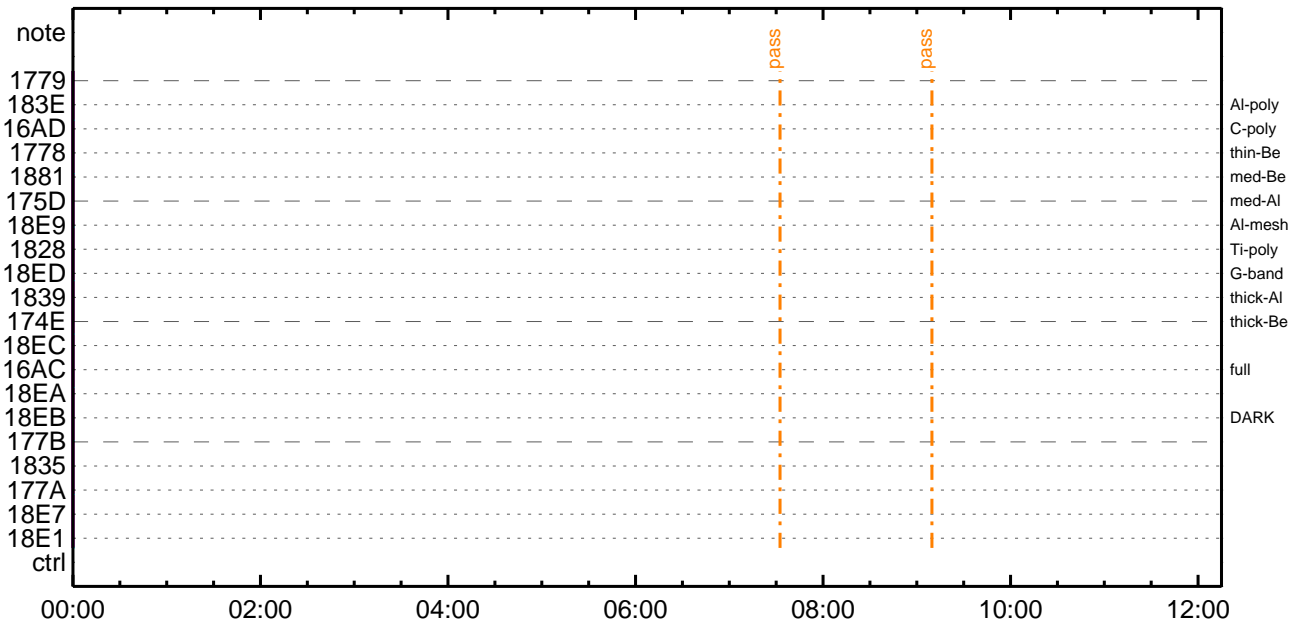
CMDI #0644 2012/05/29



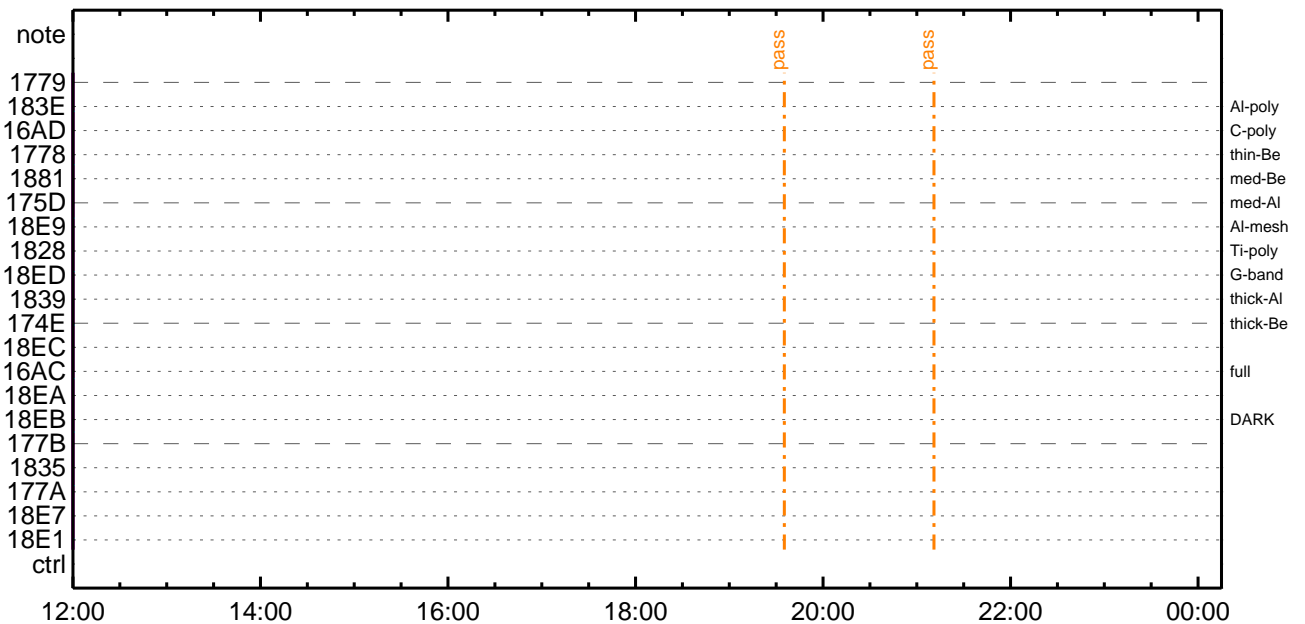
CMDI #0644 2012/05/29



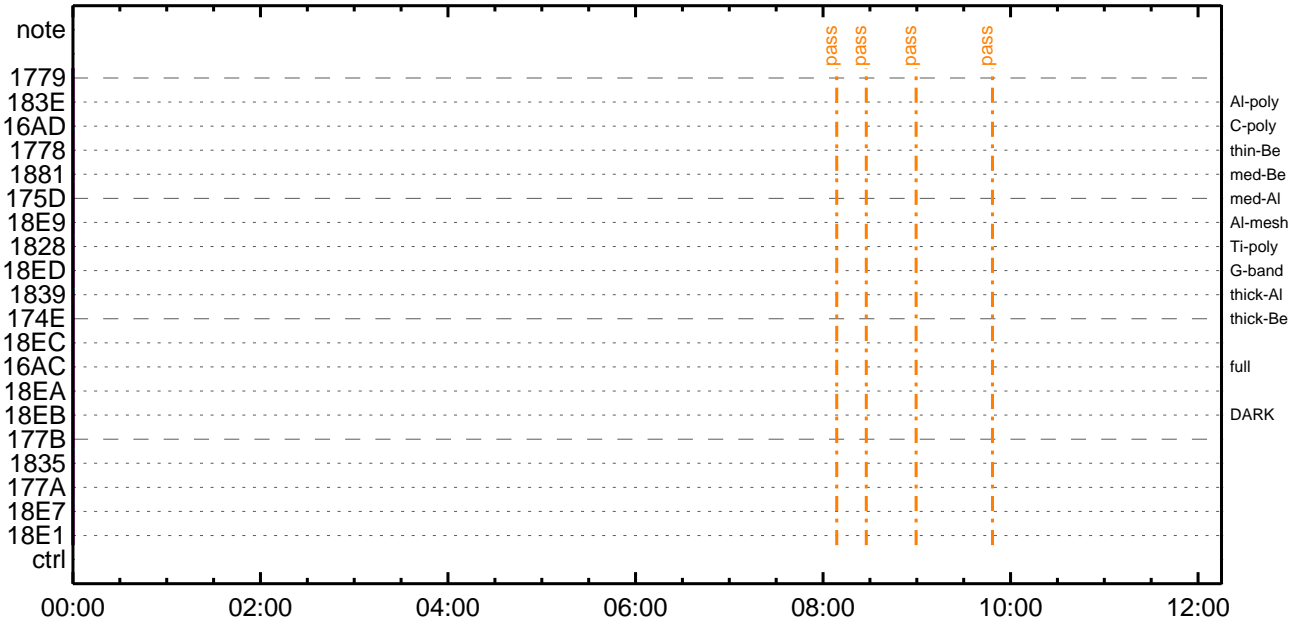
CMDI #0644 2012/05/30



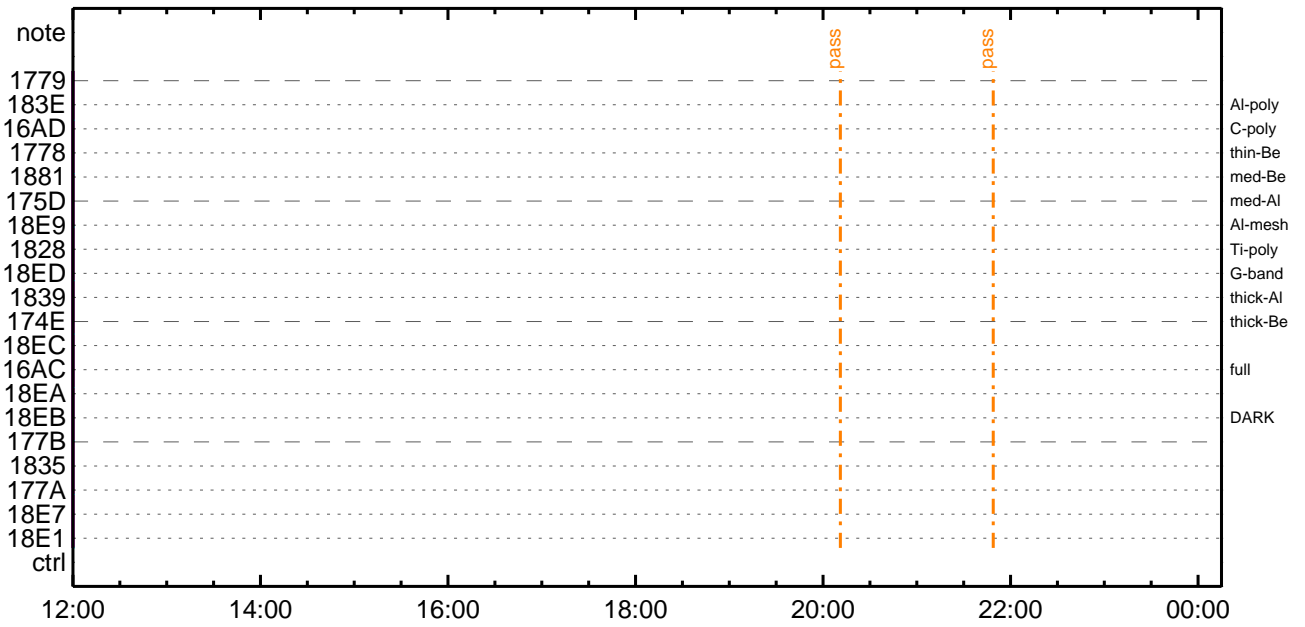
CMDI #0644 2012/05/30



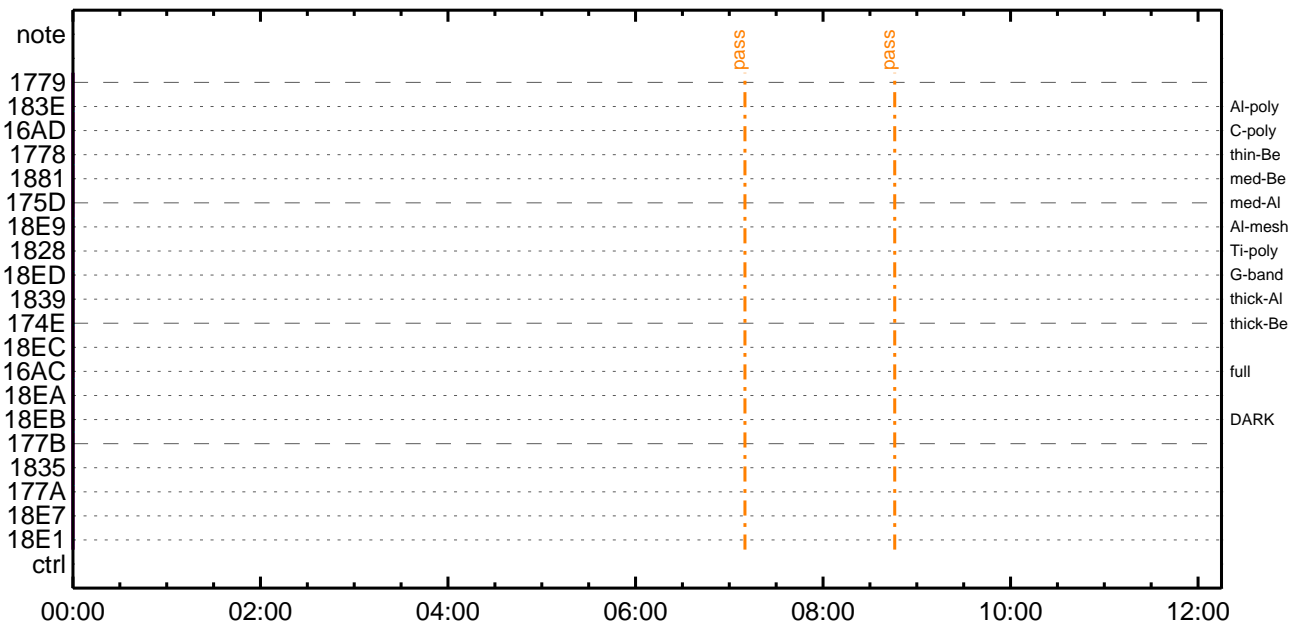
CMDI #0644 2012/05/31



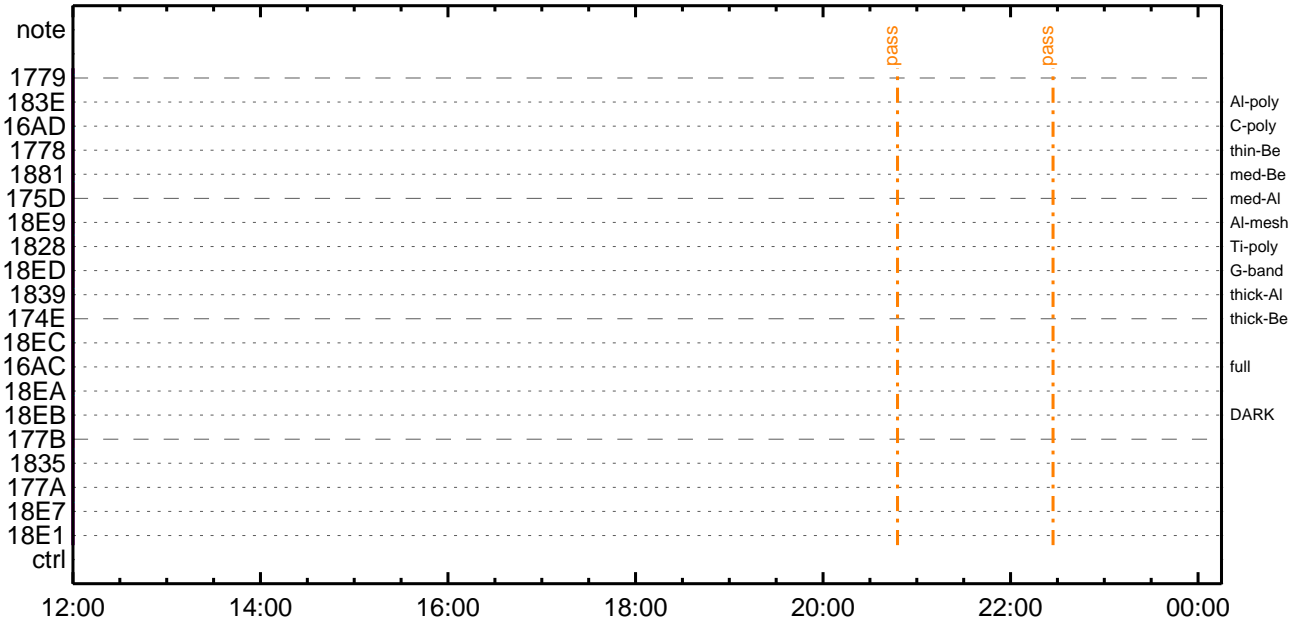
CMDI #0644 2012/05/31



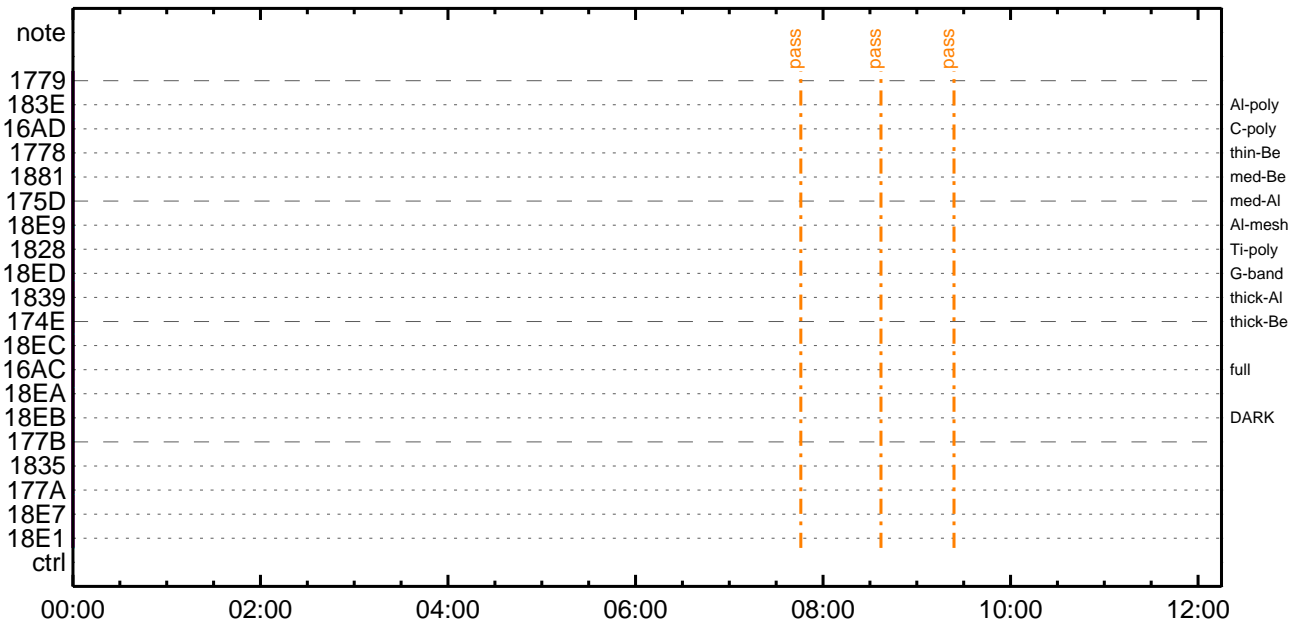
CMDI #0644 2012/06/01



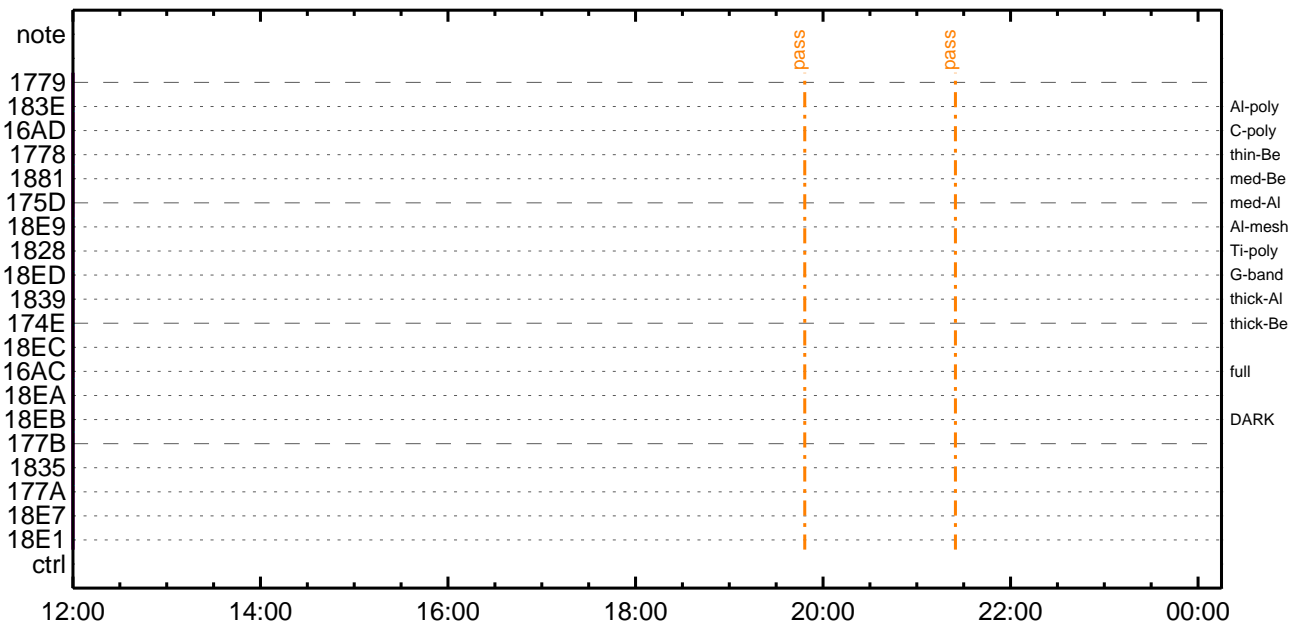
CMDI #0644 2012/06/01



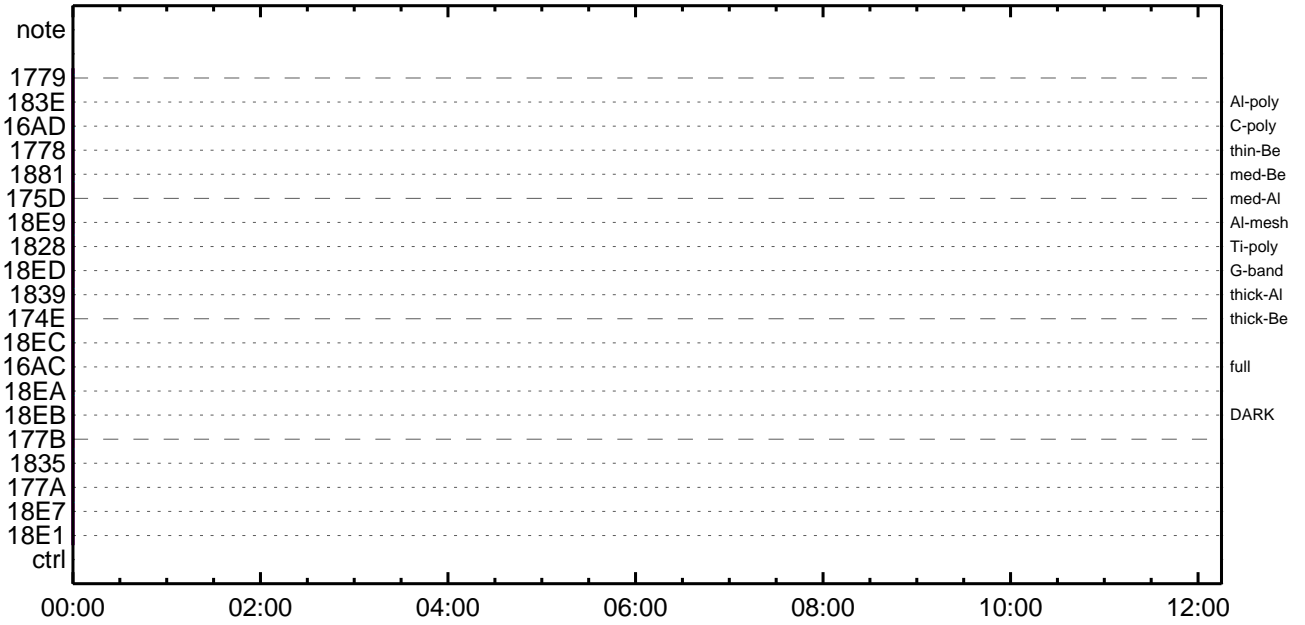
CMDI #0644 2012/06/02



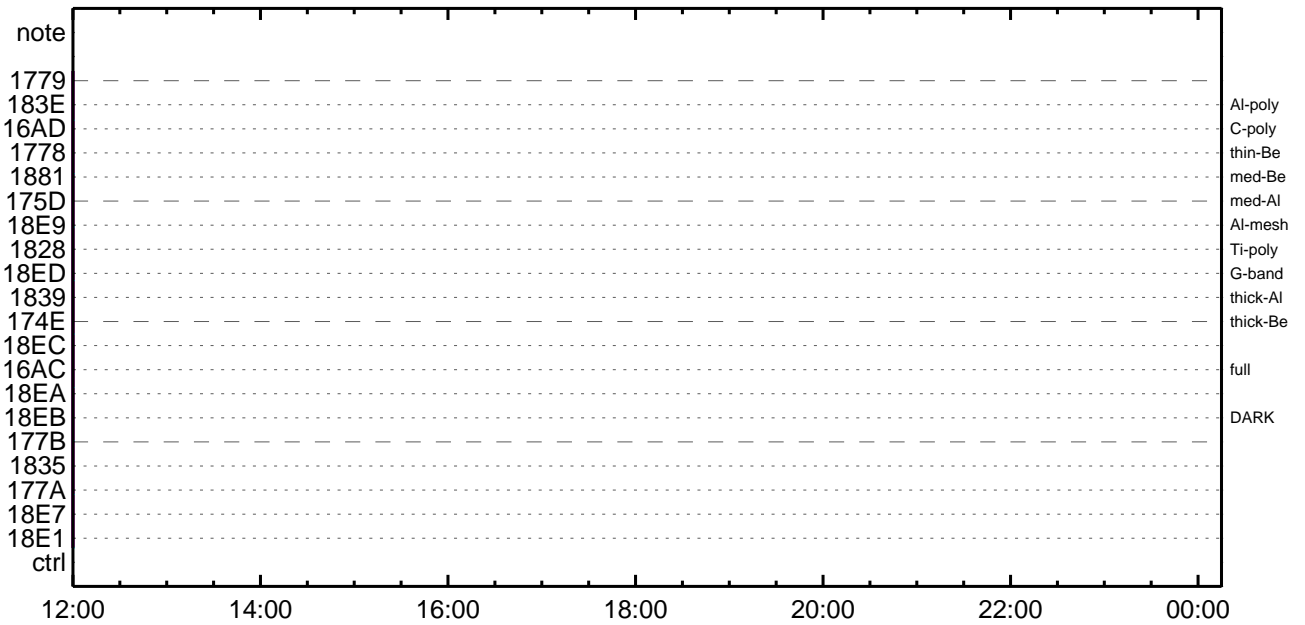
CMDI #0644 2012/06/02



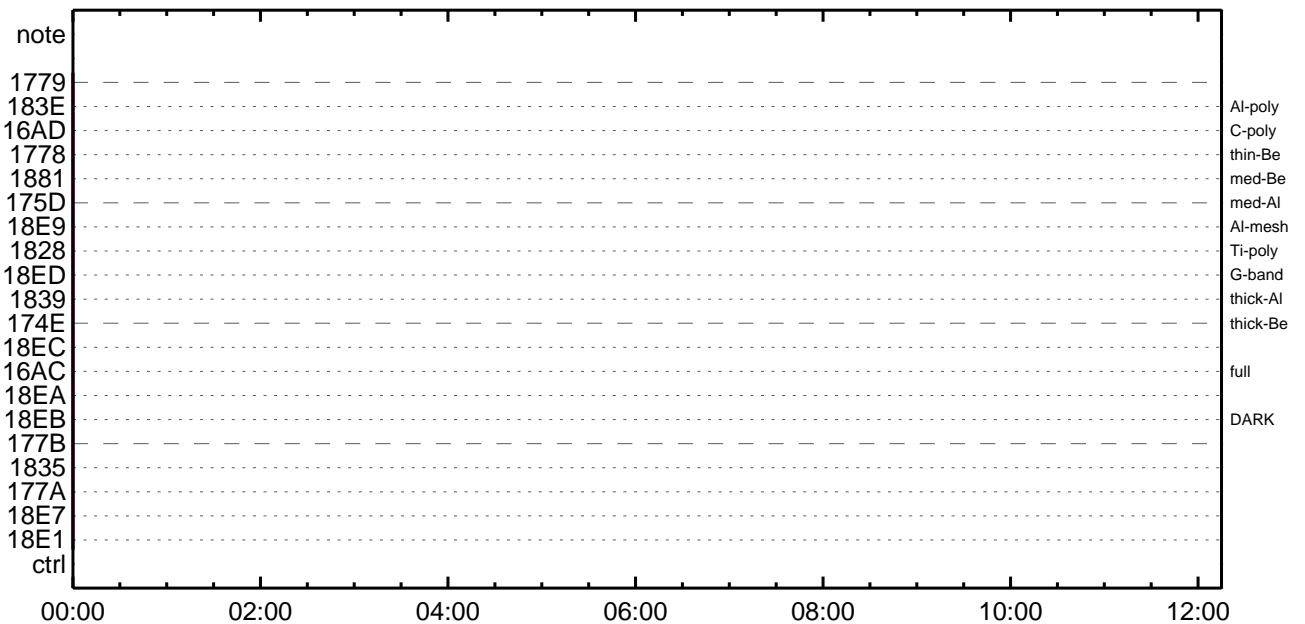
CMDI #0644 2012/06/03



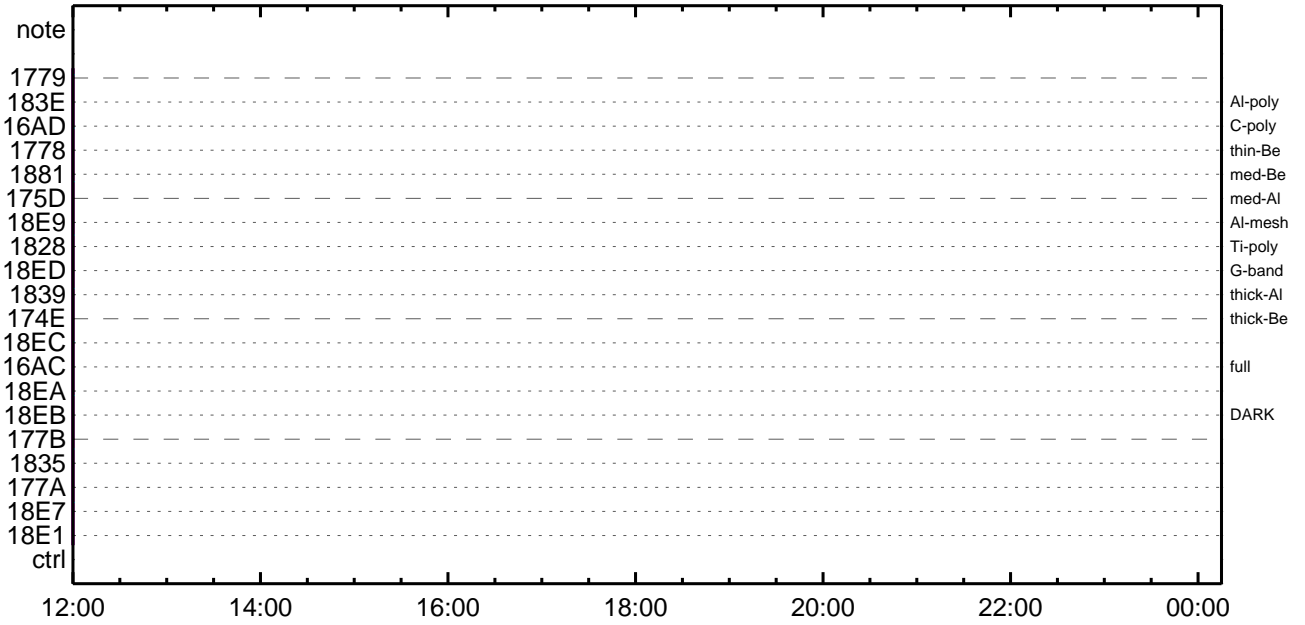
CMDI #0644 2012/06/03



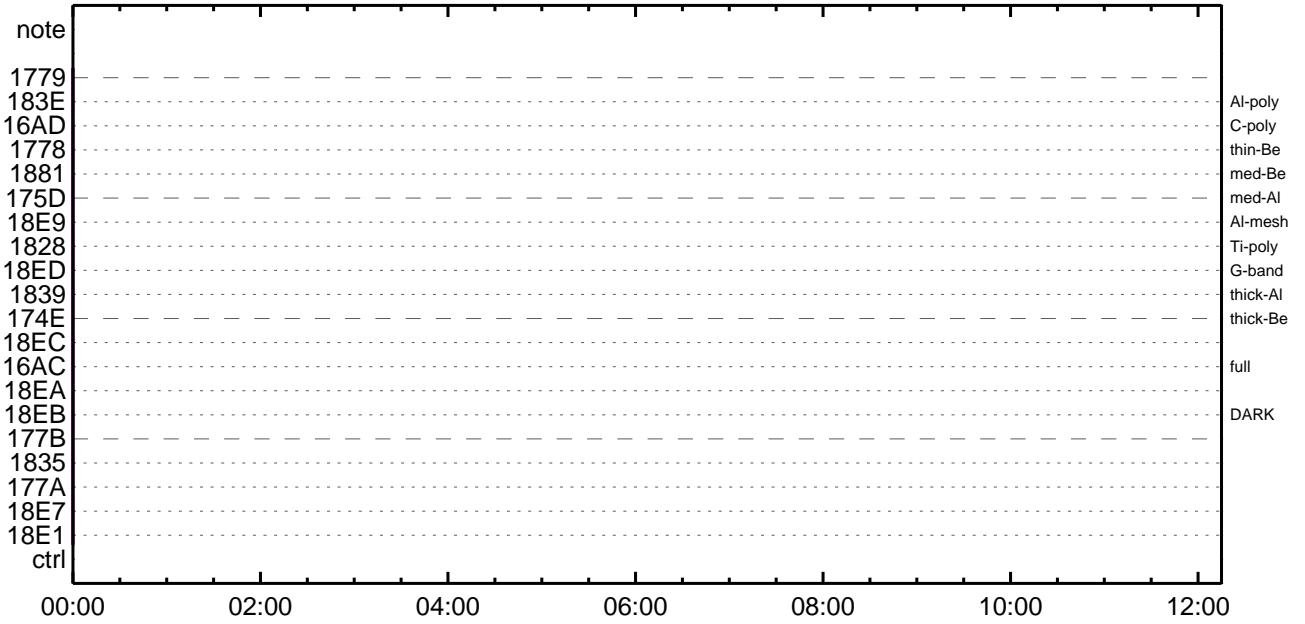
CMDI #0644 2012/06/04



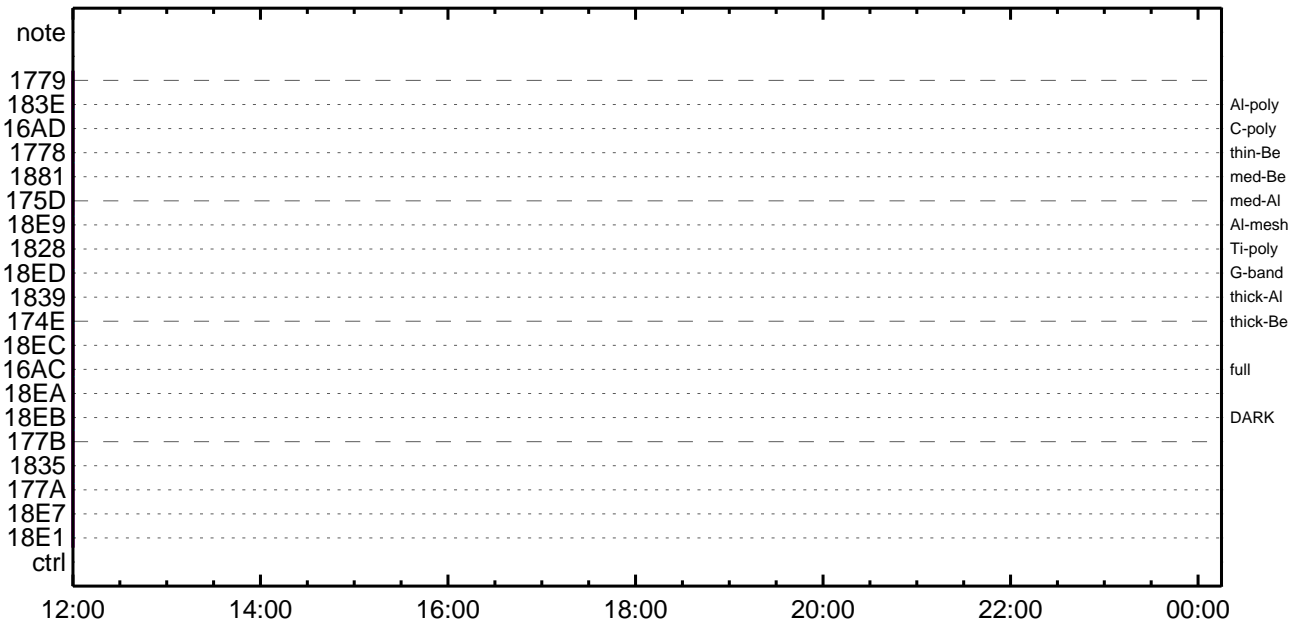
CMDI #0644 2012/06/04



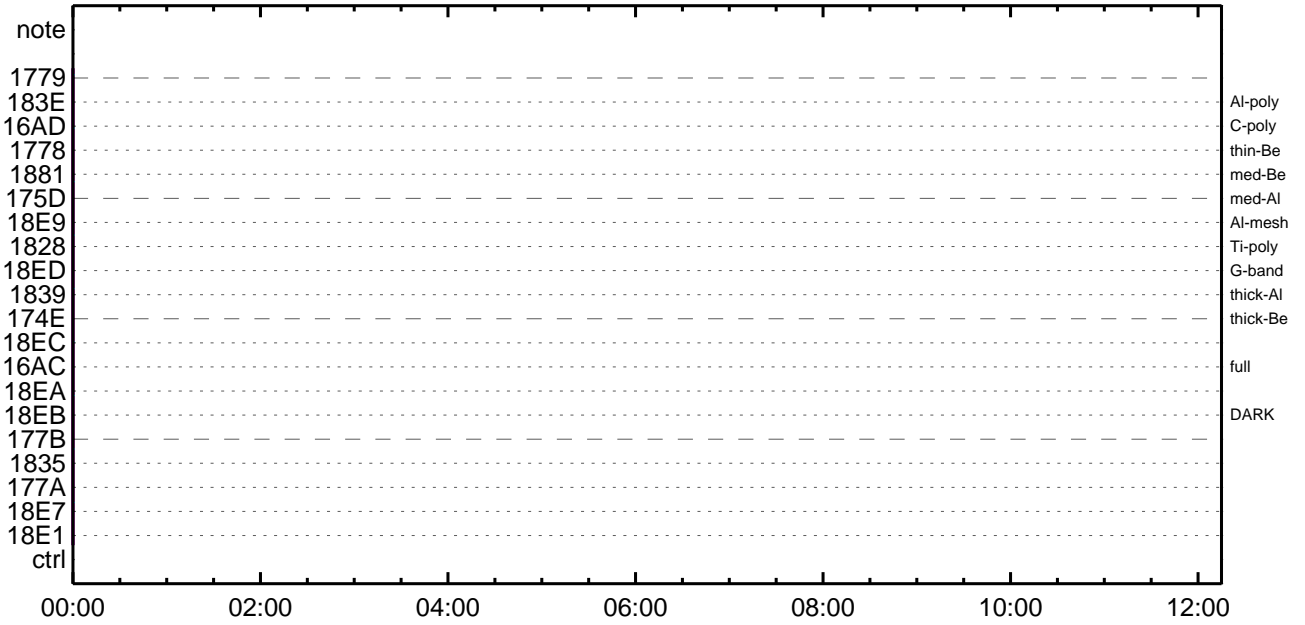
CMDI #0644 2012/06/05



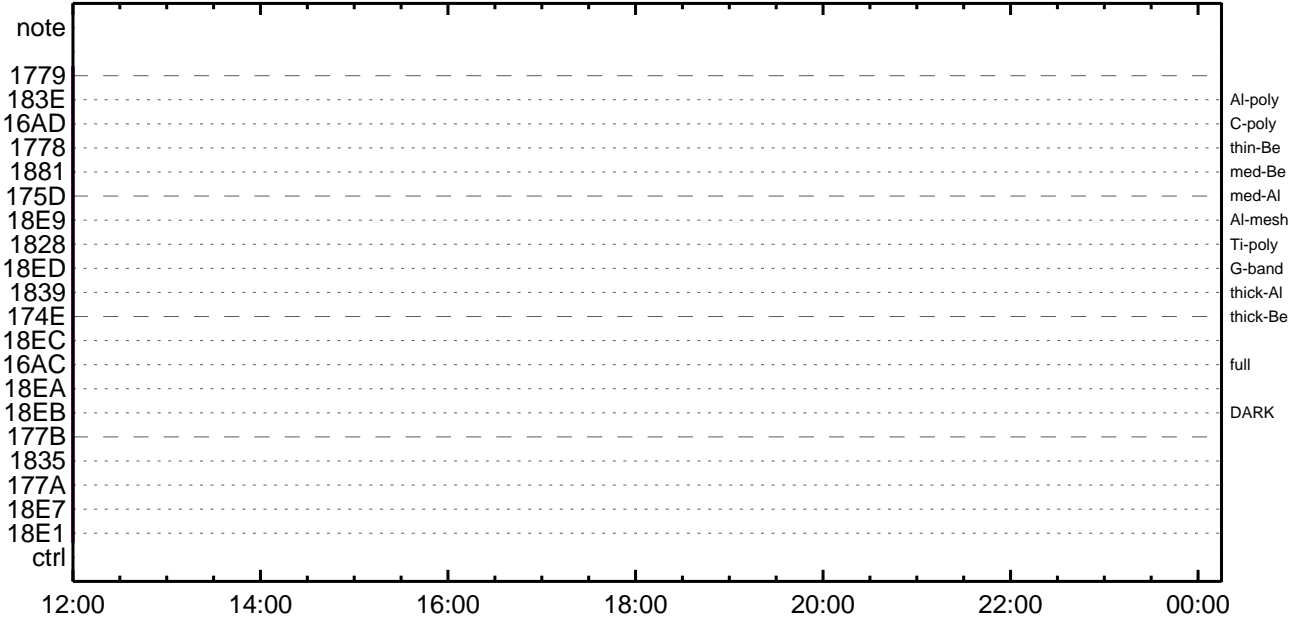
CMDI #0644 2012/06/05



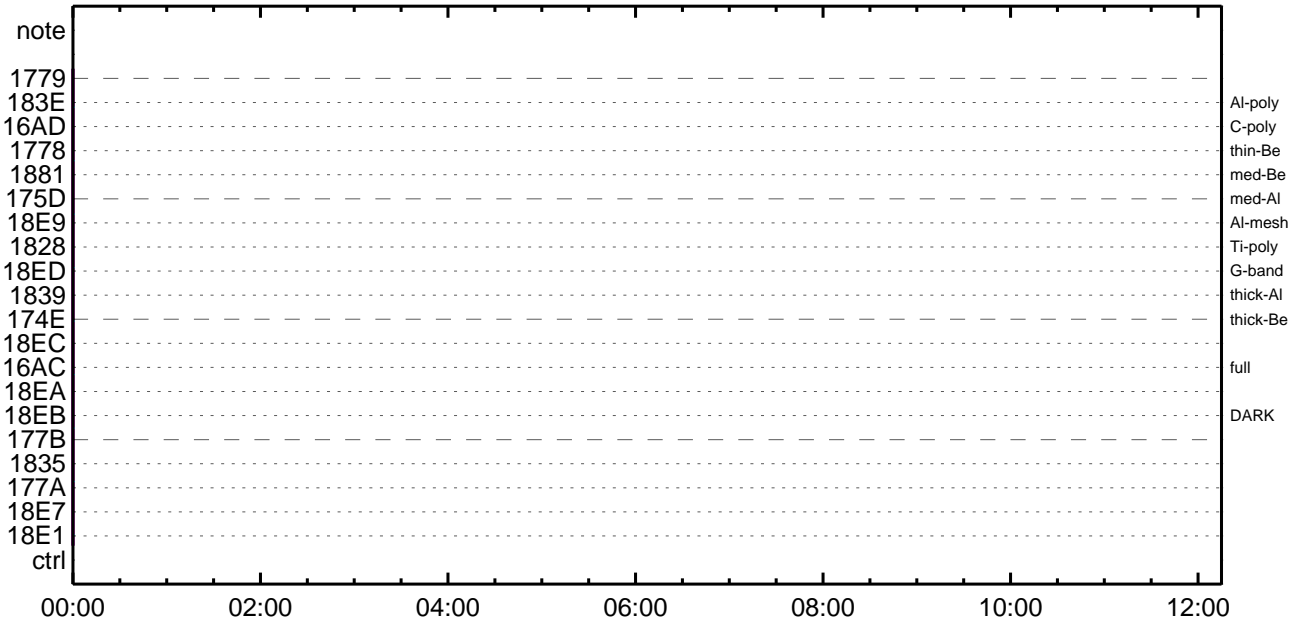
CMDI #0644 2012/06/06



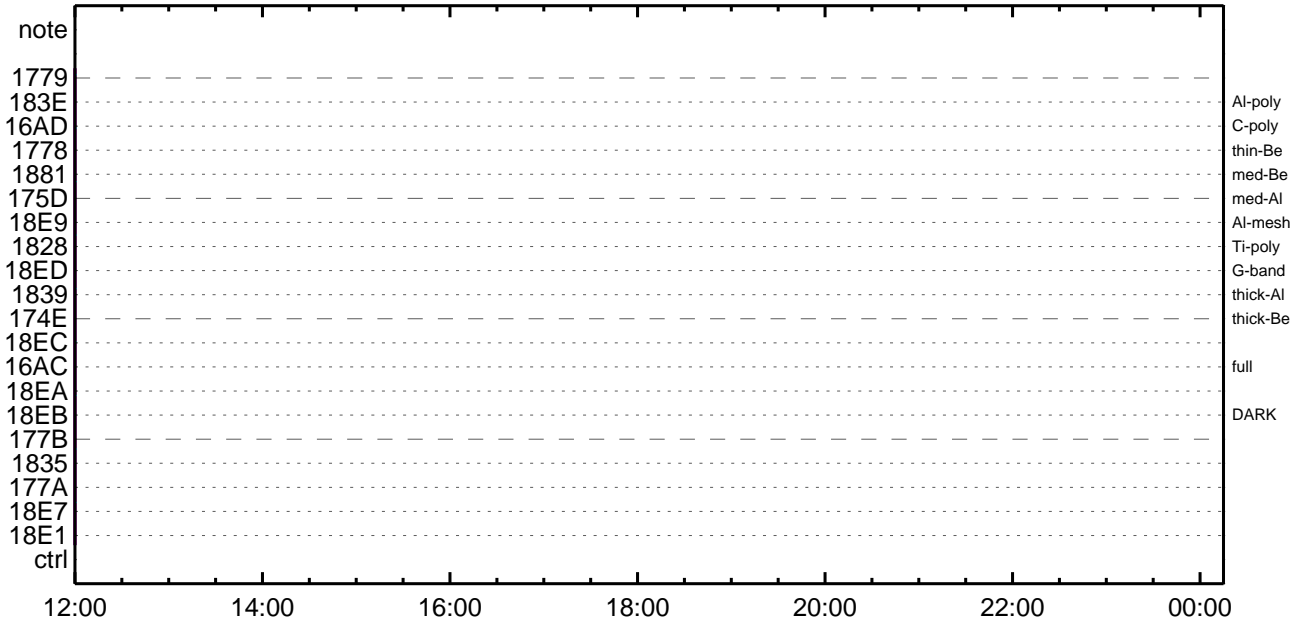
CMDI #0644 2012/06/06



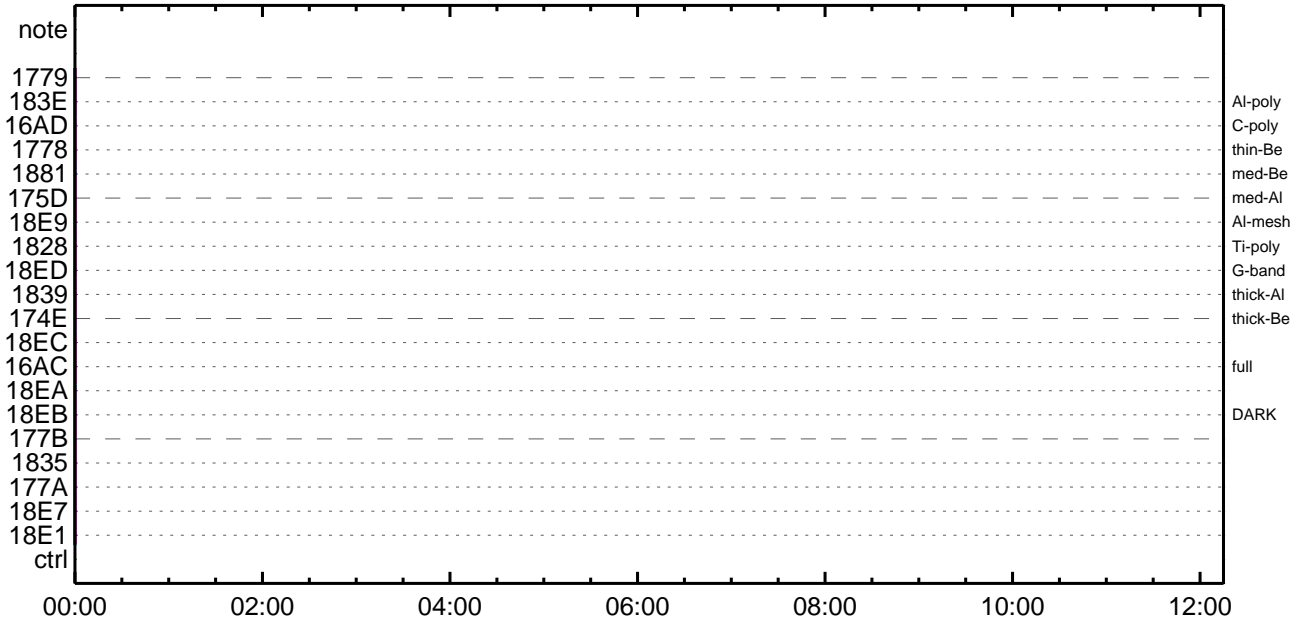
CMDI #0644 2012/06/07



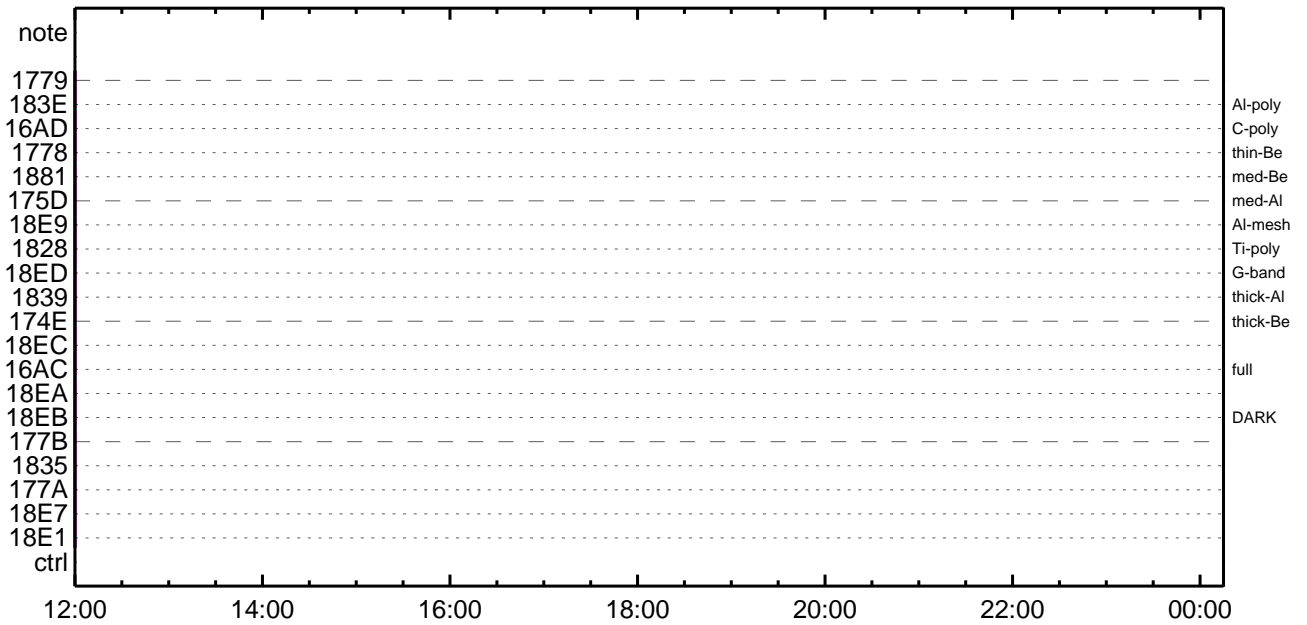
CMDI #0644 2012/06/07



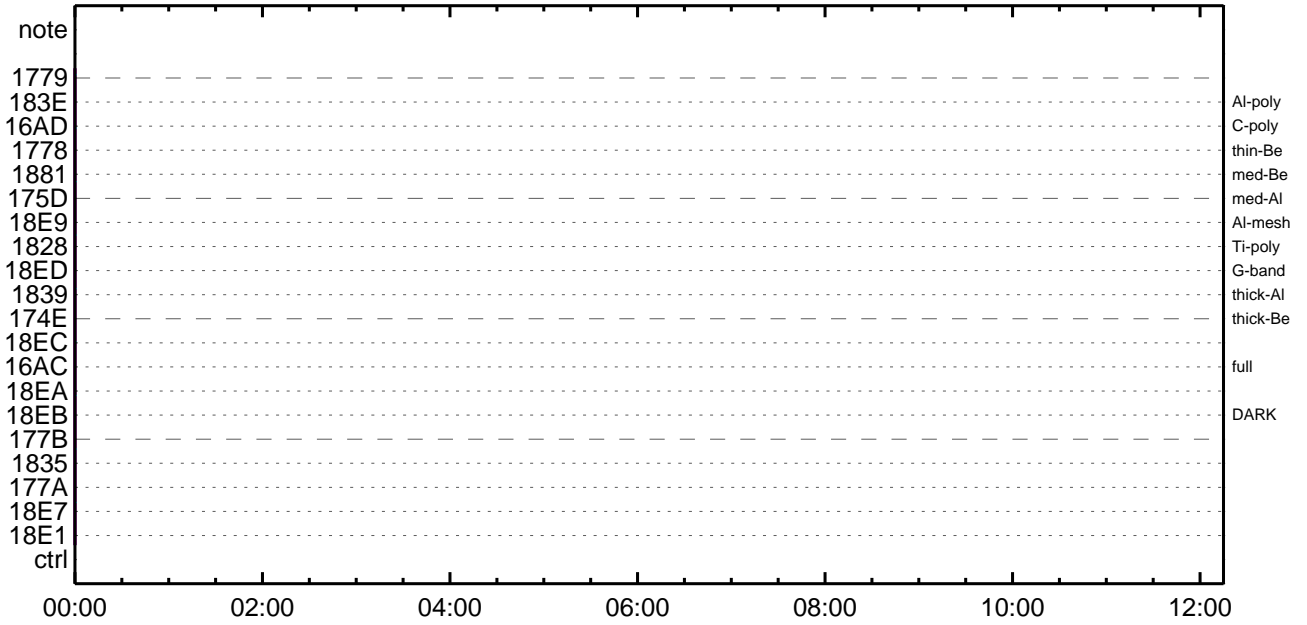
CMDI #0644 2012/06/08



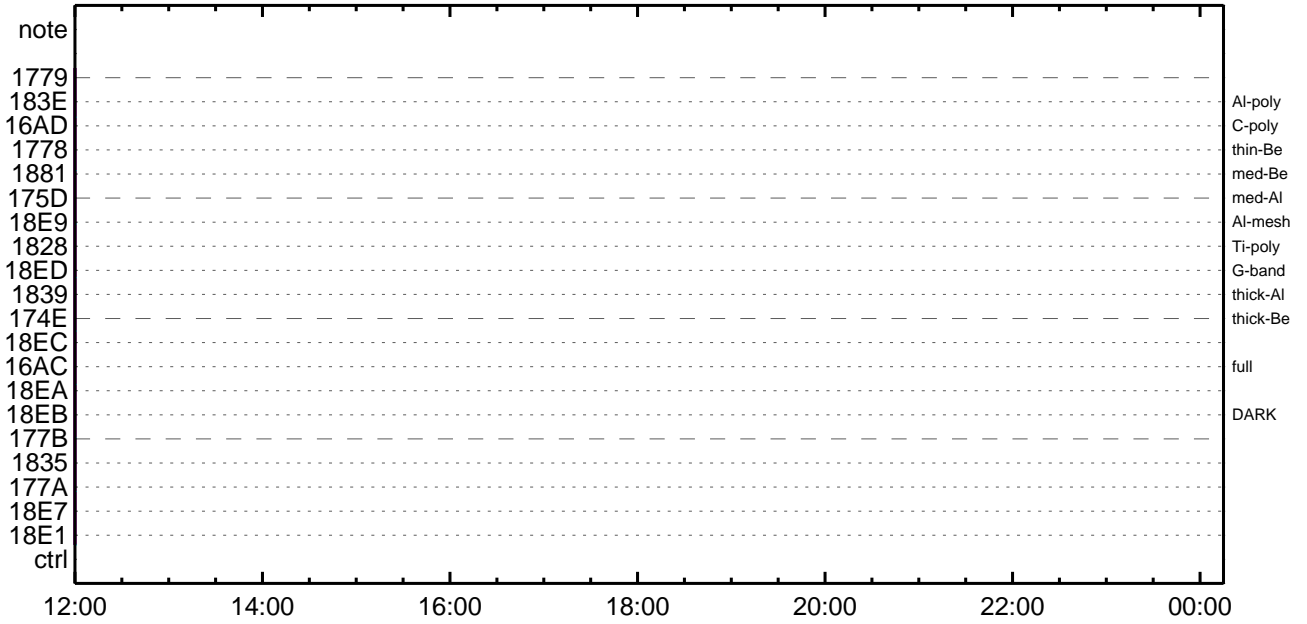
CMDI #0644 2012/06/08



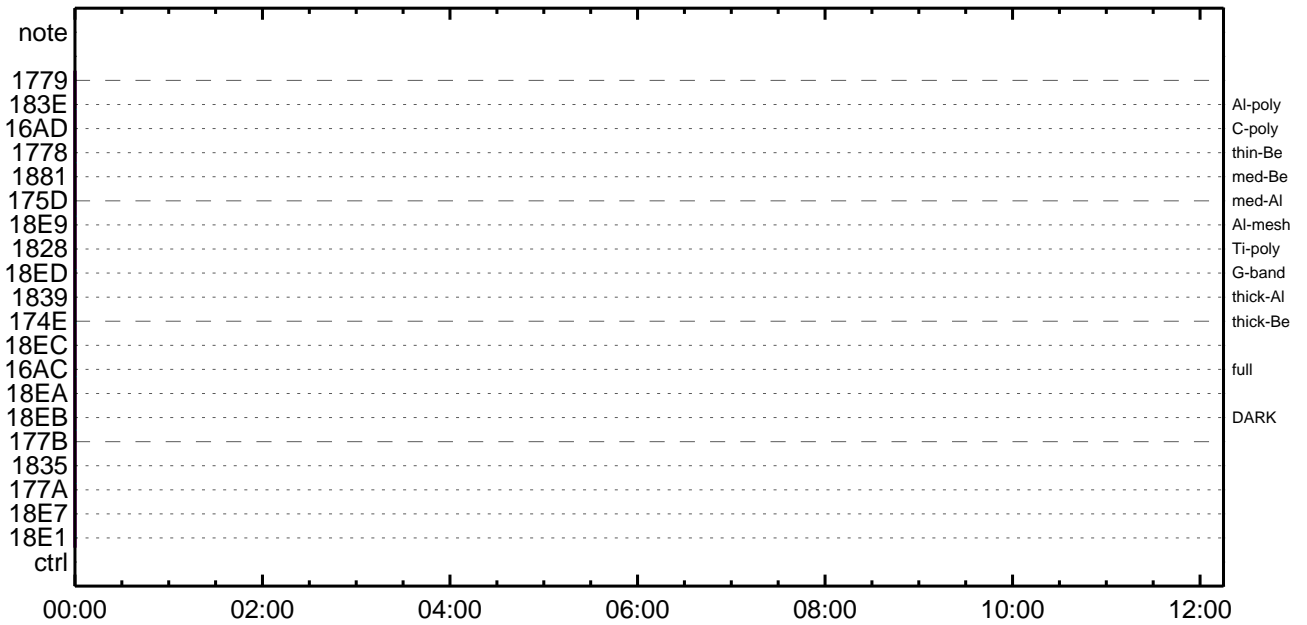
CMDI #0644 2012/06/09



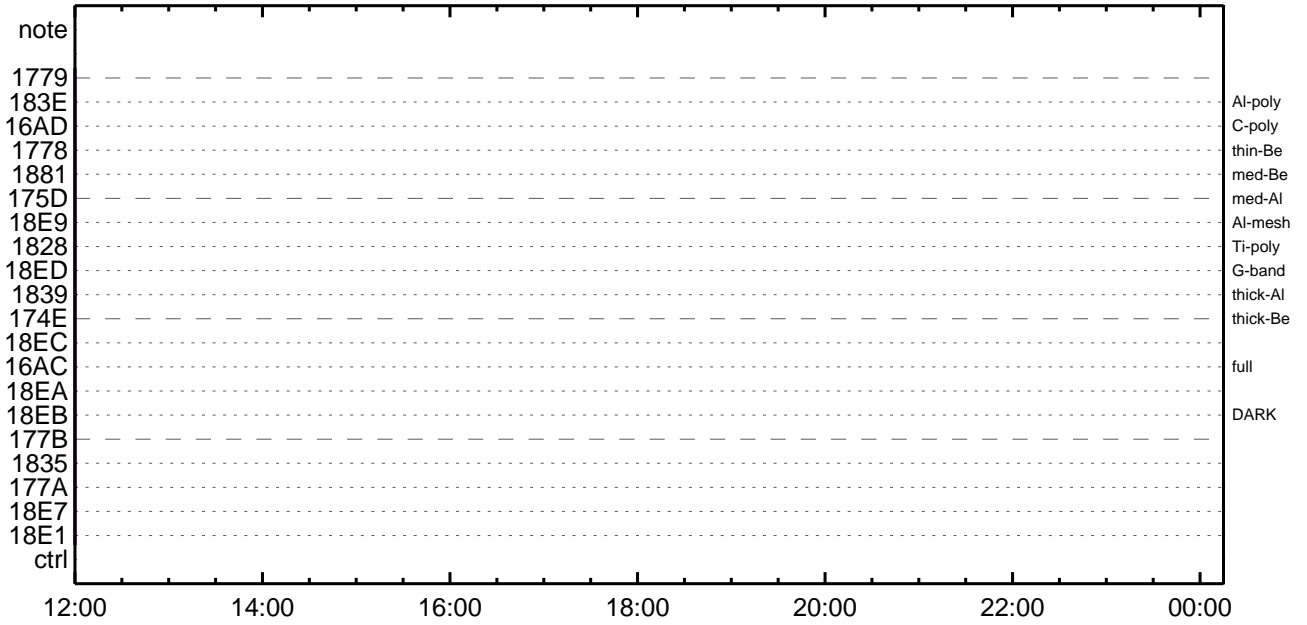
CMDI #0644 2012/06/09



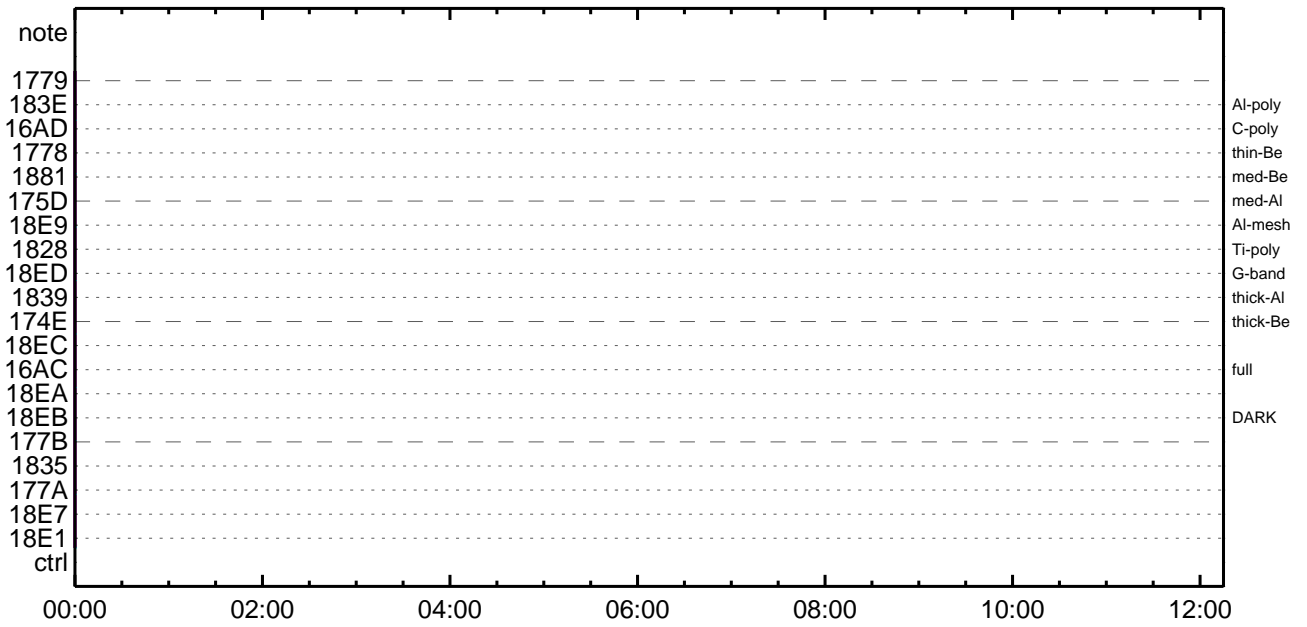
CMDI #0644 2012/06/10



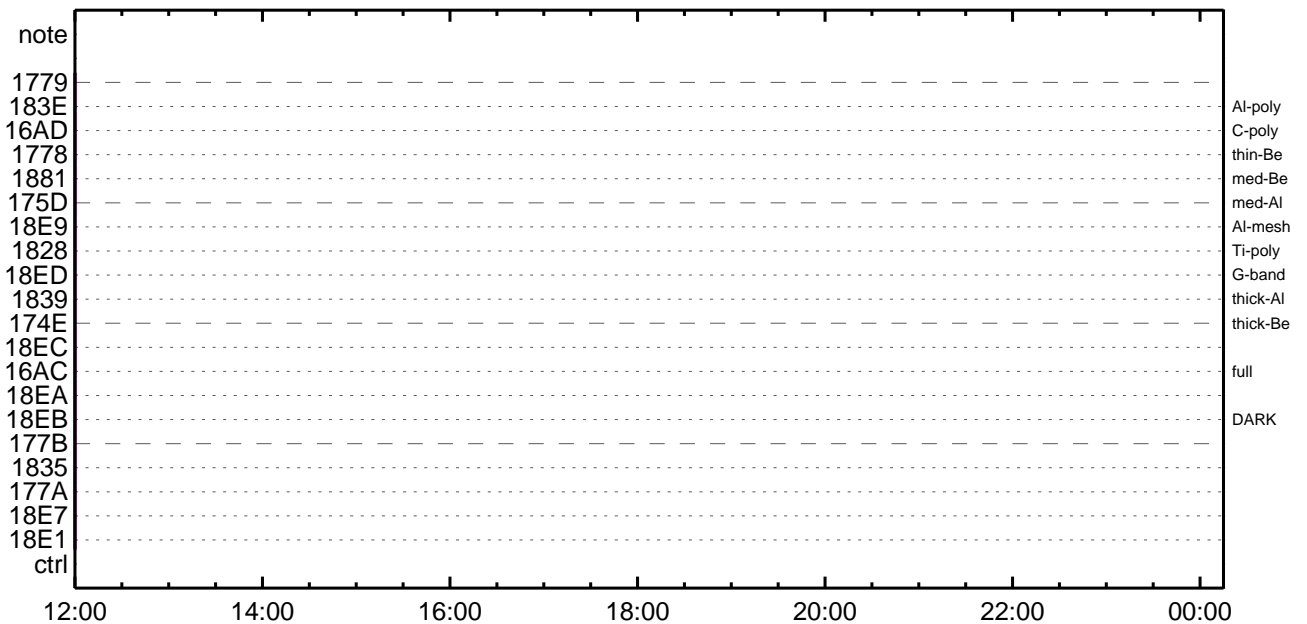
CMDI #0644 2012/06/10



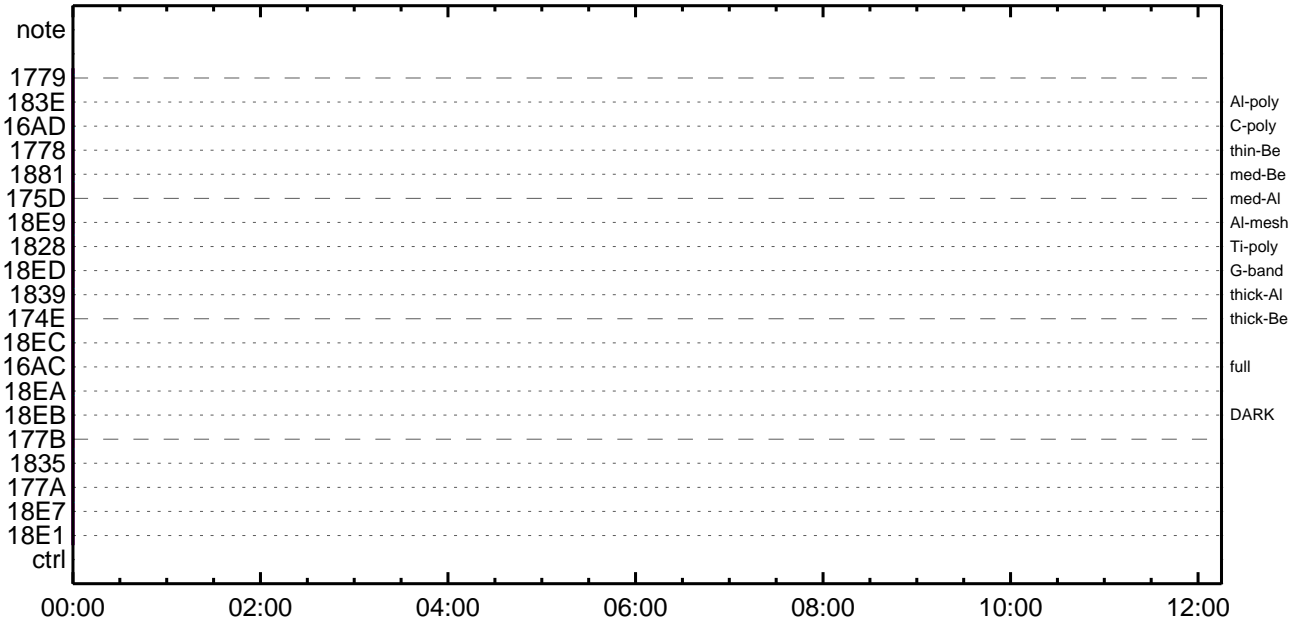
CMDI #0644 2012/06/11



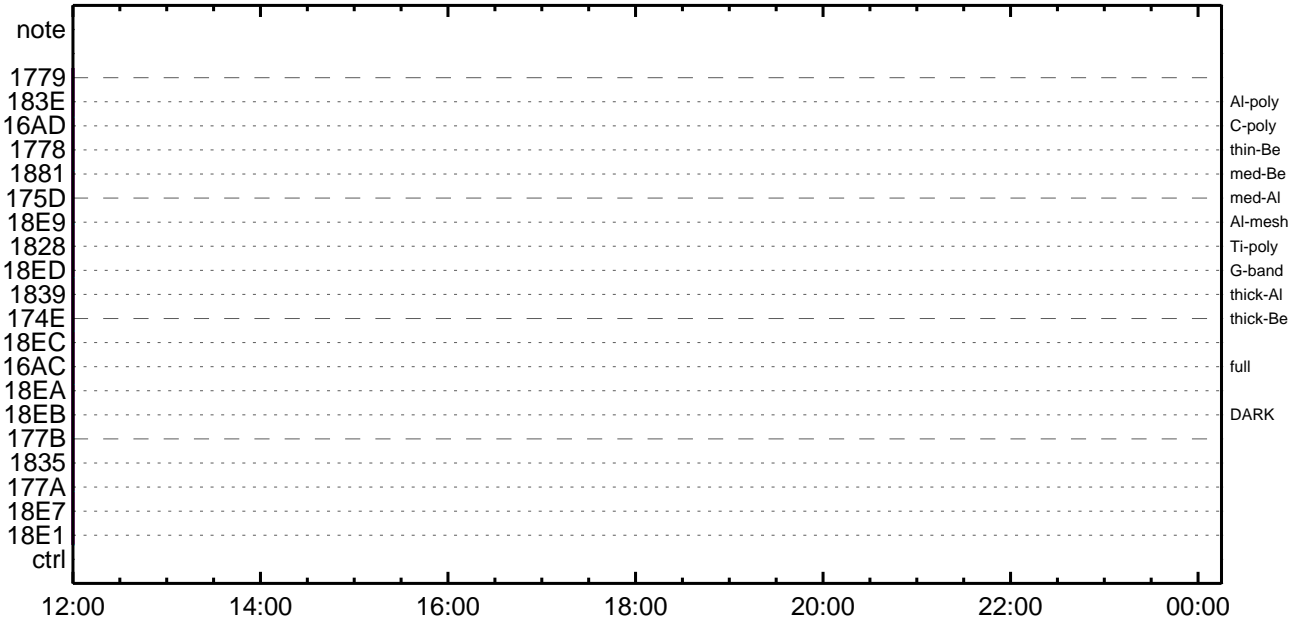
CMDI #0644 2012/06/11



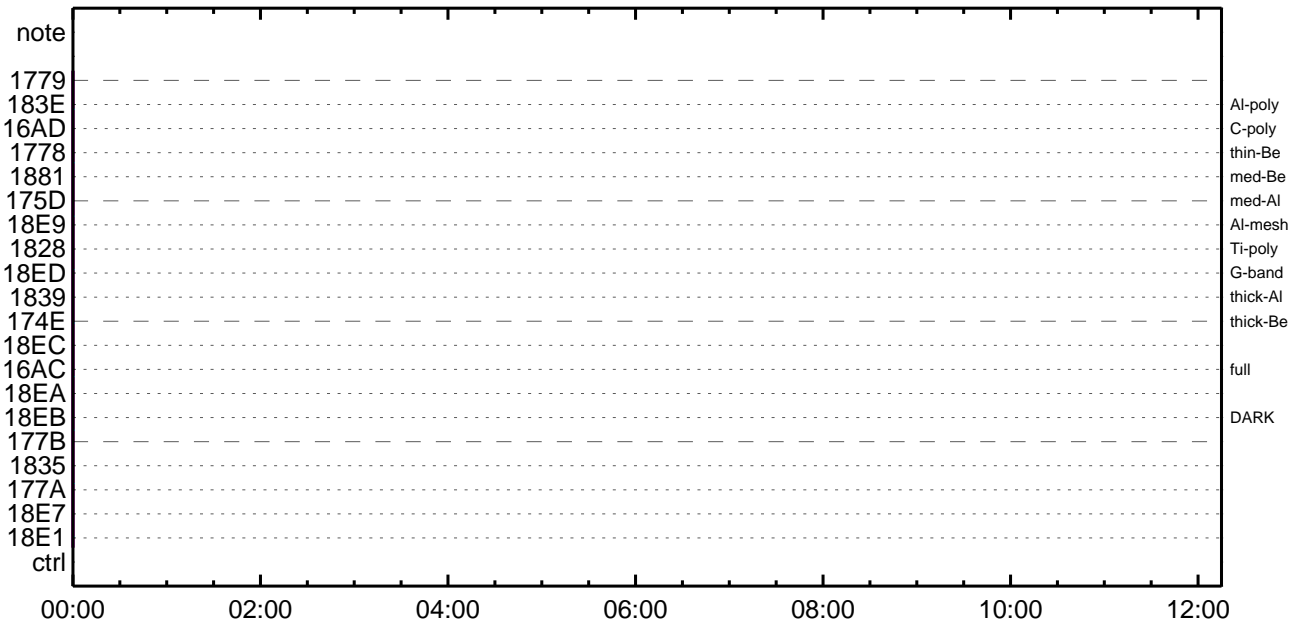
CMDI #0644 2012/06/12



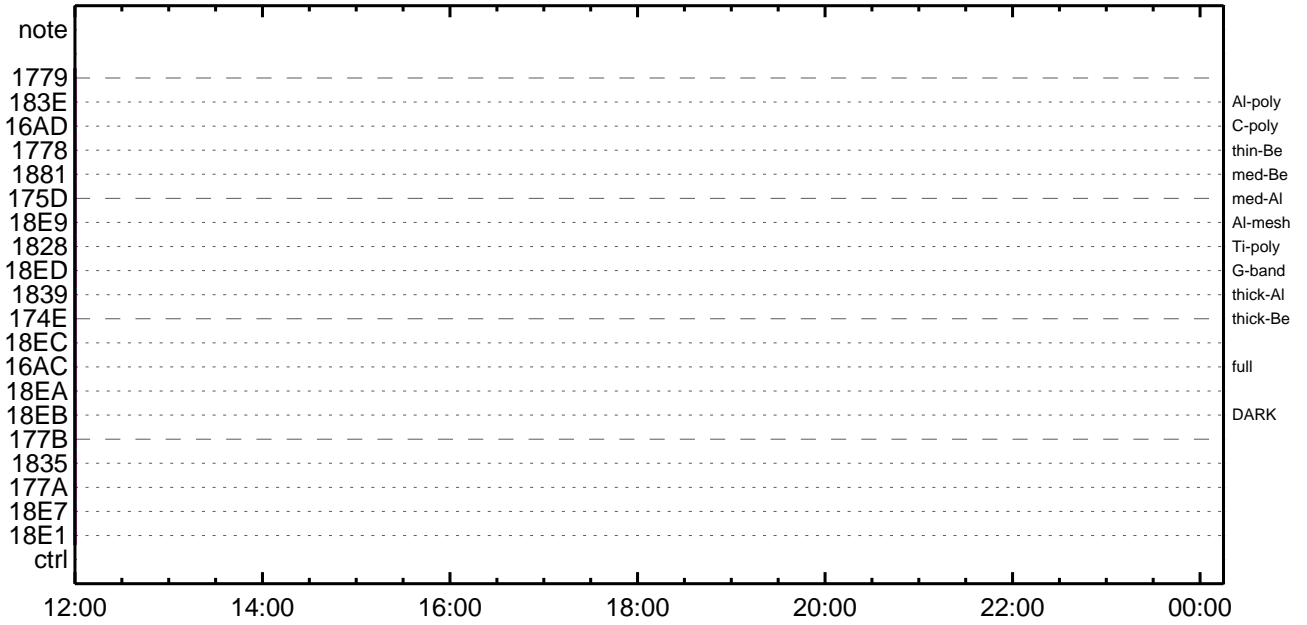
CMDI #0644 2012/06/12



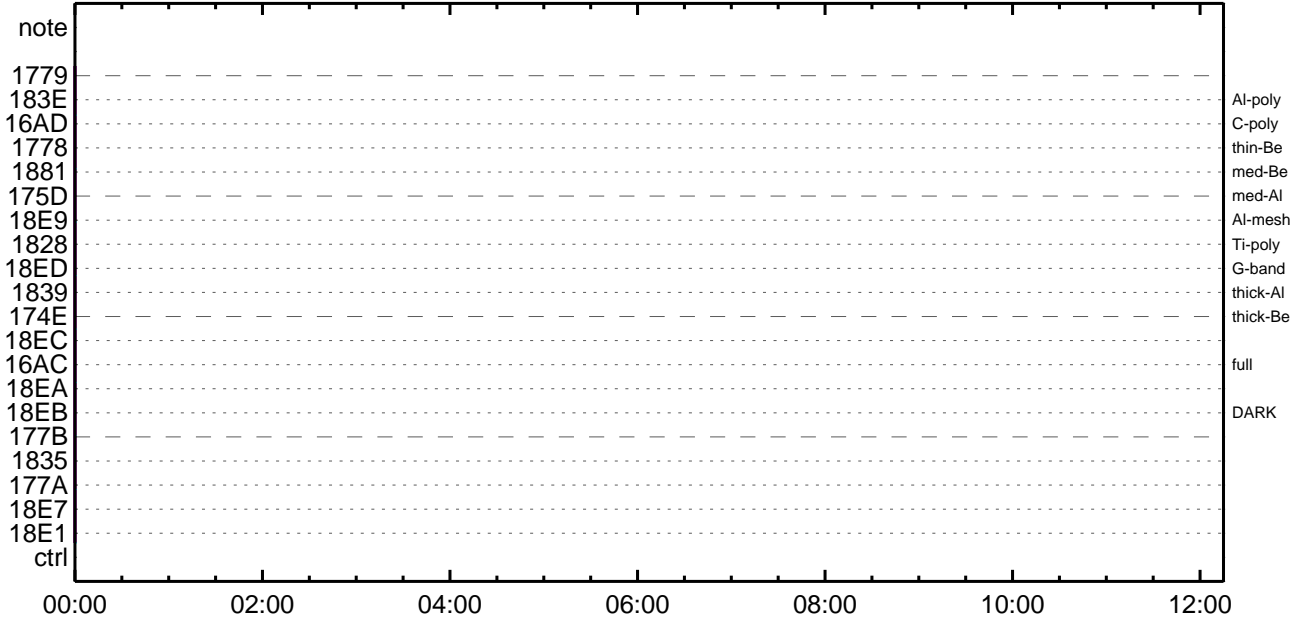
CMDI #0644 2012/06/13



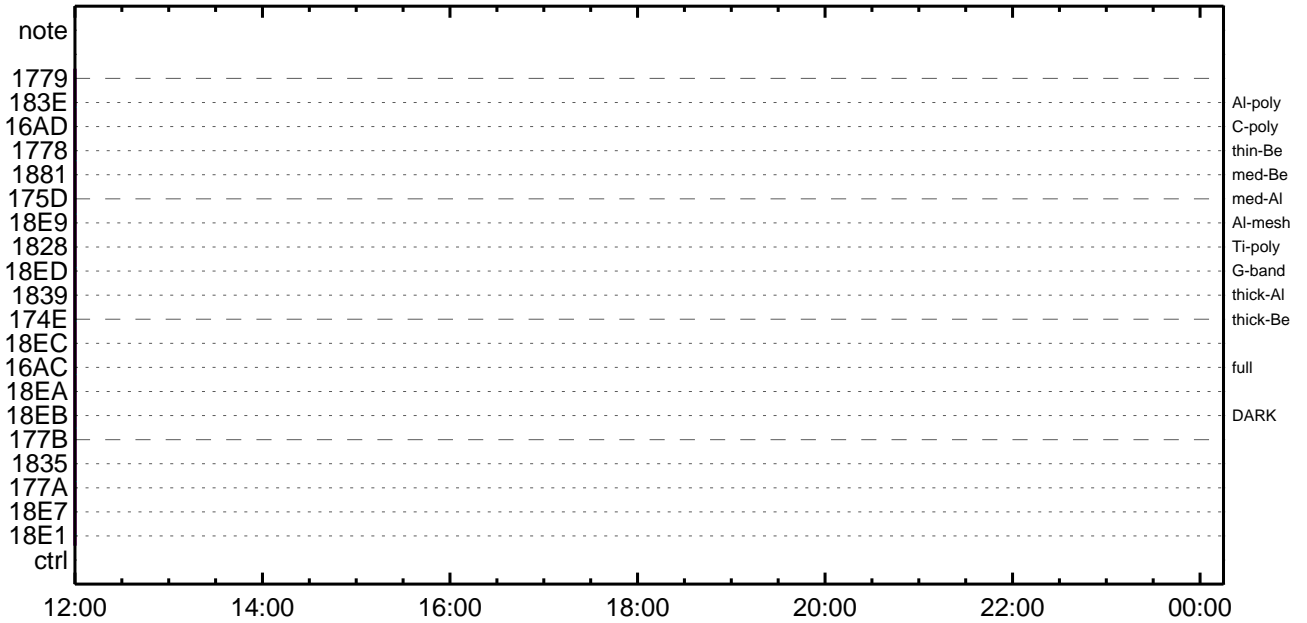
CMDI #0644 2012/06/13



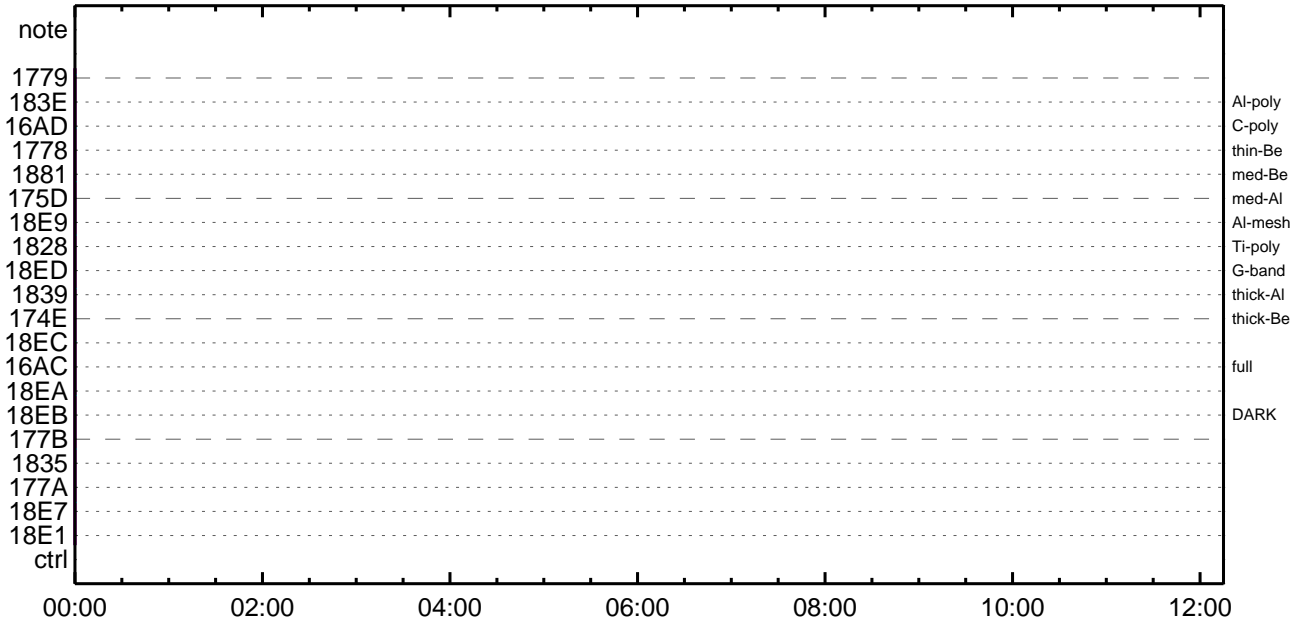
CMDI #0644 2012/06/14



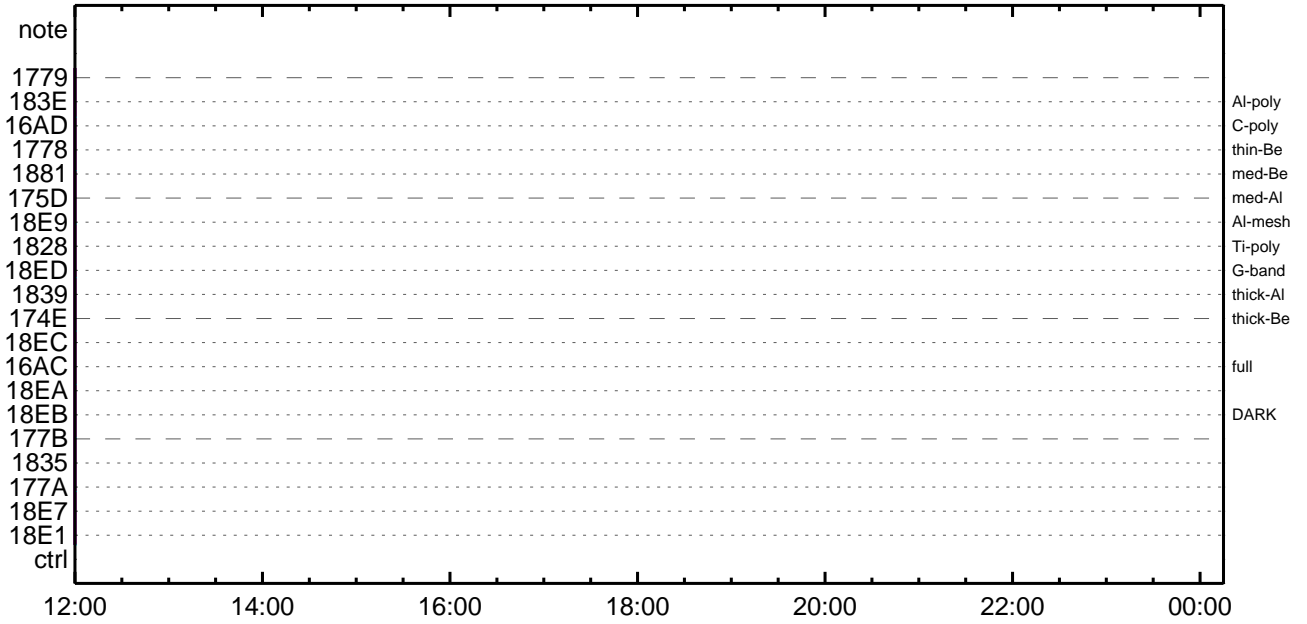
CMDI #0644 2012/06/14



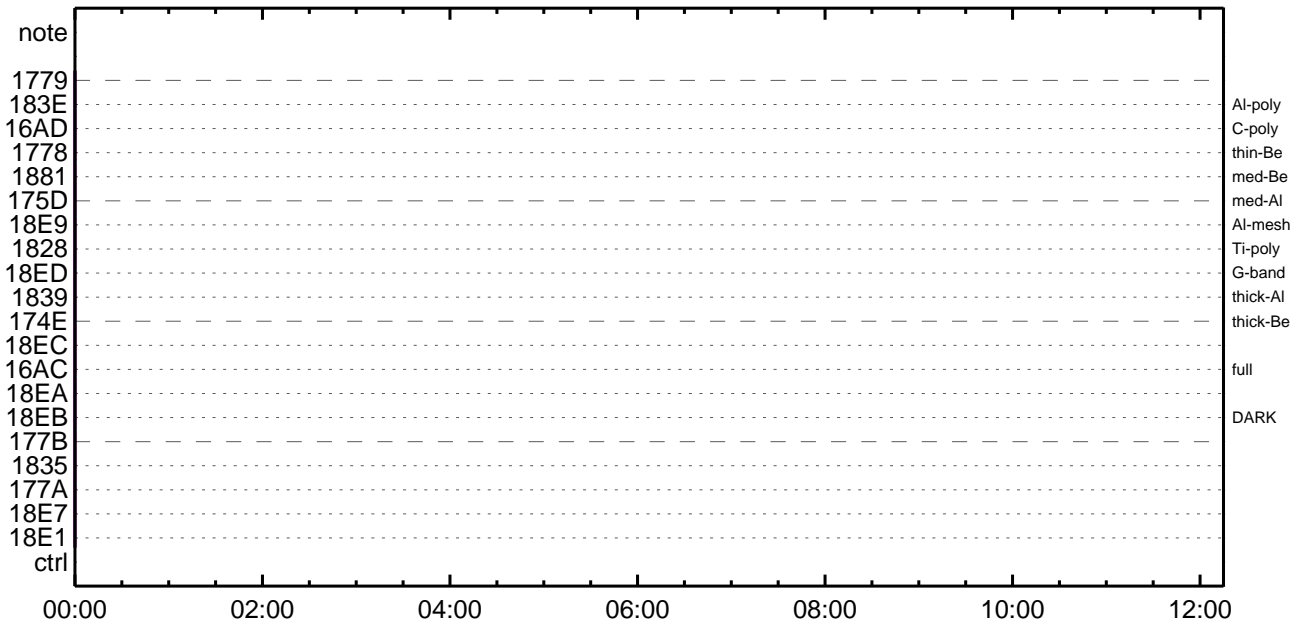
CMDI #0644 2012/06/15



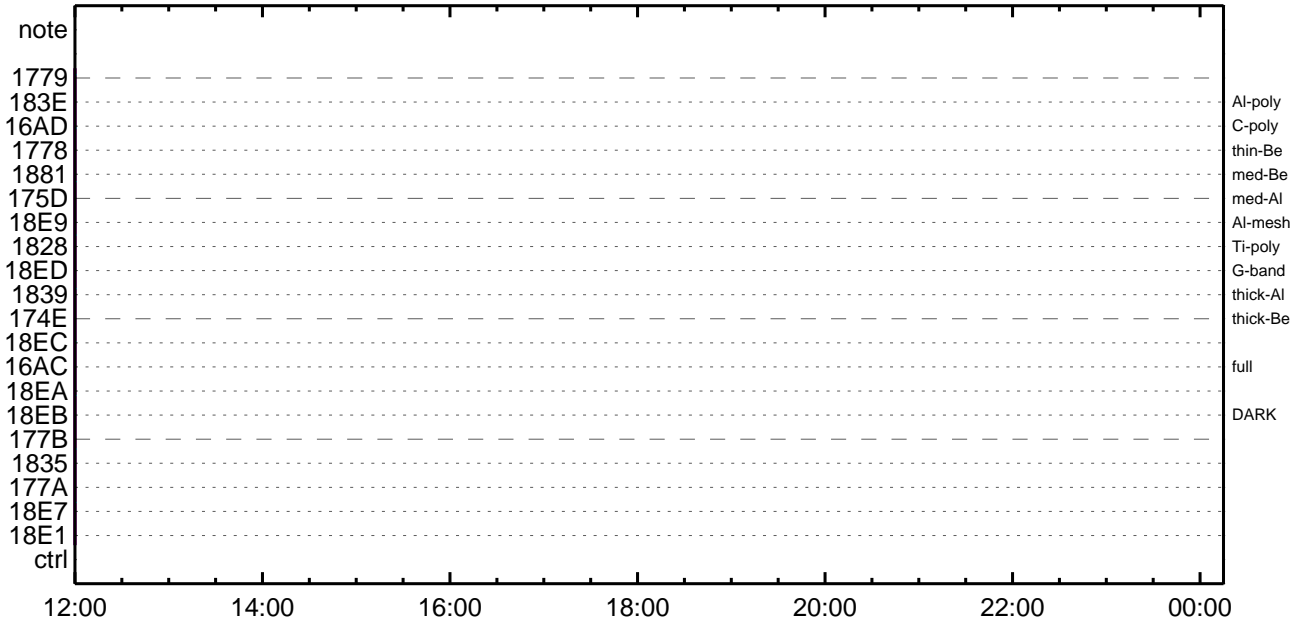
CMDI #0644 2012/06/15



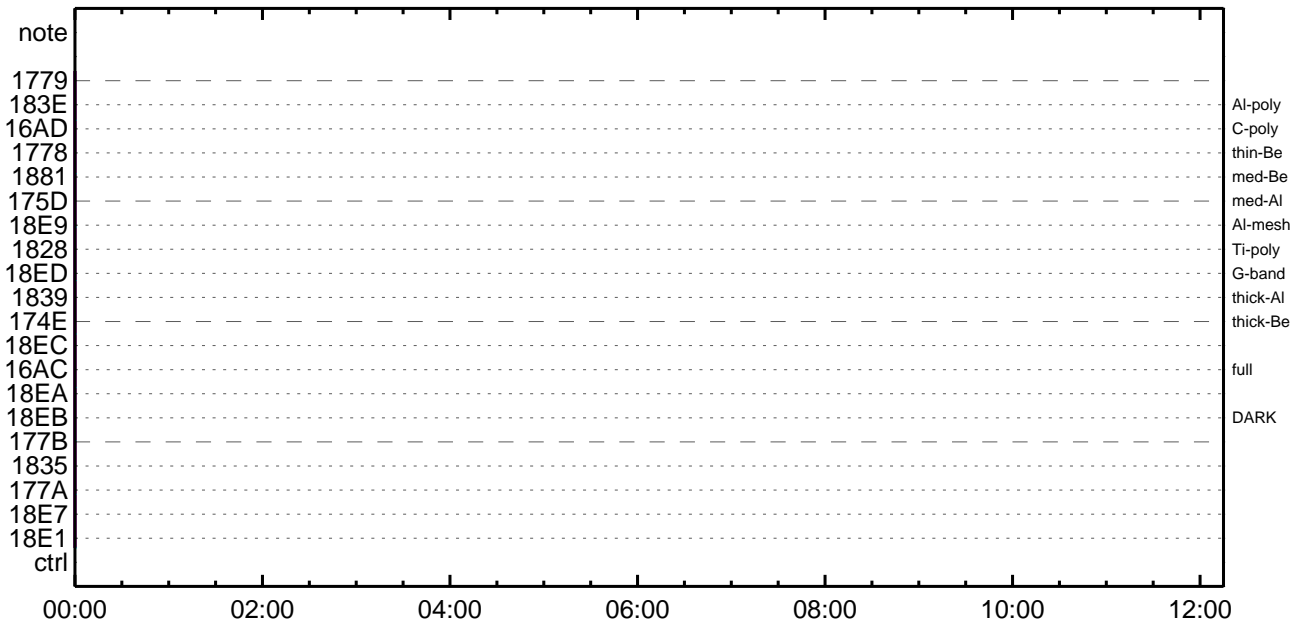
CMDI #0644 2012/06/16



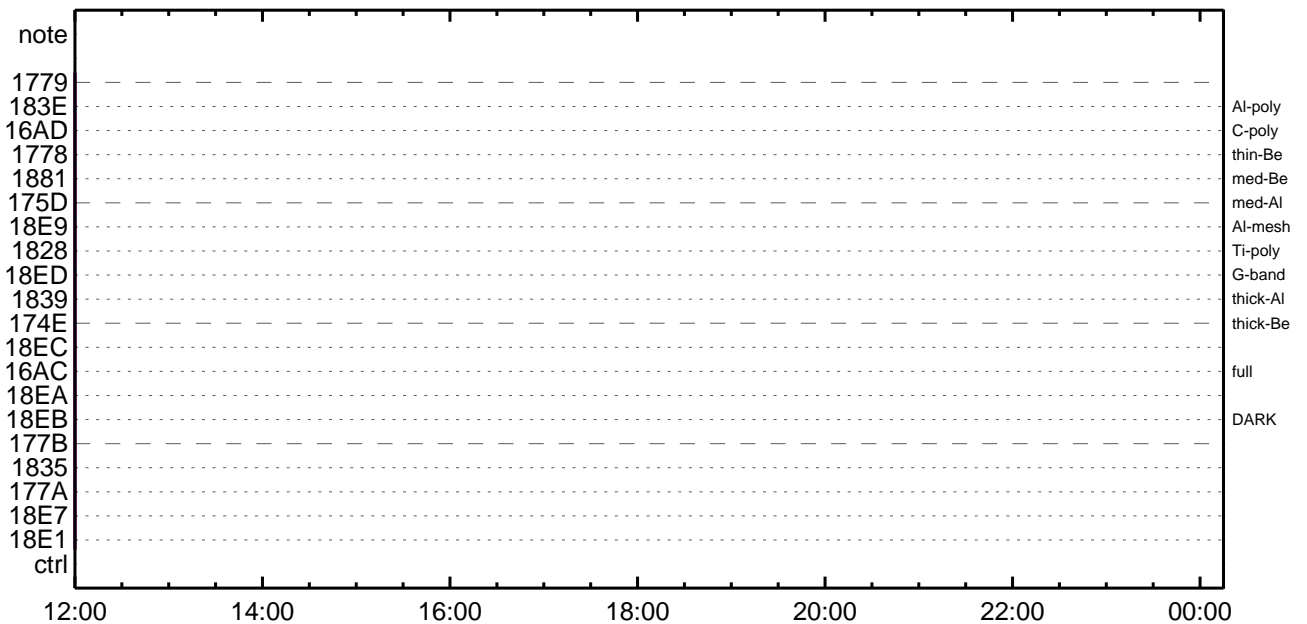
CMDI #0644 2012/06/16



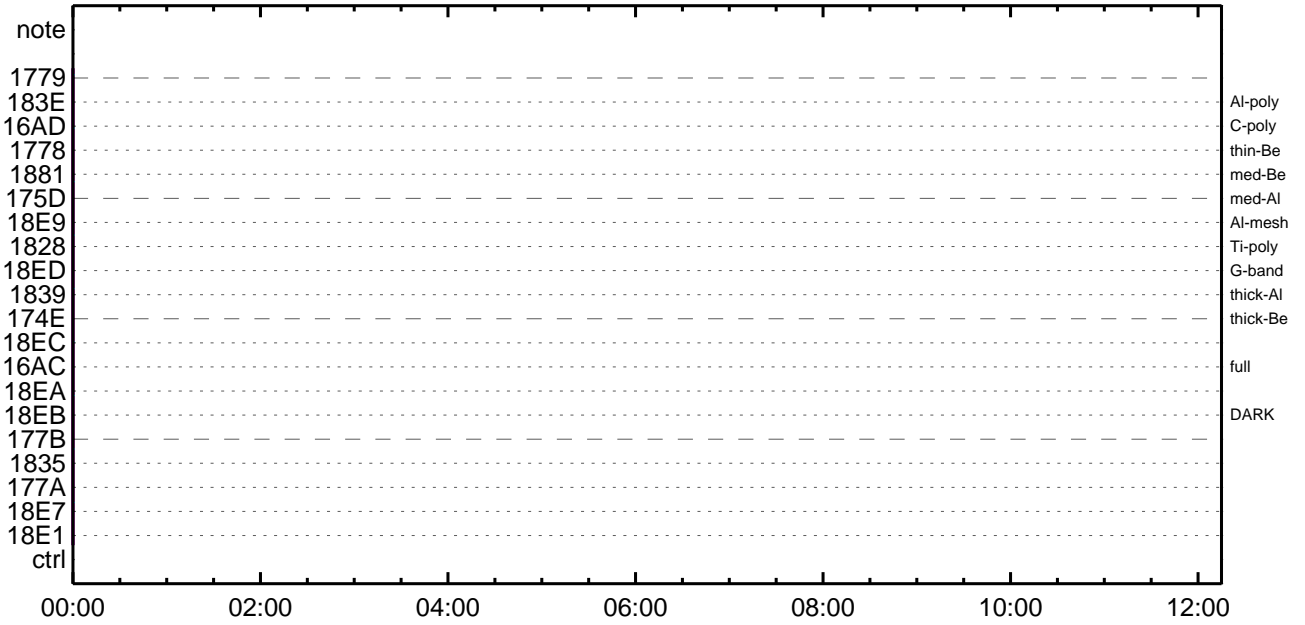
CMDI #0644 2012/06/17



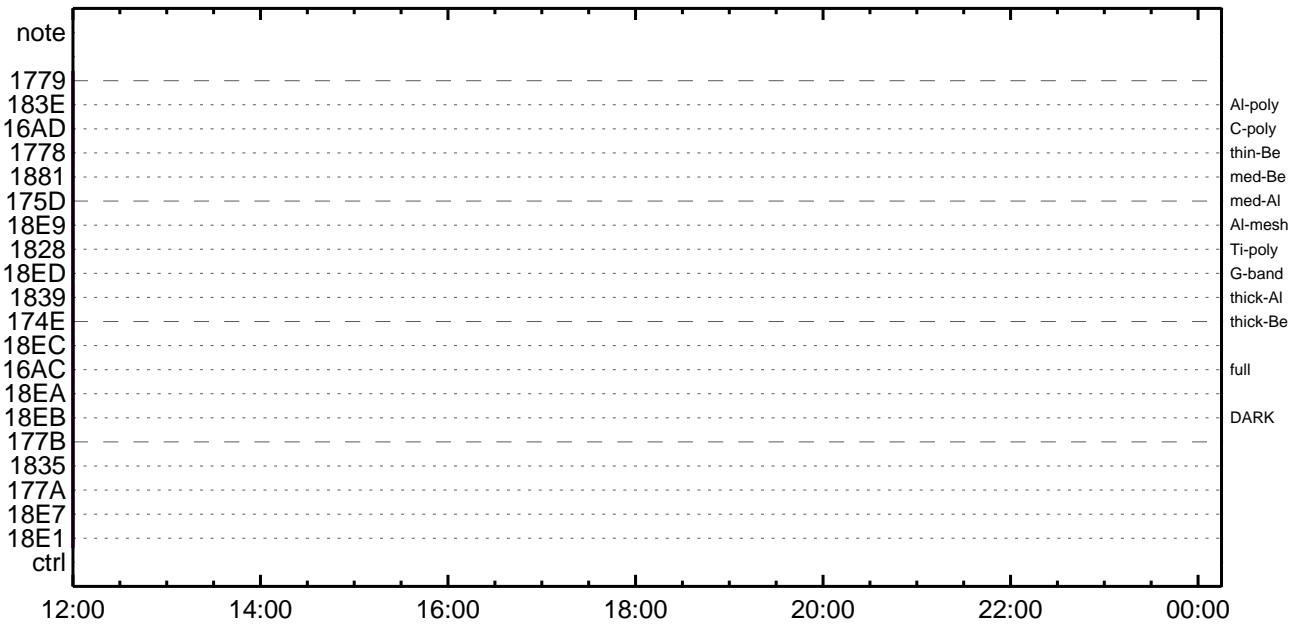
CMDI #0644 2012/06/17



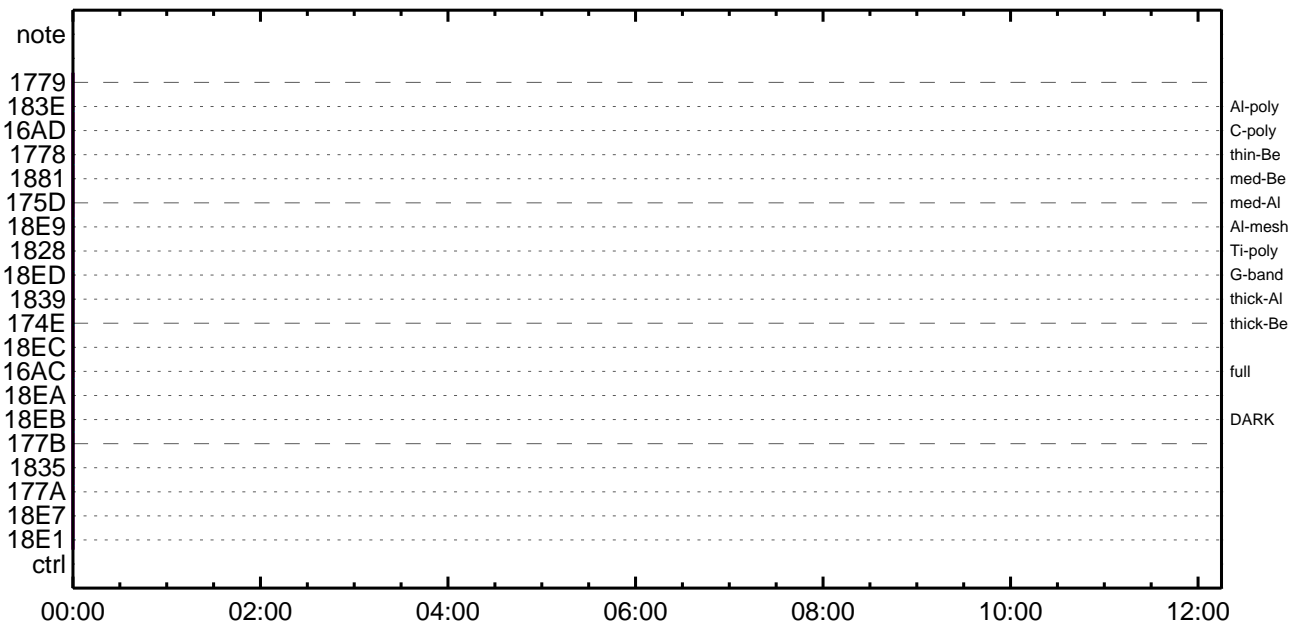
CMDI #0644 2012/06/18



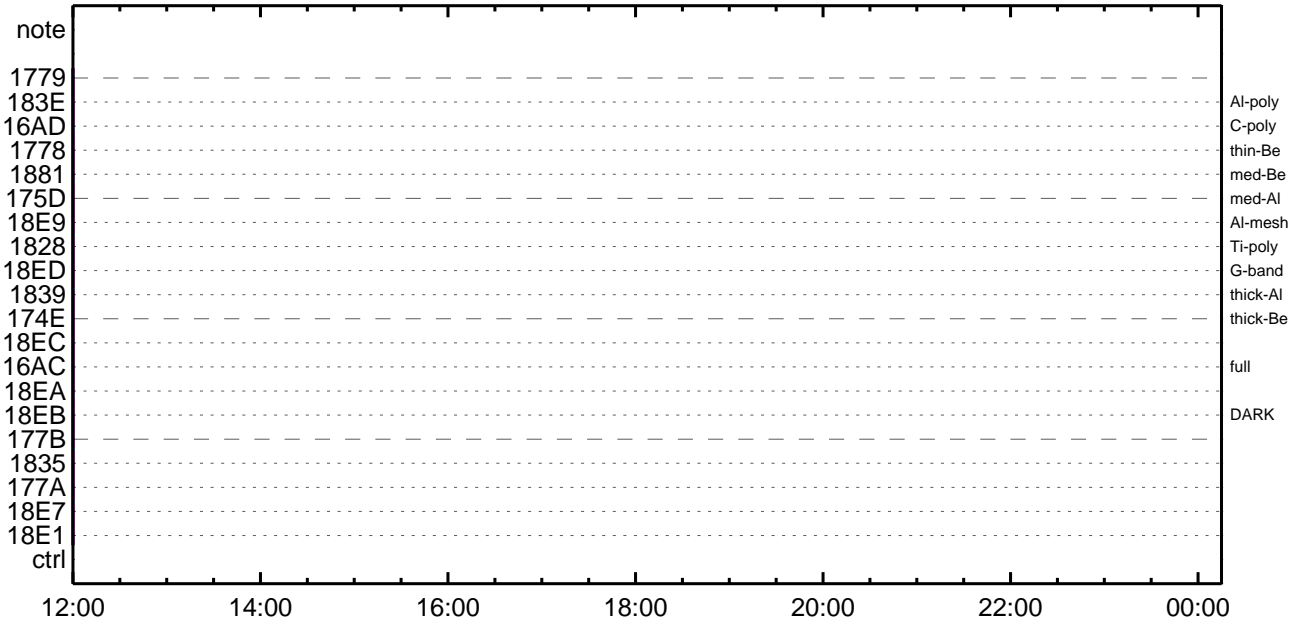
CMDI #0644 2012/06/18



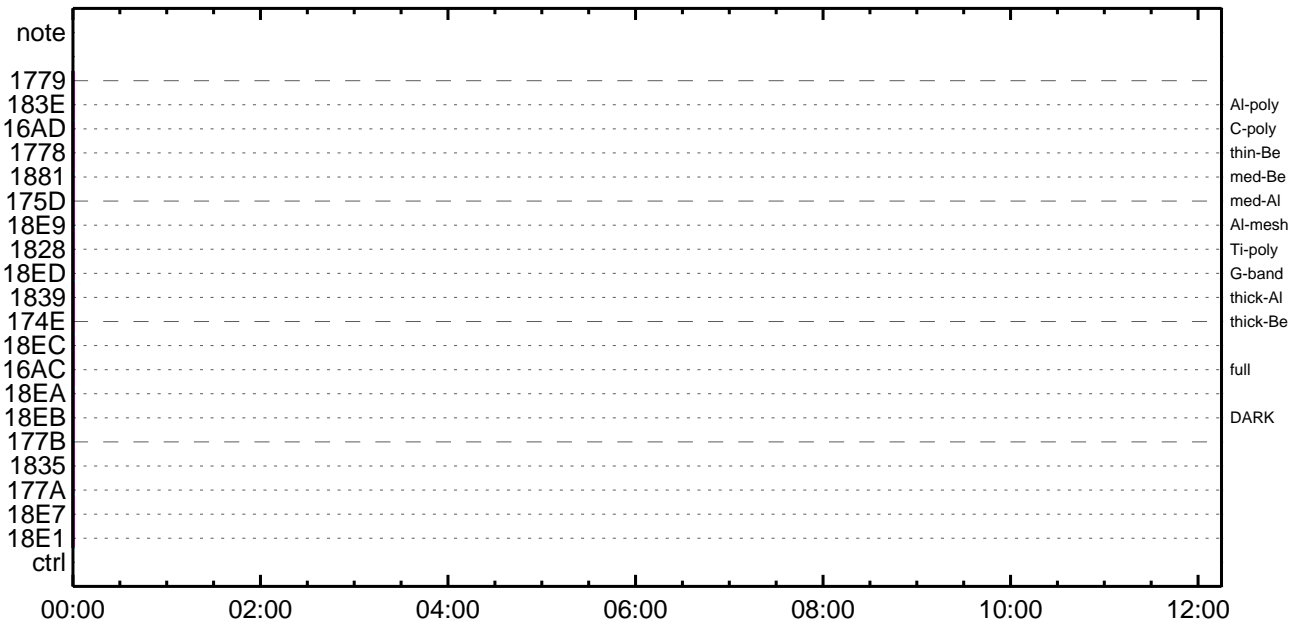
CMDI #0644 2012/06/19



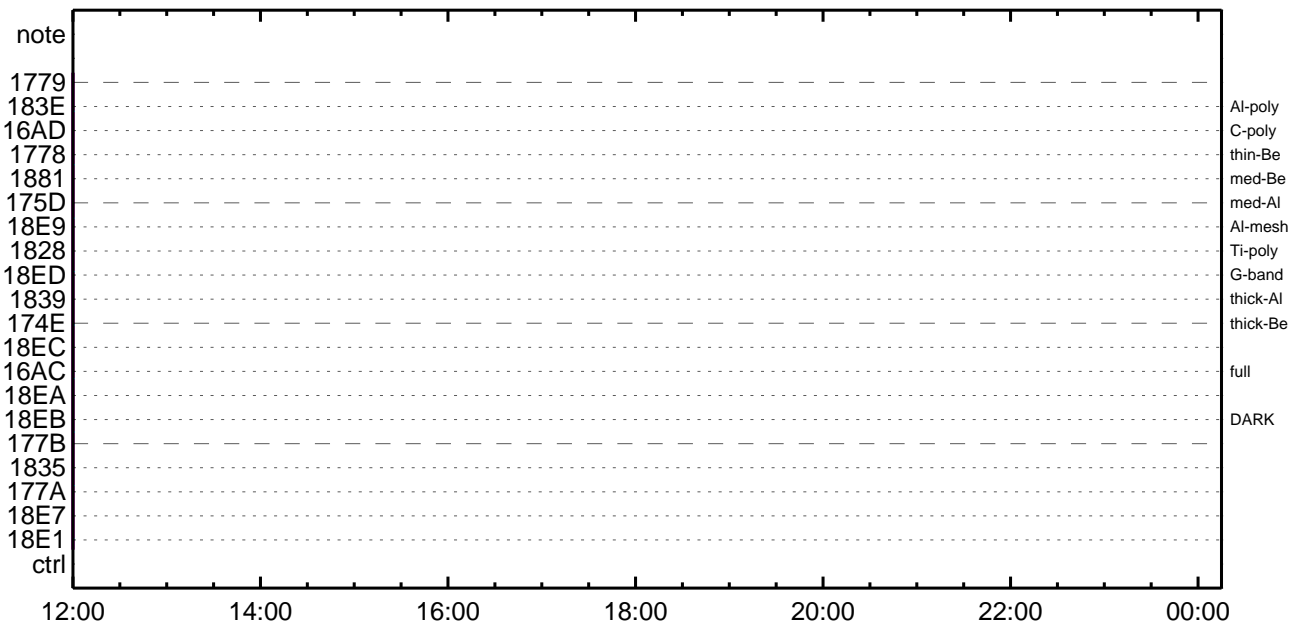
CMDI #0644 2012/06/19



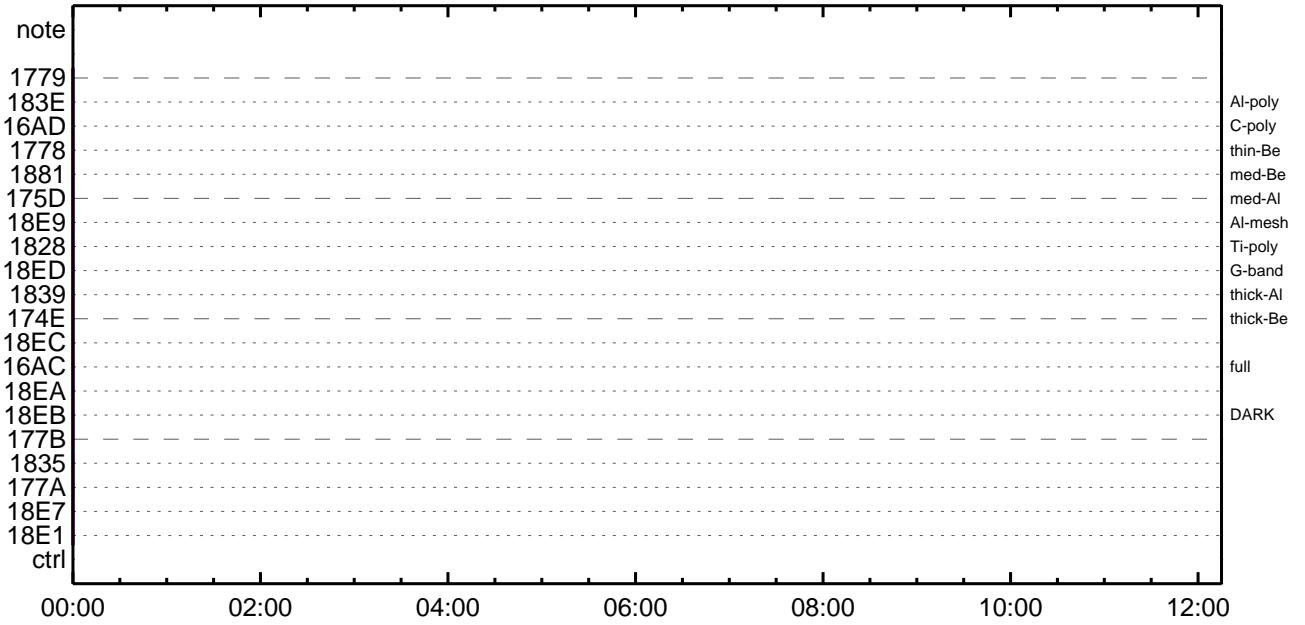
CMDI #0644 2012/06/20



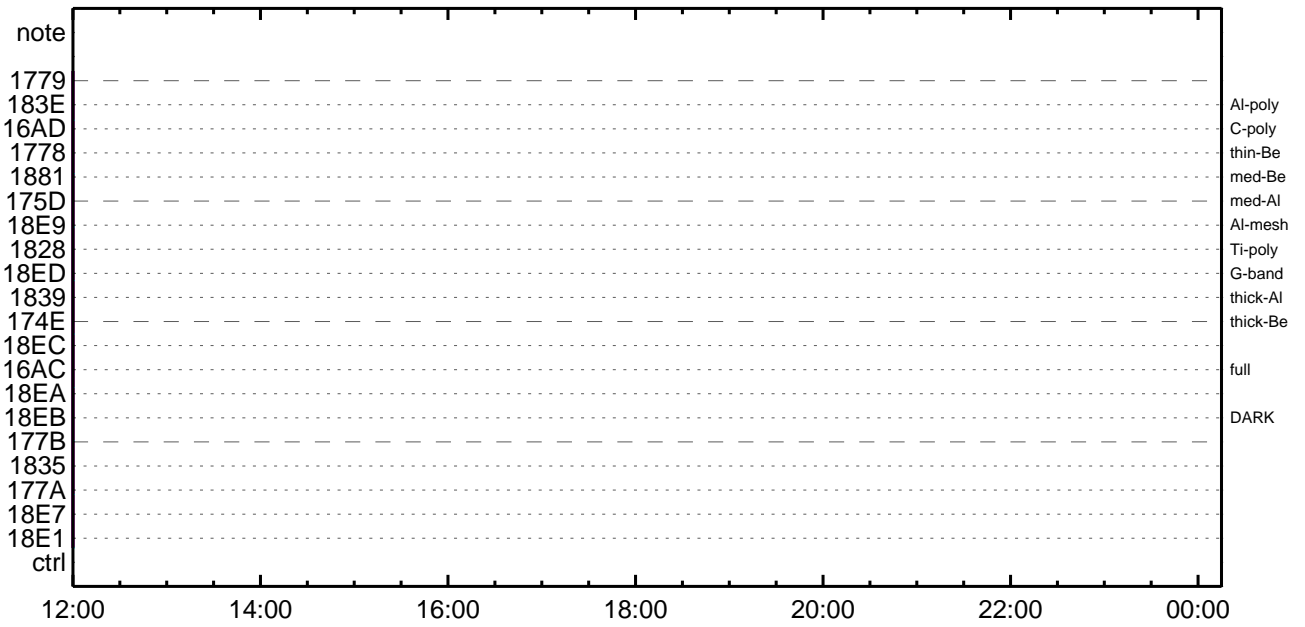
CMDI #0644 2012/06/20



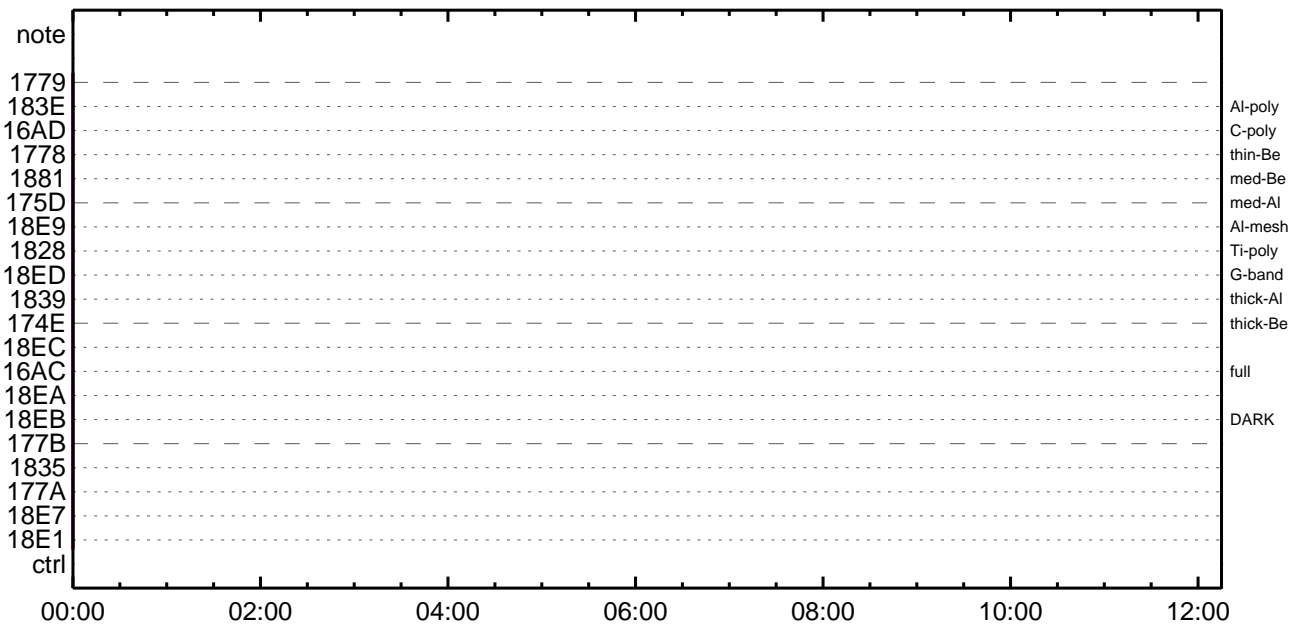
CMDI #0644 2012/06/21



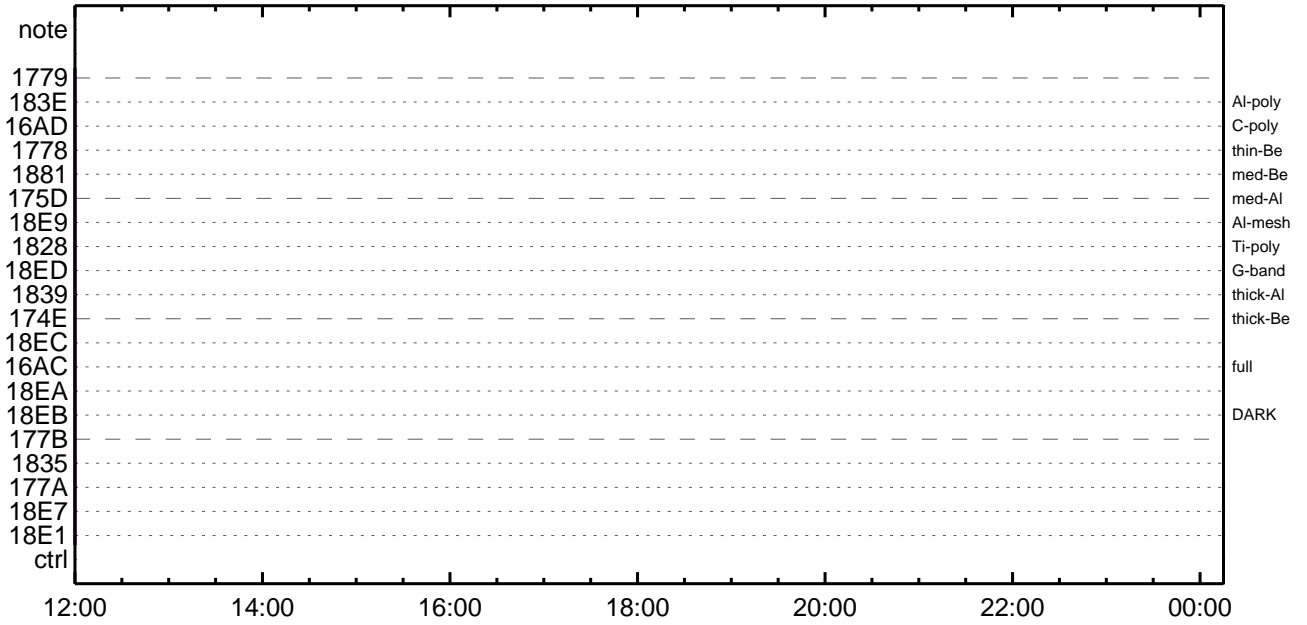
CMDI #0644 2012/06/21



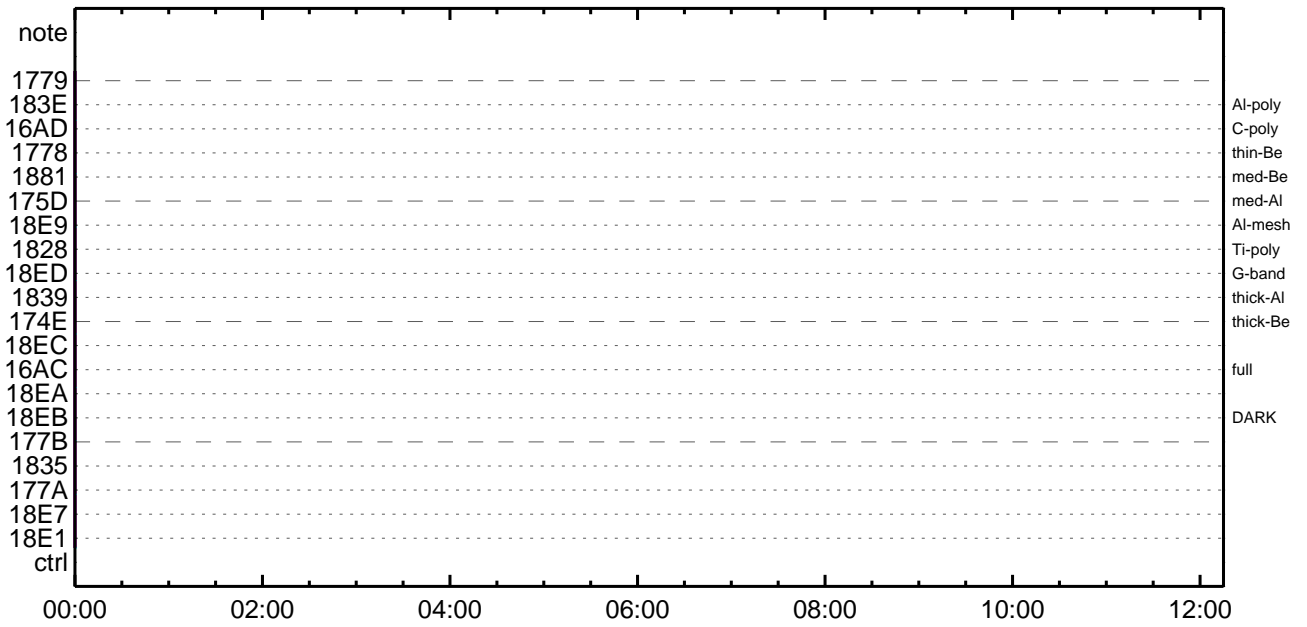
CMDI #0644 2012/06/22



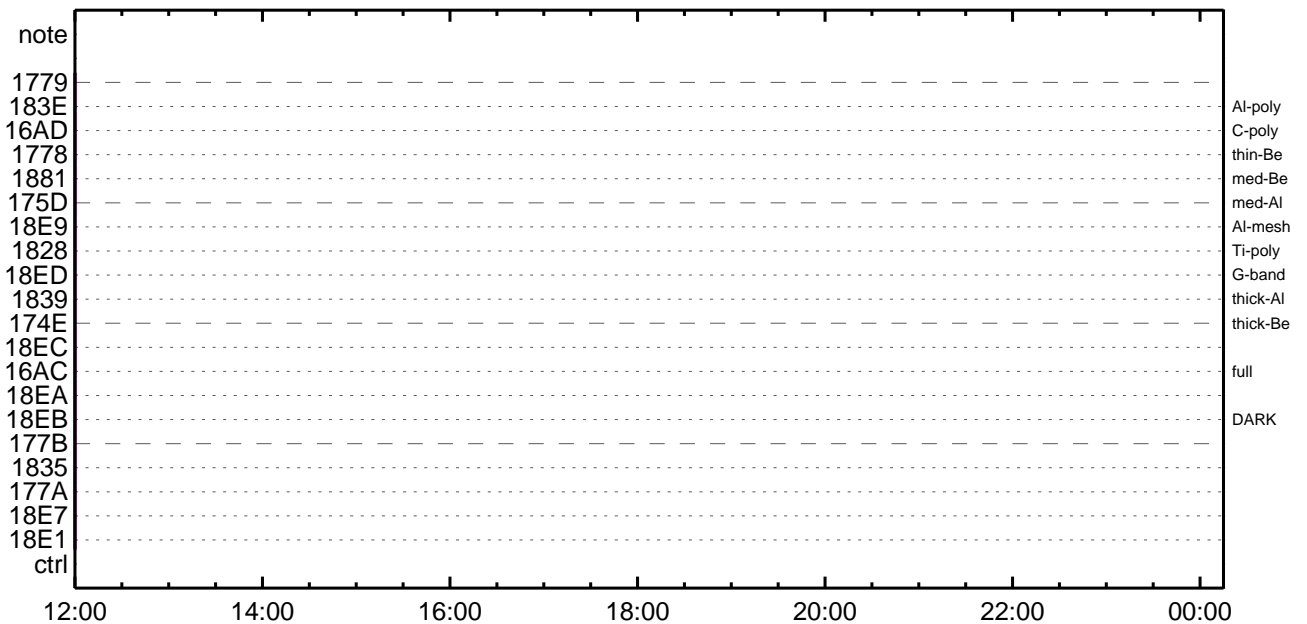
CMDI #0644 2012/06/22



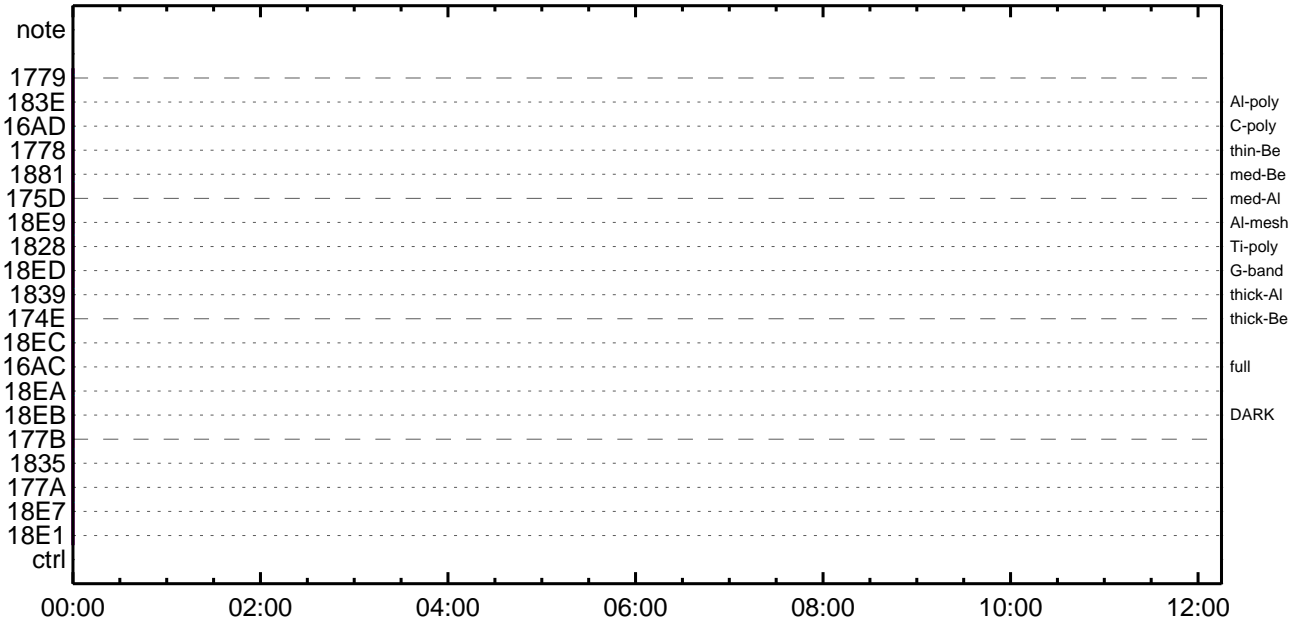
CMDI #0644 2012/06/23



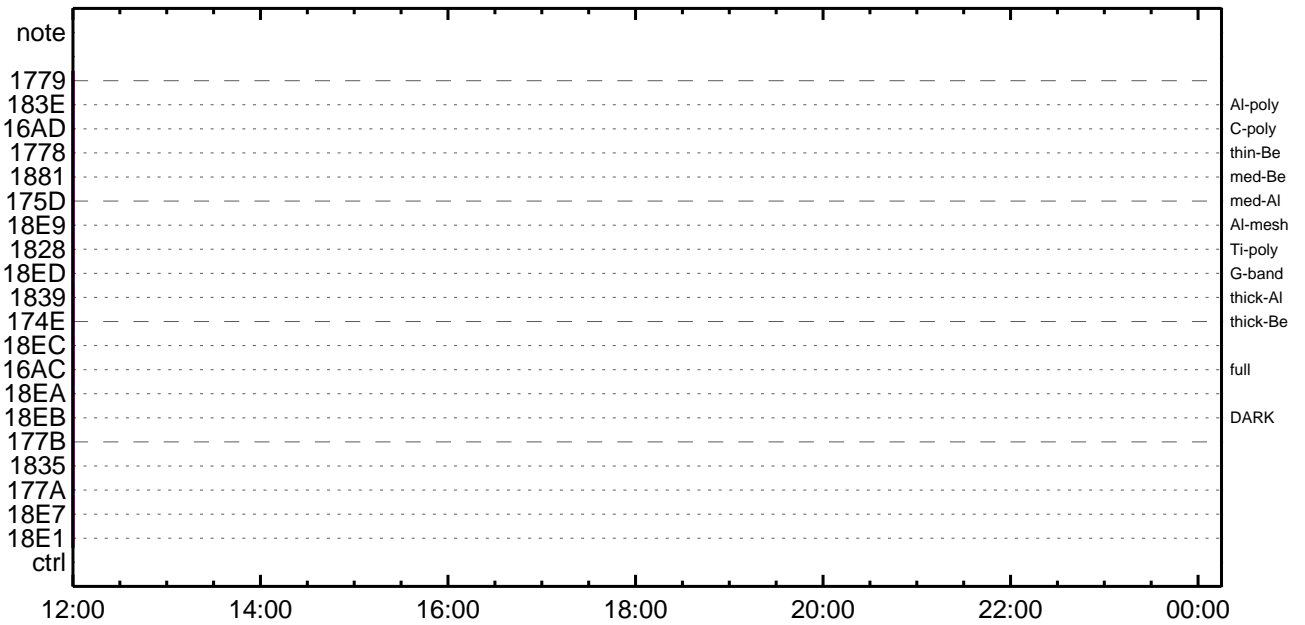
CMDI #0644 2012/06/23



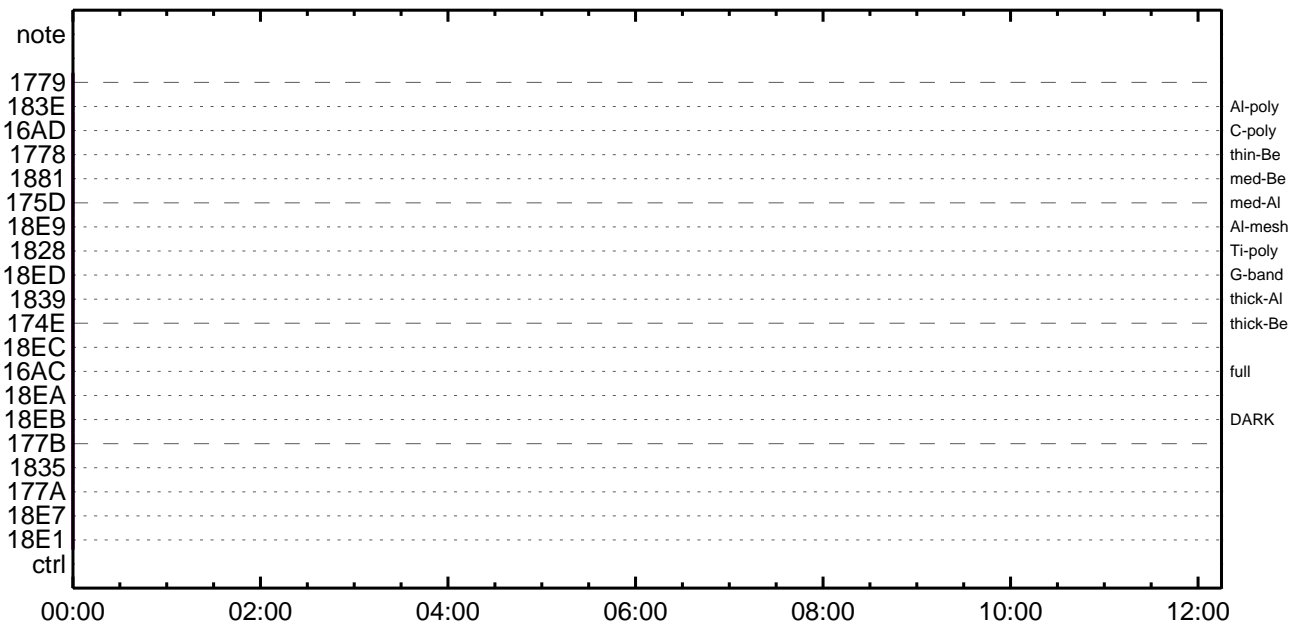
CMDI #0644 2012/06/24



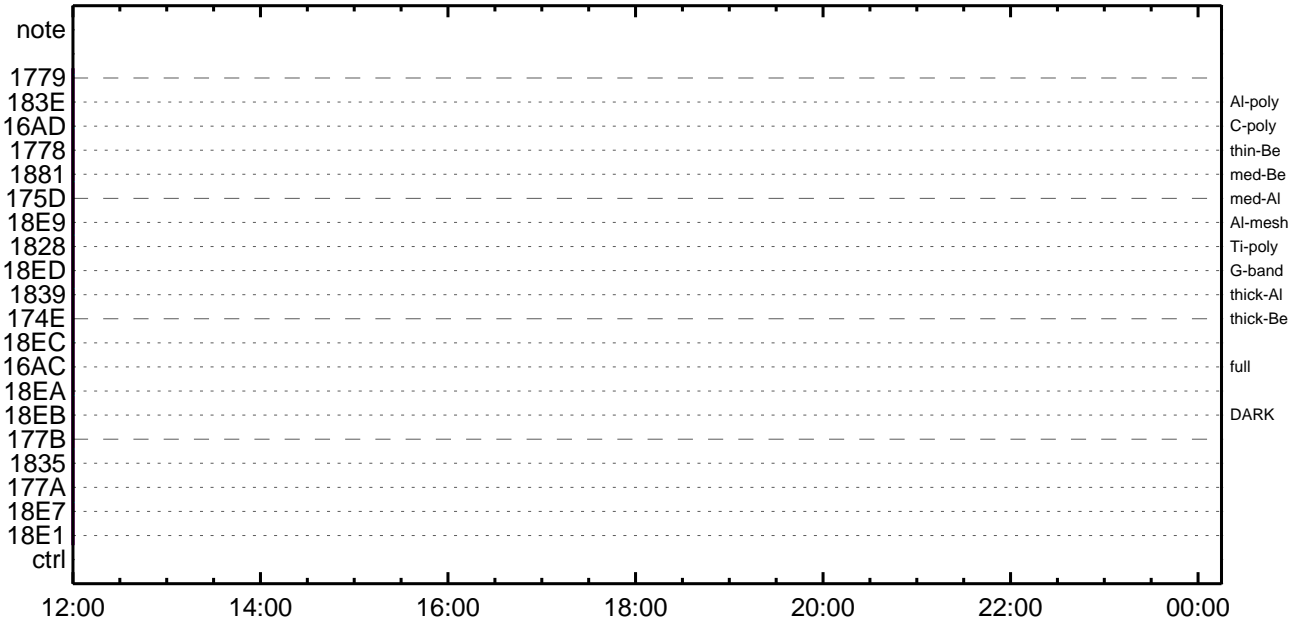
CMDI #0644 2012/06/24



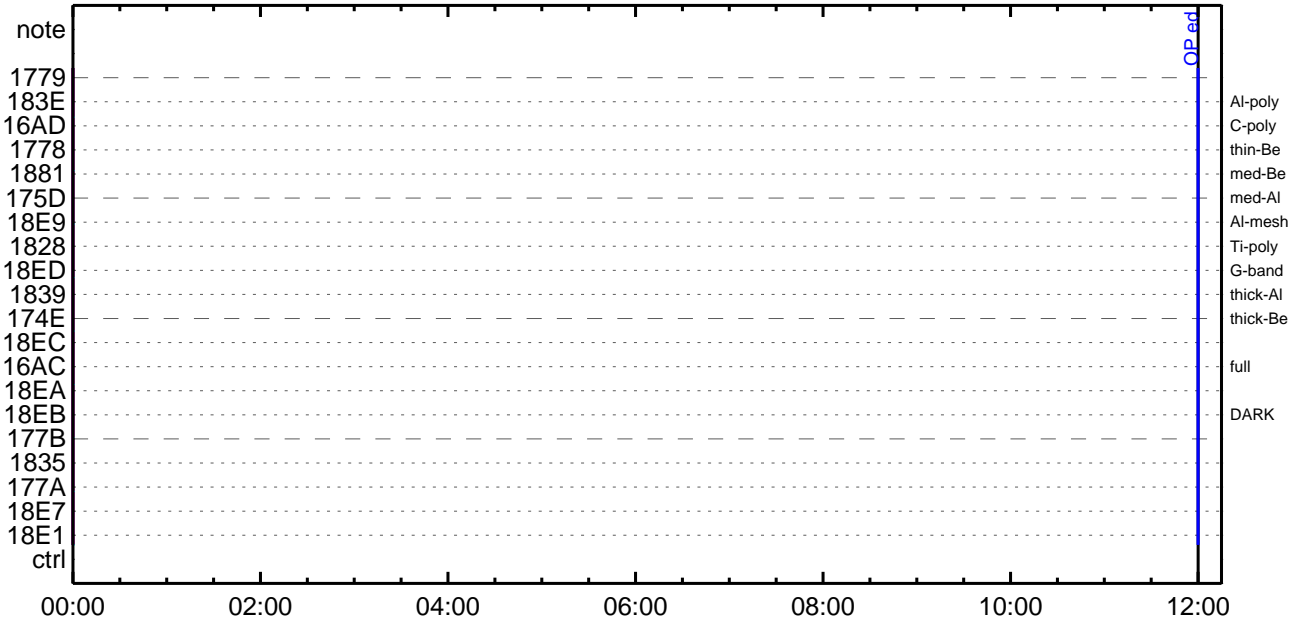
CMDI #0644 2012/06/25



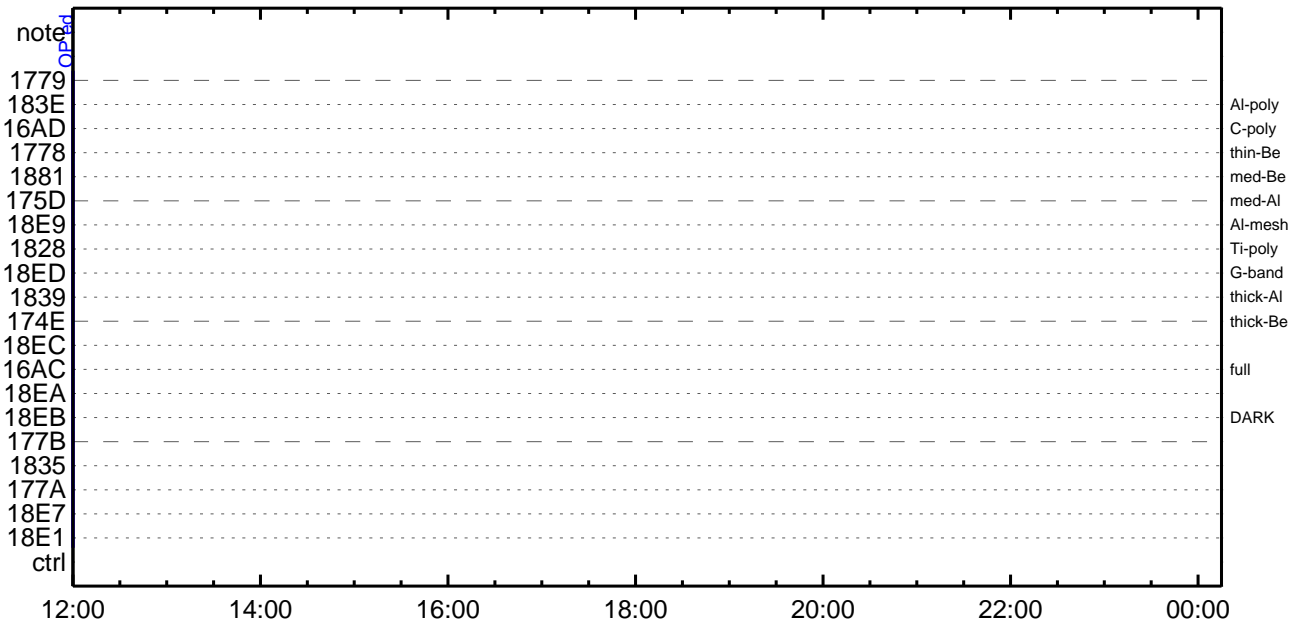
CMDI #0644 2012/06/25



CMDI #0644 2012/06/26



CMDI #0644 2012/06/26




```

0194 C.
0195 +. TI 2012-05-22 10:18:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °È²¼ãîÄè%îíñòîîŷÄŷ§ŷÄŷ-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ          0xB3
0204 C.
0205 C. *****
0206 C. TÍîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]    EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]    EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]    EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]       EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]      EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ          EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLòîî¼È¹ç•è²îOKò³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èòðîáò¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]       EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]      EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-05-22 10:18:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-05-22 10:18:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2012-05-22 10:18:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-05-22 10:18:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîãî»ò¼ŷ¼òÈÄò¹òèDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÈŷŷŷÈŷáŷçŷèòÈ¼òò¼Ä»Ûò¹òè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîñòÈ´Øò¹òèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-888 2012-05-22 15:18:04 91 33 SOLAR-B MAIN //  
0001 C.  
0002 . C. ***** AOS *****  
0003 C.  
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã  
0005 C.  
0006 C. YÀYB;¼Y³YFÝÖYÉÁ+¿®  
0007 +. DC 00-00 NULL_DUMMY_CMD  
0008 C.  
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****  
0010 C. ÁíËõ¿òÃð•µ°Æ»Í×ÁÇõÍYçYÁY×Yí;¼YÉ;ËÈè%µ•íÉ;ËõE¼°ÇÕð•ò¿¼í¹çðÍ;çÀ®, ùò¹òèòòòçÁ+¿®ð•òÈòòò³òÈ;f  
0011 +. DC 02-8E AOCU_ORB_UPD  
0012 C.  
0013 C.  
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****  
0015 C. Upload the Orbit Element and the Target Attitude  
0016 C. RAM-ID:TARGET_ATT  
0017 . S. RAM ram-150:TARGET_ATT  
0018 ( )  
0019 C.  
0020 C.  
0021 C. Set the dump memory area of TARGET_ATT  
0022 +. DC 02-48 AOCU_DUMP_SET  
0023 BC (07 00 00 00 18 00)  
0024 C.  
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]  
0026 C.  
0027 C.  
0028 C. Change the TLMFormatNo for the AOCS Dump Format  
0029 +. DC 01-22 DHU_MODE_CHNG  
0030 BC (04 0b f8)  
0031 C.  
0032 C. Wait for AOCSDUMP to end  
0033 C.  
0034 . C. Check the dump memory  
0035 C.  
0036 C. Result = OK [ ]  
0037 C.  
0038 +. DC 01-22 DHU_MODE_CHNG  
0039 BC (02 0a f8)  
0040 C.  
0041 C. <A_***>[TLM STS] FMT = 2 [ ]  
0042 C.  
0043 +. DC 02-8E AOCU_ORB_UPD  
0044 . C.  
0045 . C. ***** AOCS Commands (Orbital Element Update) *****  
0046 C. Update the orbital element  
0047 +. DC 02-50 AOCU_ORB_PRPGT_START  
0048 BC (16)  
0049 +. DC 02-8E AOCU_ORB_UPD  
0050 C.  
0051 C. <A_ORB>[ORBIT] EPC = 2181566.1 +- 1.0 (s) [ ]  
0052 C.  
0053 . C.  
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes  
0055 +. DC 07-FC EIS_MODE_MANU  
0056 BC (21 02)  
0057 . C. Verify EIS in MANUAL mode  
0058 . C. Estimated OBSTBL upload time is 52s  
0059 C. *****  
0060 C. EIS START OBSTBL LOAD  
0061 C. *****  
0062 . S. RAM ram-820:EIS_OBSTBL  
0063 ( )  
0064 +. DC 07-FC EIS_DUMP_OBSTBL  
0065 BC (07 07 07 00 00 70 00)  
0066 C.  
0067 C. Execute, after the success of OBSTBL upload.  
0068 C. Set EIS TI-commands  
0069 +. TI 2012-05-22 10:18:50.0  
0070 DC 07-FC EIS_MODE_CHG_ENA  
0071 BC (20)  
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP  
0073 C. *****  
0074 C. EIS END OBSTBL LOAD  
0075 C. *****  
0076 C.  
0077 . C. ***** MDP `úÃÎòÎ`ò¼YòÉÁðð¹òèDCBC•×²è *****  
0078 C. (¼á°íYÓYÁYÉYbYÉYáYçYèòE¾¼ò¼Á»Ûò¹òè)  
0079 . S. DC-BC dcbc-402:DCBC  
0080 (MDP_known_event)  
0081 C.  
0082 C.  
0083 . C. ***** YDY¹•Ï Daily±¿ÎÑòE`Øò¹òèDCBC•×²è *****  
0084 . S. DC-BC dcbc-153:DCBC  
0085 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0086 C.  
0087 C.  
0088 . C. ;ãLOSÁY$YÁY~¼Á»Û;ã  
0089 C.  
0090 . C. ***** LOS *****  
0091 C.
```



```

0194 C.
0195 . C. < Dump RAMID=MDP_DPL >
0196 +. DC 07-F0 MDP_DUMP_FGTBL
0197 BC (82 07 00 38 b8 00 40)
0198 C. -----
0199 C. MDP_DPL verify = OK [ ]
0200 C. -----
0201 C.
0202 C. STS_CHK=ON=
0203 C.
0204 . C. < Update MDP DSC PAR1 >
0205 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0206 BC (4c)
0207 C. MDP_CMD_CODE = F04C0700[ ]
0208 C. MDP_CMD_CNT (count-up 1) [ ]
0209 C. -----
0210 C.
0211 . C.
0212 C. *****
0213 C. SOT TI command set
0214 C. *****
0215 C. Execute, after the success of TBL upload.
0216 +. TI 2012-05-22 10:18:18.0
0217 DC 07-F0 MDP_SOT_MODE_OBSV
0218 BC (40)
0219 . C. -----
0220 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0221 C. -----
0222 C.
0223 C.
0224 . C. ***** MDP `uÃîï»ö%ÝœËÄð¹œDCBC•x²è *****
0225 C. (%Á°i¥Ó¥Ã¥È¥¥¥Ë¥á¥ç¥èœË%¼œ¼Á»Û¹œ)
0226 . S. DC-BC dcbc-402:DCBC
0227 (MDP_known_event)
0228 C.
0229 C.
0230 . C. ***** ¥D¥!•İ Daily±¿İÑœË`Ø¹œèDCBC•x²è *****
0231 . S. DC-BC dcbc-153:DCBC
0232 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0233 C.
0234 C.
0235 . C. ¡ãLOS¥Á¥$¥Ã¥¬¼Á»Û¡ä
0236 C.
0237 . C. ***** LOS *****
0238 C.

```

May 22, 12 15:18

XRT_OGLIST_0644.chk

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*** OP Sequence for XRT ***

2012/05/22	10:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	54	00	00	b4
2012/05/22	10:55:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	4e	35	00	b4
2012/05/22	11:39:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/22	11:39:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/05/22	11:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/05/22	11:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/05/22	12:07:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/22	12:07:56.0	XRT_FOCUS_RECALIBRATE_428_OG [0x1ac]							
		XRT_FOCUS_RECAL	2	07-F8	78				00
2012/05/22	12:08:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	45	58	00	b4
2012/05/22	12:11:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2012/05/22	12:12:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2012/05/22	12:12:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2012/05/22	12:12:20.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2012/05/22	12:12:22.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 07
2012/05/22	12:12:24.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/05/22	12:33:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	3c	72	00	b4
2012/05/22	13:17:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/22	13:17:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/05/22	13:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/05/22	13:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/05/22	13:47:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	33	8d	00	b4
2012/05/22	14:11:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/22	14:11:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2012/05/22	14:12:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	2a	a7	00	b4
2012/05/22	14:12:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2012/05/22	14:12:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2012/05/22	14:12:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2012/05/22	14:14:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 07
2012/05/22	14:15:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/05/22	14:56:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/22	14:56:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/05/22	14:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/05/22	14:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/05/22	15:36:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/22	15:36:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/05/22	15:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/05/22	15:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/05/22	15:43:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	21	ca	00	b4
2012/05/22	16:34:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/22	16:34:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/05/22	16:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/05/22	16:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/05/22	17:20:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	18	e5	00	b4
2012/05/22	17:59:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/22	17:59:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2012/05/22	17:59:30.0	AOCS_ORe-point_Start_9_OG [0x09f]							

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2012/05/22	17:59:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2012/05/22	17:59:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2012/05/22	17:59:50.0	XRT_ARS_DIS_438_OG [0x1b6]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2012/05/22	18:02:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]		2	07-F0	c4	07			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	07			
2012/05/22	18:02:30.0	XRT_CTRL_AUTO_406_OG [0x196]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/05/22	18:13:00.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/05/22	18:13:02.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/05/22	18:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/05/22	18:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/05/22	18:57:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]		5	02-76	00	10	00	00	b4
		AOCU_NM		5	02-76	00	07	1a	00	b4
2012/05/22	19:22:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]		5	02-76	00	07	1a	00	b4
		AOCU_NM		5	02-76	00	07	1a	00	b4
2012/05/22	19:51:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/05/22	19:51:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/05/22	19:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/05/22	19:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/05/22	20:34:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]		5	02-76	00	ff	1b	00	b4
		AOCU_NM		5	02-76	00	ff	1b	00	b4
2012/05/22	20:59:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]		5	02-76	00	f6	36	00	b4
		AOCU_NM		5	02-76	00	f6	36	00	b4
2012/05/22	21:30:00.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/05/22	21:30:02.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/05/22	21:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/05/22	21:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/05/22	22:10:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]		5	02-76	00	ed	59	00	b4
		AOCU_NM		5	02-76	00	ed	59	00	b4
2012/05/22	22:34:54.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/05/22	22:34:56.0	XRT_FOCUS_POSITION_401_OG [0x191]		4	07-F8	22	ff	aa	00	
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2012/05/22	22:35:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]		5	02-76	00	e4	73	00	b4
		AOCU_NM		5	02-76	00	e4	73	00	b4
2012/05/22	22:35:16.0	XRT_FLD_DIS_402_OG [0x192]		1	07-F0	d9				
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2012/05/22	22:35:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2012/05/22	22:35:20.0	XRT_ARS_DIS_438_OG [0x1b6]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2012/05/22	22:37:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]		2	07-F0	c4	07			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	07			
2012/05/22	22:38:00.0	XRT_CTRL_AUTO_406_OG [0x196]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2012/05/22	23:08:30.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/05/22	23:08:32.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/05/22	23:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/05/22	23:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/05/22	23:38:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]		5	02-76	00	db	8e	00	b4
		AOCU_NM		5	02-76	00	db	8e	00	b4
2012/05/23	00:03:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]		5	02-76	00	d2	a8	00	b4
		AOCU_NM		5	02-76	00	d2	a8	00	b4
2012/05/23	00:47:00.0	XRT_CTRL_MANU_408_OG [0x198]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/05/23	00:47:02.0	XRT_FLD_RESET_412_OG [0x19c]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2012/05/23	00:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2012/05/23	00:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2012/05/23	01:15:54.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2012/05/23	01:15:56.0	XRT_FOCUS_POSITION_401_OG [0x191]		4	07-F8	22	ff	aa	00	
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2012/05/23	01:16:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]		5	02-76	00	c9	cb	00	b4
		AOCU_NM		5	02-76	00	c9	cb	00	b4
2012/05/23	01:16:16.0	XRT_FLD_DIS_402_OG [0x192]		1	07-F0	d9				
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2012/05/23	01:16:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2012/05/23	01:16:20.0	XRT_ARS_DIS_438_OG [0x1b6]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				

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2012/05/23	01:18:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2012/05/23	01:19:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	01:41:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	c0 e6 00 b4			
2012/05/23	02:24:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	02:24:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	02:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/23	02:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/23	02:55:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	b8 00 00 b4			
2012/05/23	03:20:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00	af 1b 00 b4			
2012/05/23	03:44:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	03:45:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	02	00 00 00 00			
2012/05/23	03:47:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2012/05/23	03:47:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/23	03:47:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/23	03:47:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/23	03:47:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/23	03:47:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	03:47:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2012/05/23	03:47:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2012/05/23	03:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	03:52:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	03:52:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	03:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/23	03:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/23	04:32:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/23	04:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	05:32:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	05:32:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	05:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/23	05:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/23	06:11:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/23	06:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	06:15:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	06:15:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2012/05/23	06:15:30.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	00 00 00 00			
2012/05/23	06:15:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/05/23	06:15:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/05/23	06:15:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/23	06:18:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2012/05/23	06:18:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	06:25:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	06:25:30.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00	56 7e 00 b4			
2012/05/23	06:27:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2012/05/23	06:28:16.0	XRT_FLD_ENA_405_OG [0x195]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/23	06:28:18.0	XRT_FLRCTRL_ENA_440_OG [0x1b8]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/23	06:28:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/23	06:28:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/23	06:28:24.0	XRT_FLD_RESET_412_OG [0x19c]							

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2012/05/23	06:28:26.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	06:28:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2012/05/23	06:28:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2012/05/23	07:13:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	07:13:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	07:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	07:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/23	07:49:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/23	07:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]							
2012/05/23	08:53:00.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_CTRL_AUTO_419_OG [0x1a3]							
2012/05/23	08:53:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	08:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	08:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	09:28:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/23	09:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/23	10:35:00.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_Custom_418_OG [0x1a2]							
2012/05/23	10:35:02.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_CTRL_AUTO_419_OG [0x1a3]							
2012/05/23	10:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	10:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	10:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	11:00:00.0	AOCS_OrE-point_Start_22_OG [0x0ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/23	11:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/23	11:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02	00	00	00	00
2012/05/23	11:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/05/23	11:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	XRT_FLD_ENA_411_OG [0x19b]							
2012/05/23	11:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_FLRCTRL_ENA	1	07-F0	d8				
2012/05/23	11:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/23	11:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/23	11:02:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	11:06:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2012/05/23	11:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2012/05/23	12:16:30.0	XRT_CTRL_MANU_408_OG [0x198]	XRT_Custom_418_OG [0x1a2]							
2012/05/23	12:16:32.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_CTRL_AUTO_419_OG [0x1a3]							
2012/05/23	12:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	12:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	12:45:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	12:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/23	13:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/23	13:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	XRT_Custom_418_OG [0x1a2]							
2012/05/23	13:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	XRT_CTRL_AUTO_419_OG [0x1a3]							
2012/05/23	13:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	14:30:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	14:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	15:33:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/23	15:33:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/23	15:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	XRT_Custom_418_OG [0x1a2]							
2012/05/23	15:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	XRT_CTRL_AUTO_419_OG [0x1a3]							
2012/05/23	16:19:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	16:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2012/05/23	17:12:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	17:12:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	17:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/23	17:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/23	17:57:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	17:57:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/23	17:58:00.0	AOCS_Ore-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/05/23	17:58:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/05/23	17:58:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/05/23	17:58:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/23	18:00:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2012/05/23	18:01:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	18:07:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	18:08:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/05/23	18:10:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/05/23	18:10:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/23	18:10:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/23	18:10:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/23	18:10:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/23	18:10:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	18:10:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2012/05/23	18:10:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2012/05/23	18:11:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	18:50:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	18:50:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	18:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/23	18:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/23	19:32:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/23	19:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	20:29:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	20:29:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	20:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/23	20:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/23	21:10:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/23	21:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	22:07:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	22:07:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	22:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/23	22:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/23	22:45:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/23	22:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/23	23:46:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/23	23:46:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/23	23:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/23	23:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/24	00:14:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/24	00:15:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/24	01:24:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/24	01:24:32.0	XRT_FLD_RESET_412_OG [0x19c]							

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2012/05/24	01:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/24	01:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/24	01:53:00.0	XRT_Custom_418_OG [0x1a2]				
2012/05/24	01:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/24	03:00:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/24	03:00:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/24	03:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/24	03:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/24	03:31:30.0	XRT_Custom_418_OG [0x1a2]				
2012/05/24	03:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/24	04:30:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/24	04:30:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/24	04:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/24	04:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/24	05:10:00.0	XRT_Custom_418_OG [0x1a2]				
2012/05/24	05:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/24	05:42:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/24	05:42:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/05/24	05:43:00.5	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 00 00 00 00
2012/05/24	05:43:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/05/24	05:43:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/05/24	05:43:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/05/24	05:45:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2012/05/24	05:46:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/24	05:52:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/24	05:53:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00 56 7e 00 b4
2012/05/24	05:55:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/05/24	05:55:46.0	XRT_FLD_ENA_405_OG [0x195]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/05/24	05:55:48.0	XRT_FLRCTRL_ENA_440_OG [0x1b8]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/05/24	05:55:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/05/24	05:55:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/05/24	05:55:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/24	05:55:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2012/05/24	05:55:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2012/05/24	05:56:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/24	06:11:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/24	06:11:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/24	06:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/24	06:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/24	06:48:30.0	XRT_Custom_418_OG [0x1a2]				
2012/05/24	06:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/24	07:51:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/24	07:51:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/24	07:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/24	07:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/24	08:27:00.0	XRT_Custom_418_OG [0x1a2]				
2012/05/24	08:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/24	08:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/24	09:31:00.0	XRT_CTRL_MANU_408_OG [0x198]				

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2012/05/24	09:31:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/24	09:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/24	09:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/24	10:23:00.0	AOCS_OrE-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 00 00 00 00