

XRT Timeline to be uploaded on 2012/05/24

Period: 2012/05/24 10:13:00 - 2012/06/29 12:00:00

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Normal mode

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XOB #18EA: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-band(16)

Term	Pointing (x, y)	Comment
05/24 11:48:00 - 05/24 12:20:54	Fixed (-490.0, -35.0)	
05/24 12:24:00 - 05/24 12:54:00	Fixed (106.0, -35.0)	
05/24 13:38:00 - 05/24 14:32:30	Fixed (703.0, -35.0)	
05/24 15:25:00 - 05/24 15:56:54	Fixed (-562.0, -257.0)	
05/24 16:00:00 - 05/24 16:11:00	Fixed (8.0, -251.0)	
05/24 17:36:00 - 05/24 17:49:30	Fixed (546.0, -257.0)	
05/24 19:00:00 - 05/24 19:06:54	Fixed (0.0, 0.0)	synoptic, shifted manually.
05/25 06:03:00 - 05/25 06:09:54	Fixed (0.0, 0.0)	synoptic
05/25 18:07:30 - 05/25 18:14:24	Fixed (0.0, 0.0)	synoptic, shifted 4.5 min
05/26 06:33:00 - 05/26 06:39:54	Fixed (0.0, 0.0)	synoptic, shifted 30.0 min

PROG= 07 1-time(s)

- └─ **Subr= 1 1-time(s) 12.0sec**
 - └─ **Seqn= 46 1-time(s) 4.0sec**
 - └─ Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - └─ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - └─ **Seqn= 5 1-time(s) 2.0sec**
 - └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
 - └─ **Seqn= 69 1-time(s) 4.0sec**
 - └─ Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - └─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - └─ **Seqn= 4 1-time(s) 2.0sec**
 - └─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #18EF: Coronal cavity (FW1 open)-HOP114 with ARs-DPCM 2x2-768FOV-AEC0-3min cad

Term	Pointing (x, y)	Comment
05/24 19:10:00 - 05/25 05:59:54	Fixed (407.0, -870.0)	# HOP 114, cavity in SW.

PROG= 15 Inf.-time(s)

- └─ **Subr= 1 5-time(s) 180.0sec**
 - └─ **Seqn= 99 1-time(s) 4.0sec**
 - └─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Norm 4.00s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec
- └─ **Subr= 2 1-time(s) 2.0sec**
 - └─ **Seqn= 14 1-time(s) 4.0sec**
 - └─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1835: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 40sec-cad

Term	Pointing (x, y)	Comment
05/25 06:13:00 - 05/25 09:59:54	Track (229.0, 260.4) ^{Ⓜ 05/25 06:10:00}	# AR 11486, EIS studies, etc.
05/25 16:03:00 - 05/25 18:04:24	Track (311.9, 259.0) ^{Ⓜ 05/25 16:00:00}	* AR 11486.
05/25 18:17:30 - 05/26 06:29:54	Track (330.5, 258.6) ^{Ⓜ 05/25 18:14:30}	# Cont.
05/26 06:43:00 - 05/26 09:07:00	Track (430.2, 256.6) ^{Ⓜ 05/26 06:40:00}	# Cont.

PROG= 04 Inf.-time(s)

- └─ **Subr= 2 1-time(s) 2.0sec**
 - └─ **Seqn= 19 1-time(s) 2.0sec**
 - └─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - └─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - └─ **Seqn= 41 4-time(s) 2.0sec**
 - └─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - └─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
- └─ **Subr= 1 1-time(s) 2.0sec**
 - └─ **Seqn= 52 45-time(s) 24.0sec**
 - └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 6.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384

Term	Pointing (x, y)	Comment
05/25 10:03:00 - 05/25 15:09:30	Fixed (-8.0, 865.0)	* HOP 81, polar monitoring, N pole.

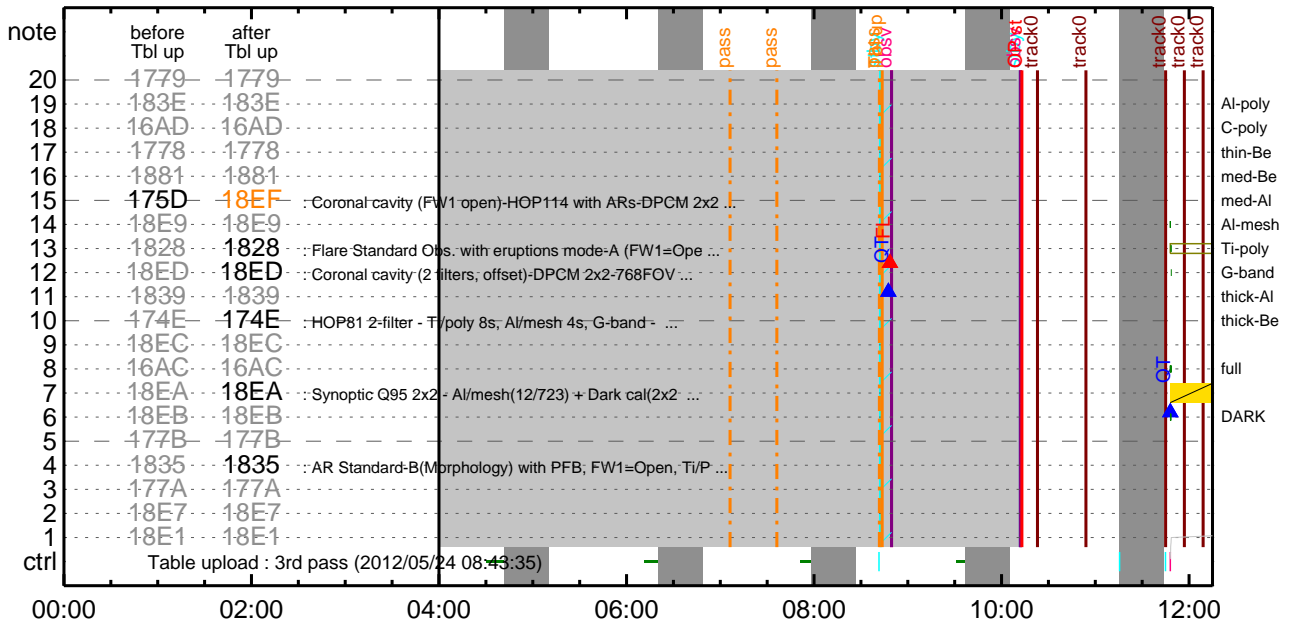
PROG= 10 Inf.-time(s)

- └─ **Subr= 1 1-time(s) 2.0sec**

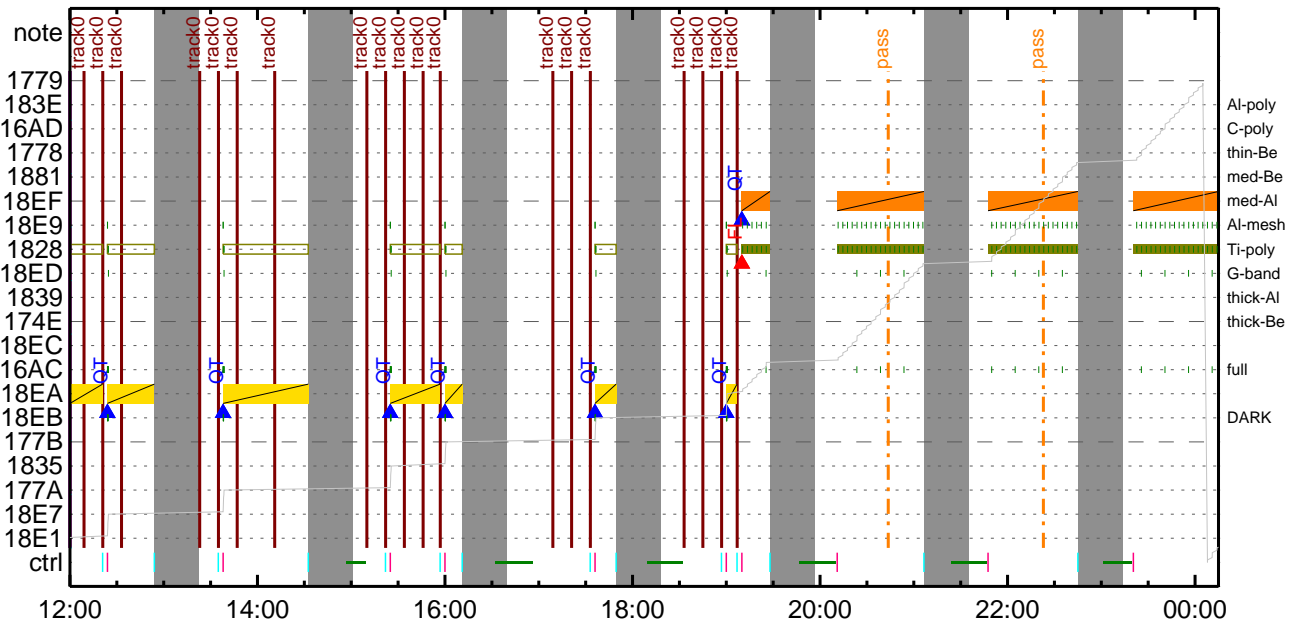
05/24 19:09:46 - 05/25 06:00:16 Fixed (407.0, -870.0) # HOP 114, cavity in SW.
 05/25 06:12:46 - 05/25 18:04:46 Track (229.0, 260.4) @ 05/25 06:10:00 # AR 11486, EIS studies, etc.
 05/25 18:17:16 - 05/26 06:30:16 Track (330.5, 258.6) @ 05/25 18:14:30 # Cont.
 05/26 06:42:46 - 06/29 12:00:00 Track (430.2, 256.6) @ 05/26 06:40:00 # Cont.

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

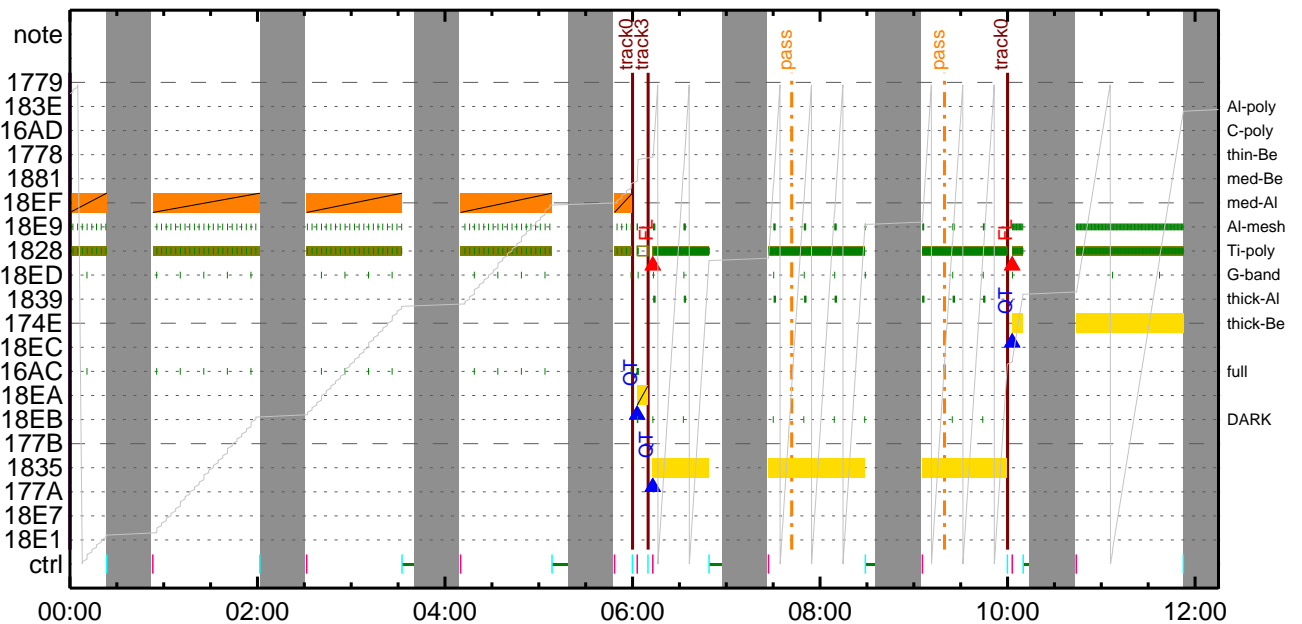
CMDI #0648 2012/05/24



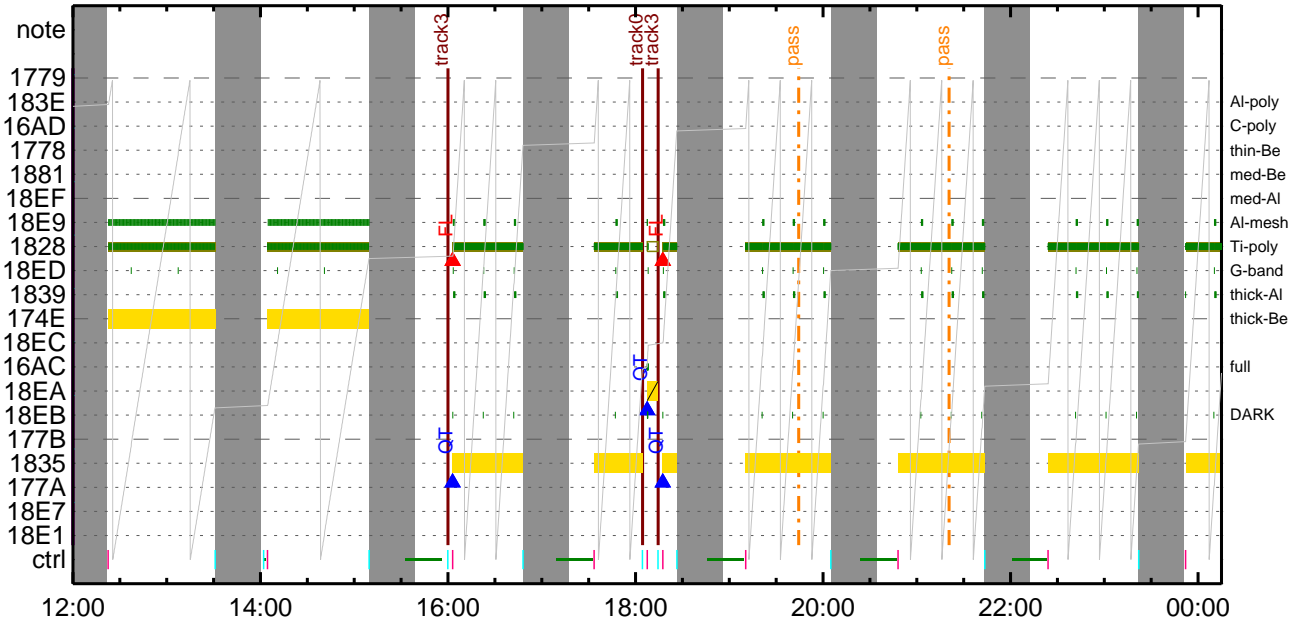
CMDI #0648 2012/05/24



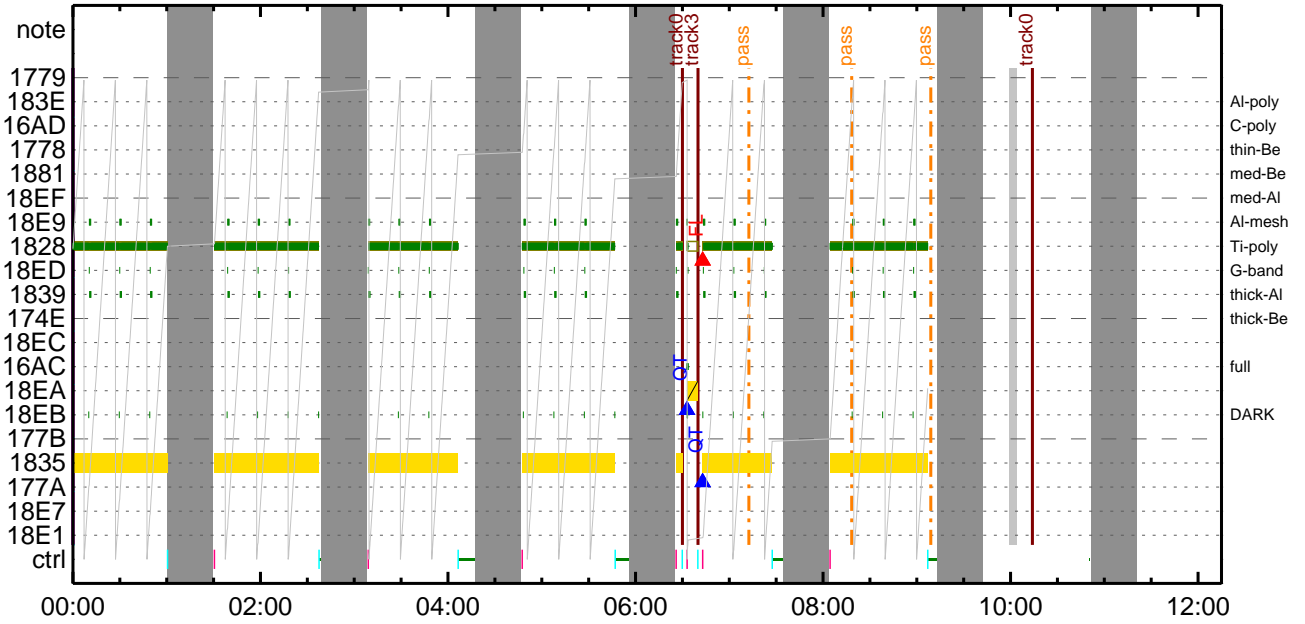
CMDI #0648 2012/05/25



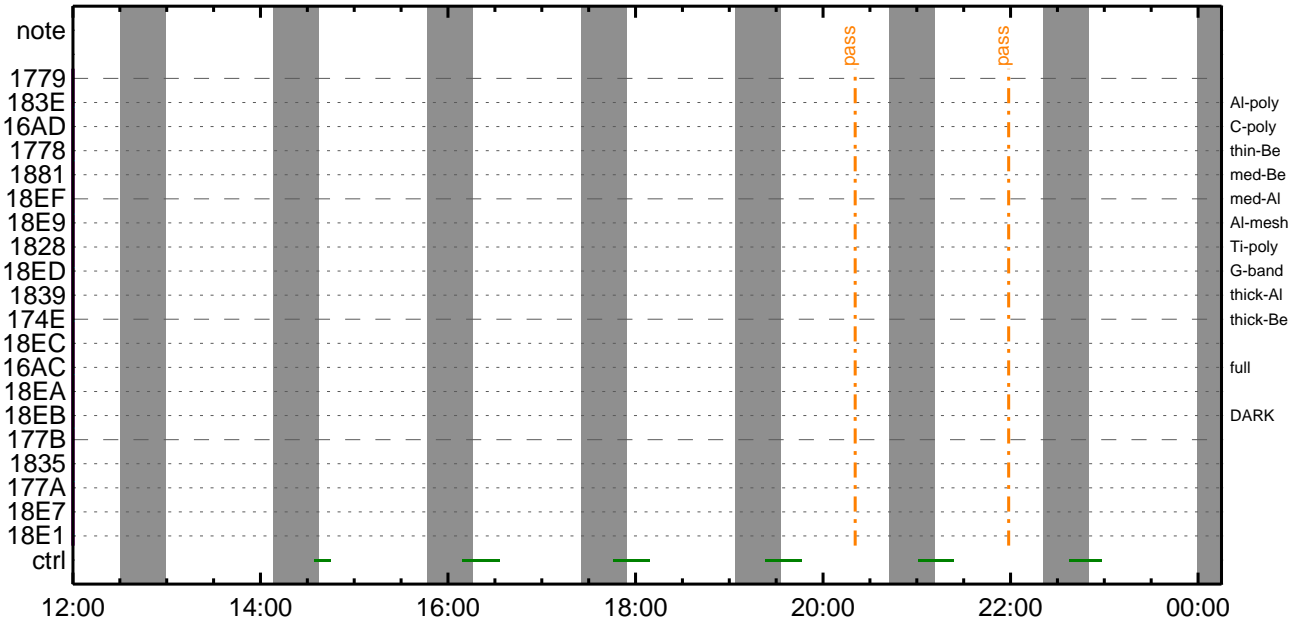
CMDI #0648 2012/05/25



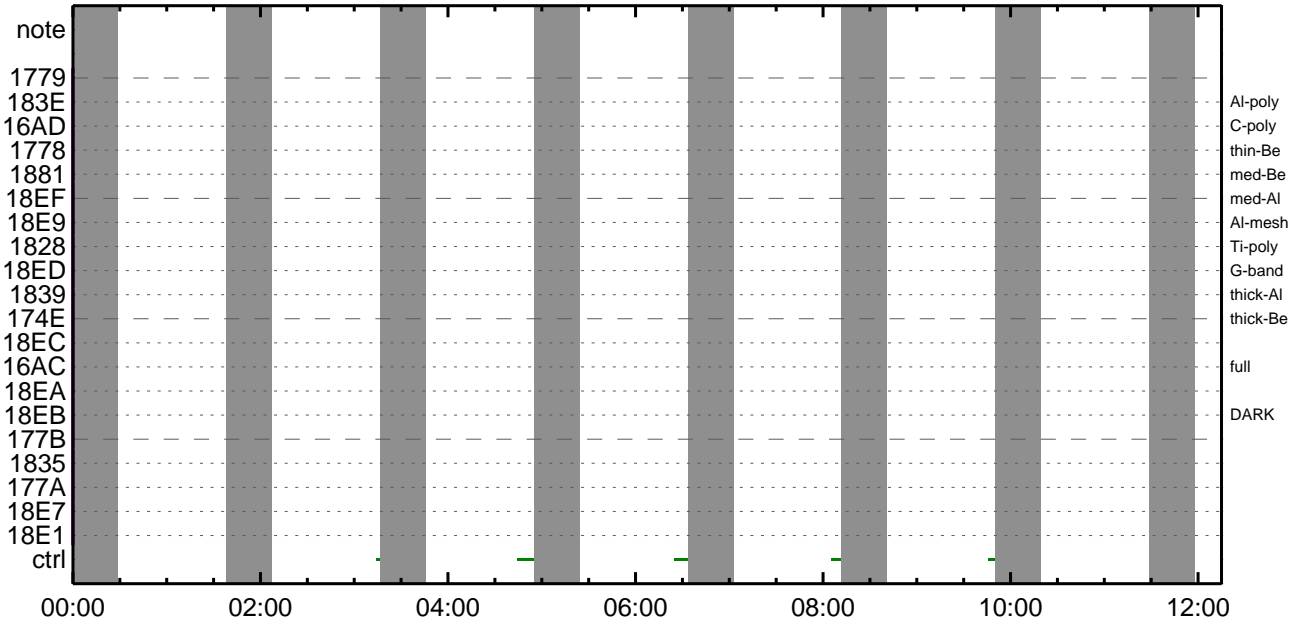
CMDI #0648 2012/05/26



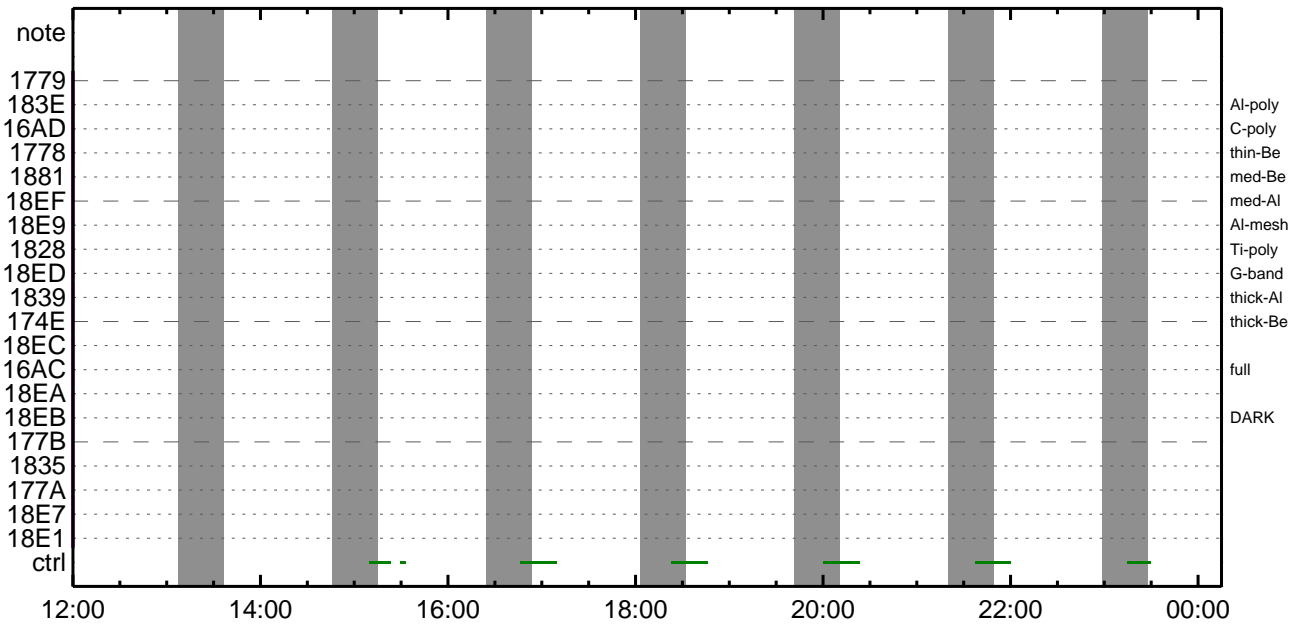
CMDI #0648 2012/05/26



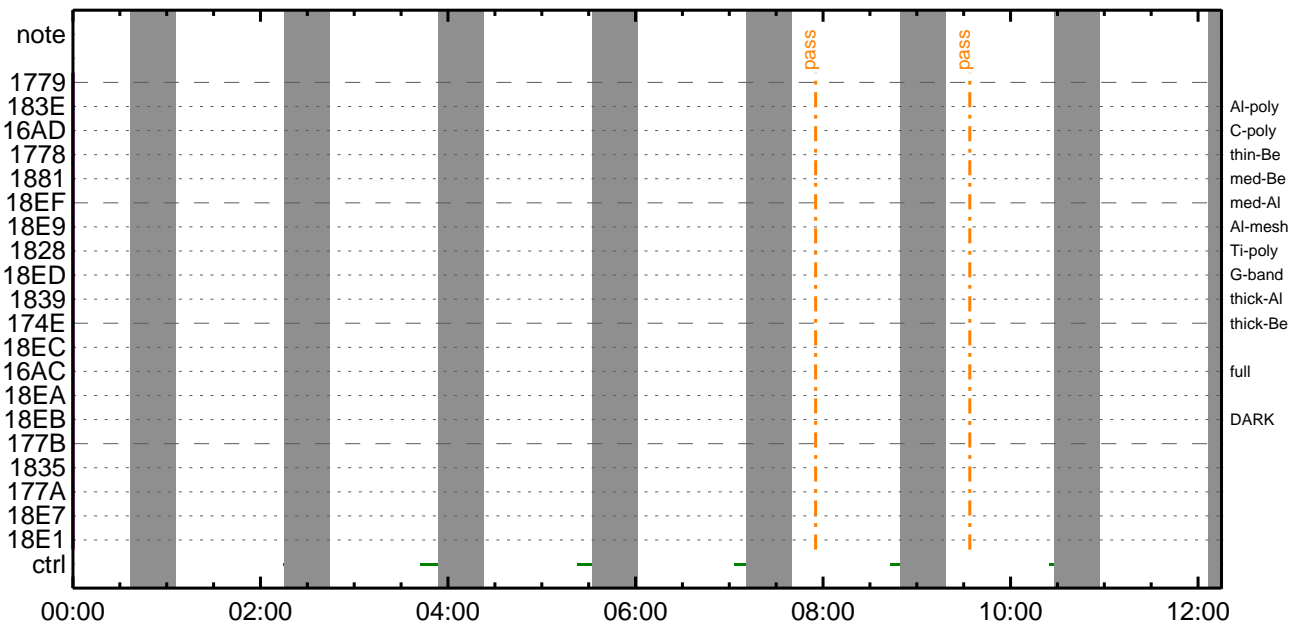
CMDI #0648 2012/05/27



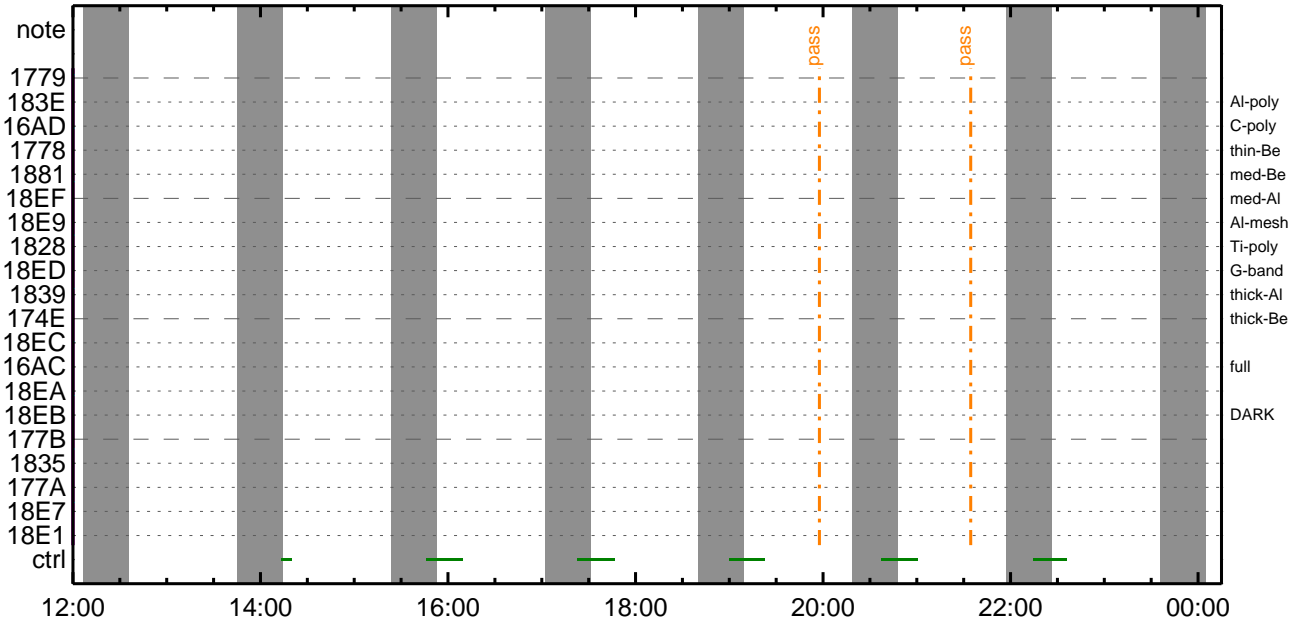
CMDI #0648 2012/05/27



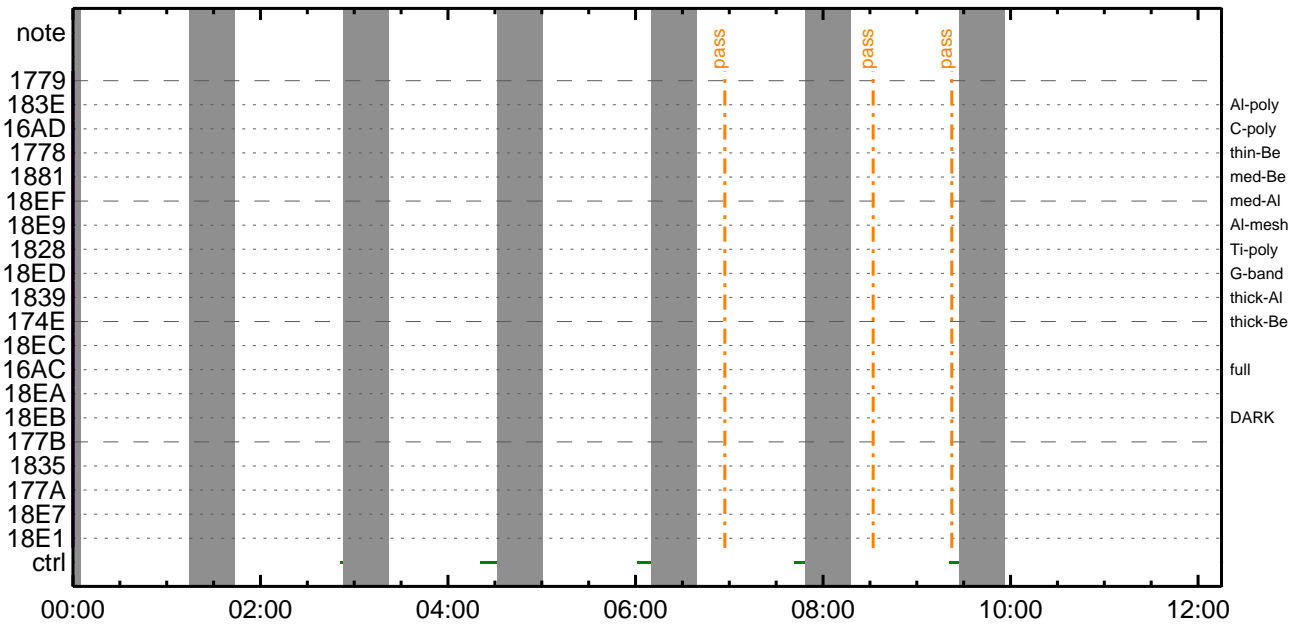
CMDI #0648 2012/05/28



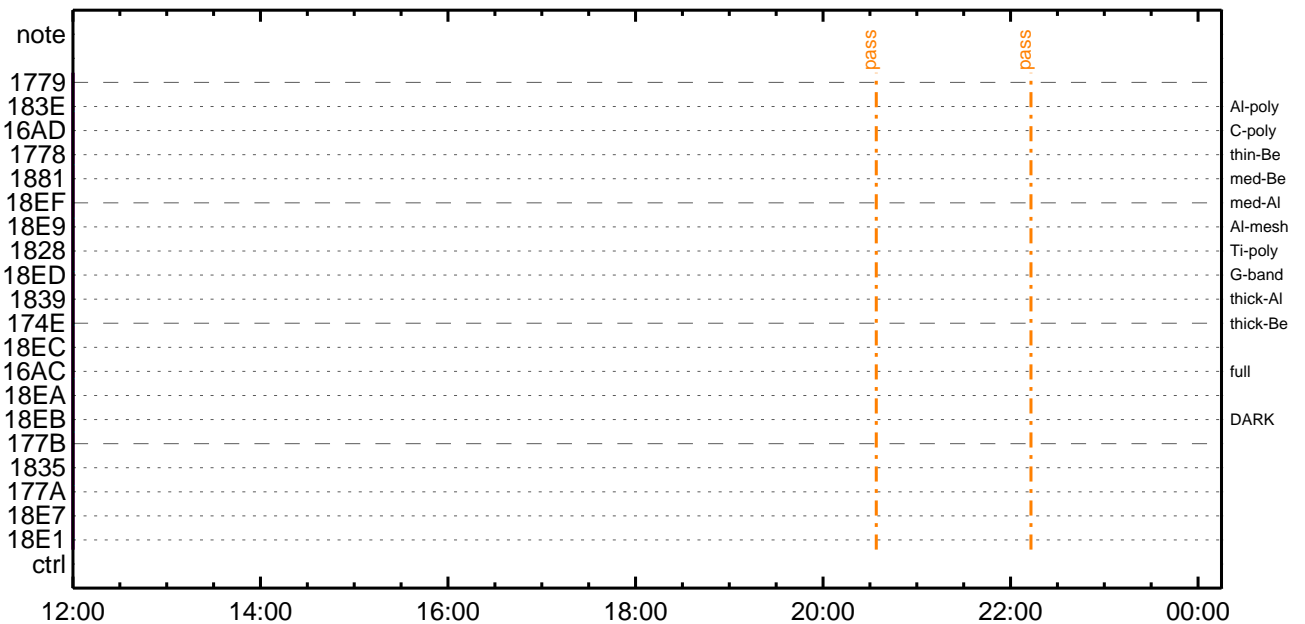
CMDI #0648 2012/05/28



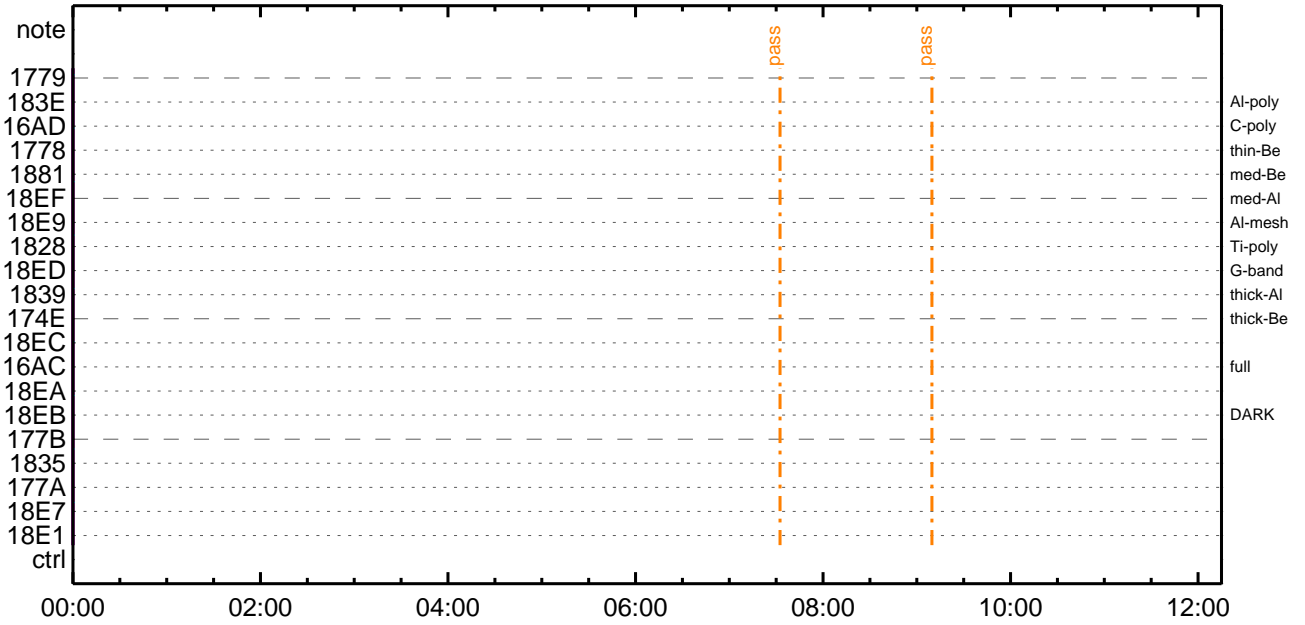
CMDI #0648 2012/05/29



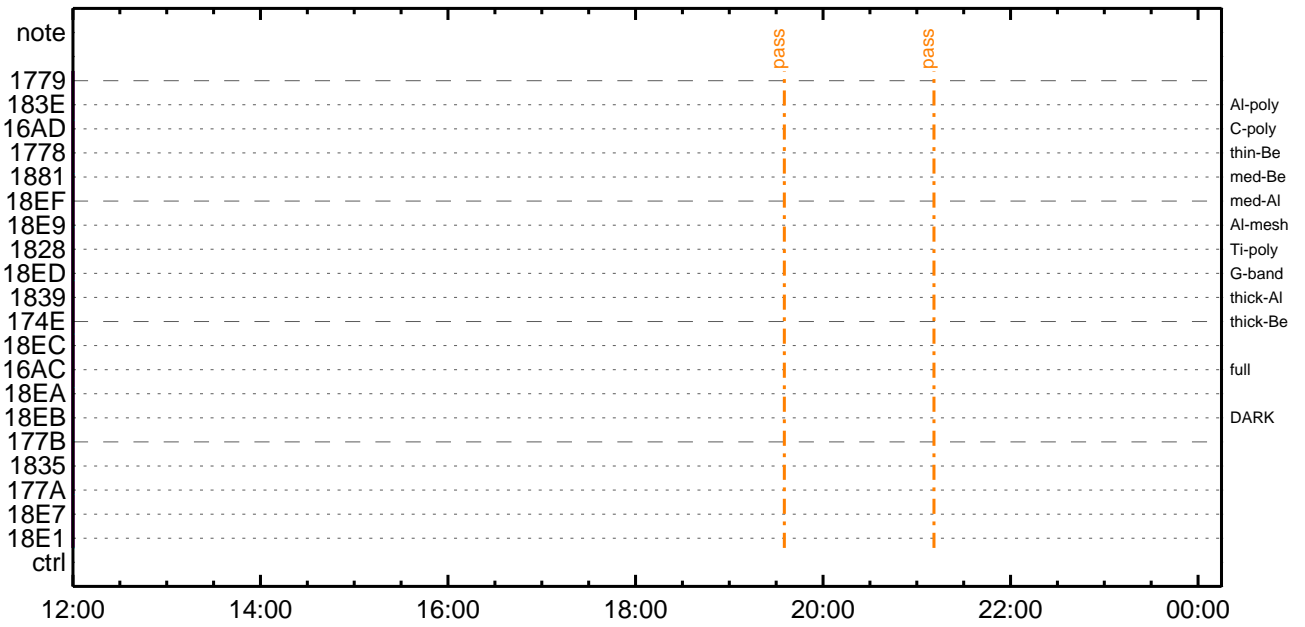
CMDI #0648 2012/05/29



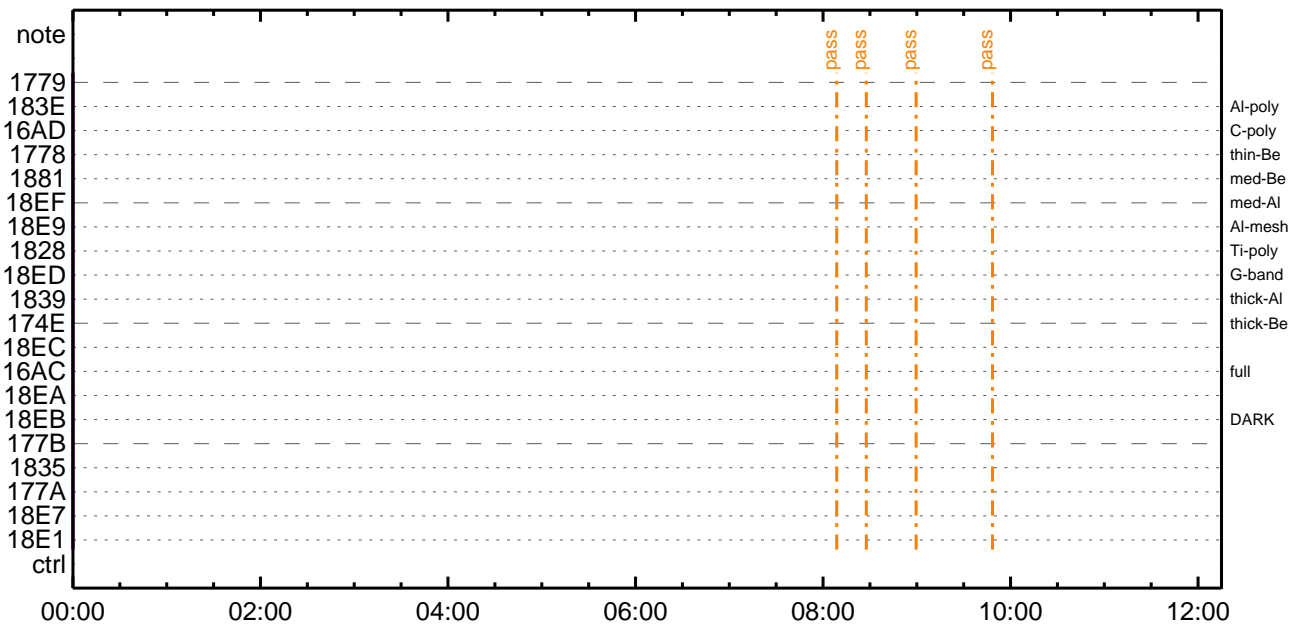
CMDI #0648 2012/05/30



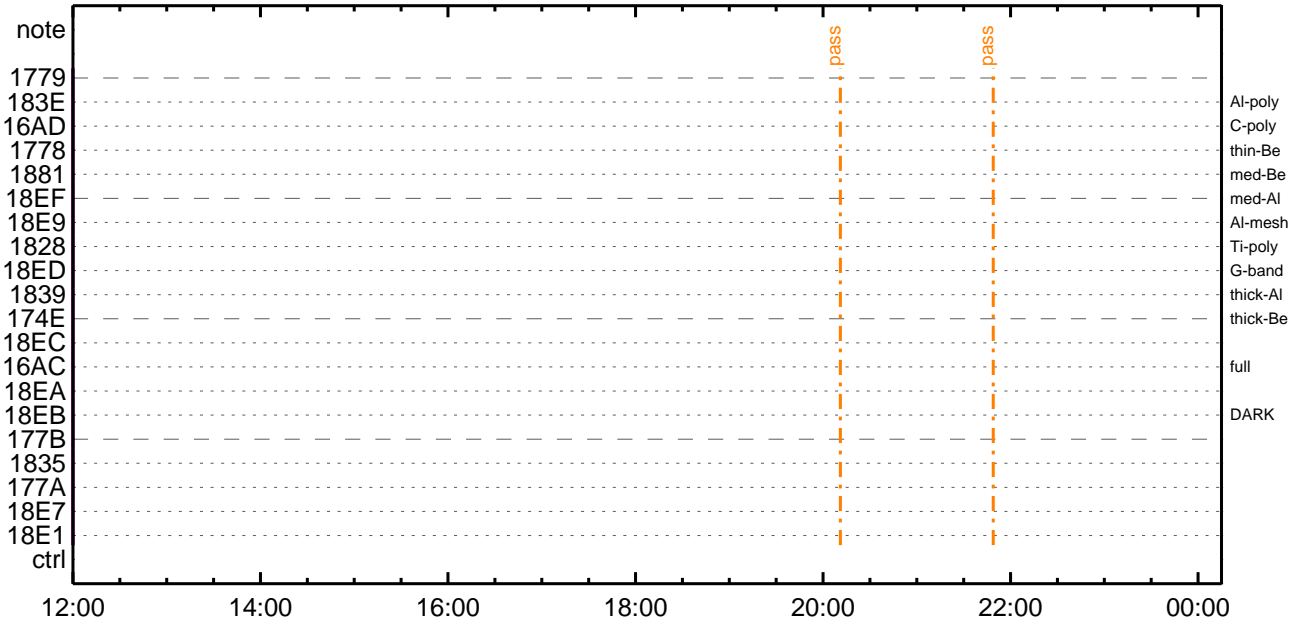
CMDI #0648 2012/05/30



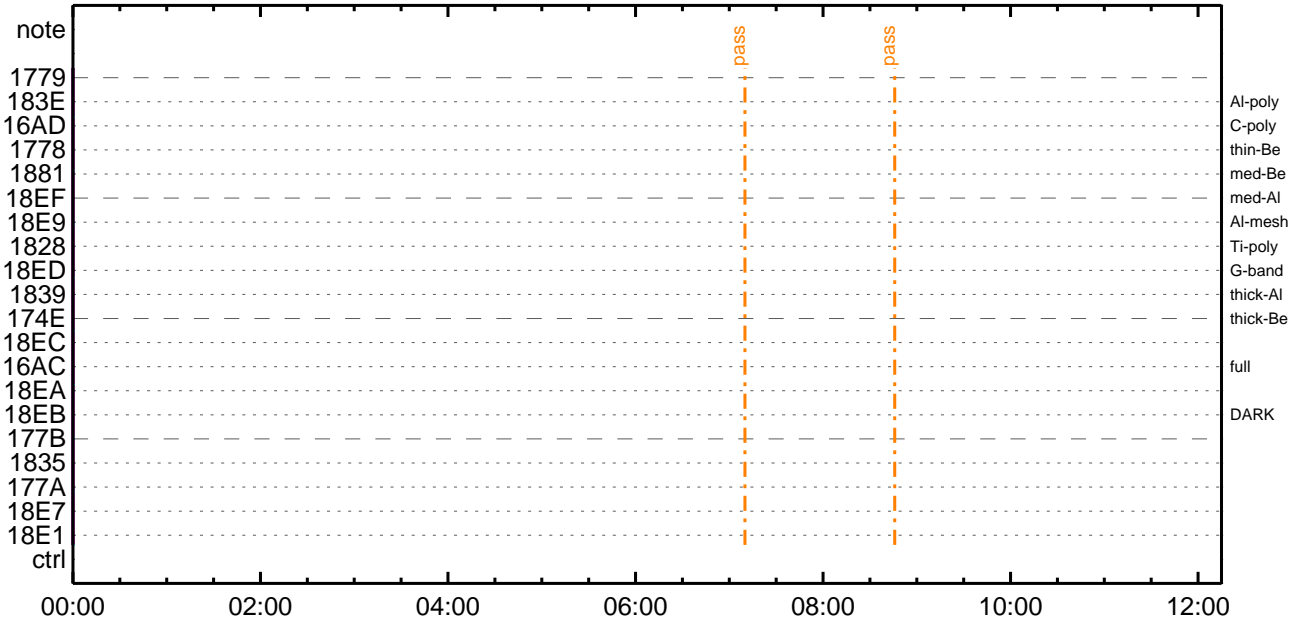
CMDI #0648 2012/05/31



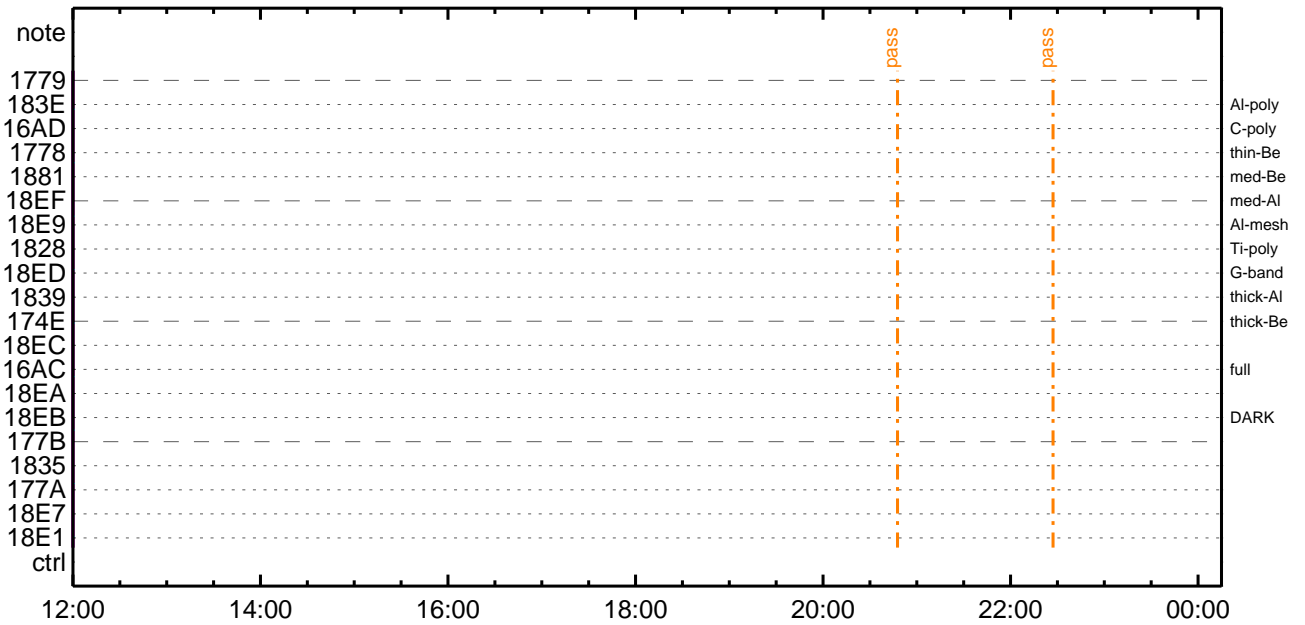
CMDI #0648 2012/05/31



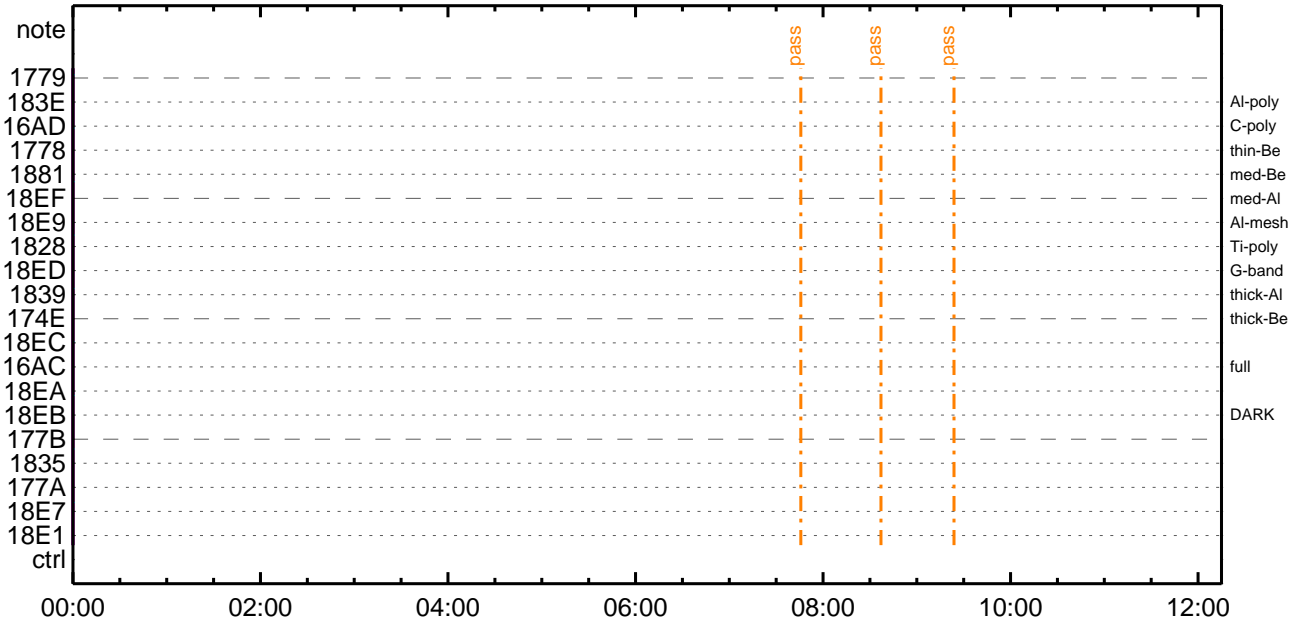
CMDI #0648 2012/06/01



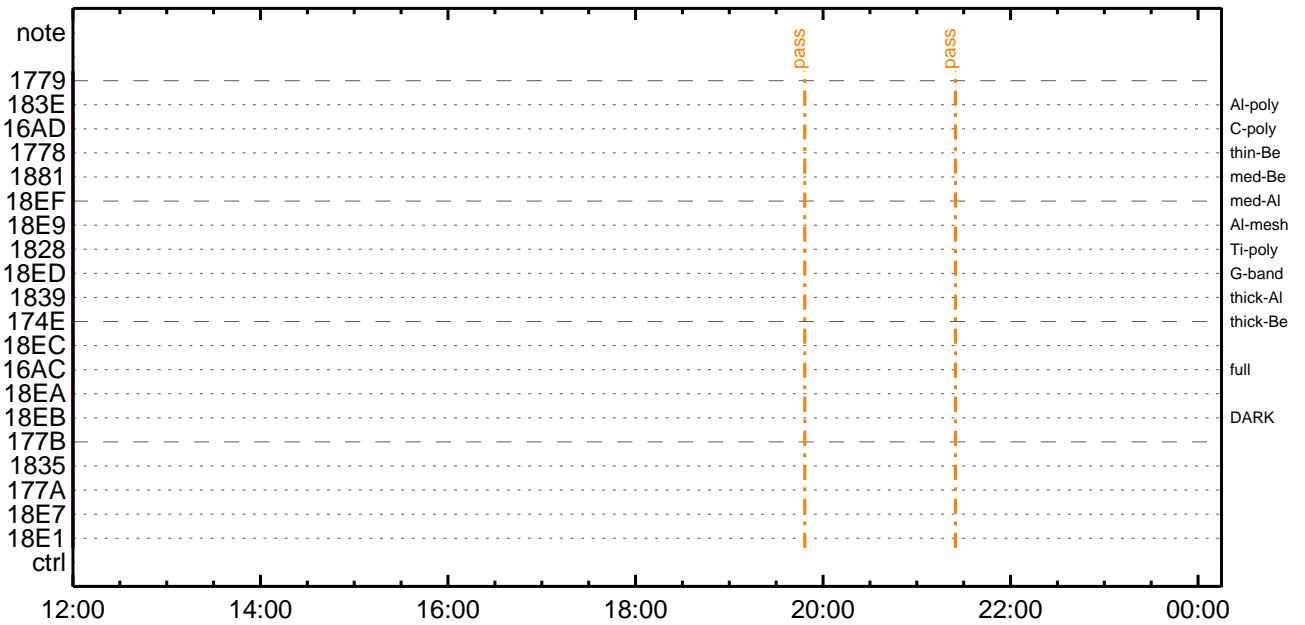
CMDI #0648 2012/06/01



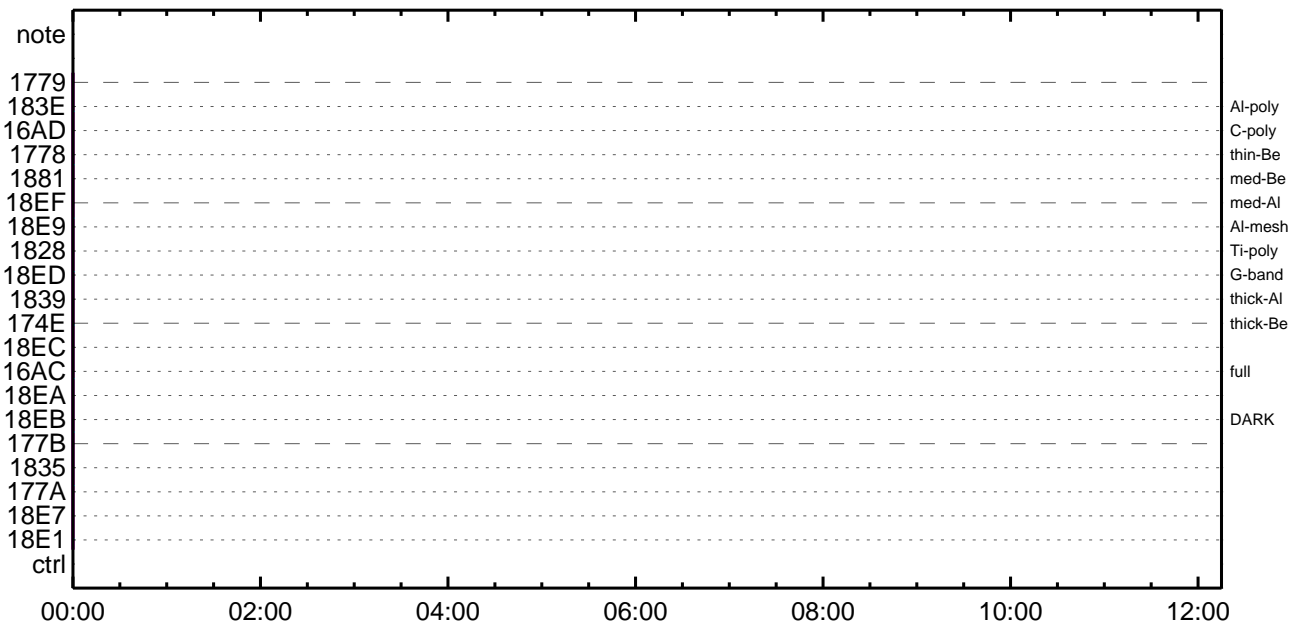
CMDI #0648 2012/06/02



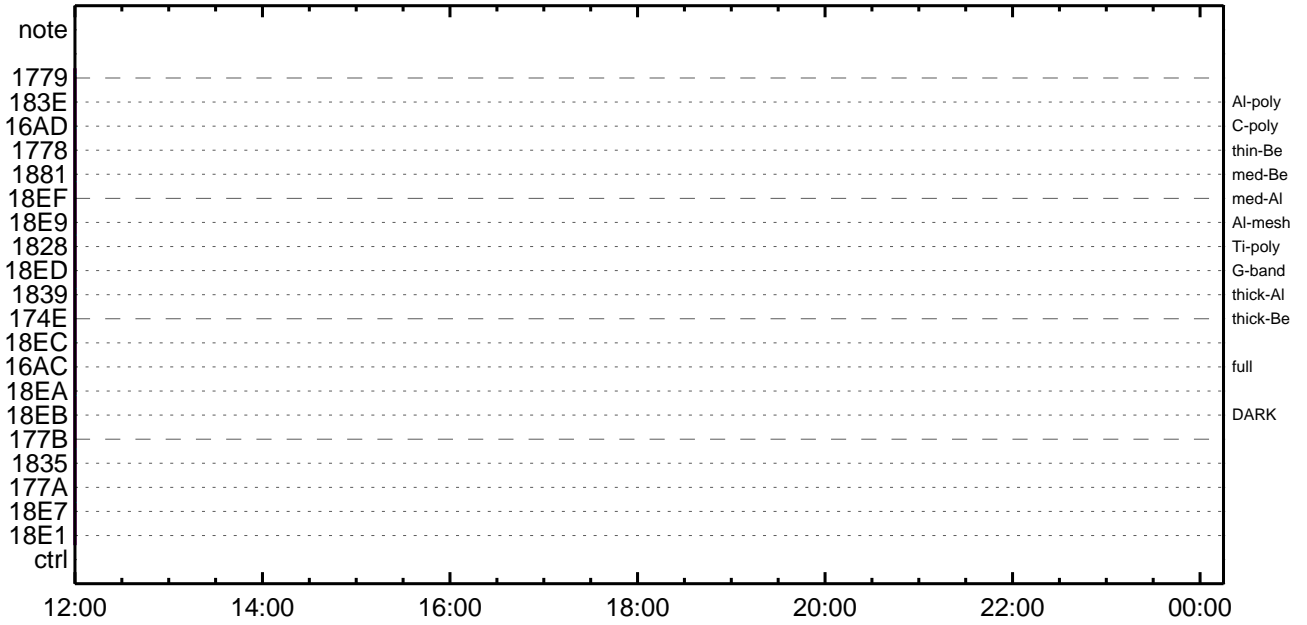
CMDI #0648 2012/06/02



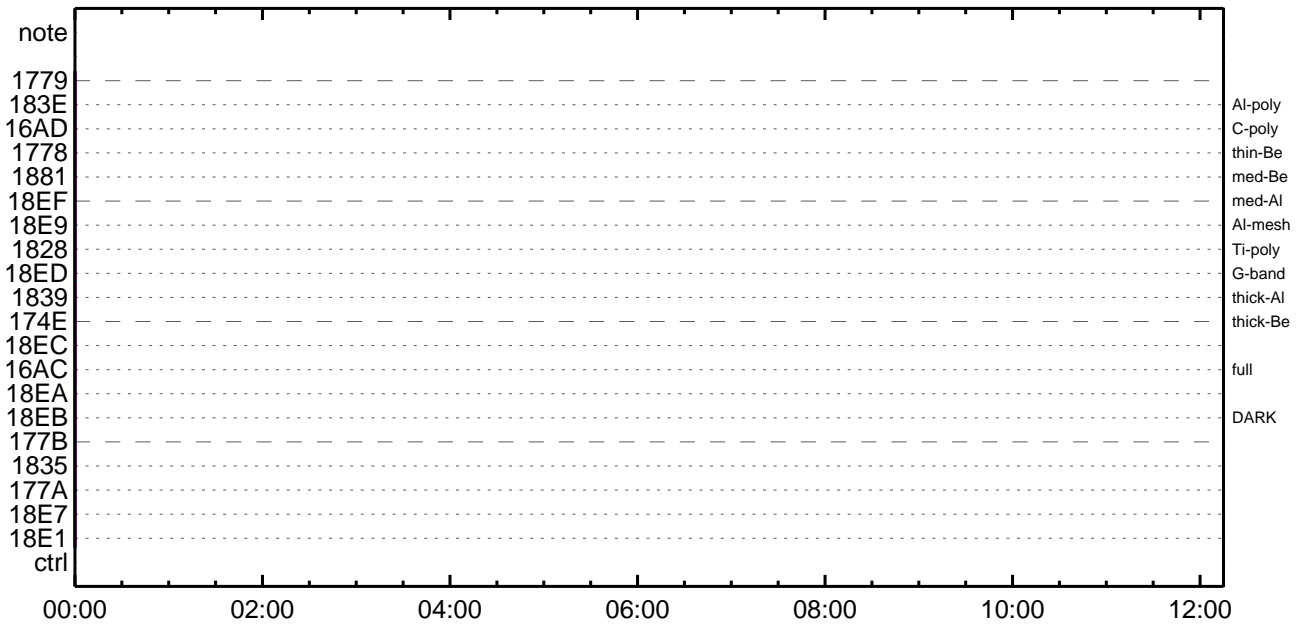
CMDI #0648 2012/06/03



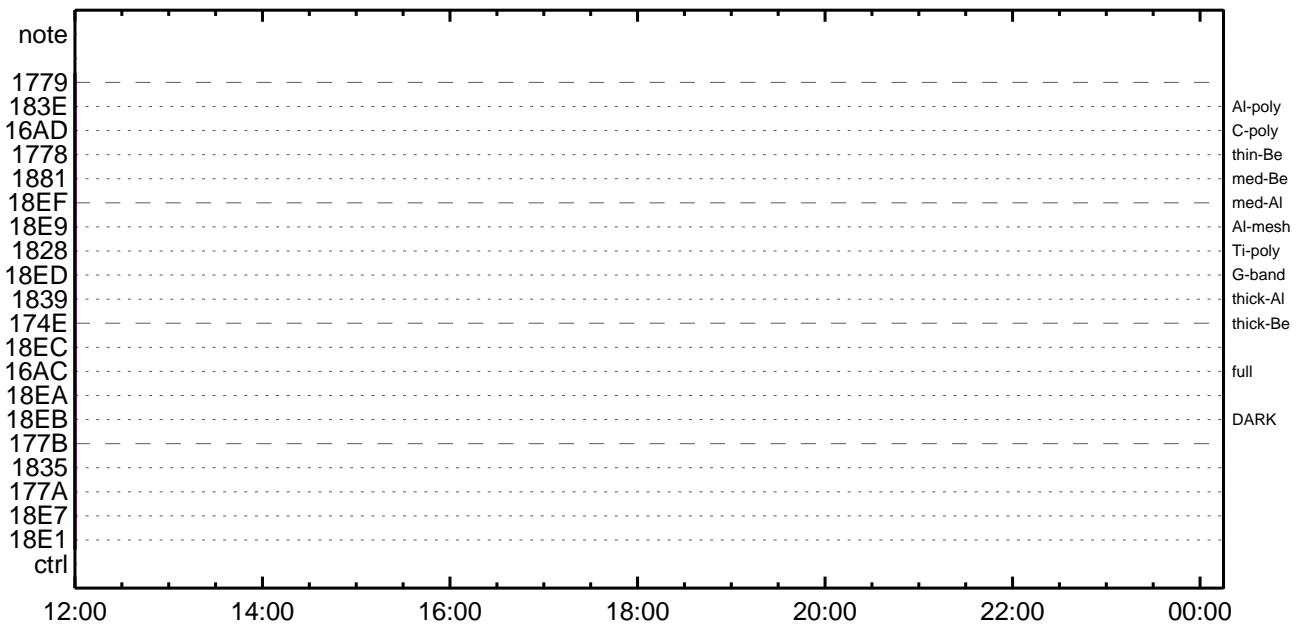
CMDI #0648 2012/06/03



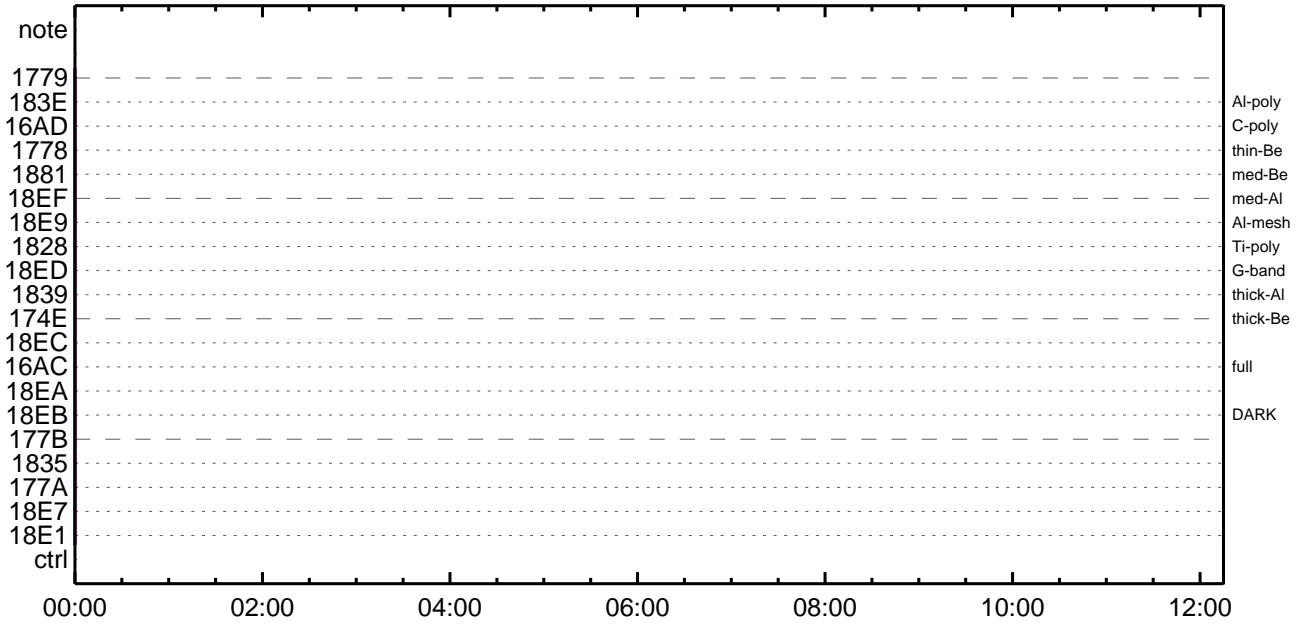
CMDI #0648 2012/06/04



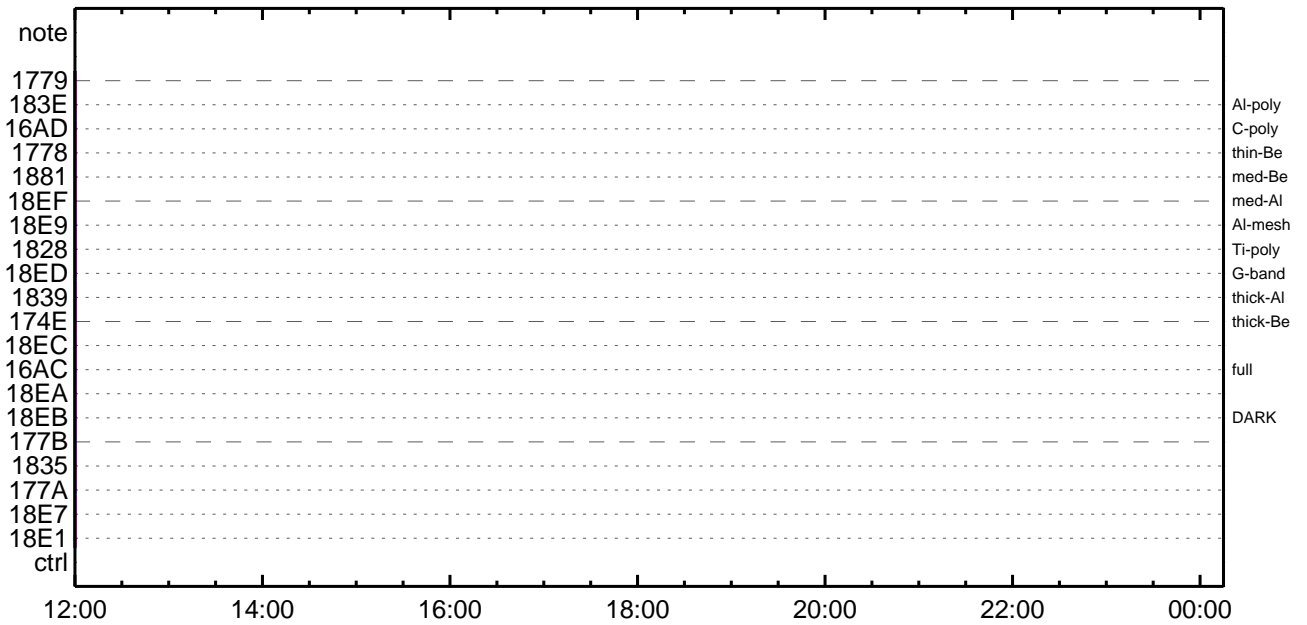
CMDI #0648 2012/06/04



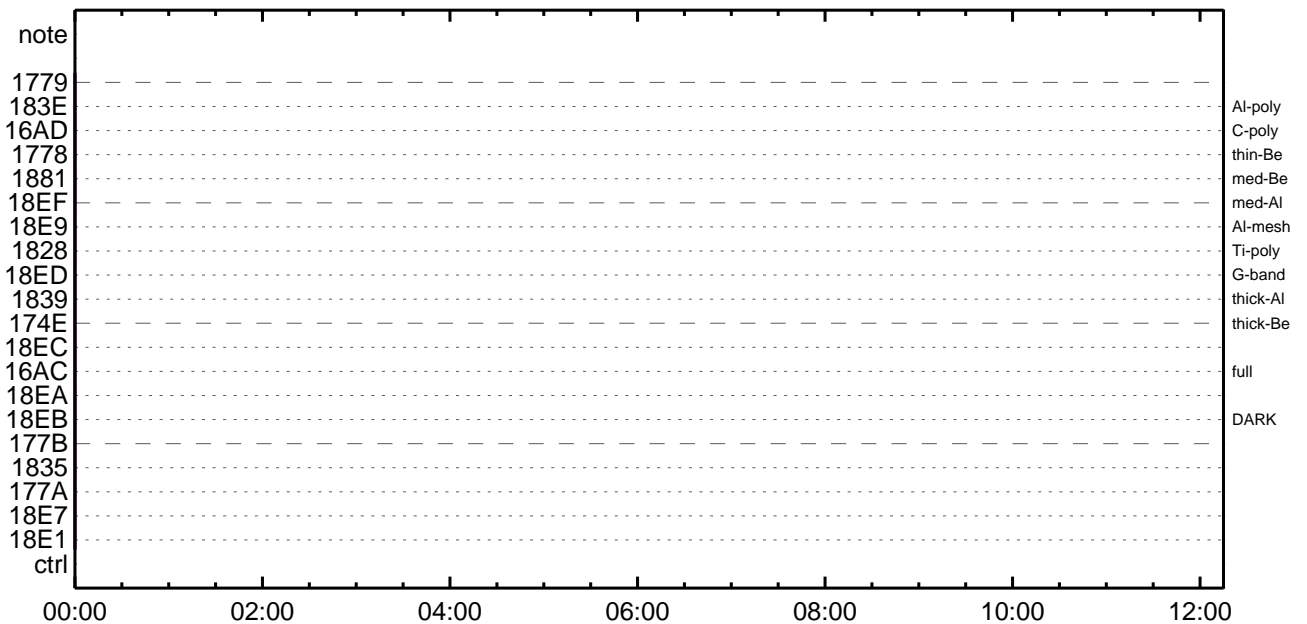
CMDI #0648 2012/06/05



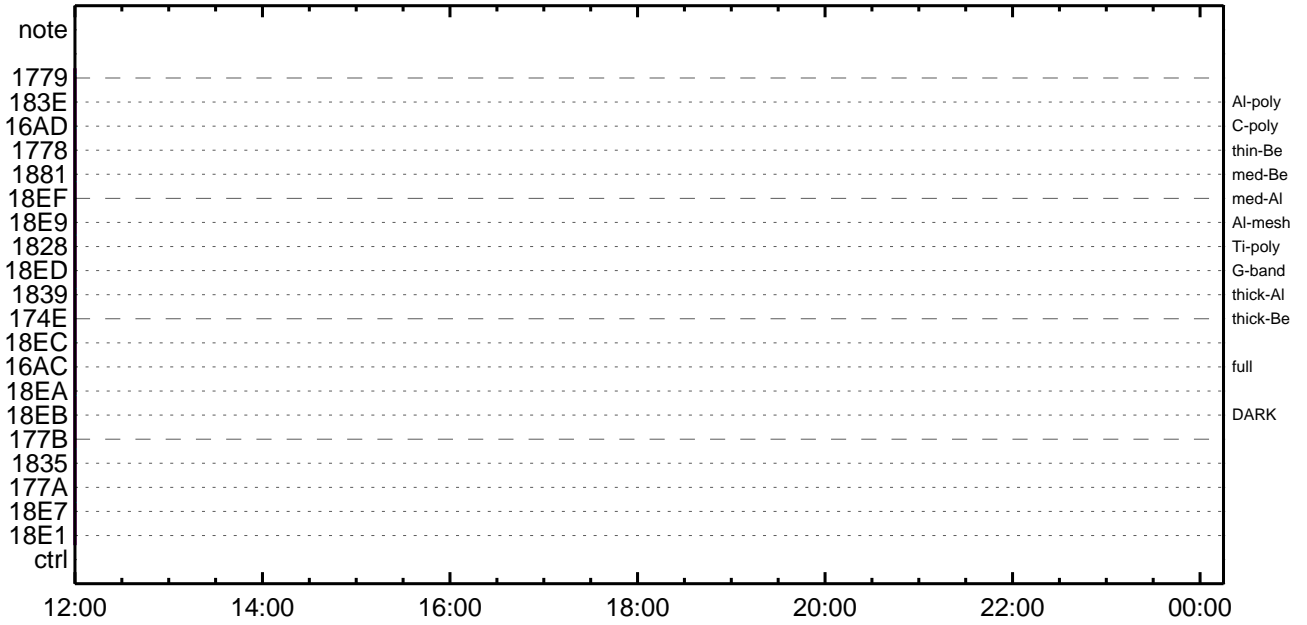
CMDI #0648 2012/06/05



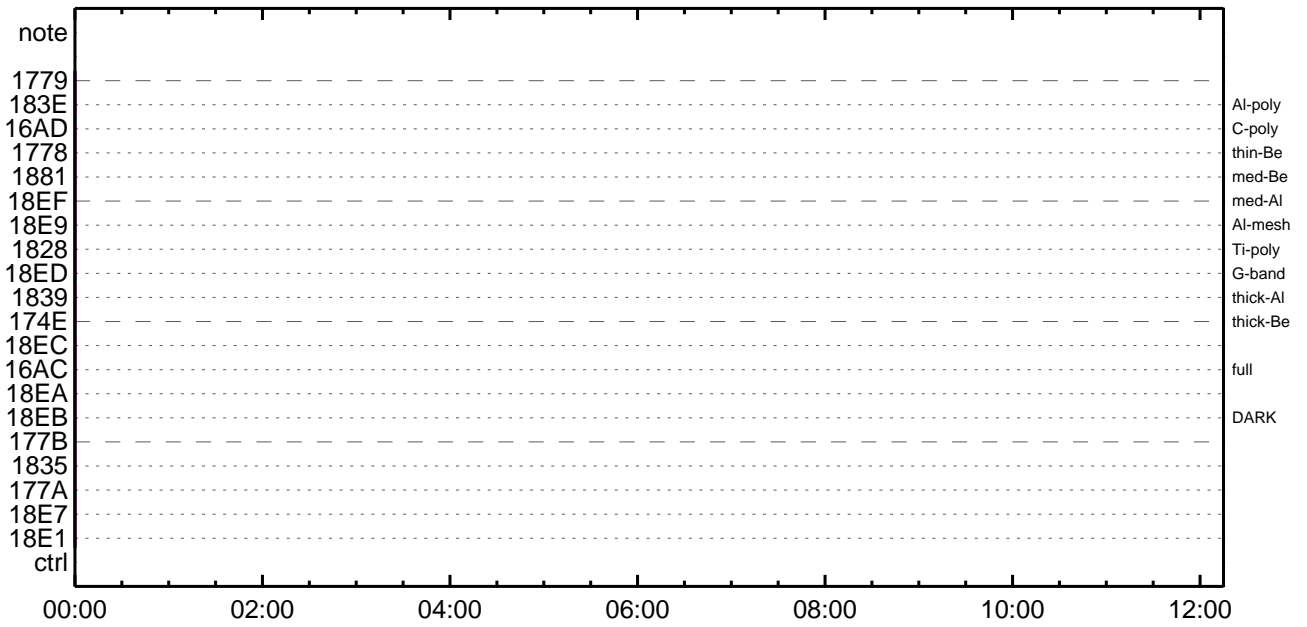
CMDI #0648 2012/06/06



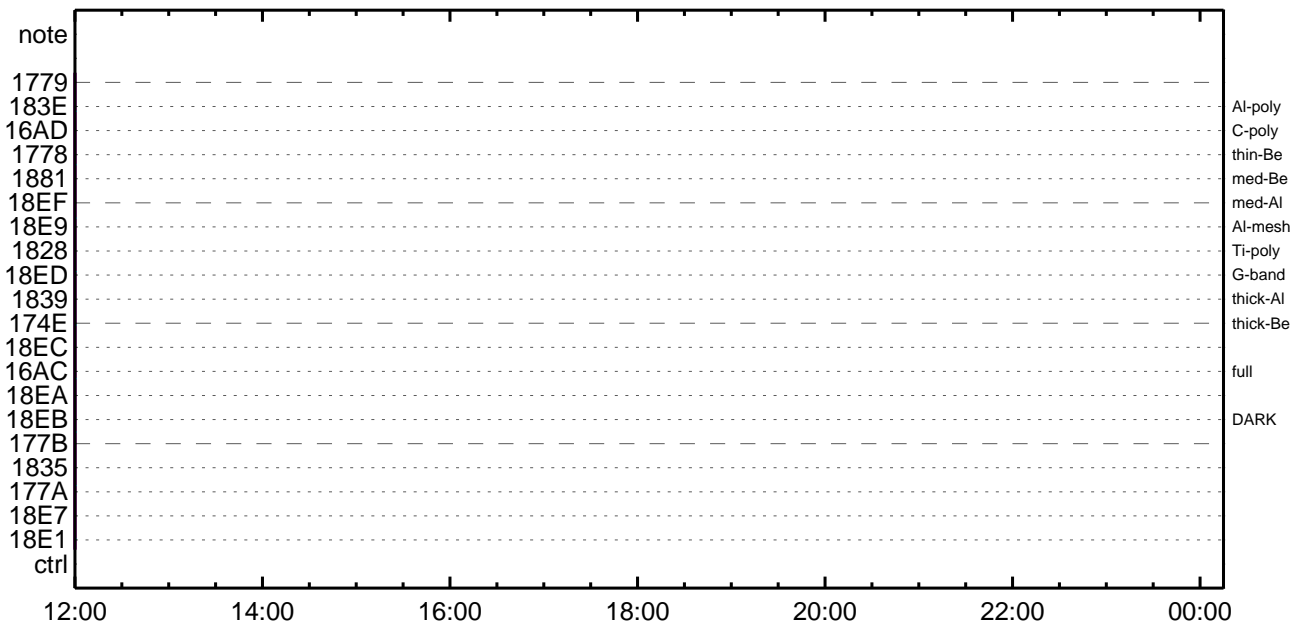
CMDI #0648 2012/06/06



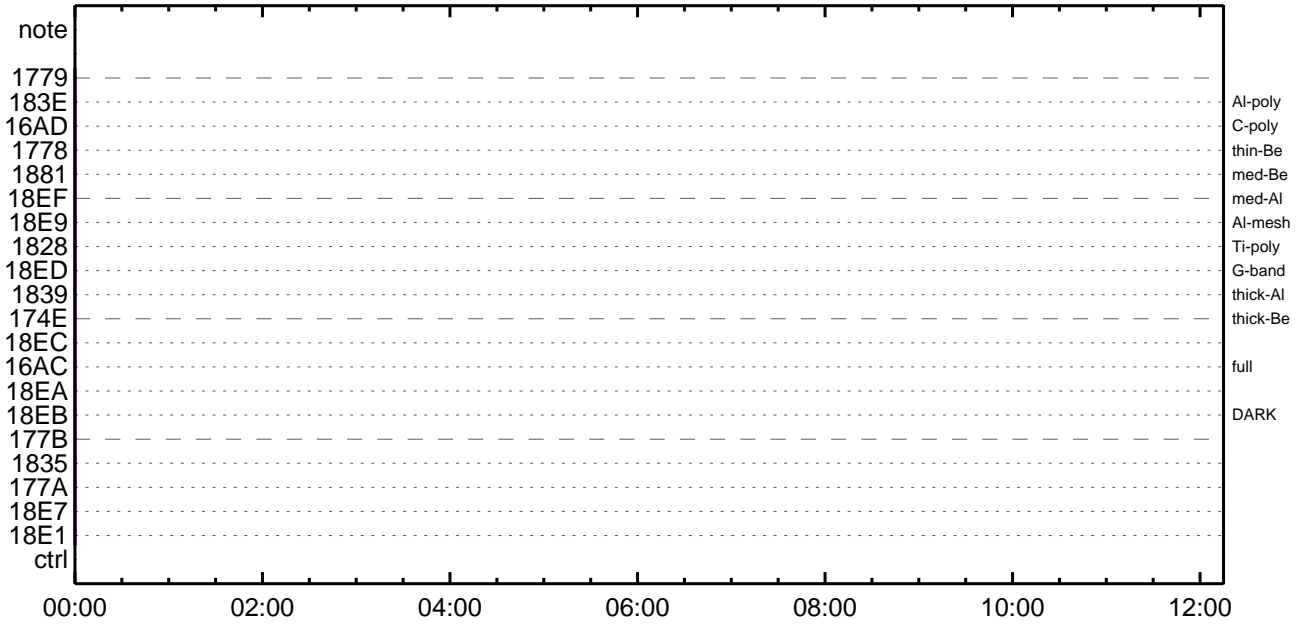
CMDI #0648 2012/06/07



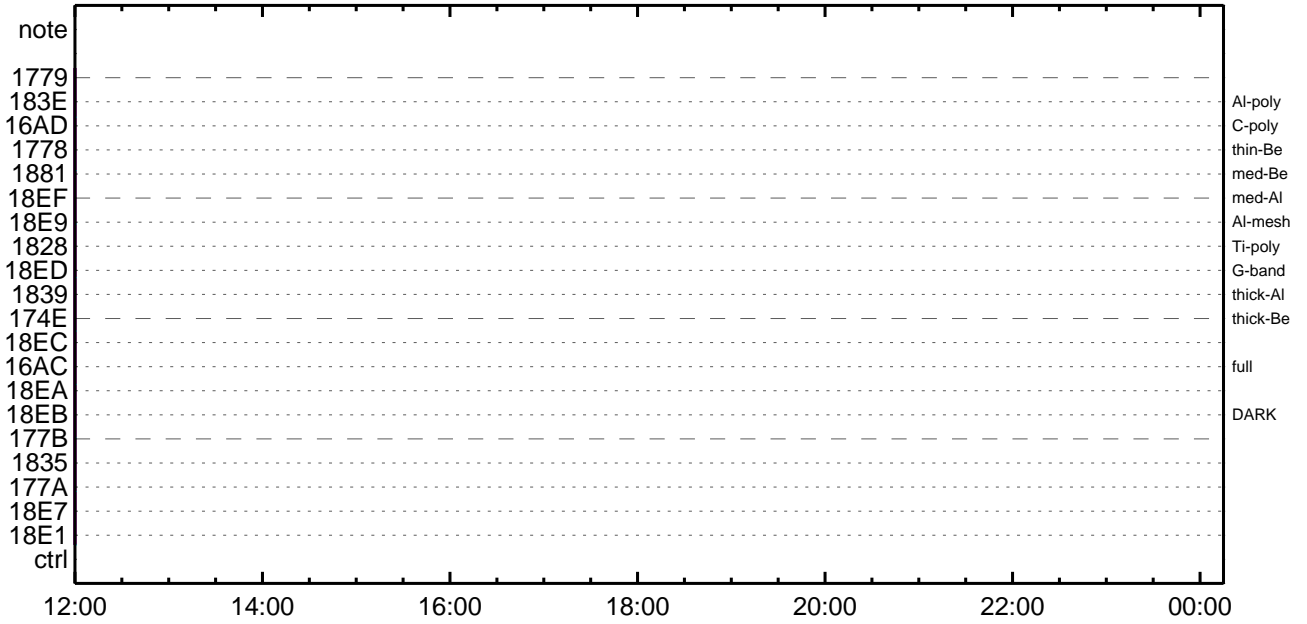
CMDI #0648 2012/06/07



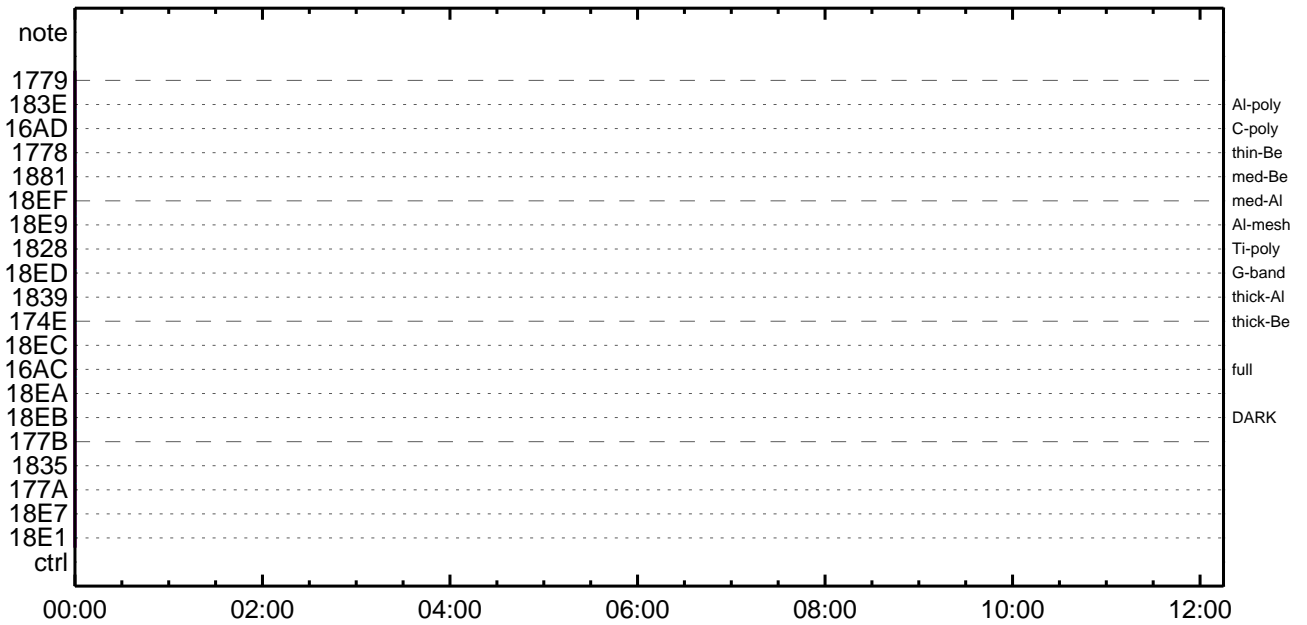
CMDI #0648 2012/06/08



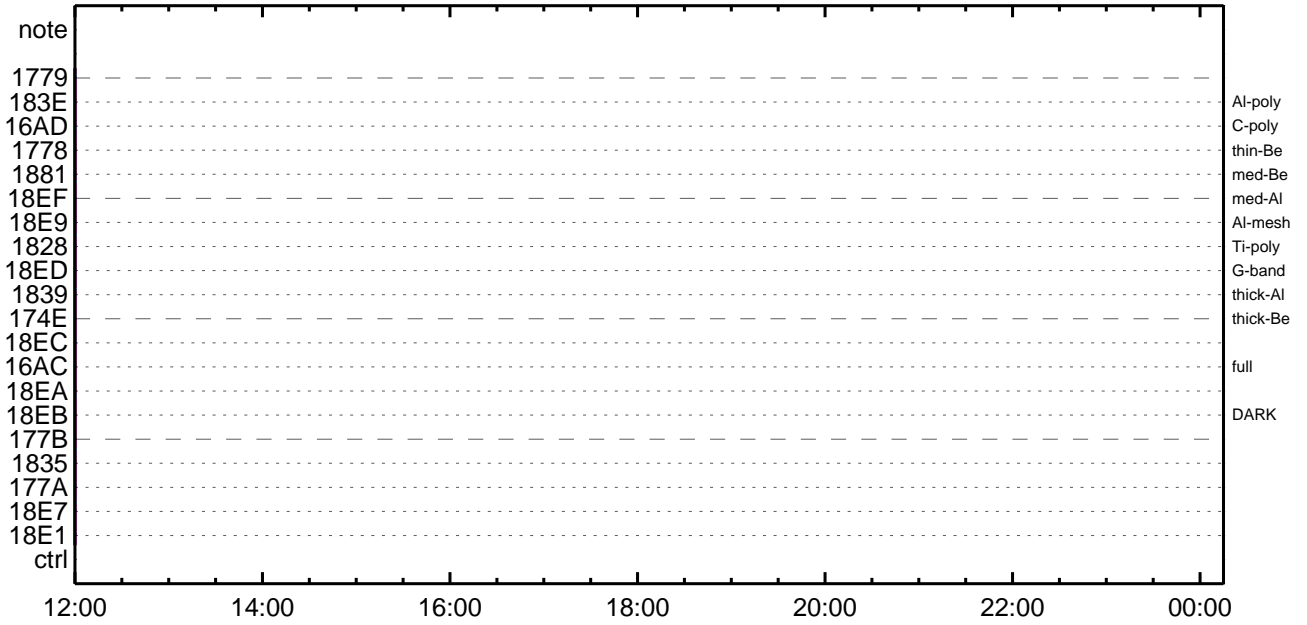
CMDI #0648 2012/06/08



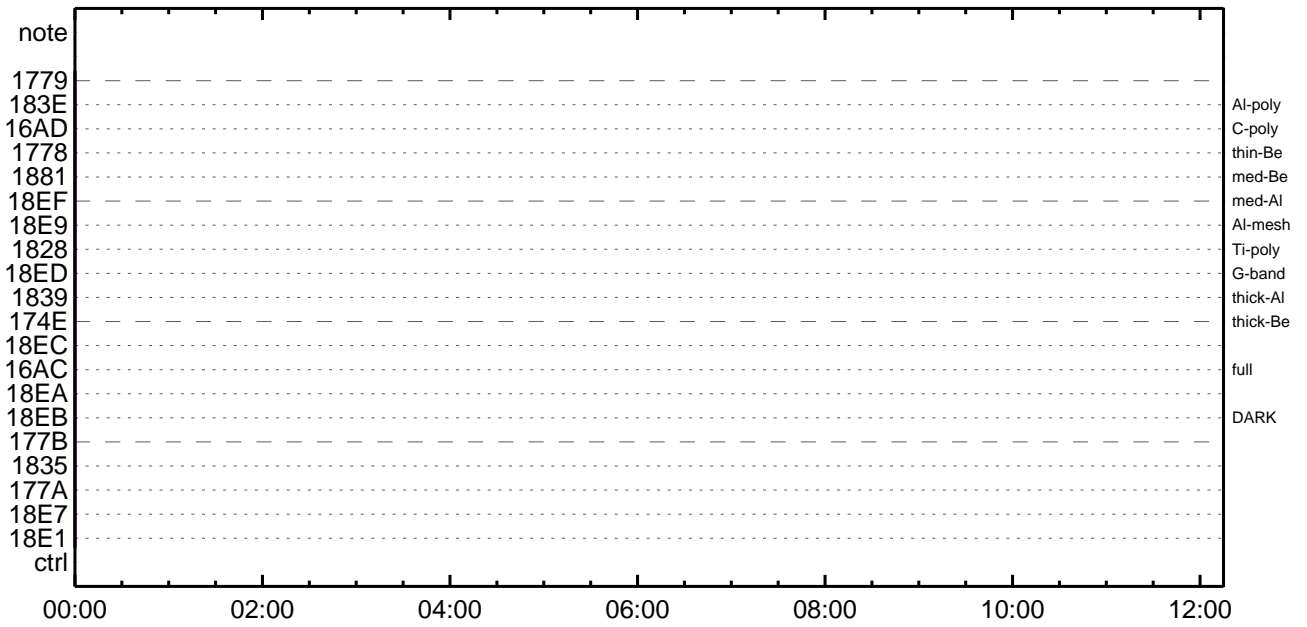
CMDI #0648 2012/06/09



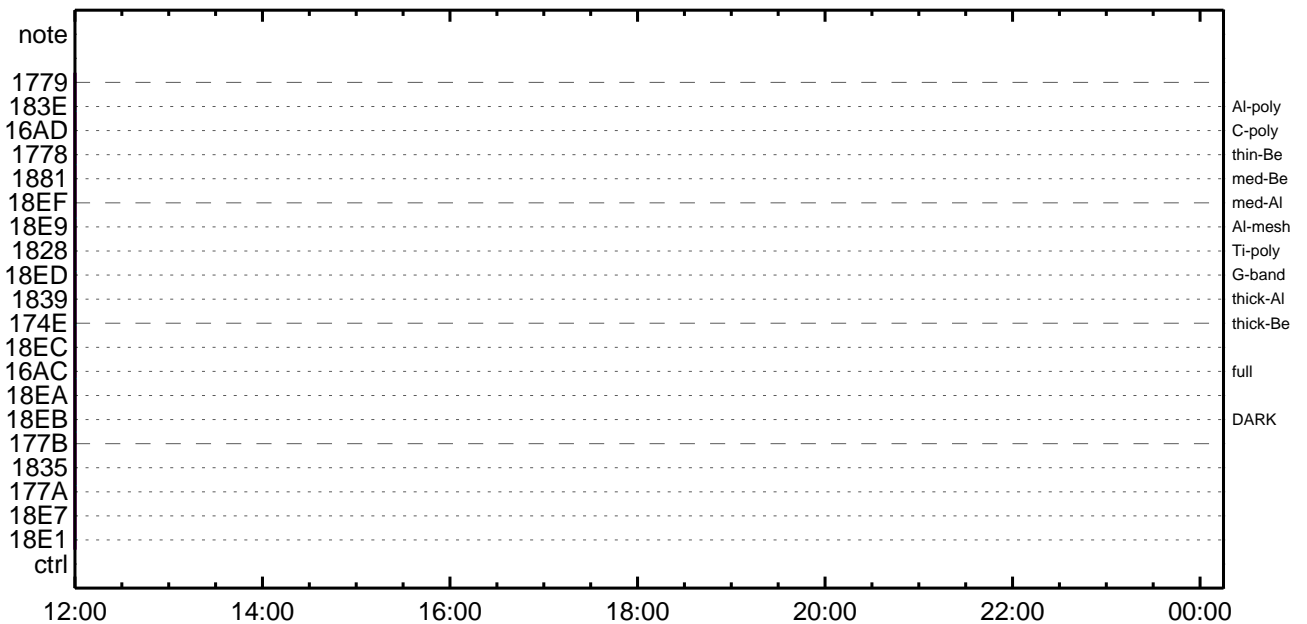
CMDI #0648 2012/06/09



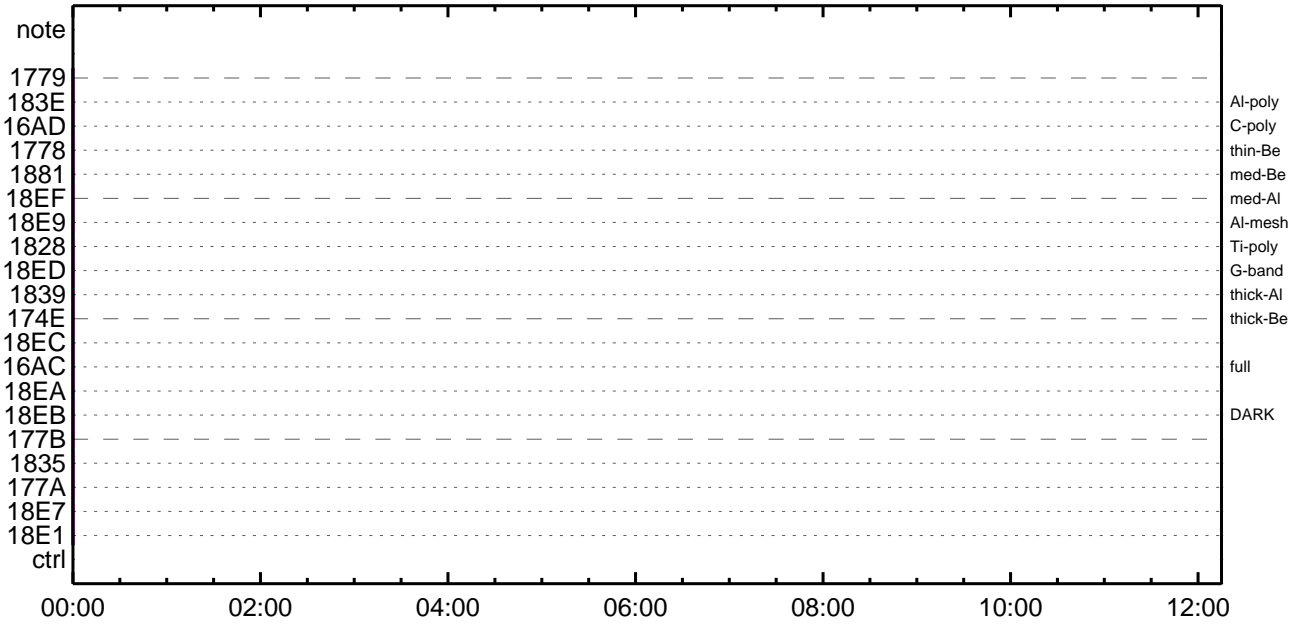
CMDI #0648 2012/06/10



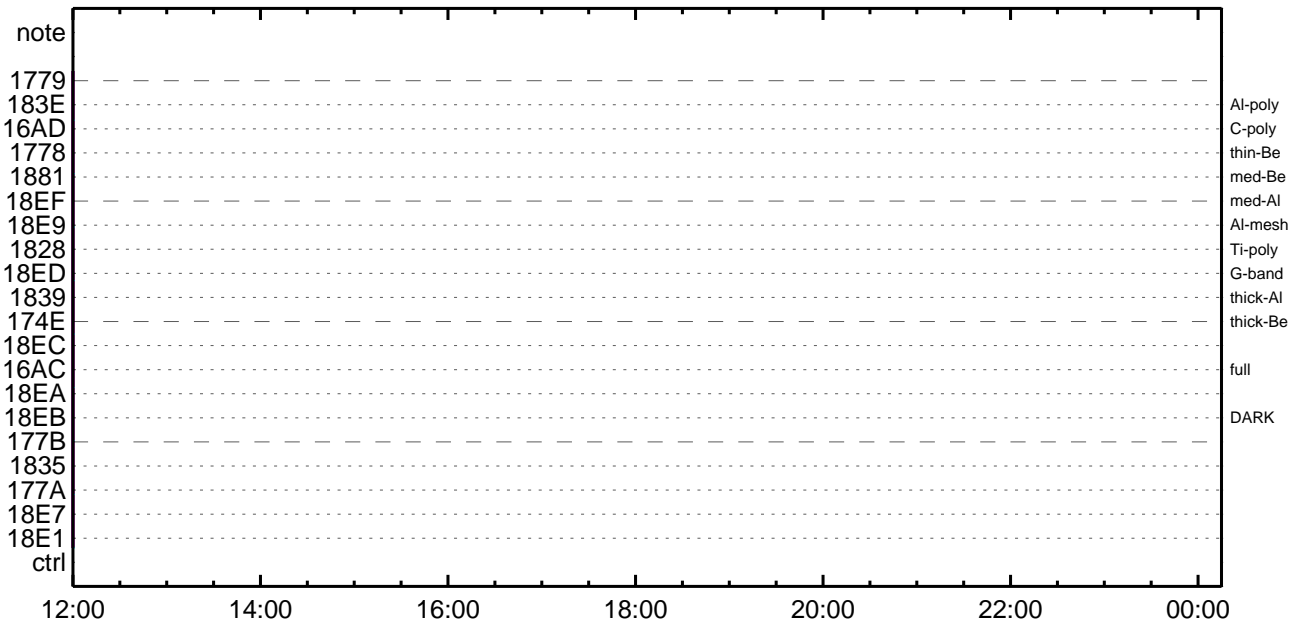
CMDI #0648 2012/06/10



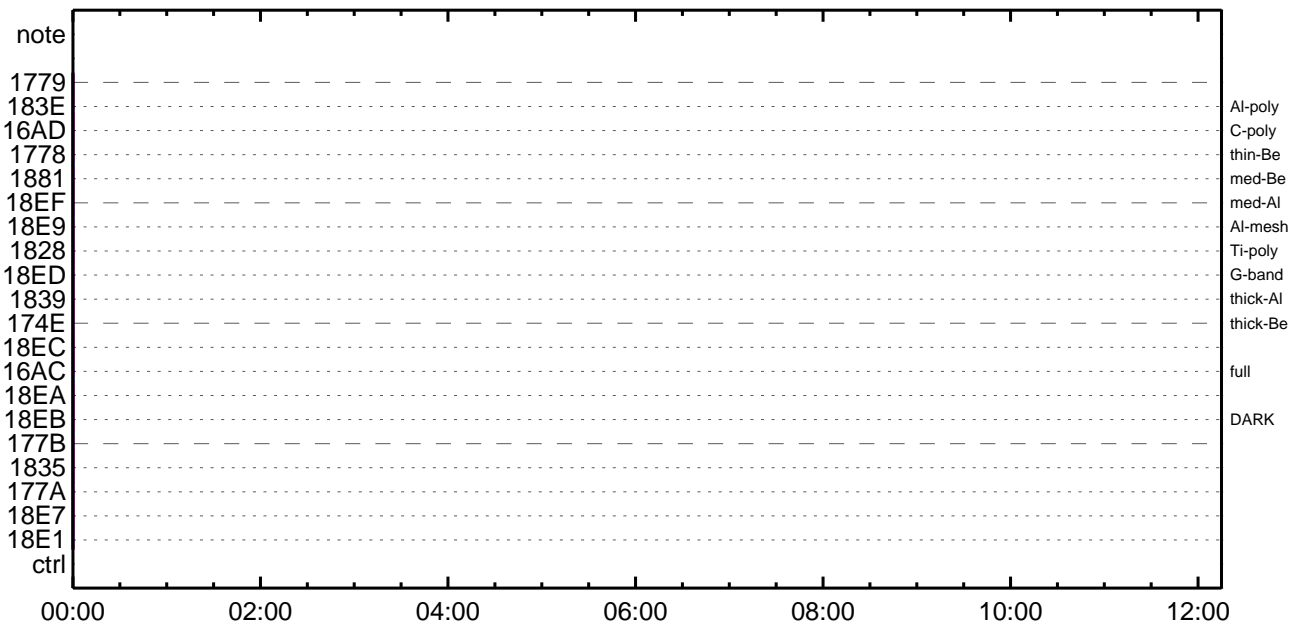
CMDI #0648 2012/06/11



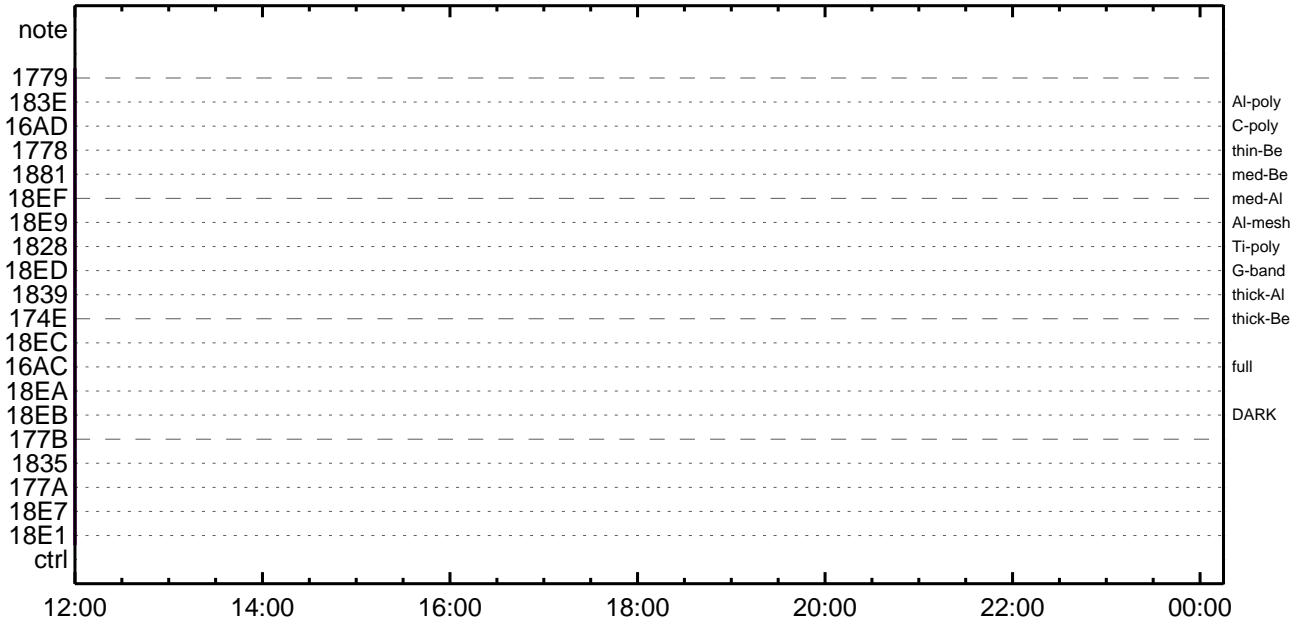
CMDI #0648 2012/06/11



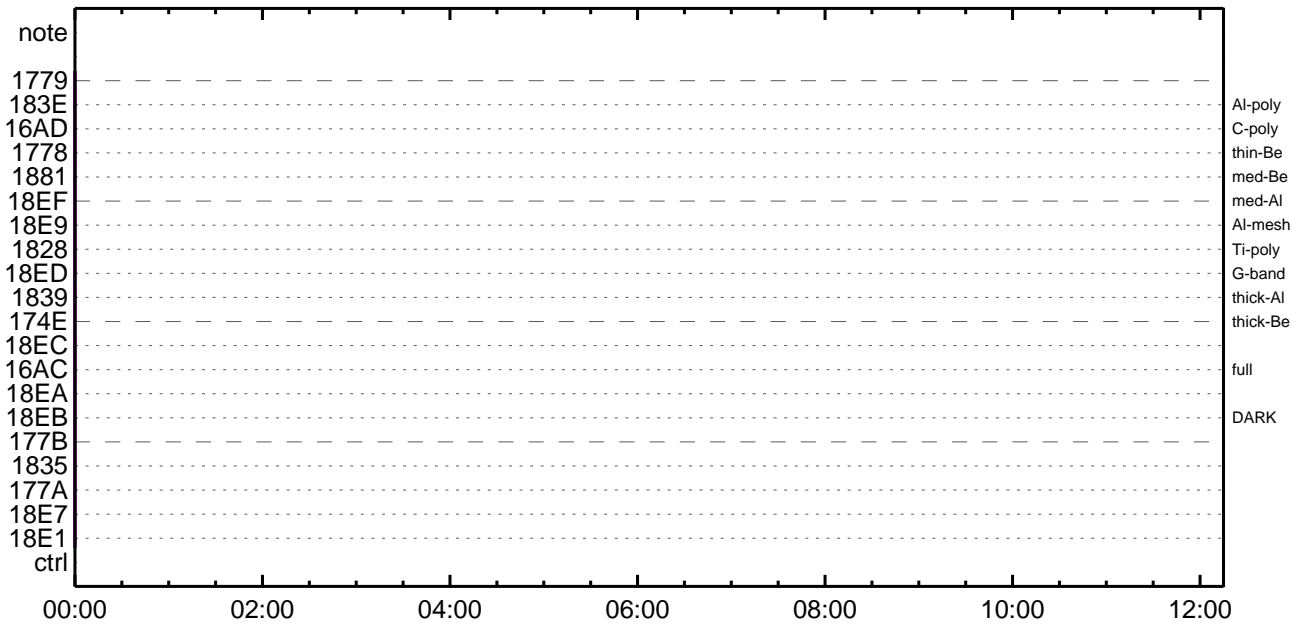
CMDI #0648 2012/06/12



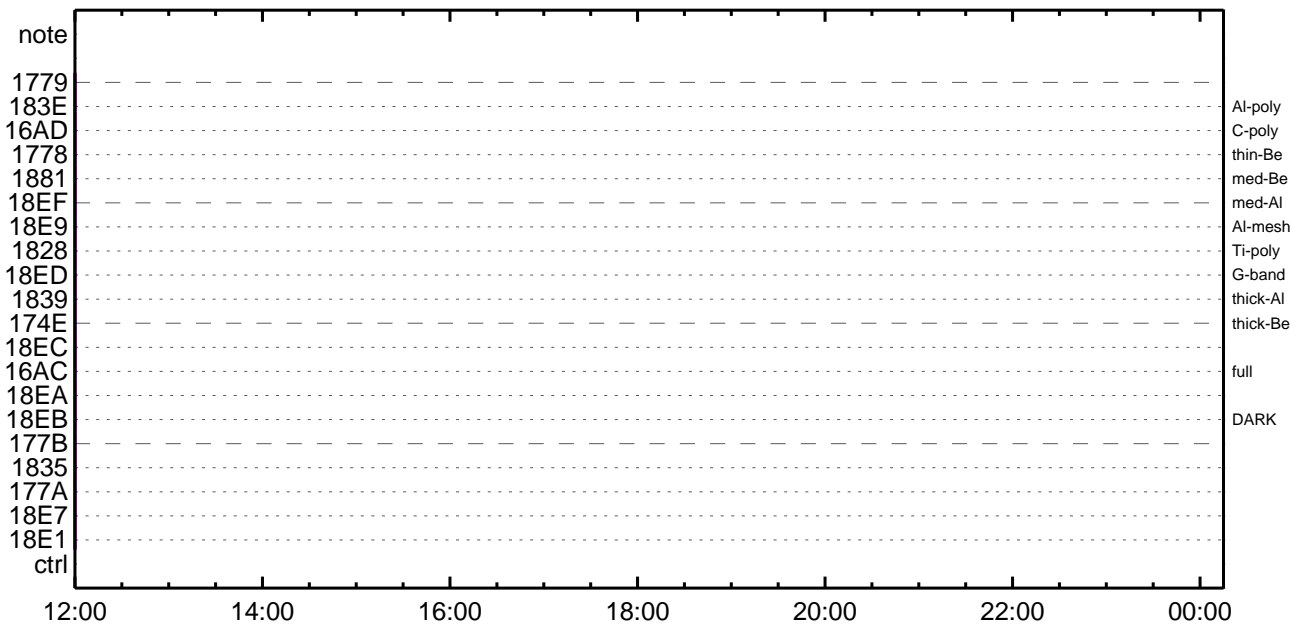
CMDI #0648 2012/06/12



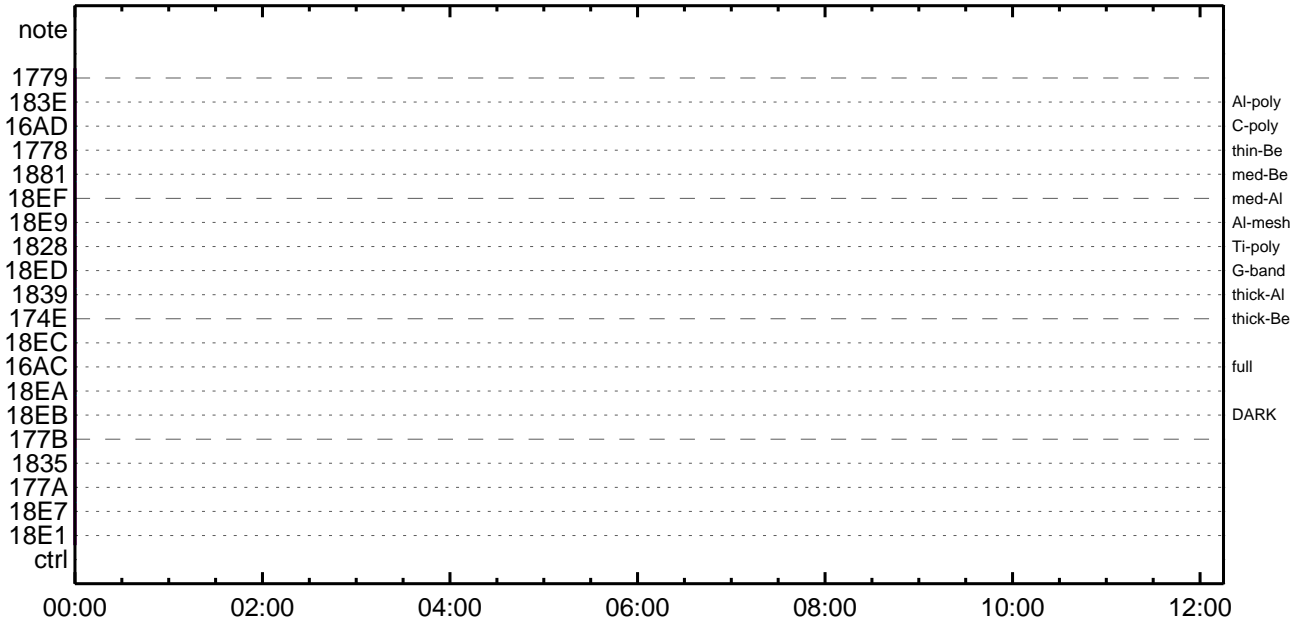
CMDI #0648 2012/06/13



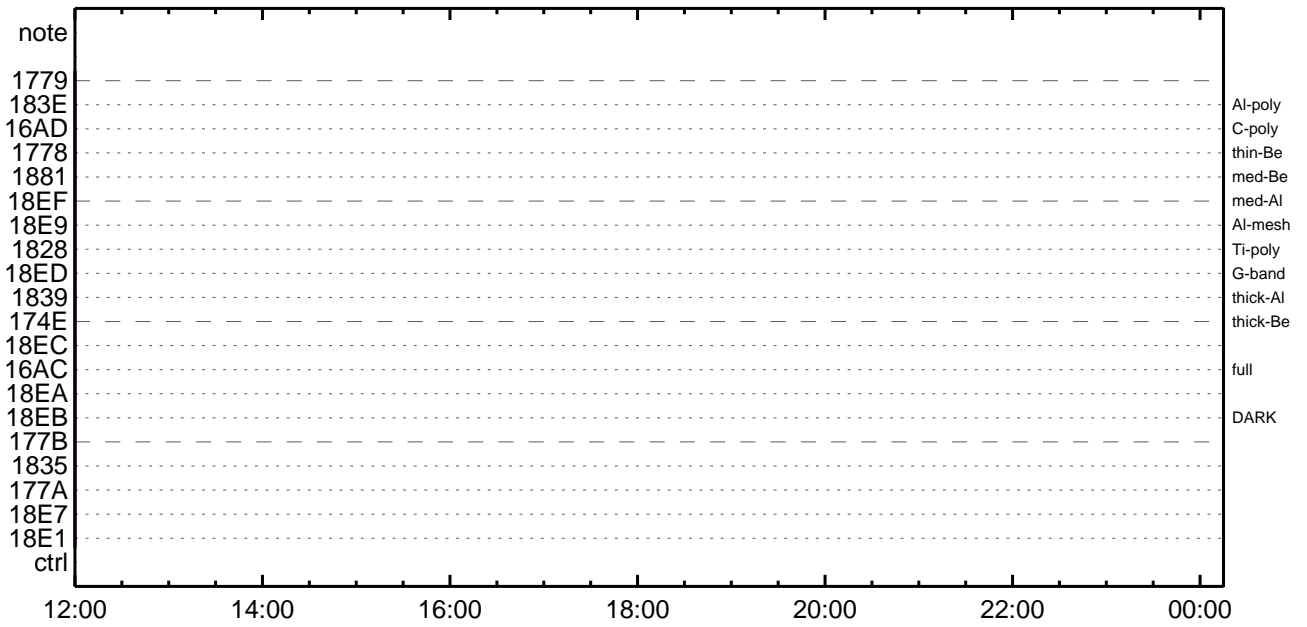
CMDI #0648 2012/06/13



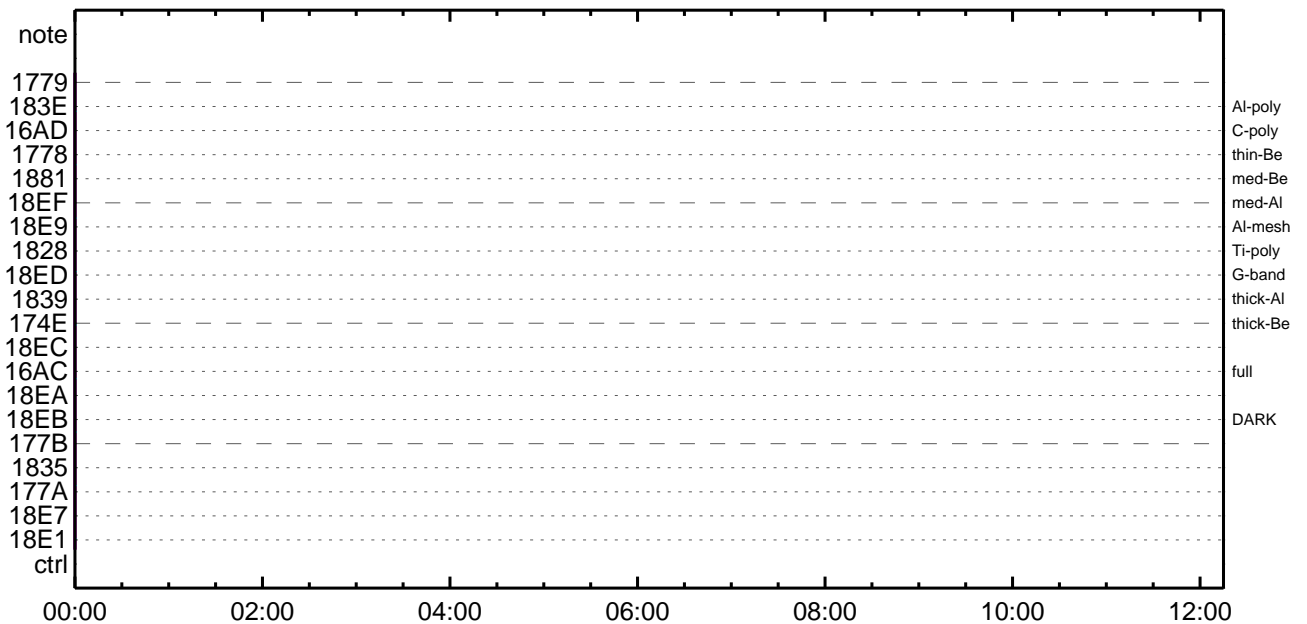
CMDI #0648 2012/06/14



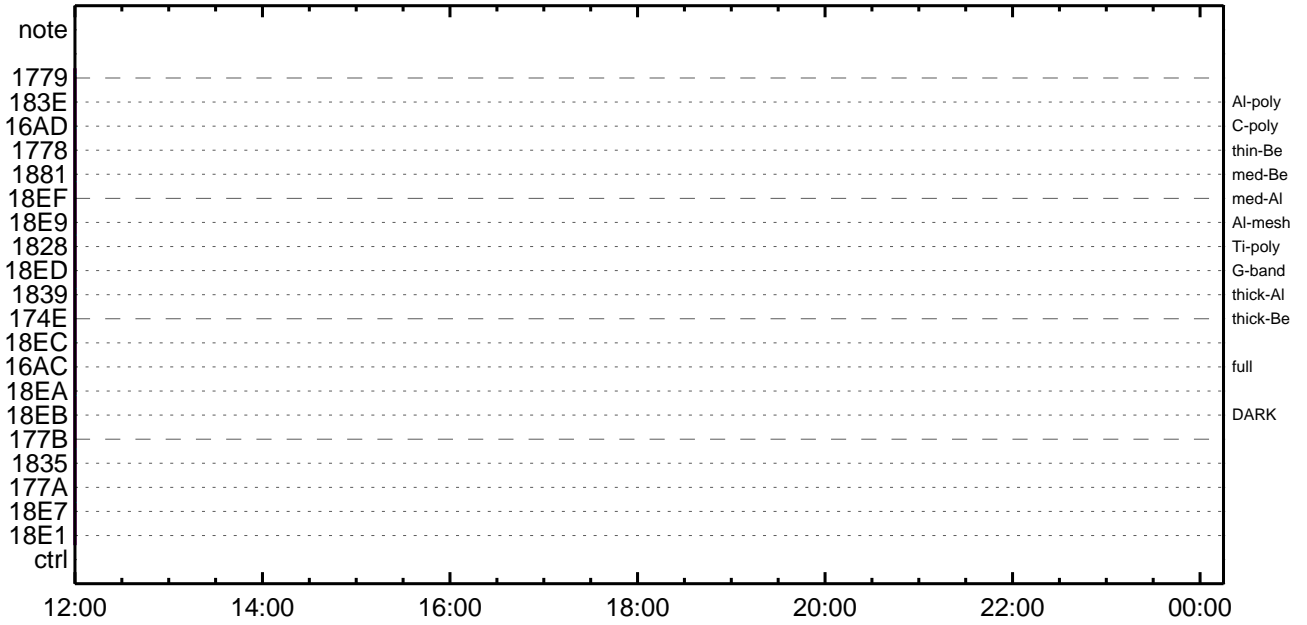
CMDI #0648 2012/06/14



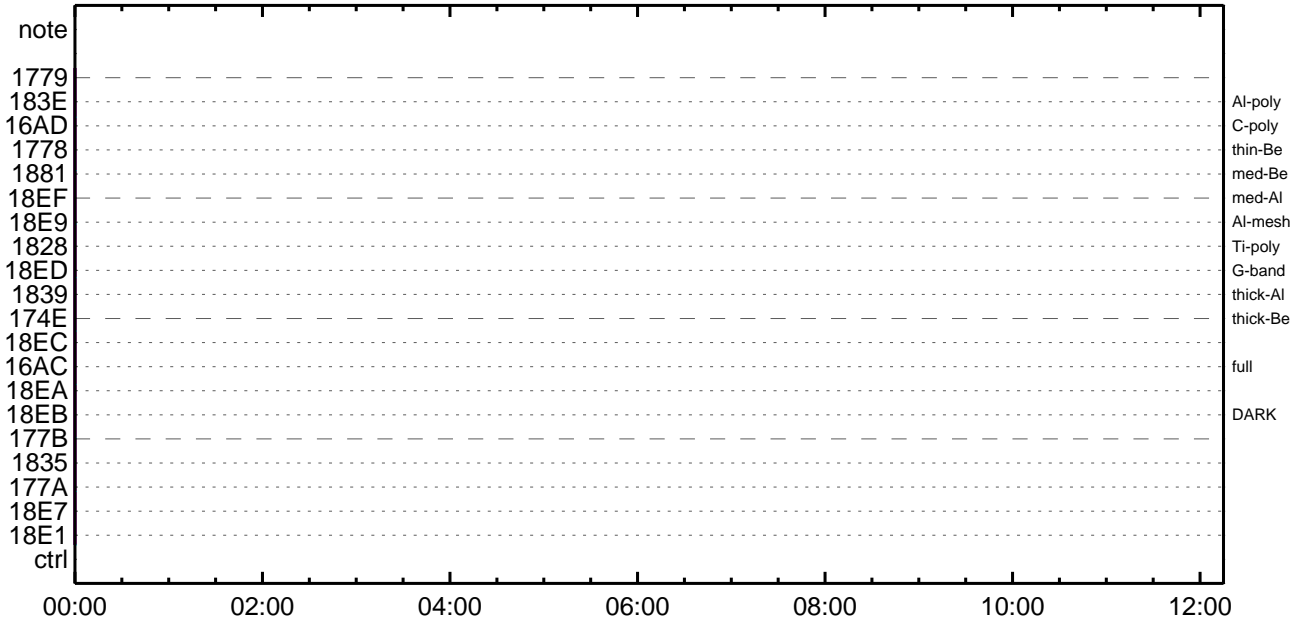
CMDI #0648 2012/06/15



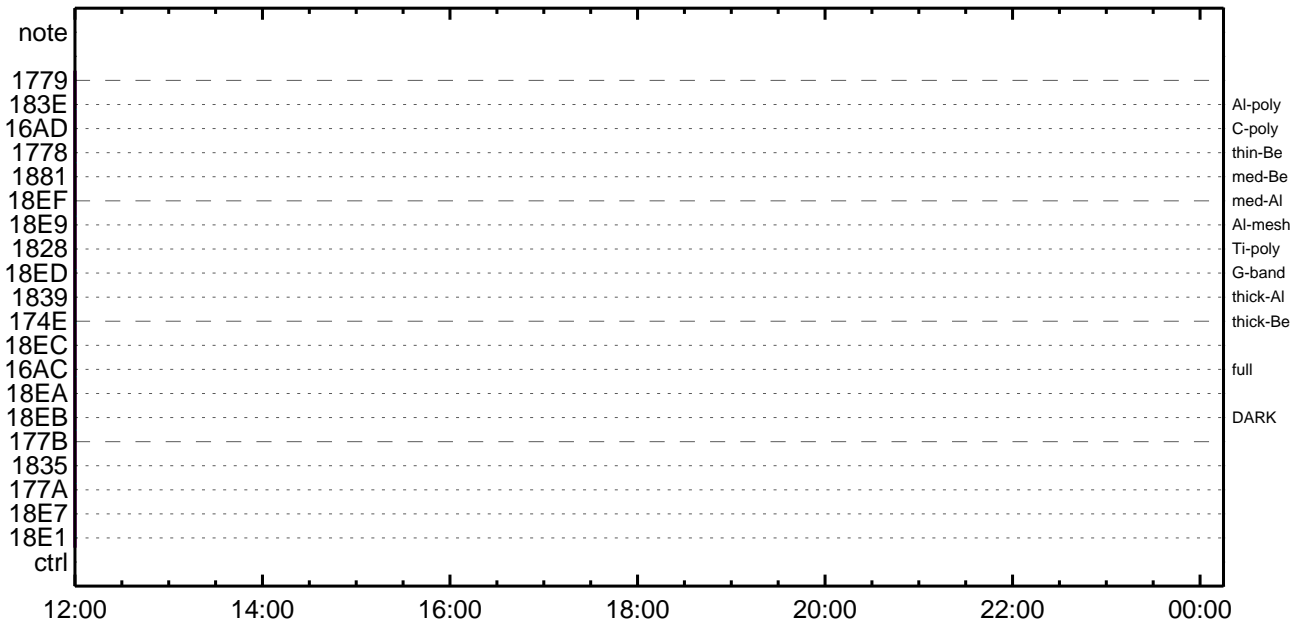
CMDI #0648 2012/06/15



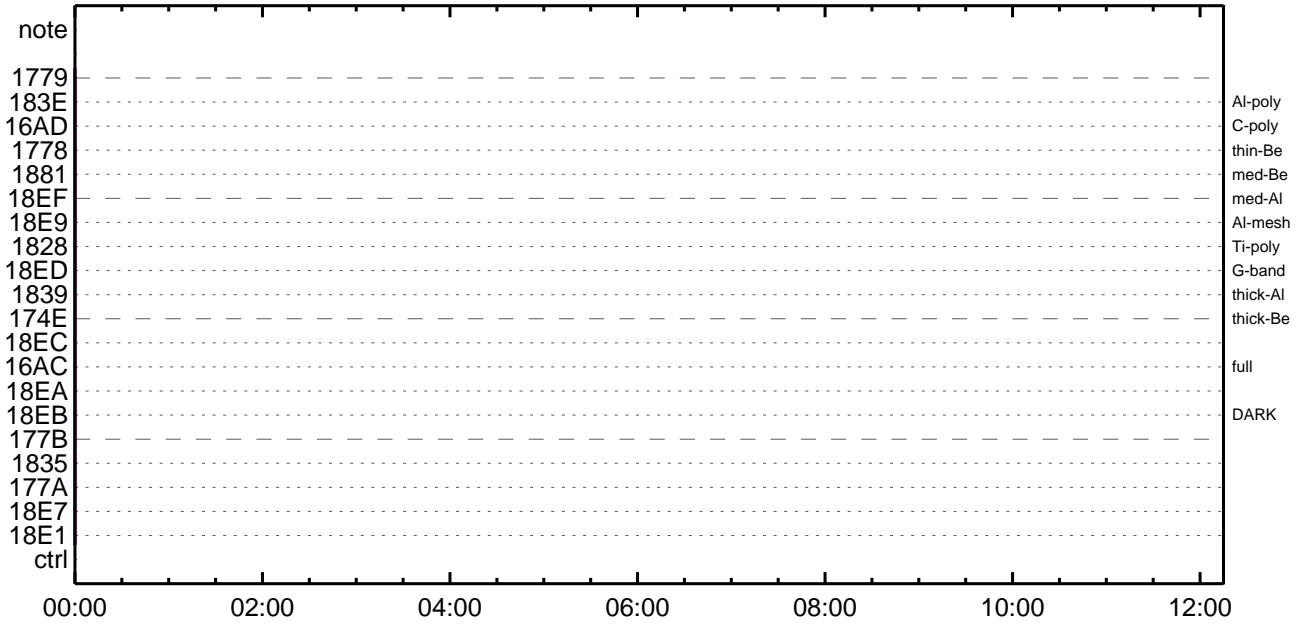
CMDI #0648 2012/06/16



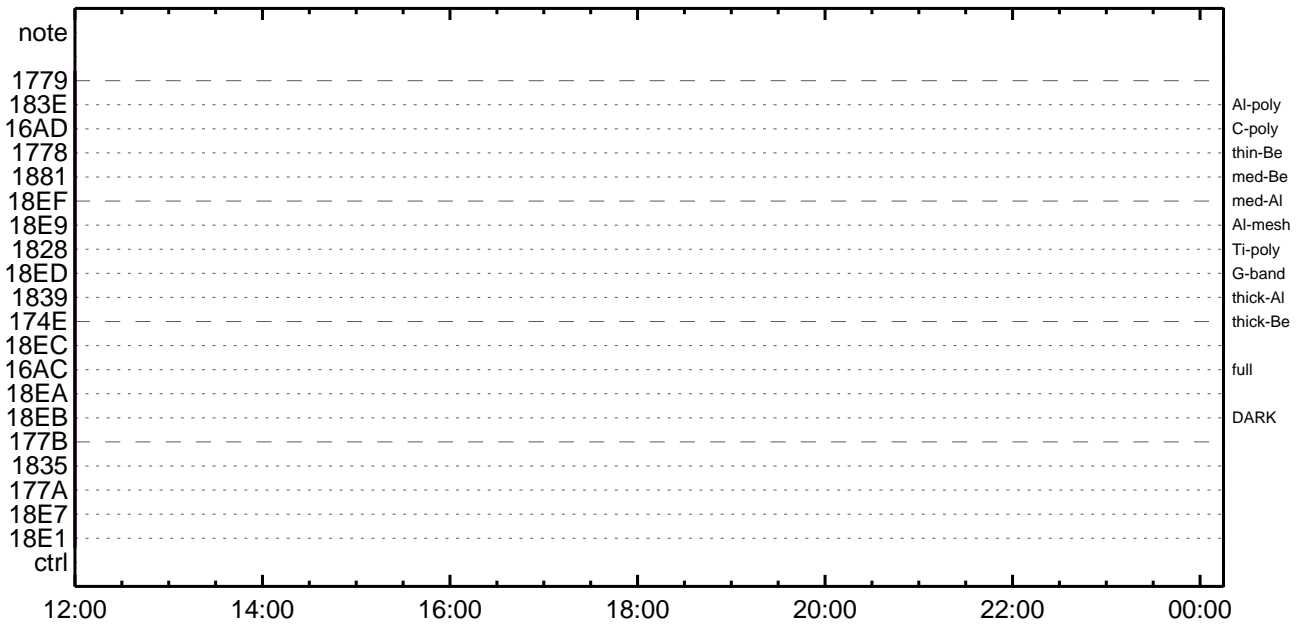
CMDI #0648 2012/06/16



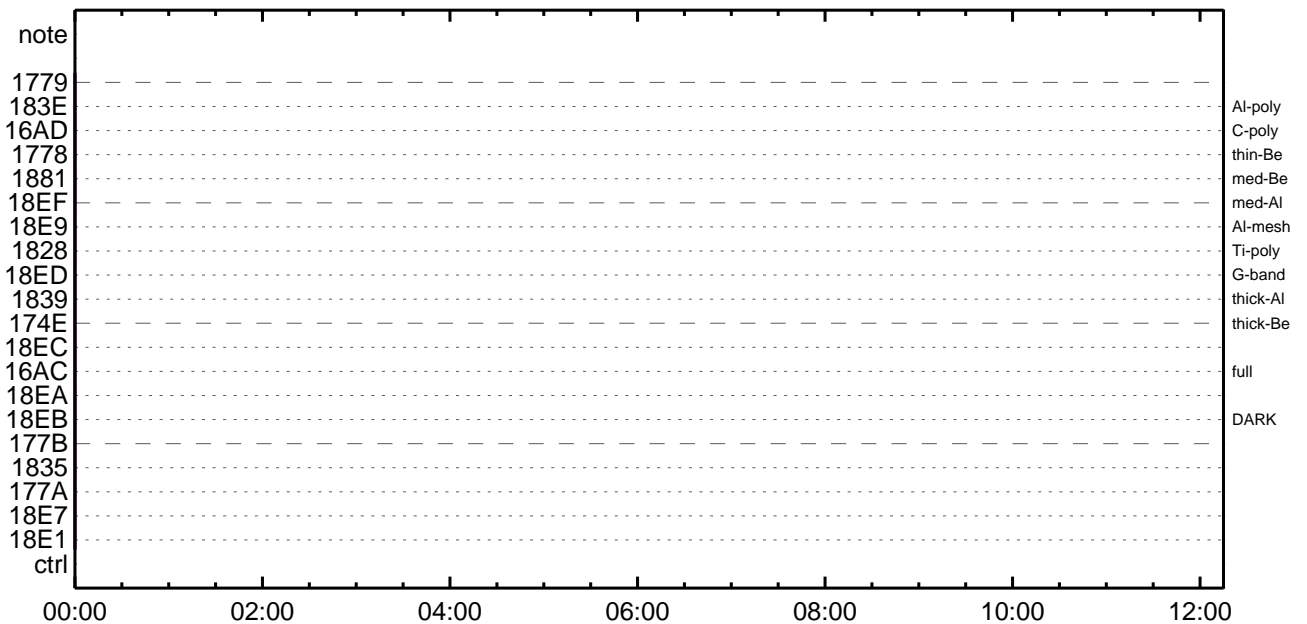
CMDI #0648 2012/06/17



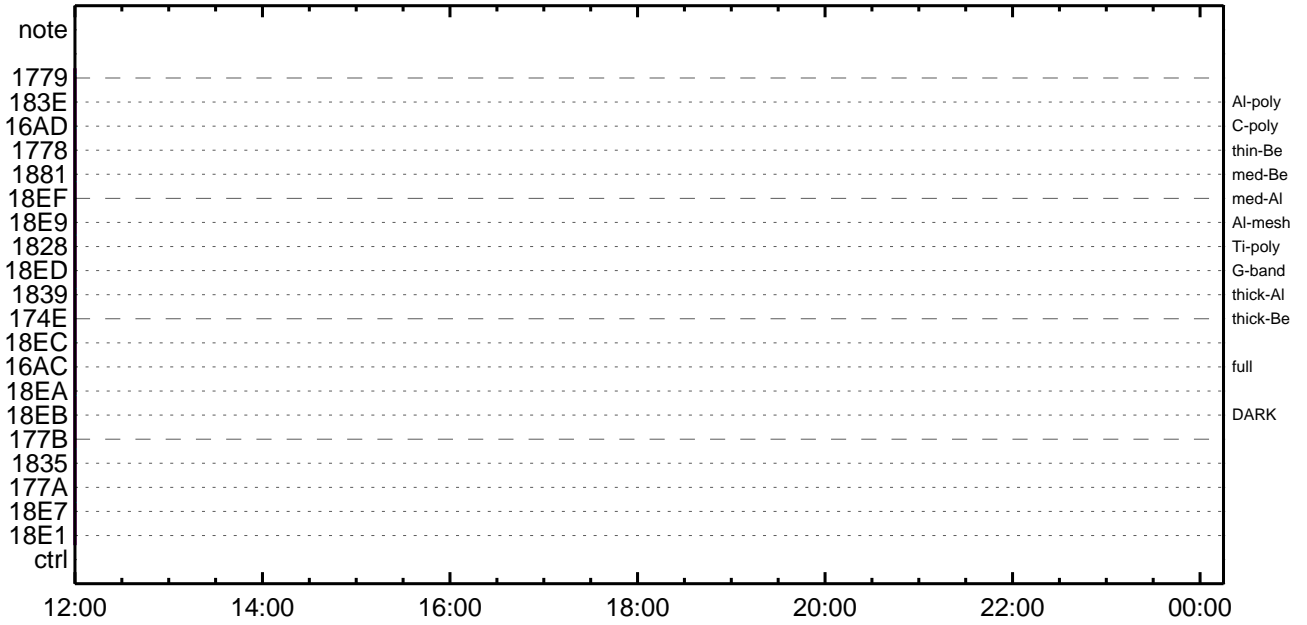
CMDI #0648 2012/06/17



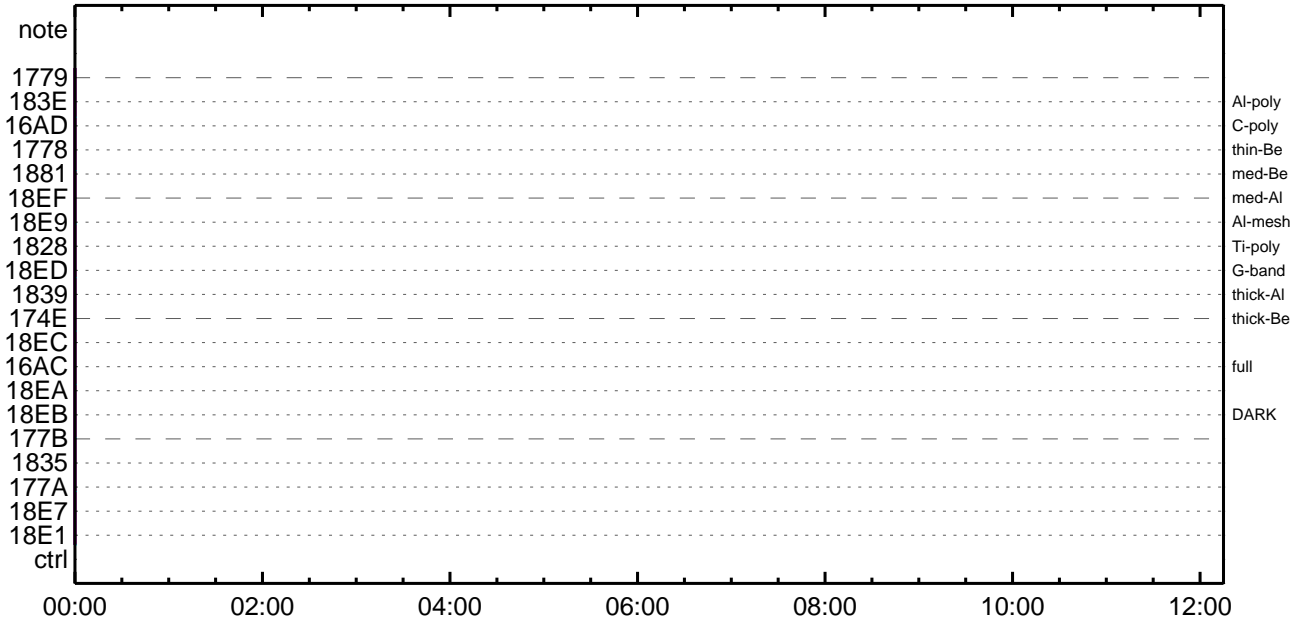
CMDI #0648 2012/06/18



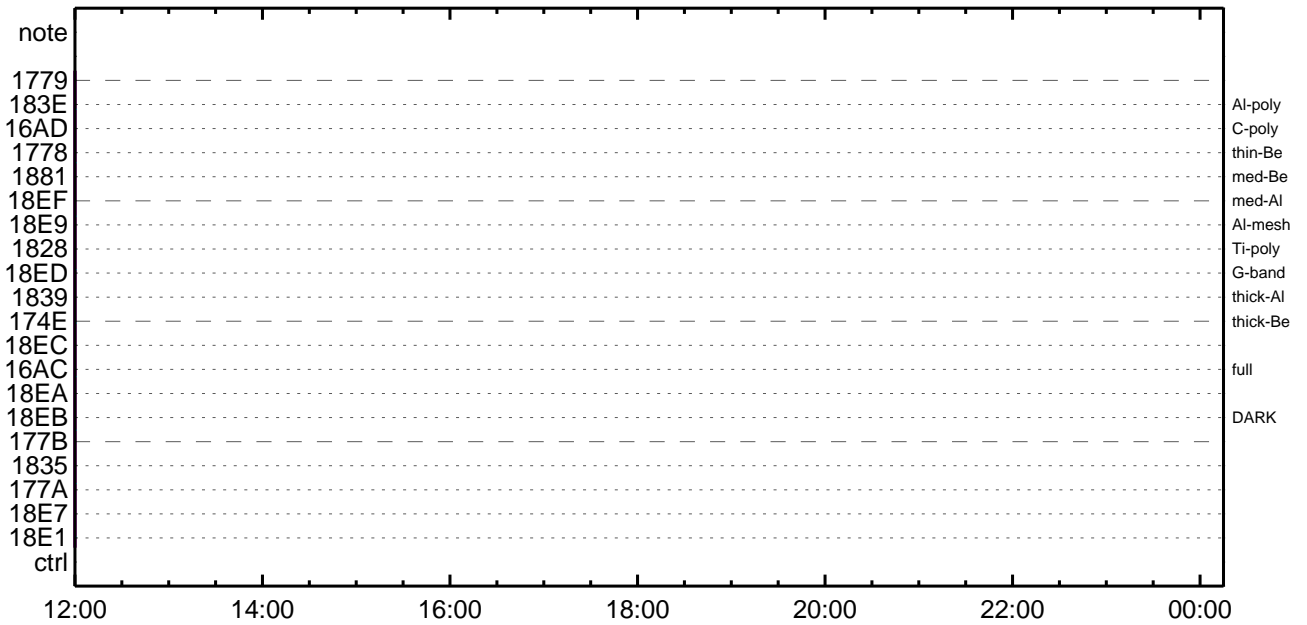
CMDI #0648 2012/06/18



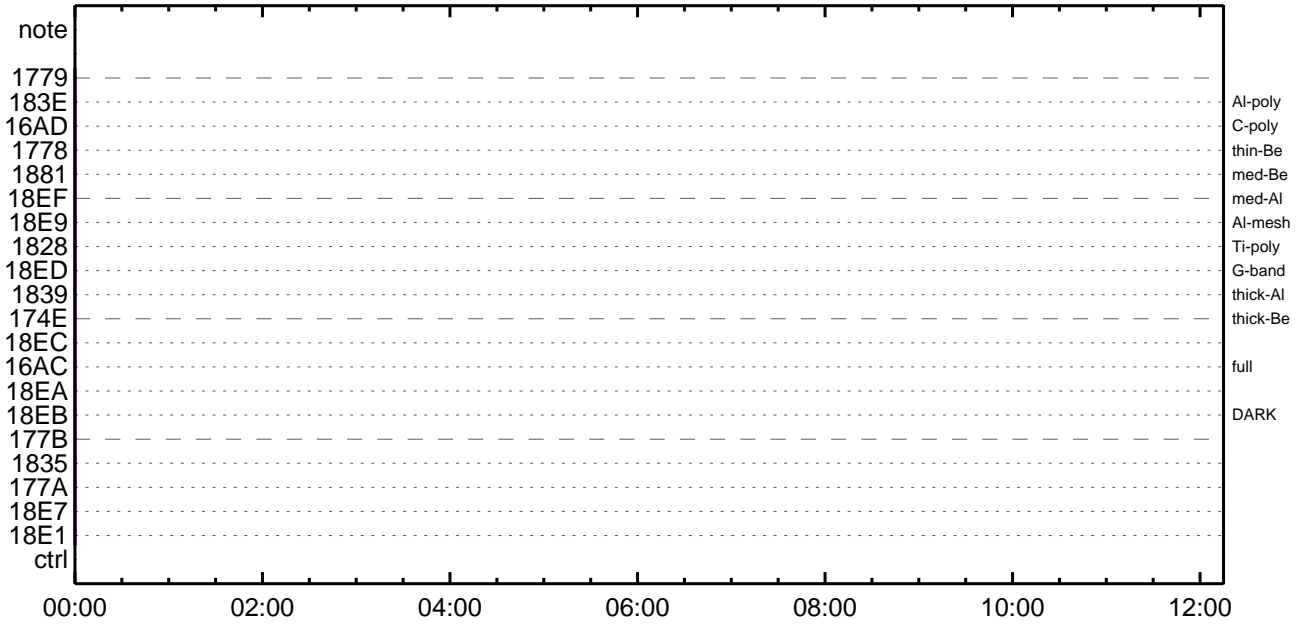
CMDI #0648 2012/06/19



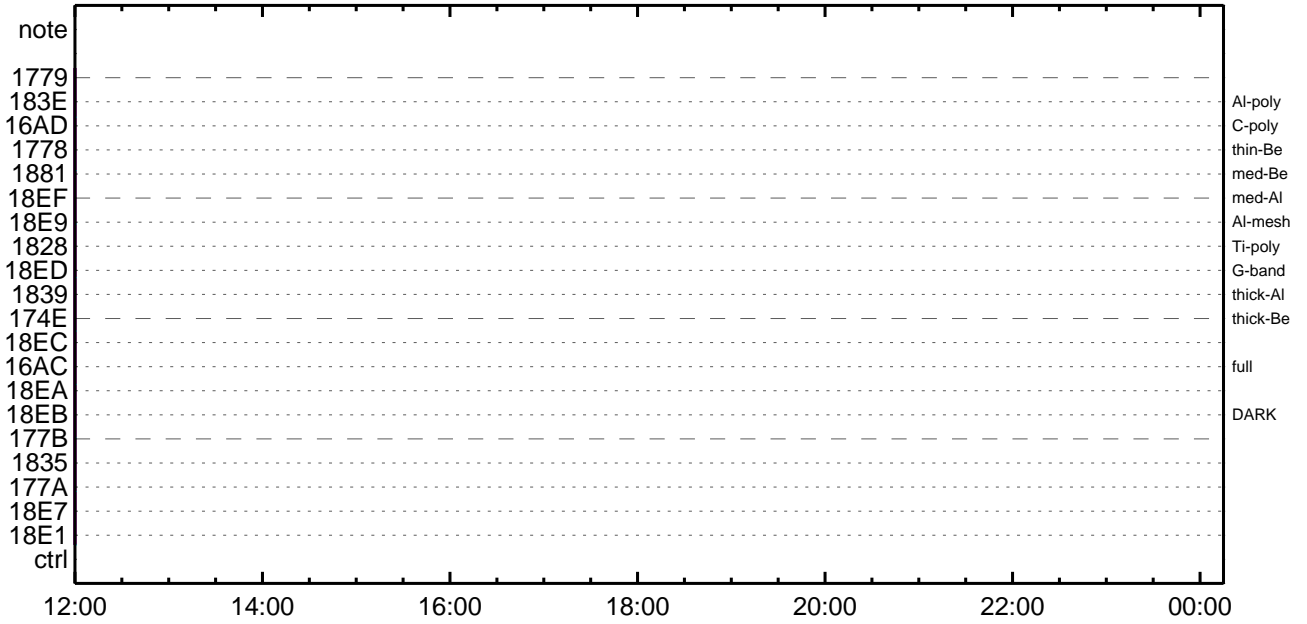
CMDI #0648 2012/06/19



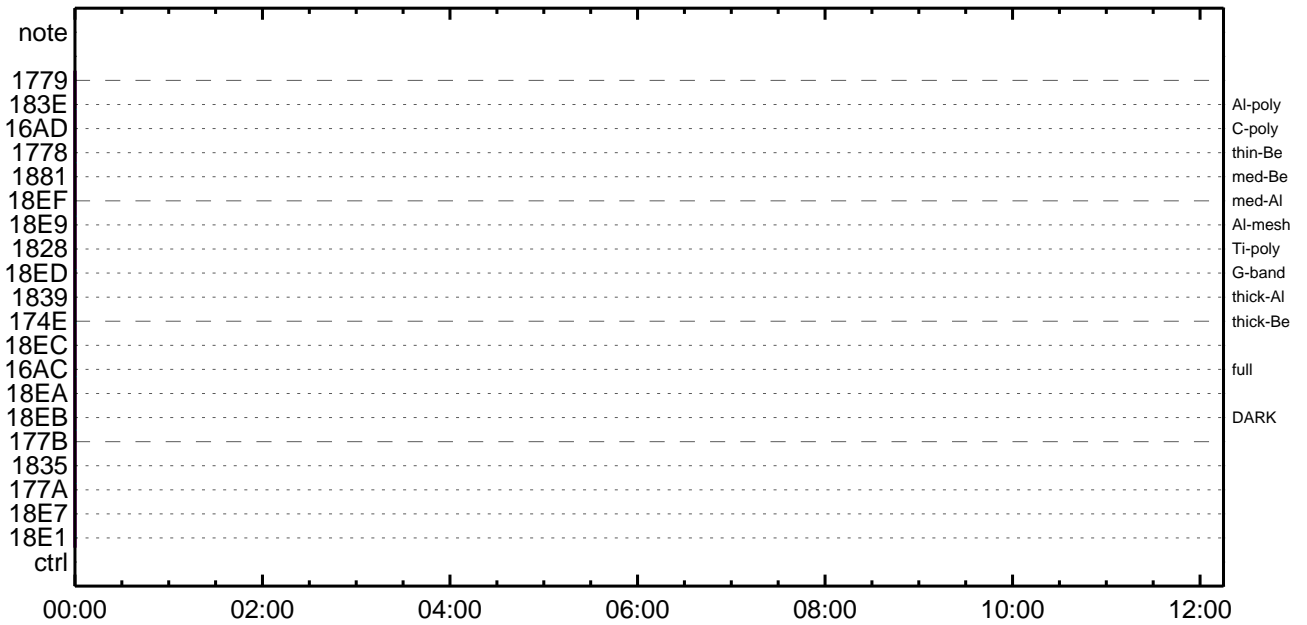
CMDI #0648 2012/06/20



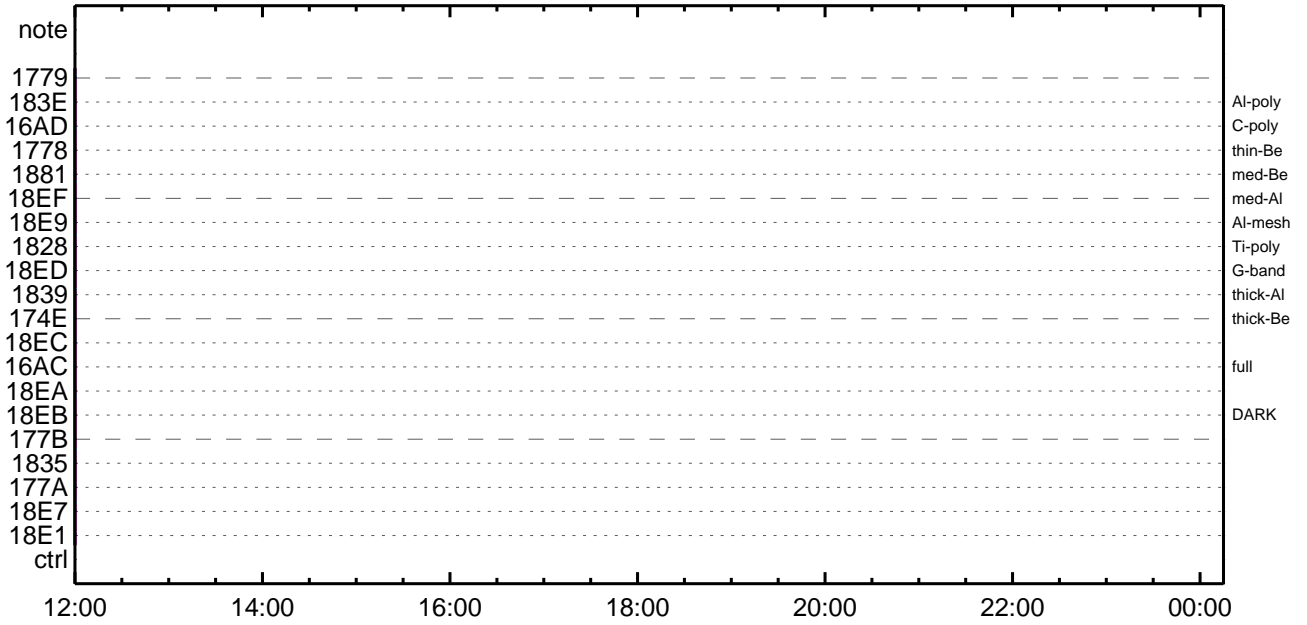
CMDI #0648 2012/06/20



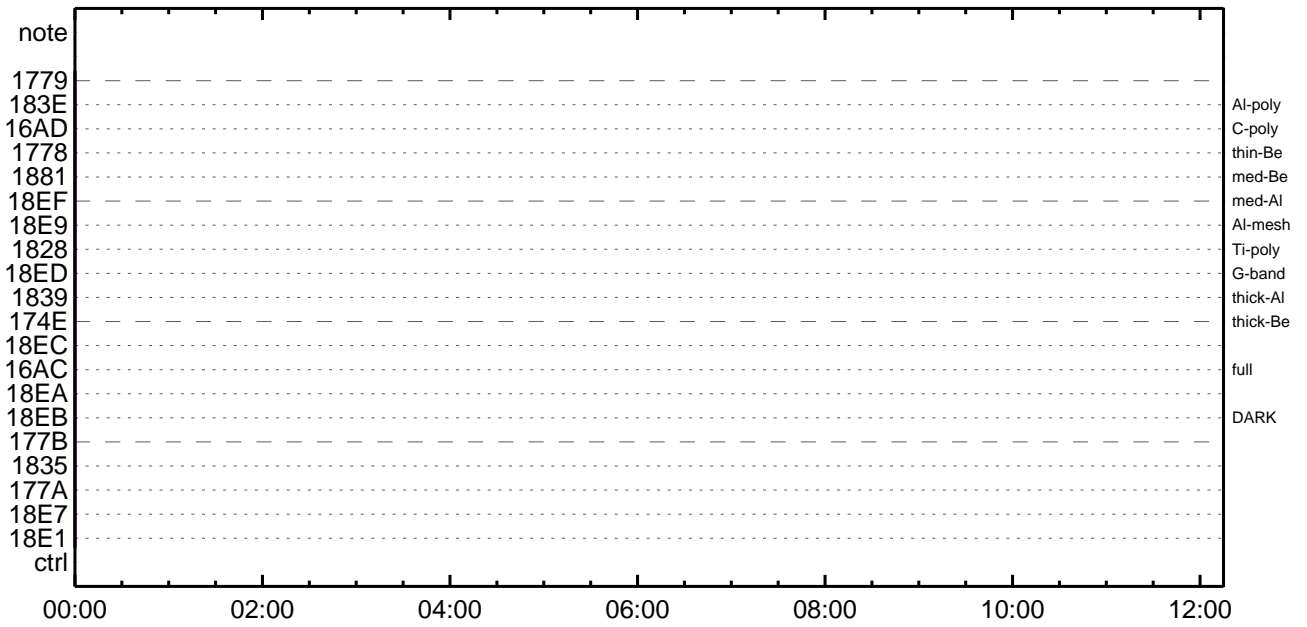
CMDI #0648 2012/06/21



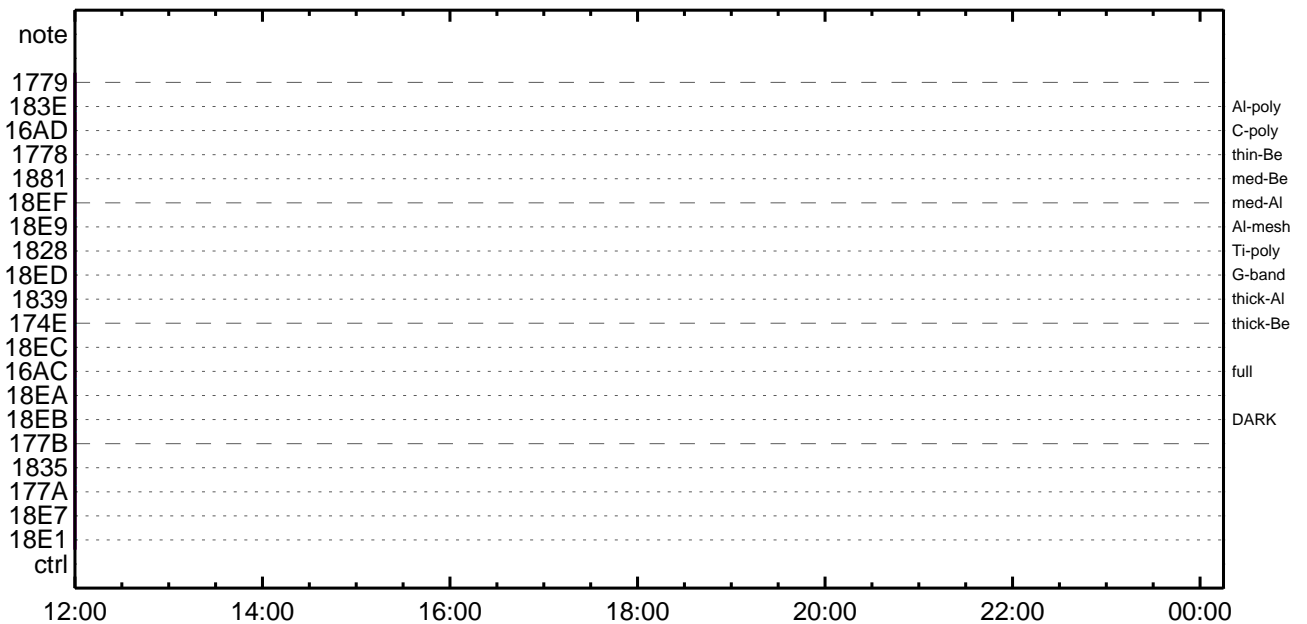
CMDI #0648 2012/06/21



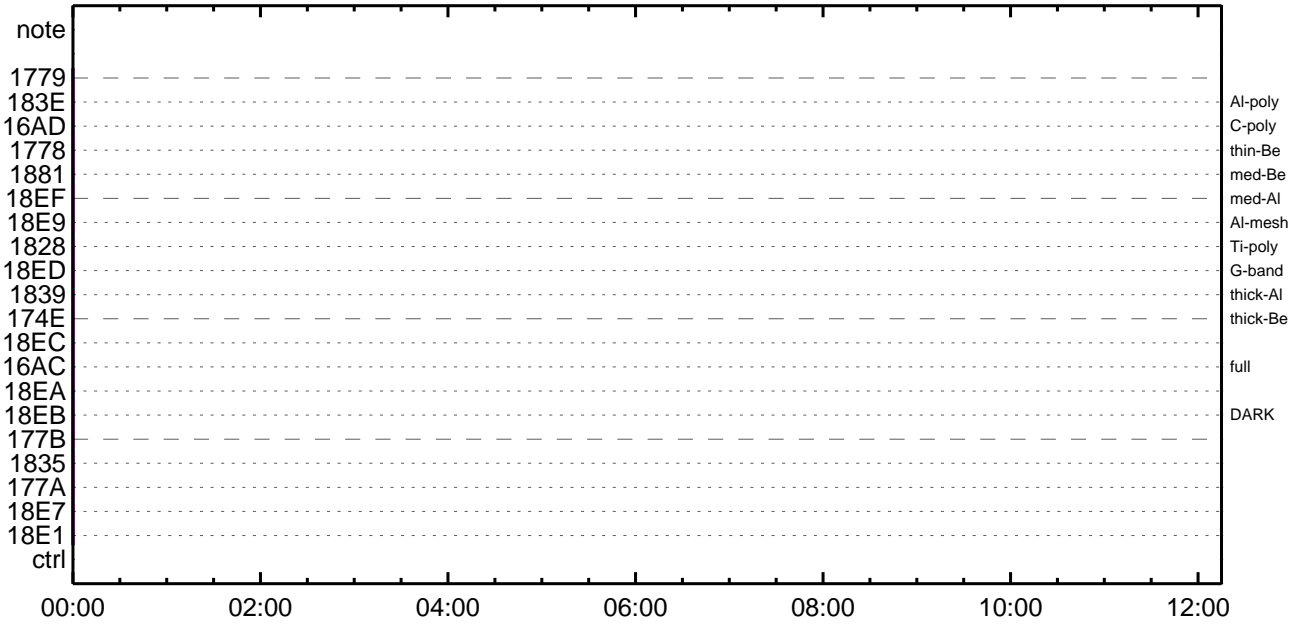
CMDI #0648 2012/06/22



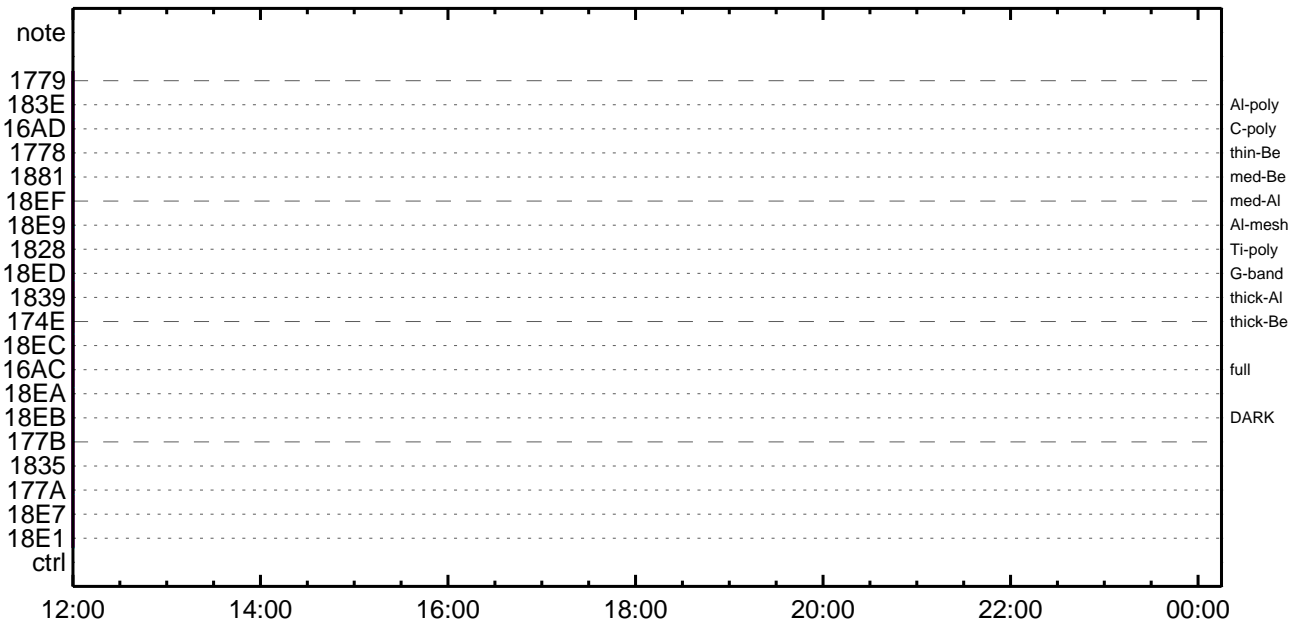
CMDI #0648 2012/06/22



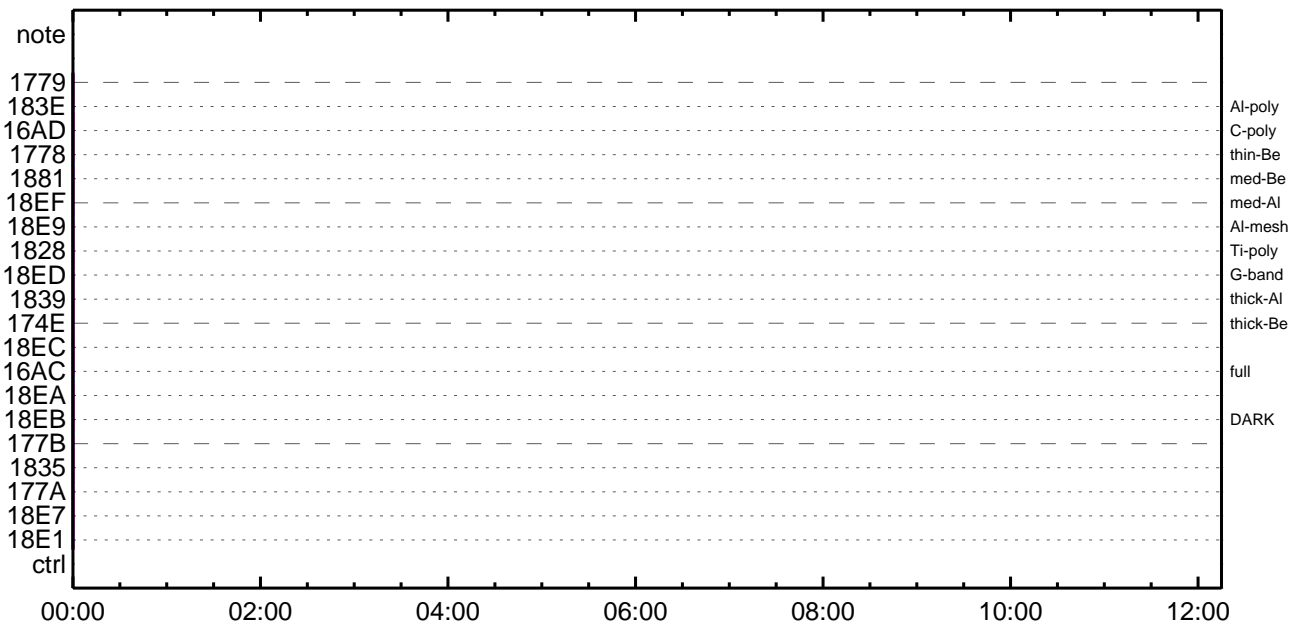
CMDI #0648 2012/06/23



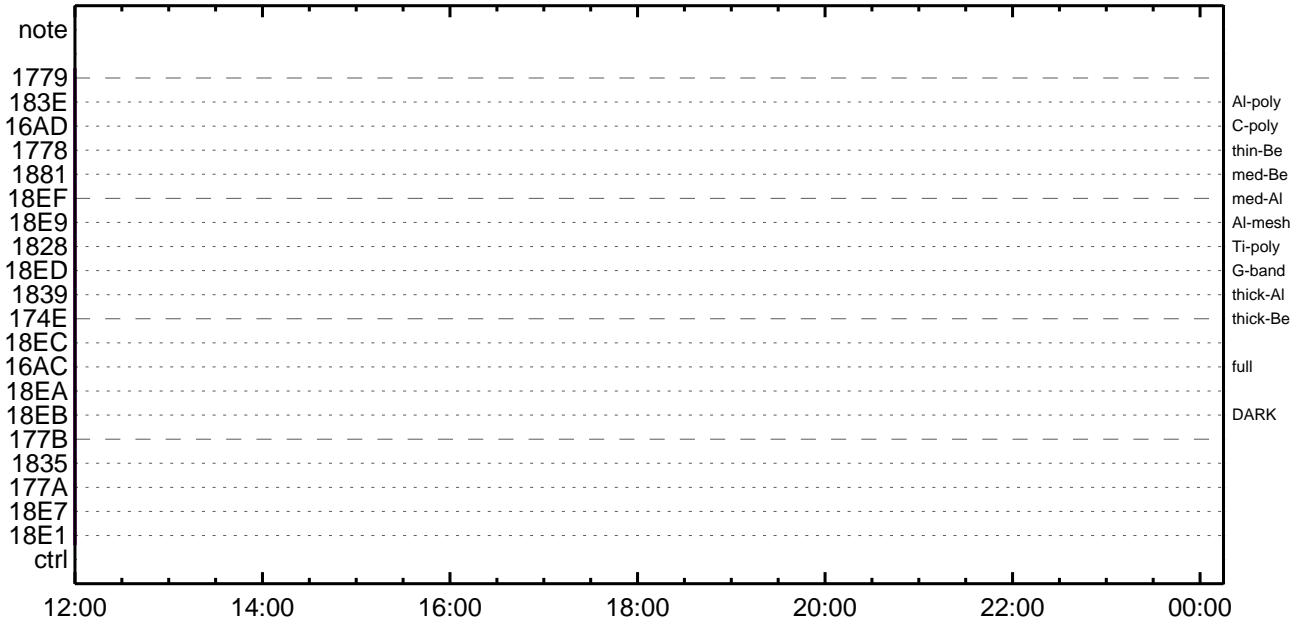
CMDI #0648 2012/06/23



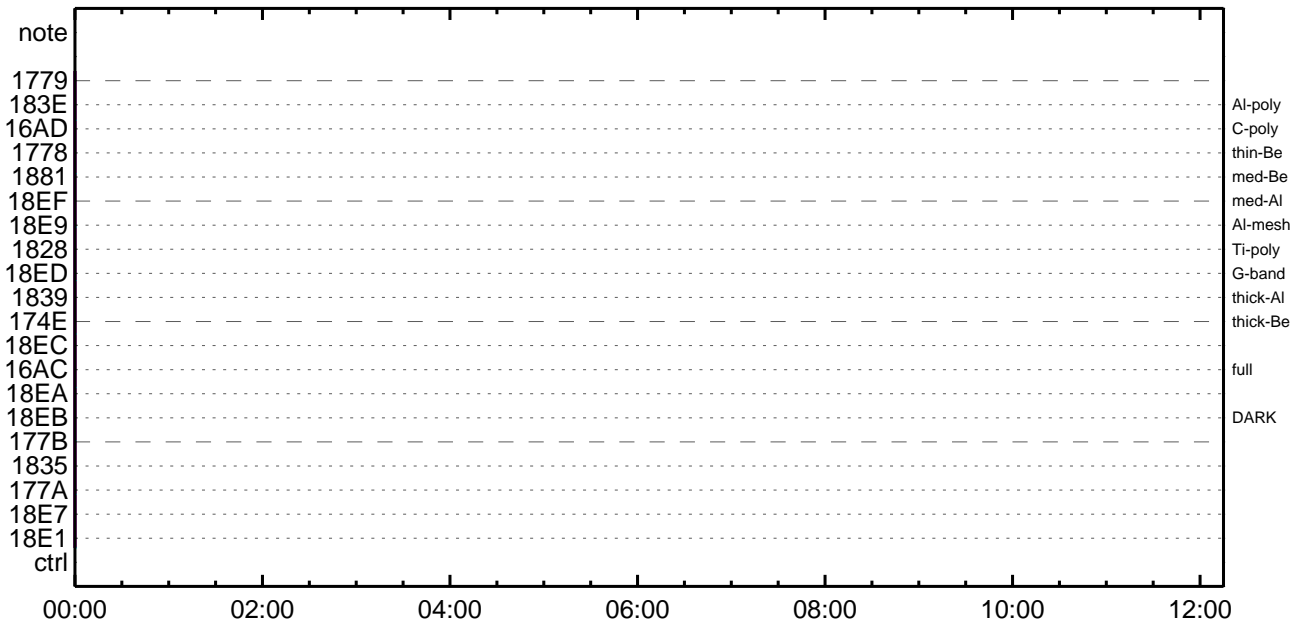
CMDI #0648 2012/06/24



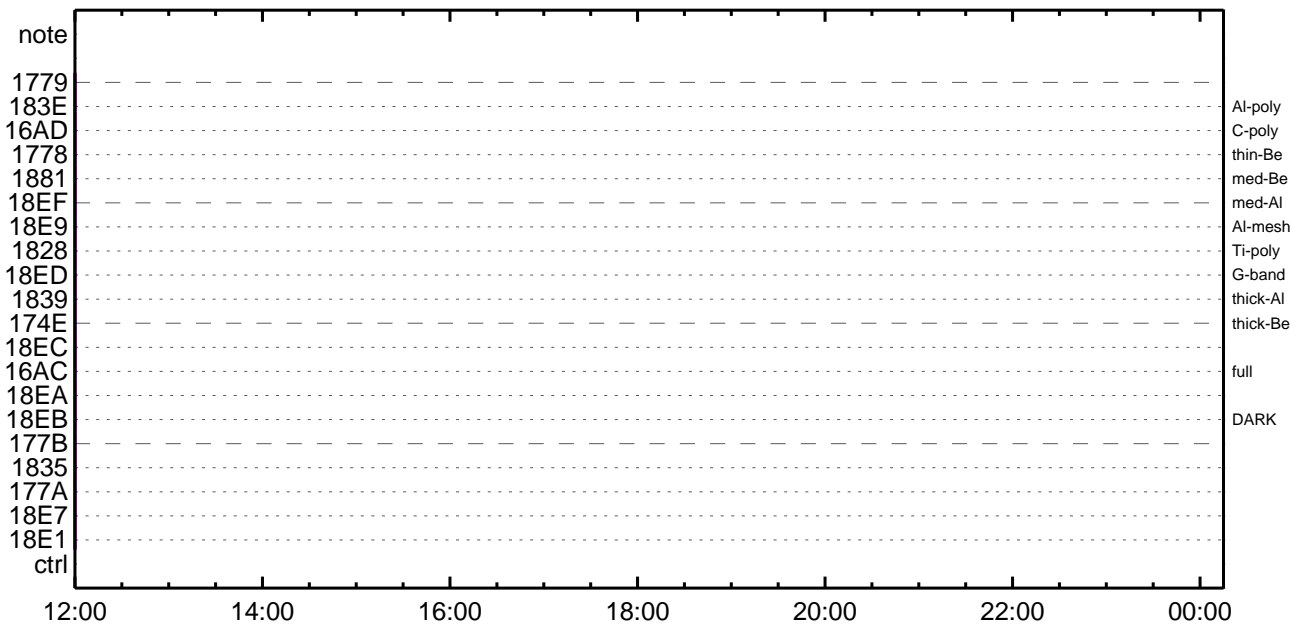
CMDI #0648 2012/06/24



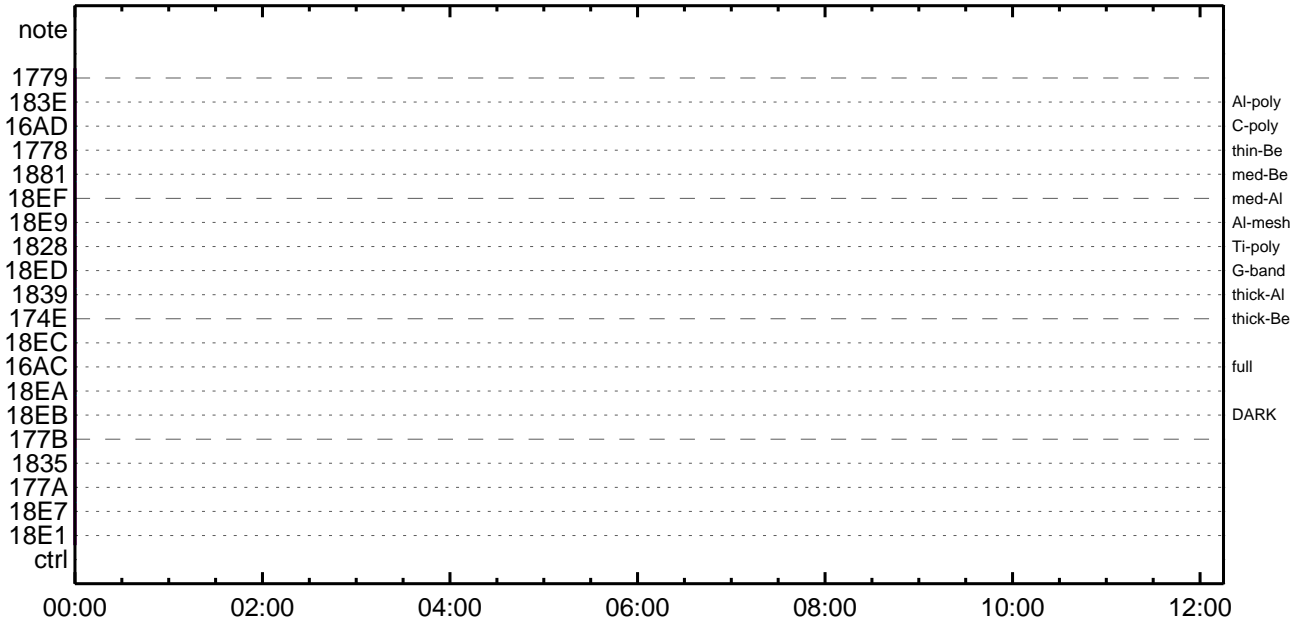
CMDI #0648 2012/06/25



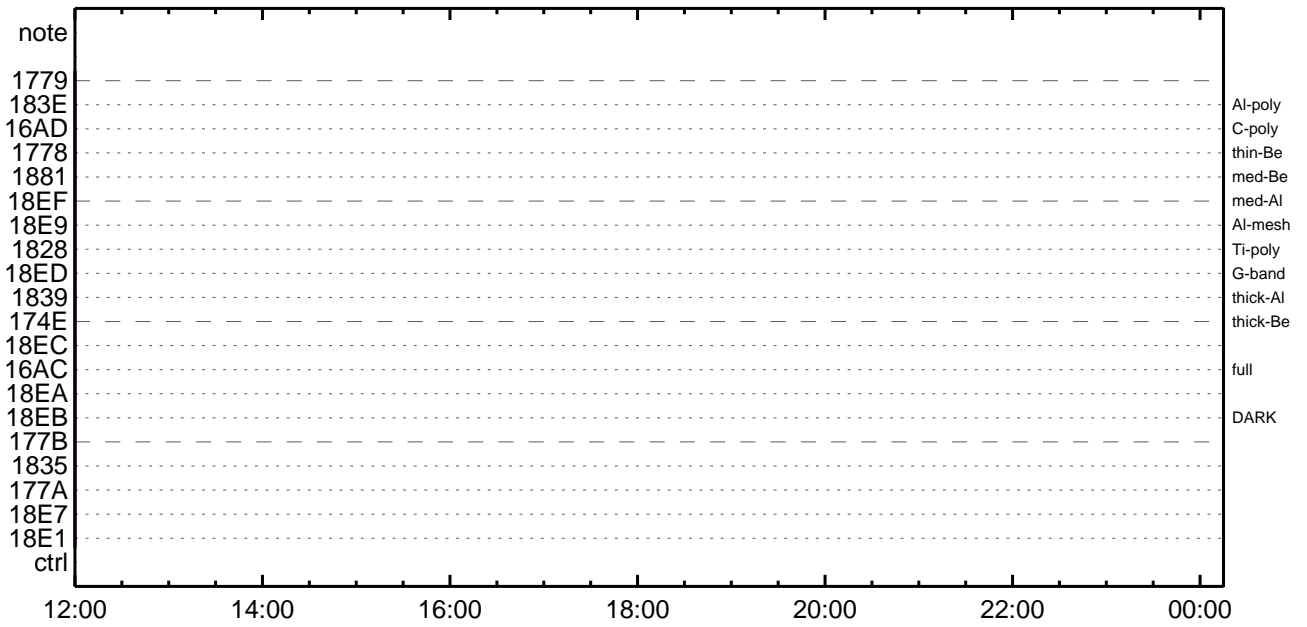
CMDI #0648 2012/06/25



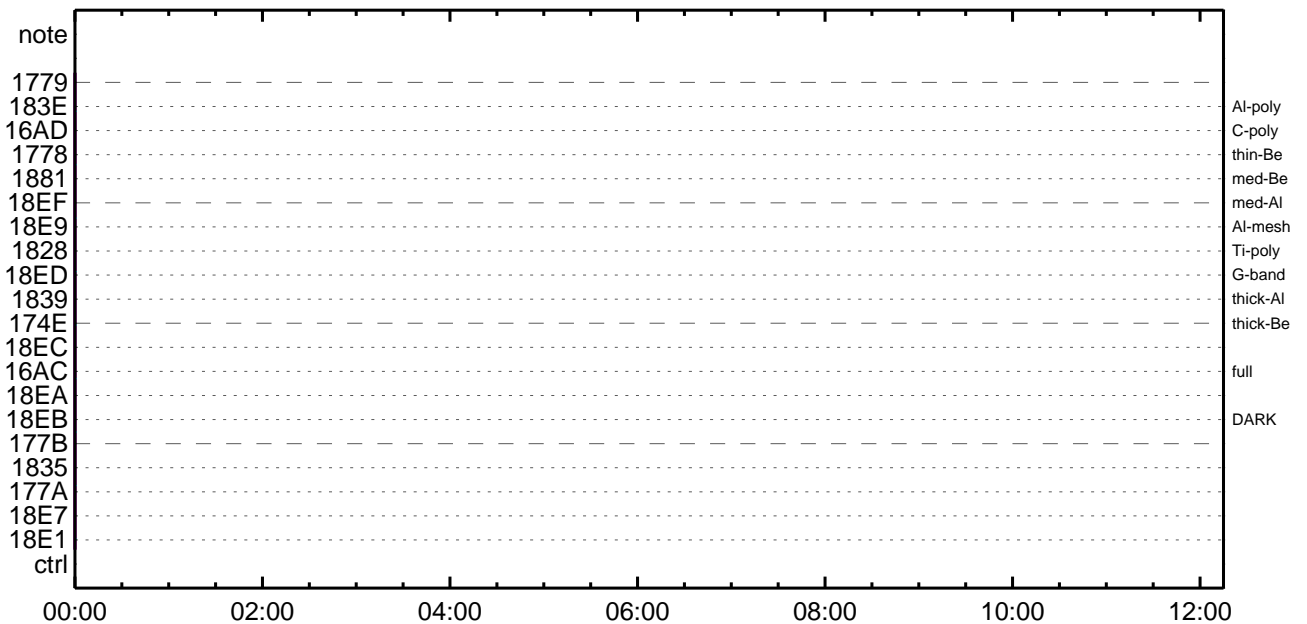
CMDI #0648 2012/06/26



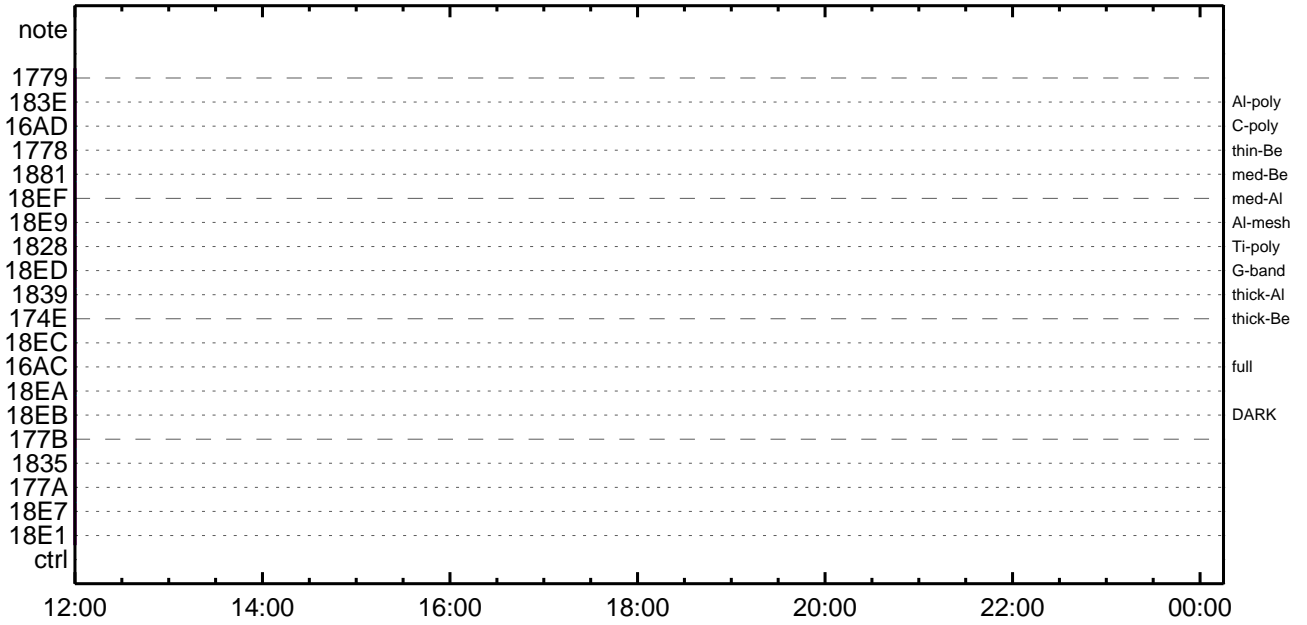
CMDI #0648 2012/06/26



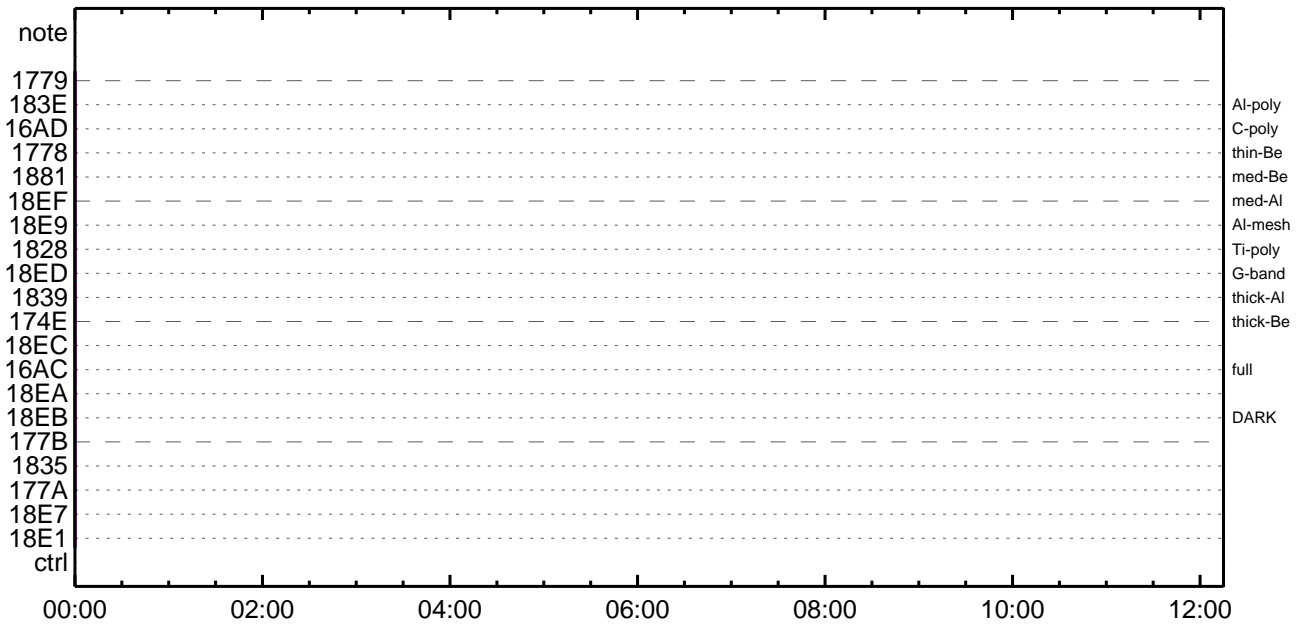
CMDI #0648 2012/06/27



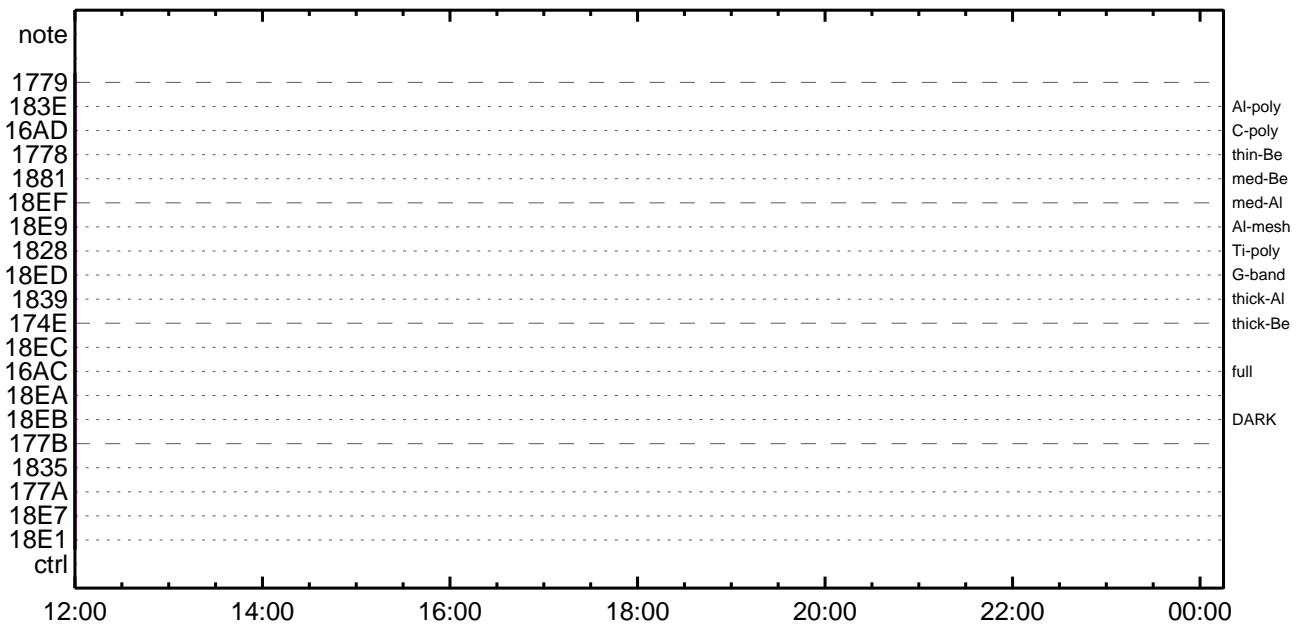
CMDI #0648 2012/06/27



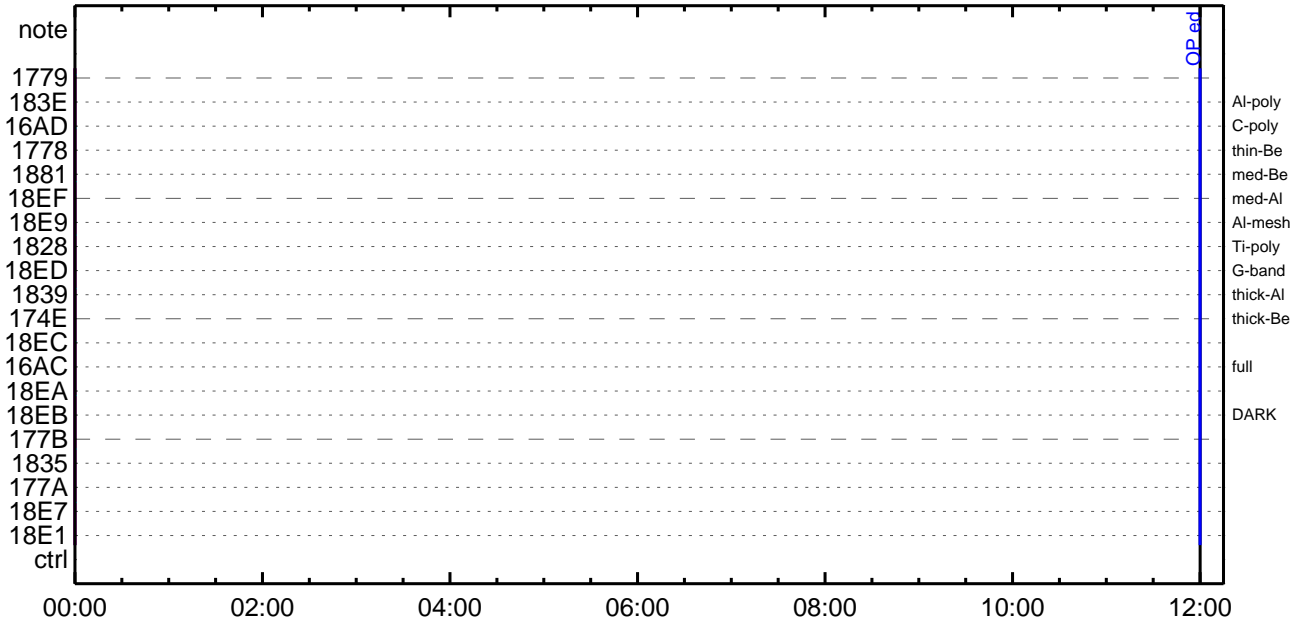
CMDI #0648 2012/06/28



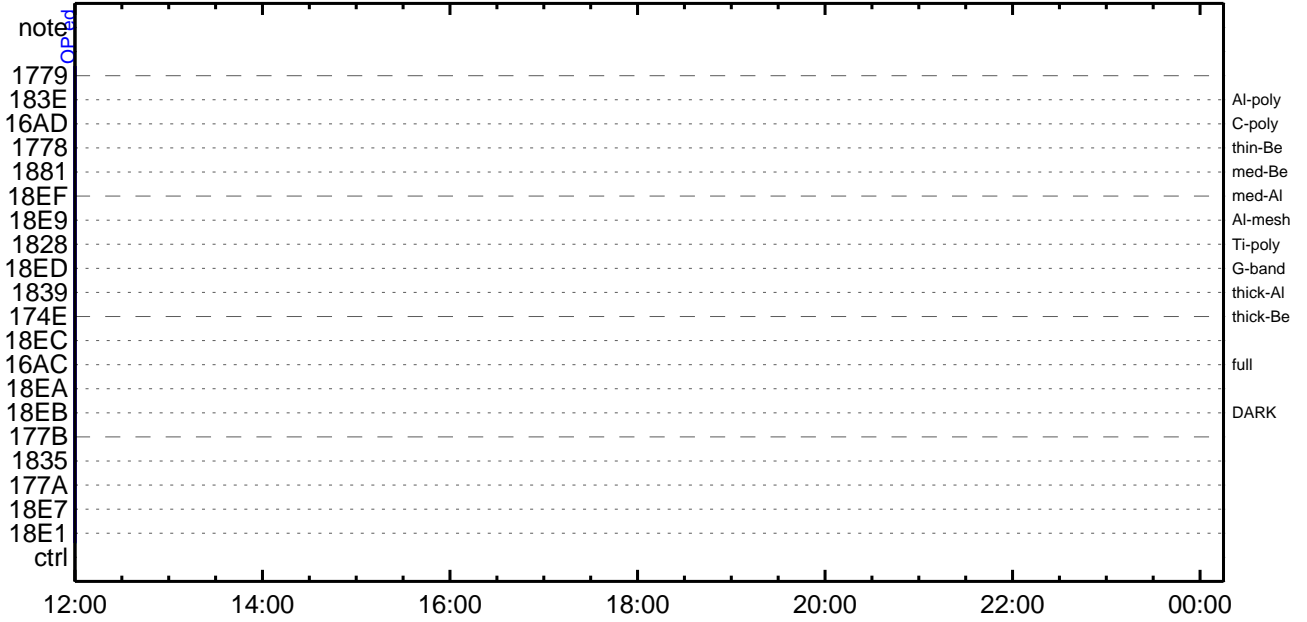
CMDI #0648 2012/06/28



CMDI #0648 2012/06/29



CMDI #0648 2012/06/29




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-892:OP
0104 ( )
0105 S. OG og-892:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½î¹çòE¼î¹çòÇòâ) *****
0180 C. çç[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 C. TIY³YpYóYEòðÁDî¿(UT)
0183 +. TI 2012-05-24 10:08:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-05-24 10:08:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-05-24 10:08:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 c0 95 0c 0c)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 85 83 0c 0c)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 85 83 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 + DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 0c)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 0d)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2012-05-24 10:12:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 . C. *****
0170 C. SOT table upload
0171 C. *****
0172 . C. < Stop FG table >
0173 +. DC 07-F0 MDP_FG_CTRL_MANU
0174 BC (51)
0175 . C. -----
0176 C. MDP_FG_CTRL_MODE = MANU [ ]
0177 C. -----
0178 C.
0179 . C. <Upload FG Observation Table>
0180 . S. RAM ram-262:MDP_OBS_F
0181 ( )
0182 C.
0183 . C. < Dump RAMID=MDP_OBS_F >
0184 +. DC 07-F0 MDP_DUMP_FGTBL
0185 BC (82 07 00 00 00 38 b8)
0186 C. -----
0187 C. MDP_OBS_F verify = OK/NG [ ]
0188 C. -----
0189 C.
0190 . C. < Stop SP table >
0191 +. DC 07-F0 MDP_SP_CTRL_MANU
0192 BC (61)
0193 C. -----

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*** OP Sequence for XRT ***

2012/05/24	10:23:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	03	1a	4e	f1
2012/05/24	10:54:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	03	1a	3d	3f
2012/05/24	11:15:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/24	11:15:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/05/24	11:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/05/24	11:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/05/24	11:44:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/24	11:44:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2012/05/24	11:45:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	03	1a	2b	8d
2012/05/24	11:45:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2012/05/24	11:45:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2012/05/24	11:45:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2012/05/24	11:47:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 07
2012/05/24	11:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/05/24	11:57:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	03	1a	19	db
2012/05/24	12:09:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	03	1a	08	31
2012/05/24	12:20:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/24	12:20:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2012/05/24	12:21:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	03	1a	f6	98
2012/05/24	12:21:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2012/05/24	12:21:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2012/05/24	12:21:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2012/05/24	12:23:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 07
2012/05/24	12:24:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/05/24	12:33:00.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	03	1a	e4	e6
2012/05/24	12:54:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/24	12:54:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/05/24	12:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/05/24	12:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/05/24	13:23:00.0	AOCS_Ore-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	03	1a	d3	34
2012/05/24	13:34:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/24	13:34:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2012/05/24	13:35:00.0	AOCS_Ore-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	03	1a	c1	82
2012/05/24	13:35:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2012/05/24	13:35:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2012/05/24	13:35:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2012/05/24	13:37:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 07
2012/05/24	13:38:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/05/24	13:47:00.0	AOCS_Ore-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	03	1a	af	cf
2012/05/24	14:11:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	15	58	50	e5
2012/05/24	14:32:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/05/24	14:32:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/05/24	14:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/05/24	14:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/05/24	15:10:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	17	64	42	0c
2012/05/24	15:21:54.0	XRT_CTRL_MANU_400_OG [0x190]							

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2012/05/24	15:21:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/24	15:22:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/24	15:22:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 16 d9 31 f3	
2012/05/24	15:22:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/05/24	15:22:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/05/24	15:24:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/24	15:25:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2012/05/24	15:34:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/24	15:46:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 16 97 21 68	
2012/05/24	15:56:54.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	00 16 66 10 8b	
2012/05/24	15:56:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/24	15:57:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/24	15:57:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 16 4d ff 4c	
2012/05/24	15:57:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/05/24	15:57:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/05/24	15:59:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/24	16:00:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2012/05/24	16:11:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/24	16:11:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/24	16:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/24	16:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/24	17:09:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/24	17:21:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 16 66 f0 e6	
2012/05/24	17:32:54.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	00 16 97 e0 00	
2012/05/24	17:32:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/24	17:33:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/24	17:33:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 16 d9 cf 75	
2012/05/24	17:33:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/05/24	17:33:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/05/24	17:35:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/24	17:36:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2012/05/24	17:49:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/24	17:49:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/24	17:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/24	17:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/24	18:33:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/24	18:45:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 17 64 bf 65	
2012/05/24	18:56:54.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	00 18 72 af cf	
2012/05/24	18:56:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/24	18:57:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/24	18:57:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00	
2012/05/24	18:57:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/05/24	18:57:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/05/24	18:59:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/24	19:00:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2012/05/24	19:06:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2012/05/25	05:47:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/25	05:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/25	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/25	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/25	06:00:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2012/05/25	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/05/25	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/05/25	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/25	06:02:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2012/05/25	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/25	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/25	06:10:00.0	AOCS_Ore-point_Start_24_OG [0x0ae]					
			AOCU_NM	5	02-76	03 00 00 00 00	
2012/05/25	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/05/25	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/25	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/25	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/25	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/25	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/25	06:12:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2012/05/25	06:12:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2012/05/25	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/25	06:49:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/25	06:49:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/25	06:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/25	06:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/25	07:26:00.0	XRT_Custom_418_OG [0x1a2]					
2012/05/25	07:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/25	08:29:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/25	08:29:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/25	08:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/25	08:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/25	09:04:30.0	XRT_Custom_418_OG [0x1a2]					
2012/05/25	09:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/25	09:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/25	10:00:00.0	AOCS_Ore-point_Start_25_OG [0x0af]					
			AOCU_NM	5	02-76	00 b3 1b 00 b4	
2012/05/25	10:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/05/25	10:02:46.0	XRT_FLD_ENA_405_OG [0x195]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/25	10:02:48.0	XRT_FLRCTRL_ENA_440_OG [0x1b8]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/25	10:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/25	10:02:52.0	XRT_ARS_DIS_431_OG [0x1af]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/25	10:02:54.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/25	10:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2012/05/25	10:02:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2012/05/25	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/25	10:10:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/25	10:10:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/25	10:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/25	10:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					

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2012/05/25	10:43:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/25	10:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2012/05/25	11:52:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/25	11:52:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/25	11:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/25	11:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/25	12:21:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/25	12:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2012/05/25	13:31:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/25	13:31:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/25	13:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/25	13:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/25	14:02:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/25	14:02:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/25	14:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/25	14:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/25	14:03:30.0	XRT_Custom_418_OG [0x1a2]								
2012/05/25	14:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2012/05/25	14:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/25	15:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/25	15:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/25	15:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/25	15:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/25	15:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/25	15:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/25	16:00:00.0	AOCS_Ore-point_Start_24_OG [0x0ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/25	16:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	03 00 00 00 00				
2012/05/25	16:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/05/25	16:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/25	16:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/25	16:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/25	16:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/25	16:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/25	16:02:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2012/05/25	16:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2012/05/25	16:48:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/25	16:48:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/25	16:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/25	16:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/25	17:32:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/25	17:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2012/05/25	18:04:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/25	18:04:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/25	18:04:30.0	AOCS_Ore-point_Start_22_OG [0x0ac]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/25	18:04:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00				
2012/05/25	18:04:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/05/25	18:04:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/05/25	18:07:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/25	18:07:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2012/05/25	18:14:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/25			MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2012/05/25	18:14:30.0	AOCS_OrE-point_Start_24_OG [0x0ae] AOCU_NM	5	02-76	03	00	00	00	00
2012/05/25	18:16:56.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/05/25	18:17:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/25	18:17:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/25	18:17:20.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/25	18:17:22.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/25	18:17:24.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/25	18:17:26.0	XRT_QT_PROG_SET_434_OG [0x1b2] MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2012/05/25	18:17:28.0	XRT_FL_PROG_SET_449_OG [0x1c1] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2012/05/25	18:17:30.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/25	18:26:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/25	18:26:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/25	18:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/25	18:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/25	19:09:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/25	19:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/25	20:05:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/25	20:05:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/25	20:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/25	20:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/25	20:47:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/25	20:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/25	21:43:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/25	21:43:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/25	21:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/25	21:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/25	22:23:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/25	22:24:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/25	23:22:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/25	23:22:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/25	23:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/25	23:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/25	23:51:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/25	23:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/26	01:00:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/26	01:00:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/26	01:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/26	01:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/26	01:29:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/26	01:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/26	02:37:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/26	02:37:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/26	02:37:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/26	02:40:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/26	03:08:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/26	03:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/26	04:06:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/26	04:06:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/26	04:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/26	04:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							

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2012/05/26	04:46:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/05/26	04:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2012/05/26	05:47:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/05/26	05:47:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/05/26	05:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/05/26	05:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/05/26	06:25:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/05/26	06:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2012/05/26	06:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/05/26	06:29:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/05/26	06:30:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/05/26	06:30:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00		
2012/05/26	06:30:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/05/26	06:30:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/05/26	06:32:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/05/26	06:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2012/05/26	06:39:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/05/26	06:40:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/05/26	06:42:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	03 00 00 00 00		
2012/05/26	06:42:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/05/26	06:42:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/05/26	06:42:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/05/26	06:42:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/05/26	06:42:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/05/26	06:42:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/05/26	06:42:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2012/05/26	06:43:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2012/05/26	07:27:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/05/26	07:27:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/05/26	07:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/05/26	07:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/05/26	08:03:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/05/26	08:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
2012/05/26	09:07:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/05/26	09:07:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/05/26	09:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/05/26	09:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/05/26	10:14:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
		AOCU_NM		5	02-76	00 00 00 00 00		