

XRT Timeline to be uploaded on 2012/05/29

Period: 2012/05/29 10:18:00 - 2012/07/02 12:00:00

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Normal mode

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| XOB #1835: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 40sec-cad | | | | | | | | | | | | |
|--|---|------------------------------|--|--|--|--|--|---------|--|--|--|--|
| Term | Pointing (x, y) | | | | | | | Comment | | | | |
| 05/29 10:32:30 - 05/29 10:44:30 | Track (-398.0, -250.5) ^{@ 05/29 10:28:00} | # OP start + 10min, AR 11492 | | | | | | | | | | |
| 05/29 11:48:00 - 05/29 17:39:00 | Track (-387.9, -250.5) ^{@ 05/29 11:45:00} | AR 11492 | | | | | | | | | | |
| PROG= 04 Inf.-time(s) | | | | | | | | | | | | |
| └─ Subr= 2 1-time(s) 2.0sec | | | | | | | | | | | | |
| └─ Seqn= 19 1-time(s) 2.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec | | | | | | | | | | | | |
| └─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec | | | | | | | | | | | | |
| └─ Seqn= 41 4-time(s) 2.0sec | | | | | | | | | | | | |
| └─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec | | | | | | | | | | | | |
| └─ Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec | | | | | | | | | | | | |
| └─ Subr= 1 1-time(s) 2.0sec | | | | | | | | | | | | |
| └─ Seqn= 52 45-time(s) 24.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 6.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec | | | | | | | | | | | | |
| Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval | | | | | | | | | | | | |

| XOB #18EA: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-band(16) | | | | | | | | | | | | |
|--|-------------------|----------------------------|--|--|--|--|--|---------|--|--|--|--|
| Term | Pointing (x, y) | | | | | | | Comment | | | | |
| 05/29 18:26:00 - 05/29 18:42:54 | Fixed (0.0, 0.0) | synoptic, shifted 23.0 min | | | | | | | | | | |
| 05/30 05:58:00 - 05/30 06:04:54 | Fixed (0.0, 0.0) | synoptic, shifted -5.0 min | | | | | | | | | | |
| 05/30 18:03:00 - 05/30 18:09:54 | Fixed (0.0, 0.0) | synoptic | | | | | | | | | | |
| 05/31 06:24:00 - 05/31 06:30:54 | Fixed (0.0, 0.0) | synoptic, shifted 21.0 min | | | | | | | | | | |

| PROG= 07 1-time(s) | | | | | | | | | | | | |
|--|--|--|--|--|--|--|--|--|--|--|--|--|
| └─ Subr= 1 1-time(s) 12.0sec | | | | | | | | | | | | |
| └─ Seqn= 46 1-time(s) 4.0sec | | | | | | | | | | | | |
| └─ Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec | | | | | | | | | | | | |
| └─ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec | | | | | | | | | | | | |
| └─ Seqn= 5 1-time(s) 2.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec | | | | | | | | | | | | |
| └─ Seqn= 69 1-time(s) 4.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec | | | | | | | | | | | | |
| └─ Seqn= 4 1-time(s) 2.0sec | | | | | | | | | | | | |
| └─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec | | | | | | | | | | | | |
| Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval | | | | | | | | | | | | |

| XOB #183E: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad | | | | | | | | | | | | |
|--|---|------------------|--|--|--|--|--|---------|--|--|--|--|
| Term | Pointing (x, y) | | | | | | | Comment | | | | |
| 05/29 18:46:00 - 05/30 04:58:30 | Track (-332.0, -250.7) ^{@ 05/29 18:43:00} | cont. | | | | | | | | | | |
| 05/30 06:08:00 - 05/30 10:00:00 | Track (-237.8, -251.1) ^{@ 05/30 06:05:00} | cont. | | | | | | | | | | |
| 05/30 16:23:00 - 05/30 17:59:54 | Track (-150.3, -251.6) ^{@ 05/30 16:20:00} | AR 11492 | | | | | | | | | | |
| 05/30 19:01:00 - 05/31 05:37:00 | Track (-134.5, -251.7) ^{@ 05/30 18:10:00} | cont. (AR 11492) | | | | | | | | | | |
| 05/31 06:34:00 - 05/31 08:57:00 | Track (-27.1, -252.6) ^{@ 05/31 06:31:00} | cont. | | | | | | | | | | |

| PROG= 19 Inf.-time(s) | | | | | | | | | | | | |
|--|--|--|--|--|--|--|--|--|--|--|--|--|
| └─ Subr= 2 1-time(s) 2.0sec | | | | | | | | | | | | |
| └─ Seqn= 19 1-time(s) 2.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec | | | | | | | | | | | | |
| └─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec | | | | | | | | | | | | |
| └─ Seqn= 41 4-time(s) 2.0sec | | | | | | | | | | | | |
| └─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec | | | | | | | | | | | | |
| └─ Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec | | | | | | | | | | | | |
| └─ Subr= 1 1-time(s) 2.0sec | | | | | | | | | | | | |
| └─ Seqn= 16 30-time(s) 80.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 20.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 20.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 20.0sec | | | | | | | | | | | | |
| └─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 20.0sec | | | | | | | | | | | | |
| Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval | | | | | | | | | | | | |

| XOB #18F2: HOP130-Synoptic 3 Filter- 1x1 Q98 Short exp 2 loop | | | | | | | | | | | | |
|--|-----------------|--|--|--|--|--|--|---------|--|--|--|--|
| Term | Pointing (x, y) | | | | | | | Comment | | | | |

PROG= 03 2-time(s)

- └─ **Subr= 1 1-time(s) 180.0sec**
 - └─ **Seqn= 55 1-time(s) 25.0sec**
 - └─ Open/Al-mesh Open/Al-mesh close Safe Norm 44ms Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - └─ Open/Al-mesh Open/Ti-poly close Safe Norm 2.83s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - └─ **Seqn= 97 1-time(s) 25.0sec**
 - └─ Open/Ti-poly Open/Ti-poly close Safe Norm 86ms Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Norm 5.66s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - └─ **Seqn= 12 1-time(s) 2.0sec**
 - └─ Open/thick-Al Open/thick-Al close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * * **Flare mode** * * * * *

XOB #18E7: Flare obs. dynamics - Ti_poly high cadence + context (thick-Al-384x384)-15 loops

| Term | Pointing (x, y) | Comment |
|---------------------------------|---|------------------------------|
| 05/29 10:32:30 - 05/29 10:44:30 | Track (-398.0, -250.5) ^{Ⓜ 05/29 10:28:00} | # OP start + 10min, AR 11492 |
| 05/29 11:48:00 - 05/29 17:39:00 | Track (-387.9, -250.5) ^{Ⓜ 05/29 11:45:00} | AR 11492 |
| 05/29 18:46:00 - 05/30 04:58:30 | Track (-332.0, -250.7) ^{Ⓜ 05/29 18:43:00} | cont. |
| 05/30 06:08:00 - 05/30 10:00:00 | Track (-237.8, -251.1) ^{Ⓜ 05/30 06:05:00} | cont. |
| 05/30 16:23:00 - 05/30 17:59:54 | Track (-150.3, -251.6) ^{Ⓜ 05/30 16:20:00} | AR 11492 |
| 05/30 19:01:00 - 05/31 05:37:00 | Track (-134.5, -251.7) ^{Ⓜ 05/30 18:10:00} | cont. (AR 11492) |
| 05/31 06:34:00 - 05/31 08:57:00 | Track (-27.1, -252.6) ^{Ⓜ 05/31 06:31:00} | cont. |

PROG= 02 15-time(s)

- └─ **Subr= 1 45-time(s) 10.0sec**
 - └─ **Seqn= 92 1-time(s) 2.0sec**
 - └─ Open/Ti-poly Open/thick-Al close Safe Norm 4ms Obs 1x1 384x384 (1024, 1024) DPCM 2 0 2.0sec
 - └─ Open/Ti-poly Open/thick-Al close Safe Norm 4ms Obs 1x1 384x384 (1024, 1024) DPCM 3 0 2.0sec
- └─ **Subr= 2 1-time(s) 10.0sec**
 - └─ **Seqn= 54 1-time(s) 2.0sec**
 - └─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) DPCM 2 0 2.0sec
 - └─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) DPCM 3 0 2.0sec
 - └─ **Seqn= 79 1-time(s) 2.0sec**
 - └─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
 - └─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * * **Active Region Search** * * * * *

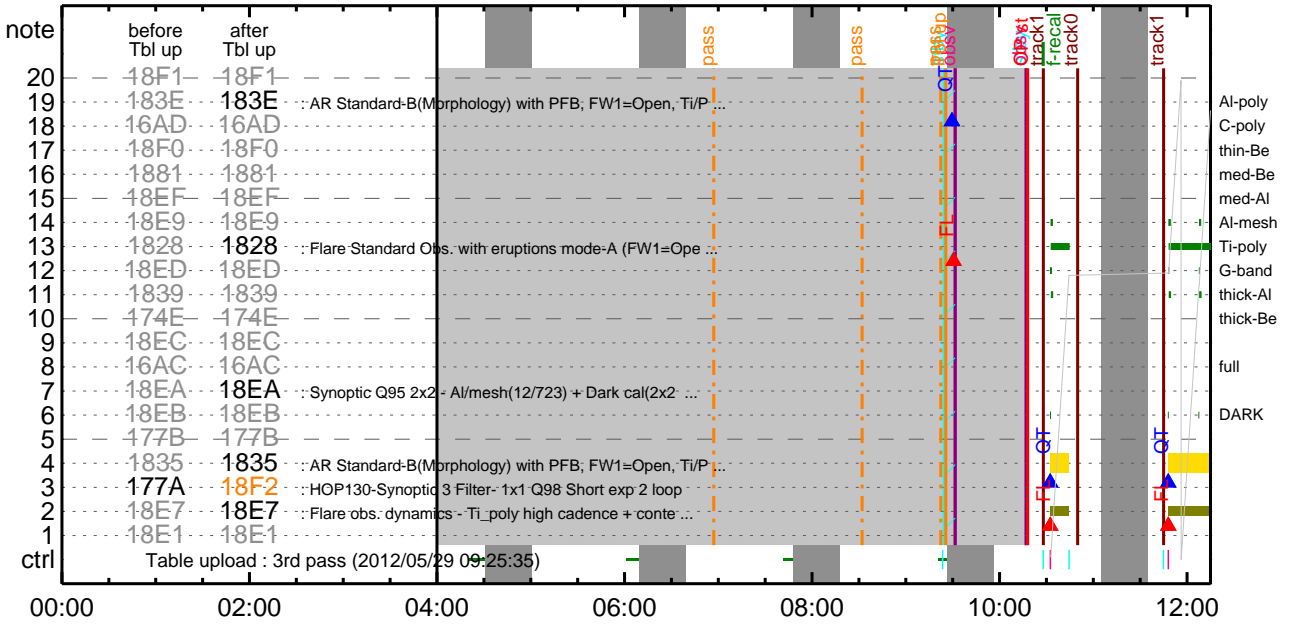
NOT USED

* * * * * **Flare Detection** * * * * *

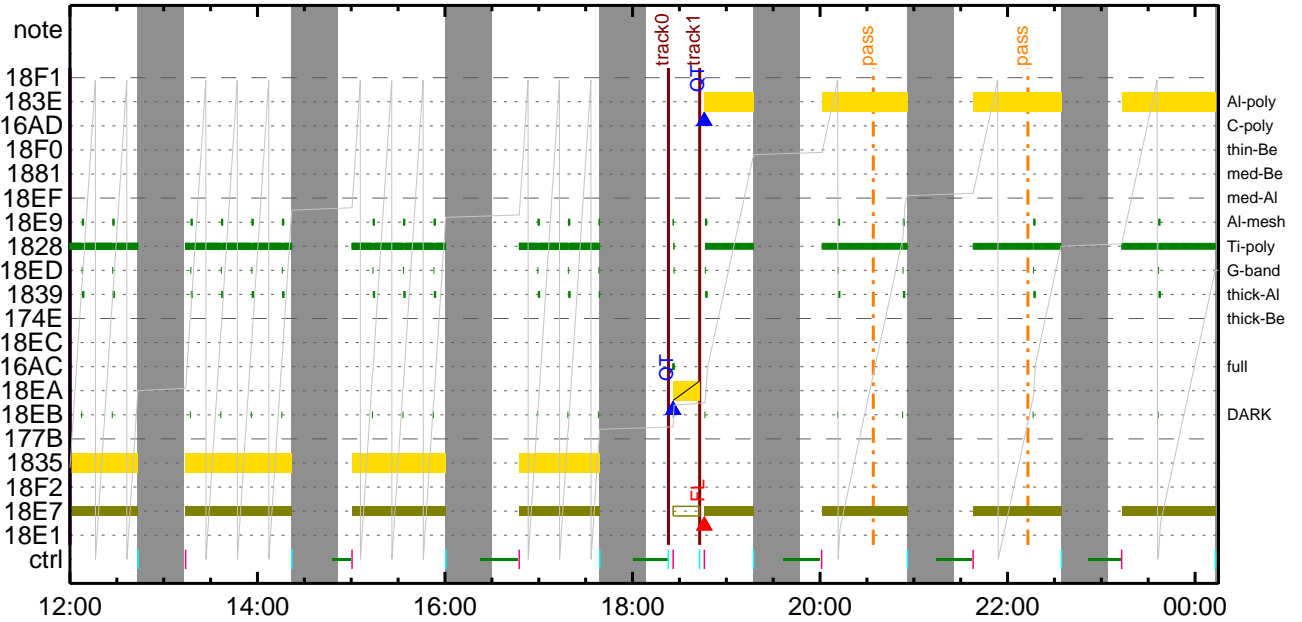
FLD Patrol

| Term | Pointing (x, y) | Comment |
|---------------------------------|--|------------------|
| 05/29 18:45:46 - 05/30 05:55:16 | Track (-332.0, -250.7) ^{Ⓜ 05/29 18:43:00} | cont. |
| 05/30 06:07:46 - 05/30 11:05:46 | Track (-237.8, -251.1) ^{Ⓜ 05/30 06:05:00} | cont. |
| 05/30 16:22:46 - 05/30 18:00:16 | Track (-150.3, -251.6) ^{Ⓜ 05/30 16:20:00} | AR 11492 |
| 05/30 18:12:46 - 05/31 06:21:16 | Track (-134.5, -251.7) ^{Ⓜ 05/30 18:10:00} | cont. (AR 11492) |
| 05/31 06:33:46 - 07/02 12:00:00 | Track (-27.1, -252.6) ^{Ⓜ 05/31 06:31:00} | cont. |
| | Open/Ti-poly Open/thick-Al close Safe Norm 8ms Obs 8x8 | Q=50 30sec |
| | Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval | |

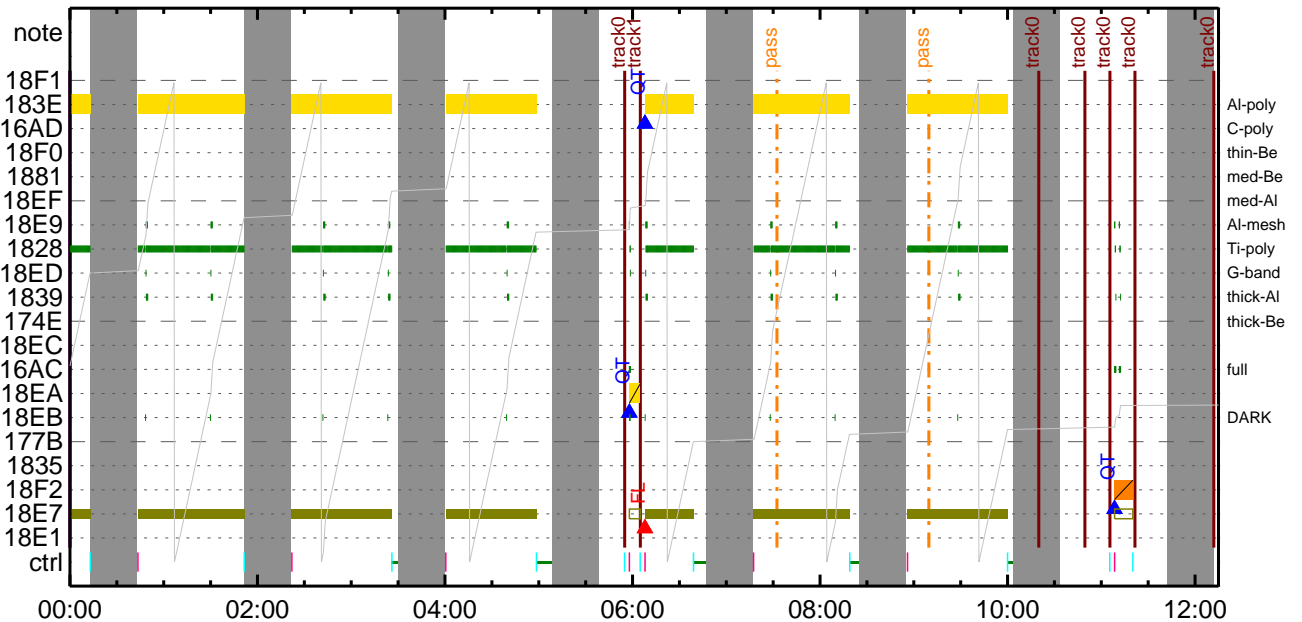
CMDI #0657 2012/05/29



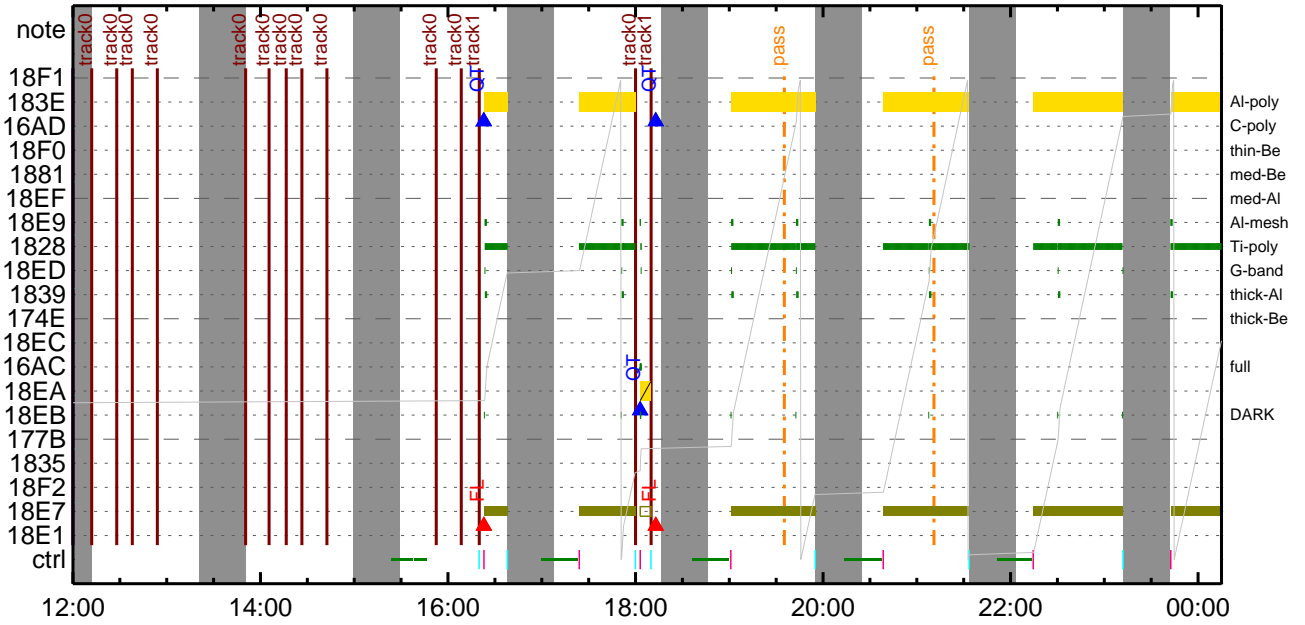
CMDI #0657 2012/05/29



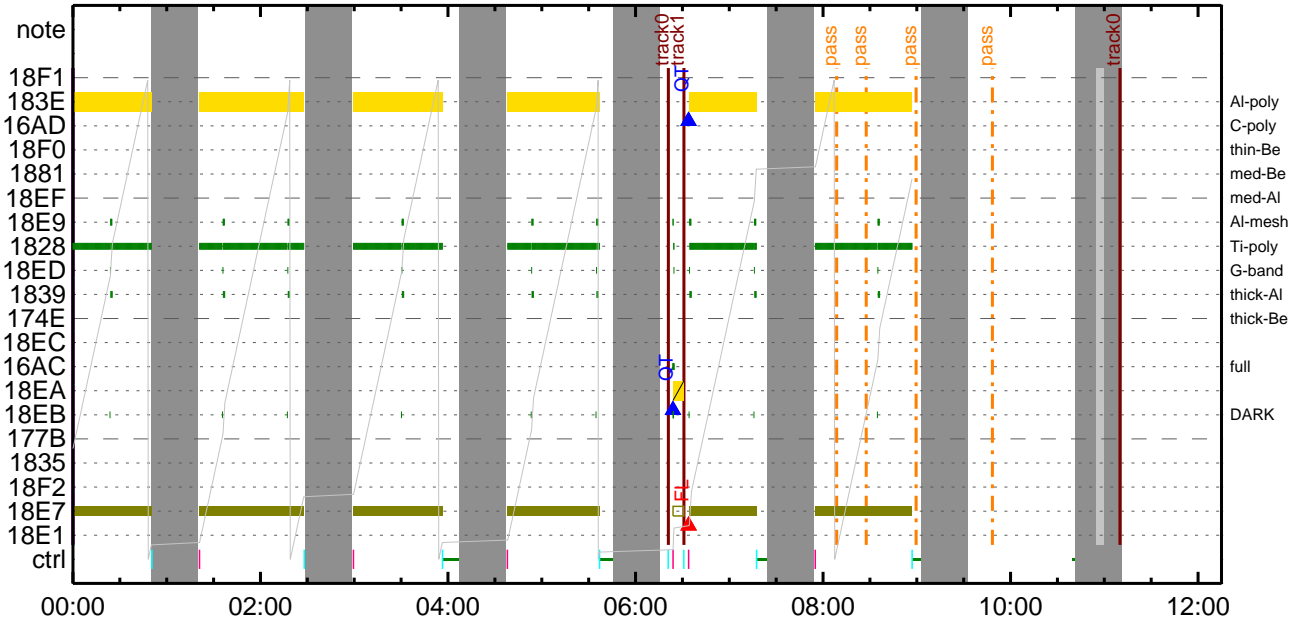
CMDI #0657 2012/05/30



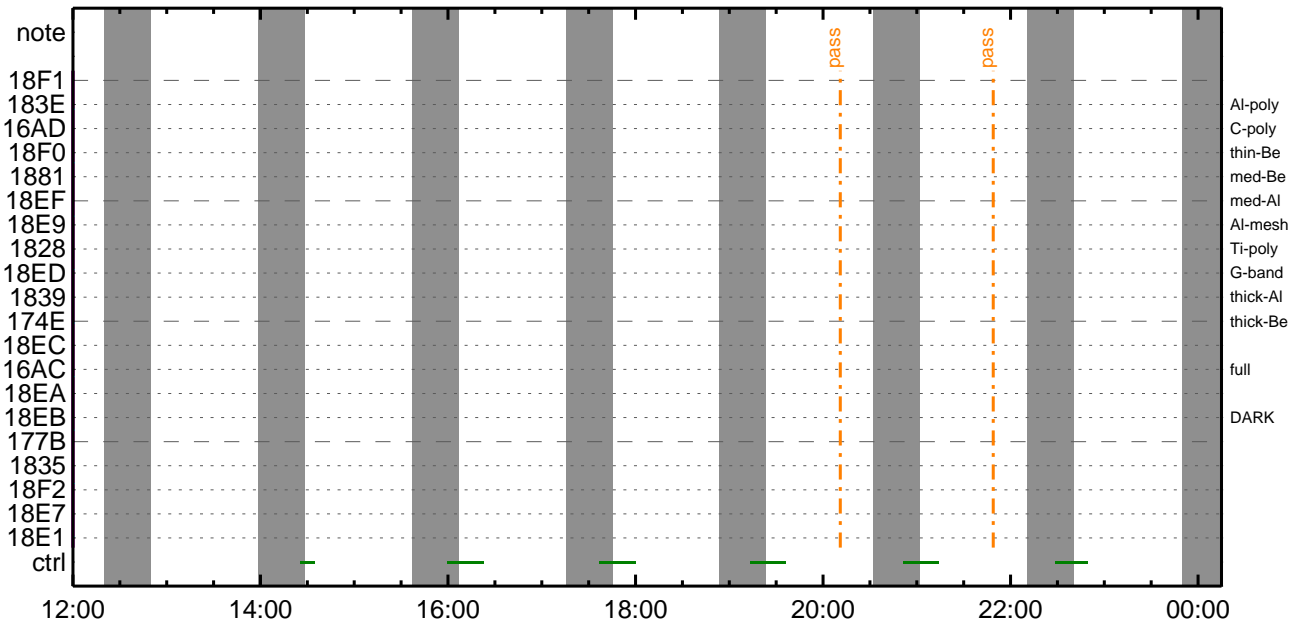
CMDI #0657 2012/05/30



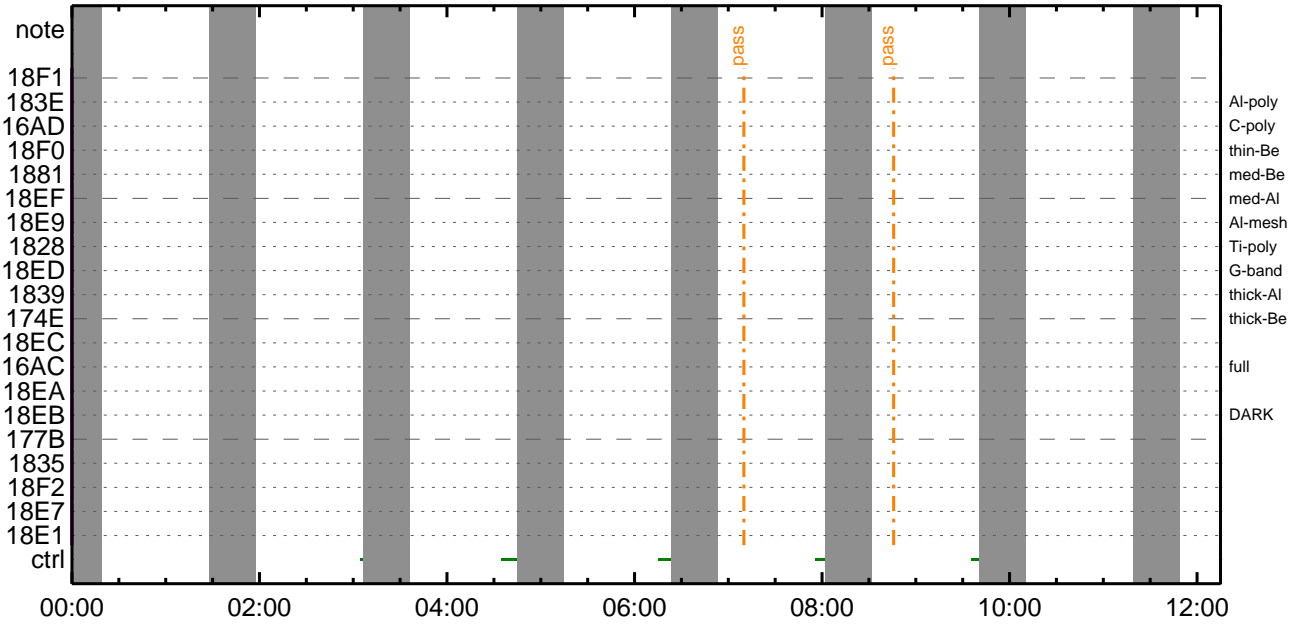
CMDI #0657 2012/05/31



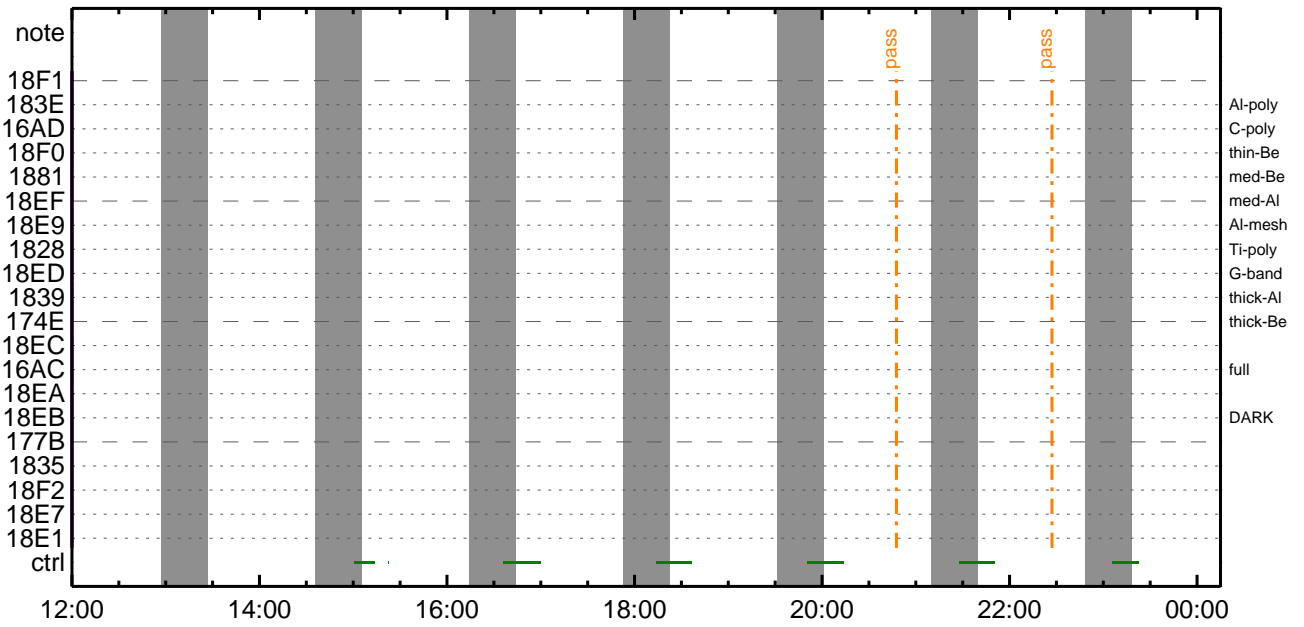
CMDI #0657 2012/05/31



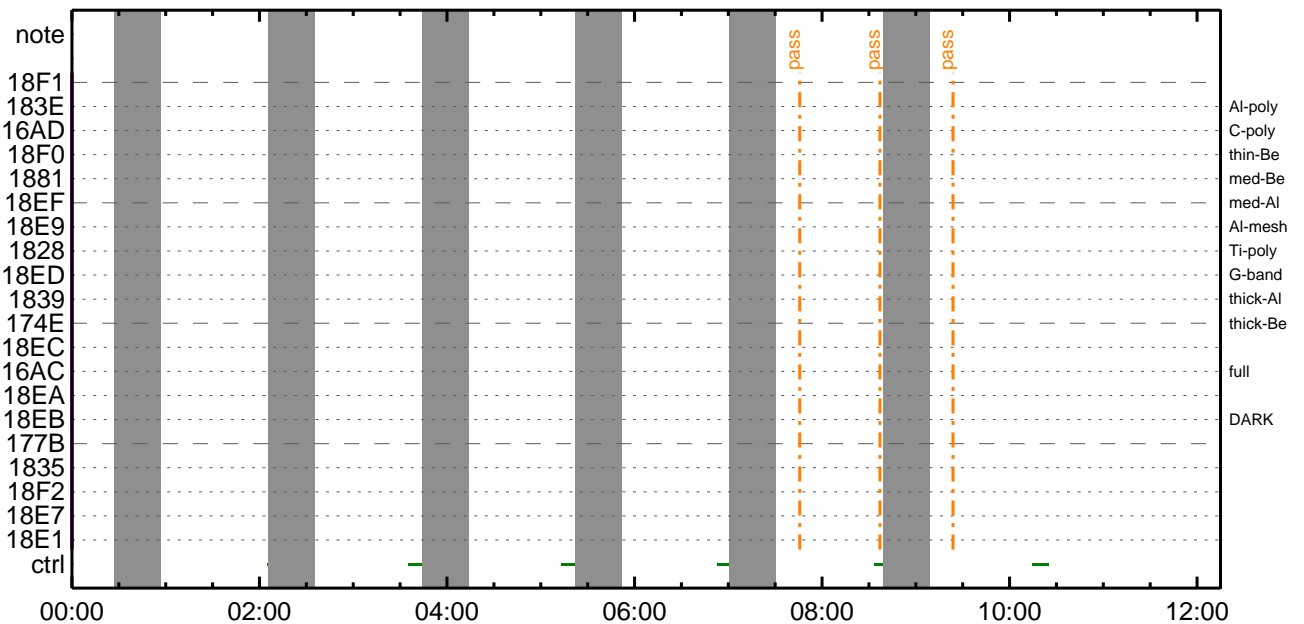
CMDI #0657 2012/06/01



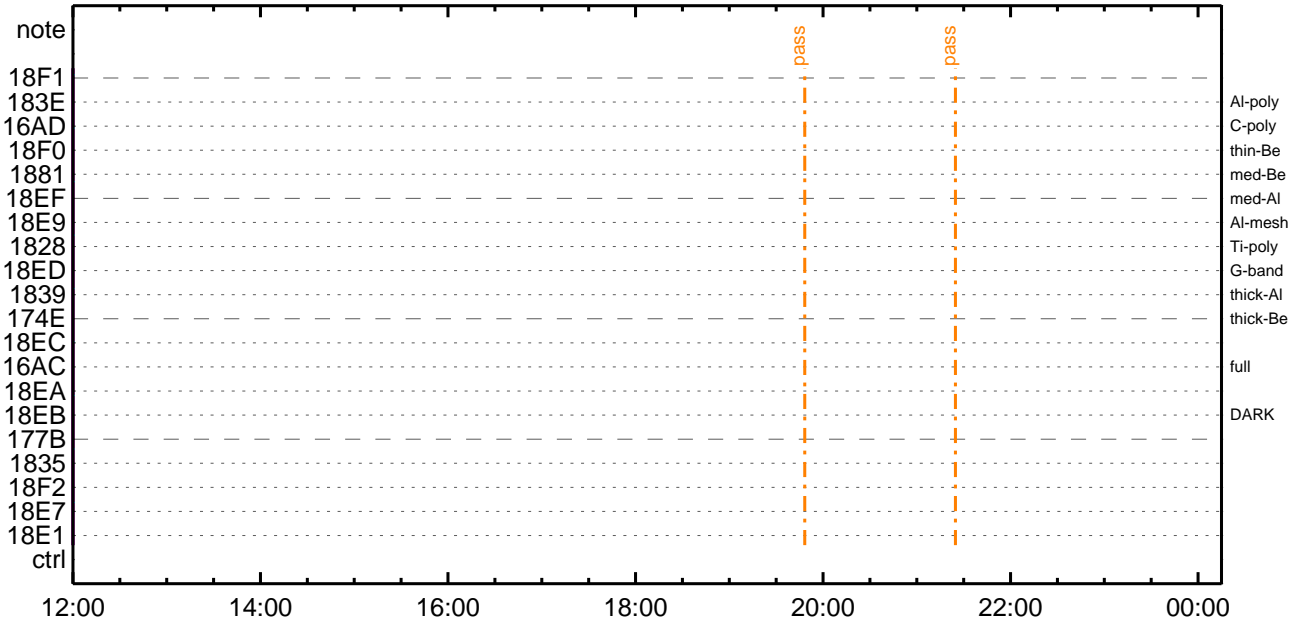
CMDI #0657 2012/06/01



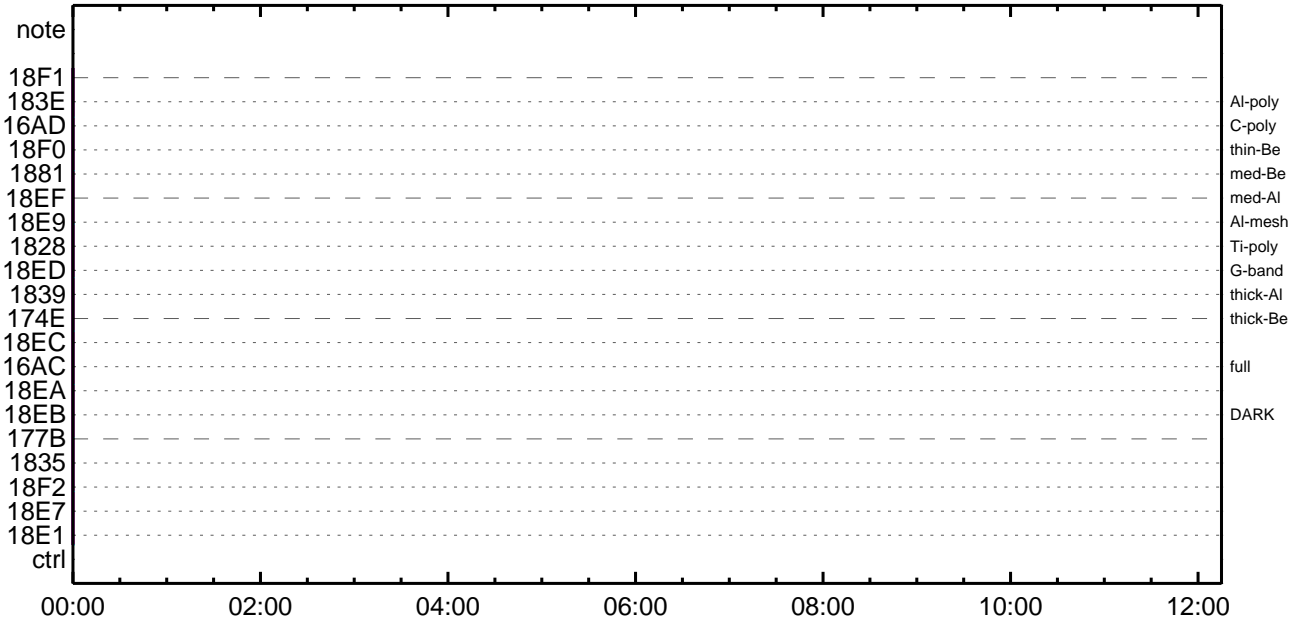
CMDI #0657 2012/06/02



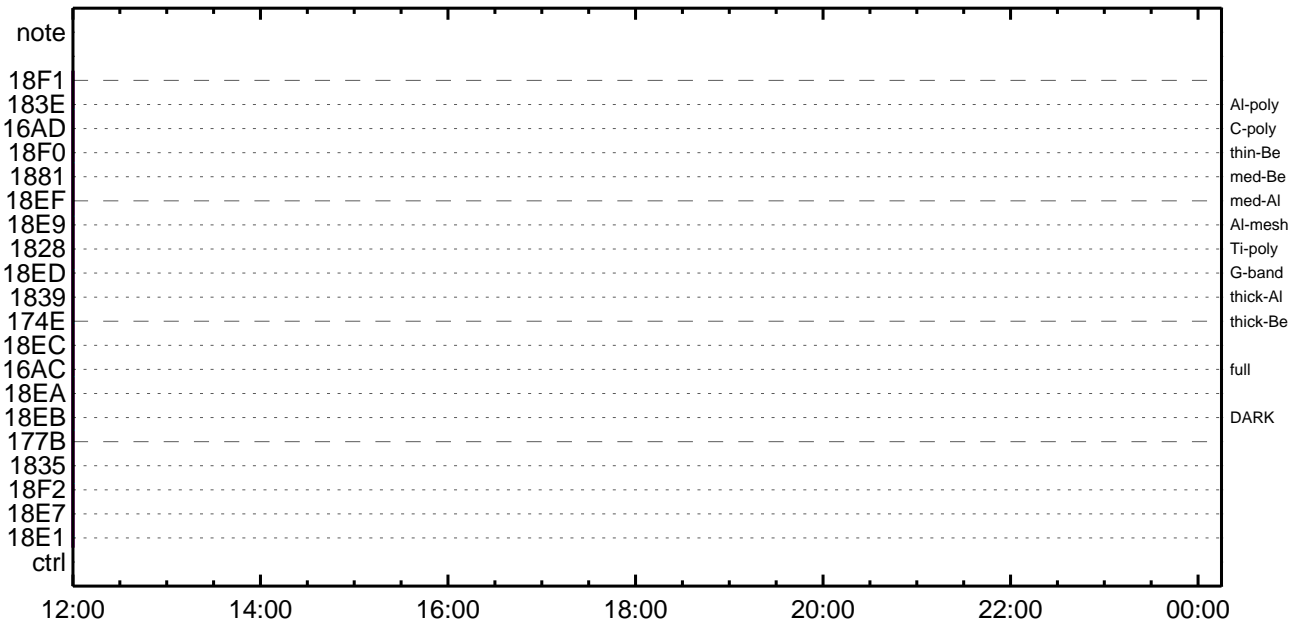
CMDI #0657 2012/06/02



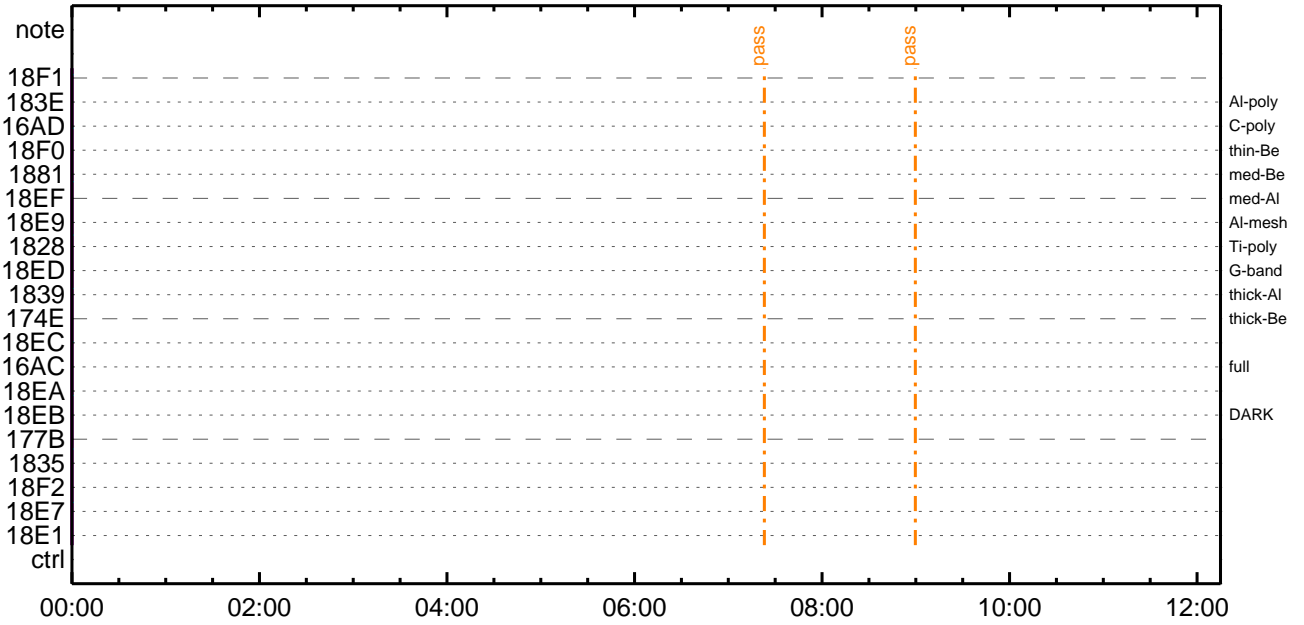
CMDI #0657 2012/06/03



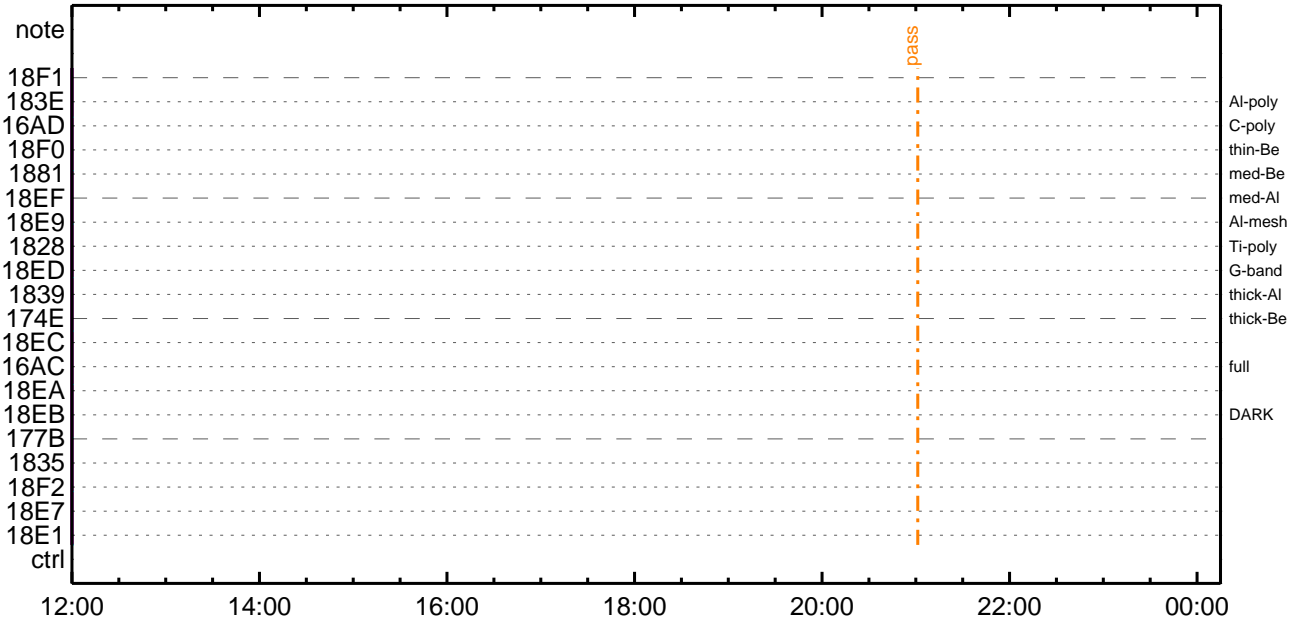
CMDI #0657 2012/06/03



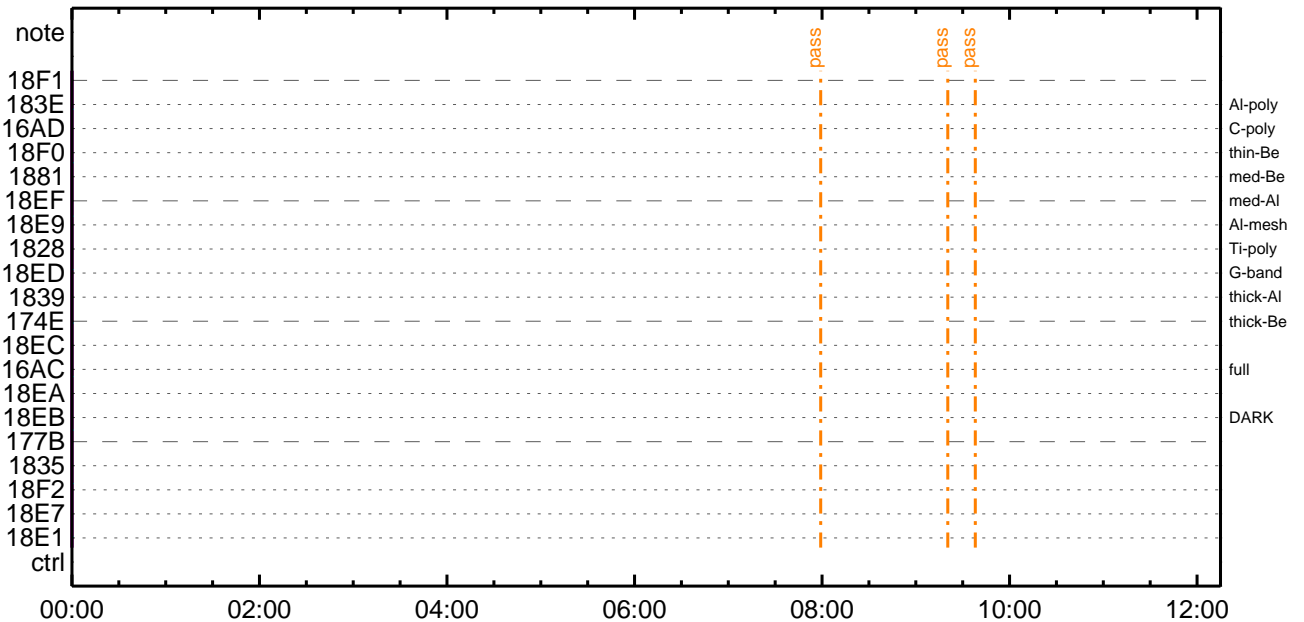
CMDI #0657 2012/06/04



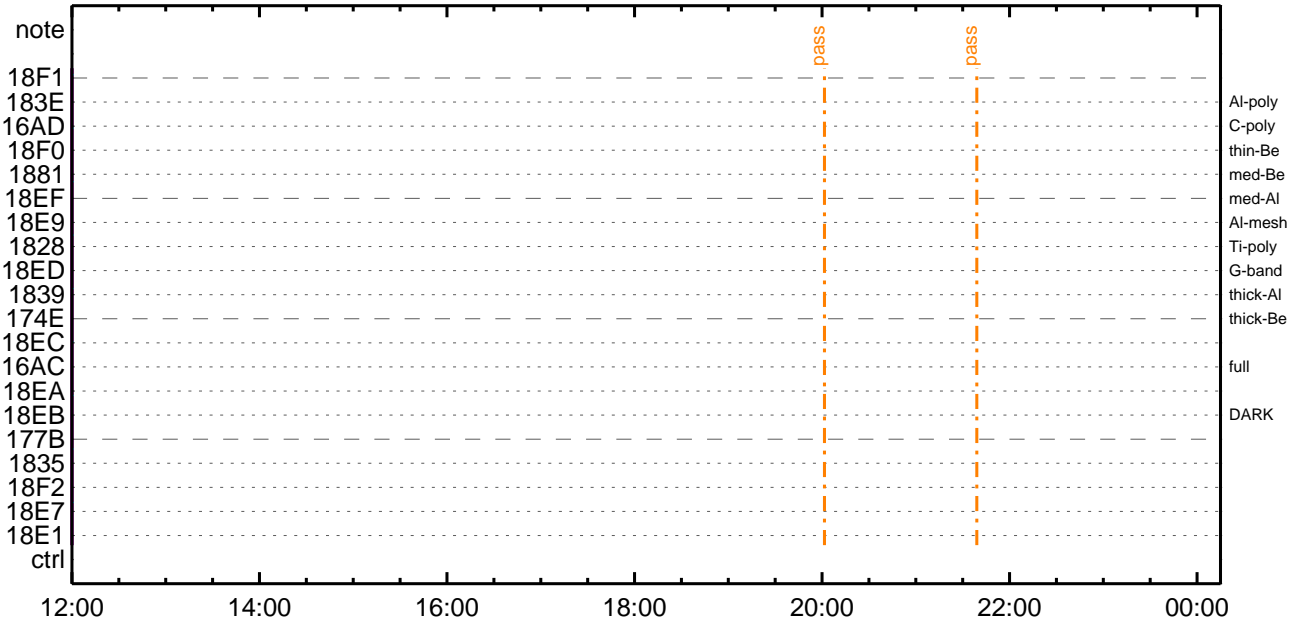
CMDI #0657 2012/06/04



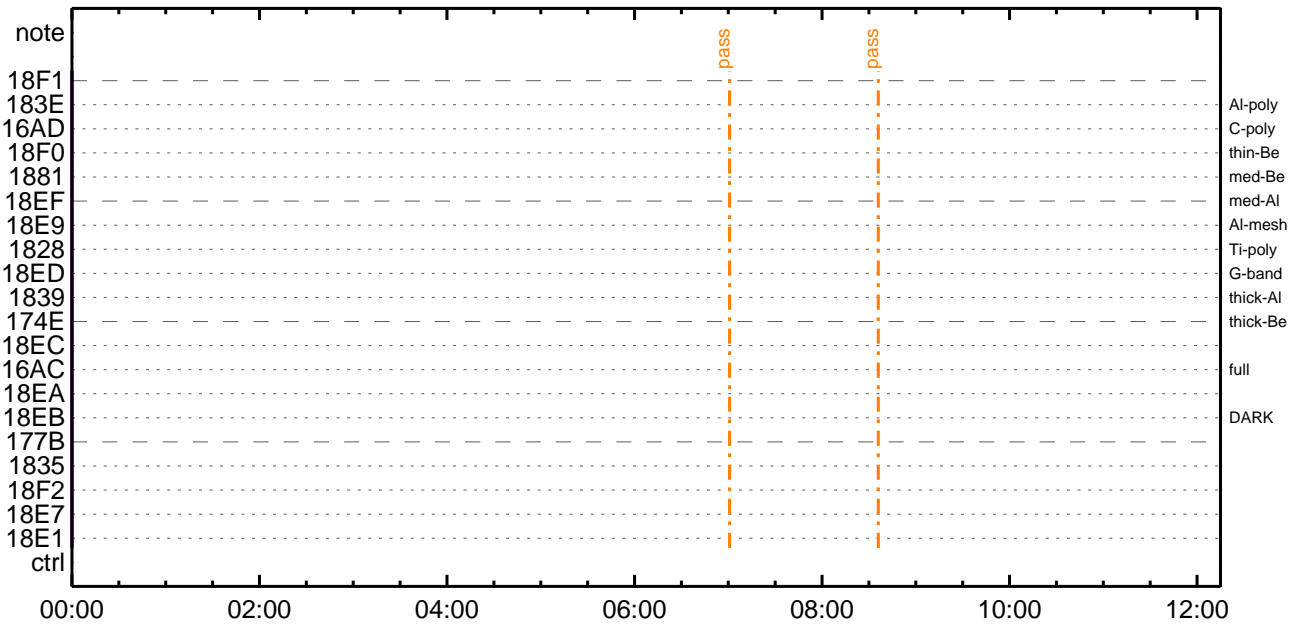
CMDI #0657 2012/06/05



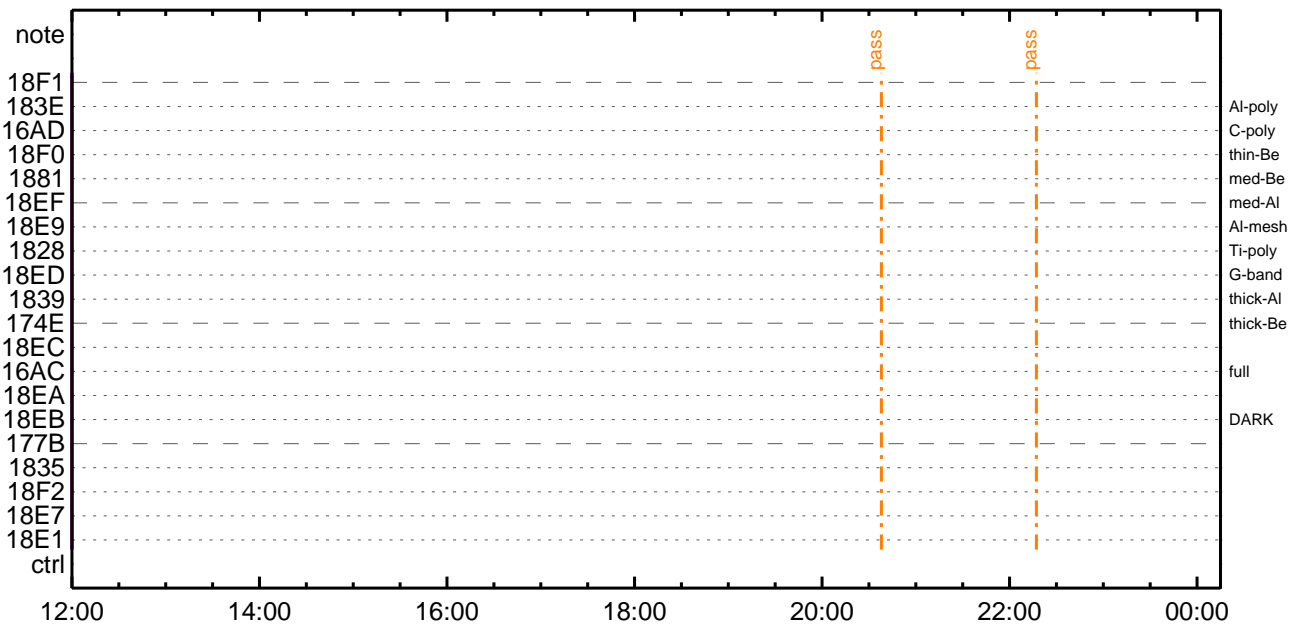
CMDI #0657 2012/06/05



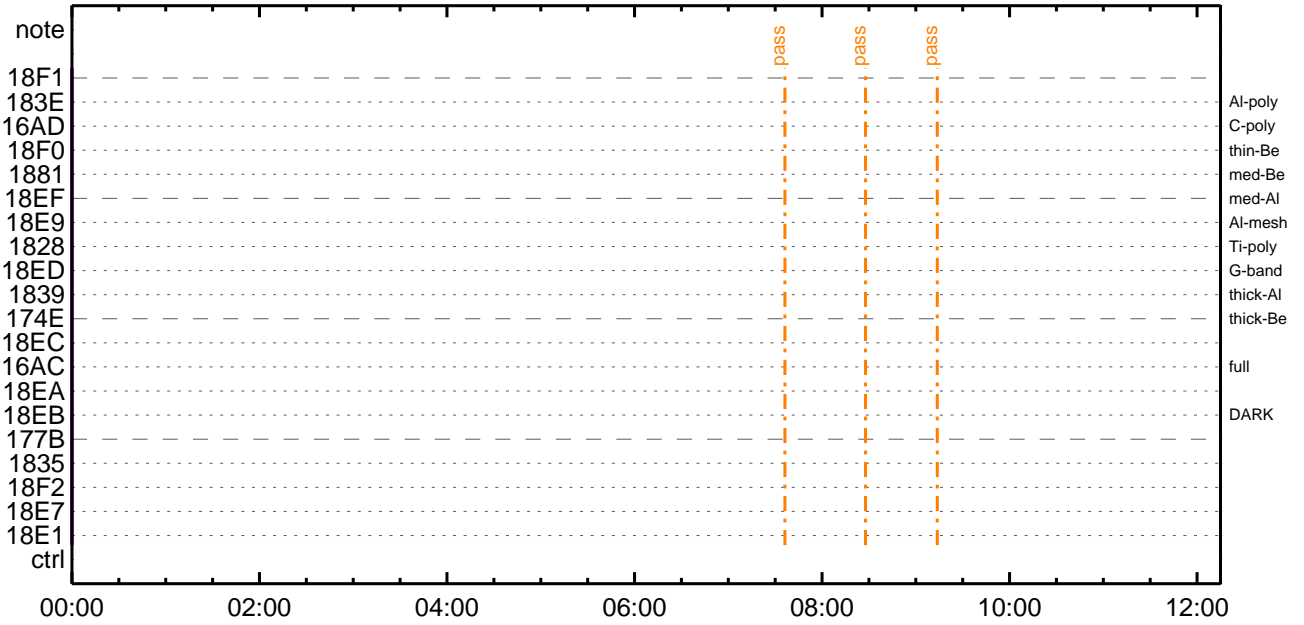
CMDI #0657 2012/06/06



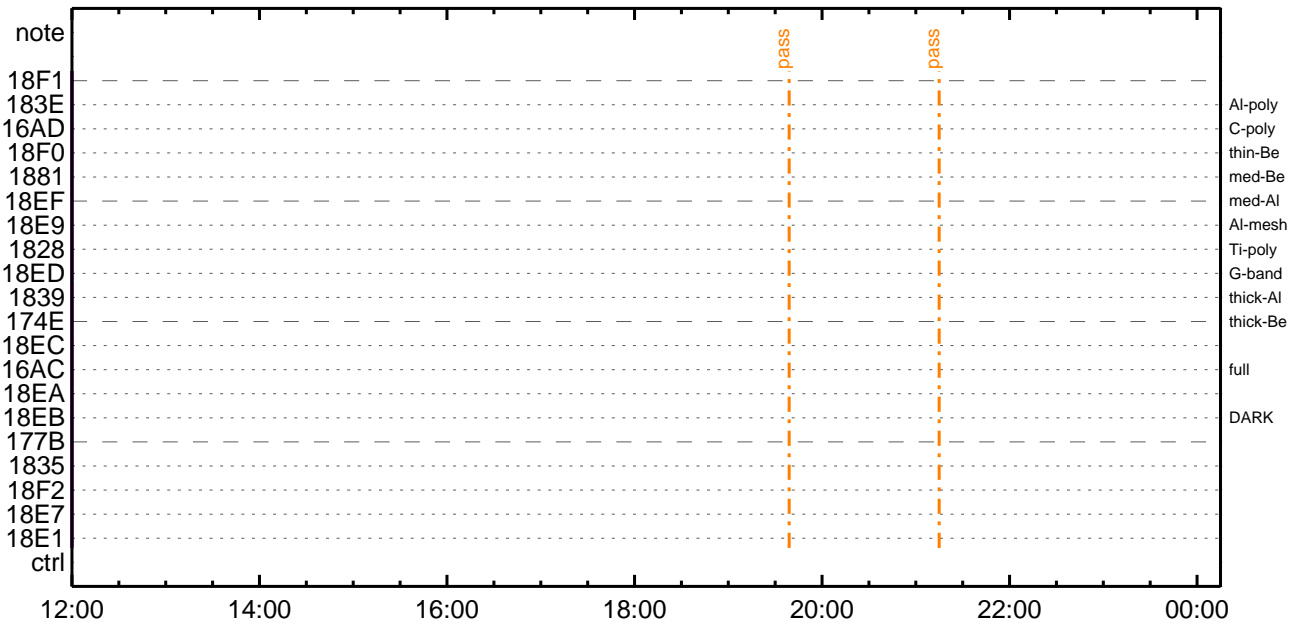
CMDI #0657 2012/06/06



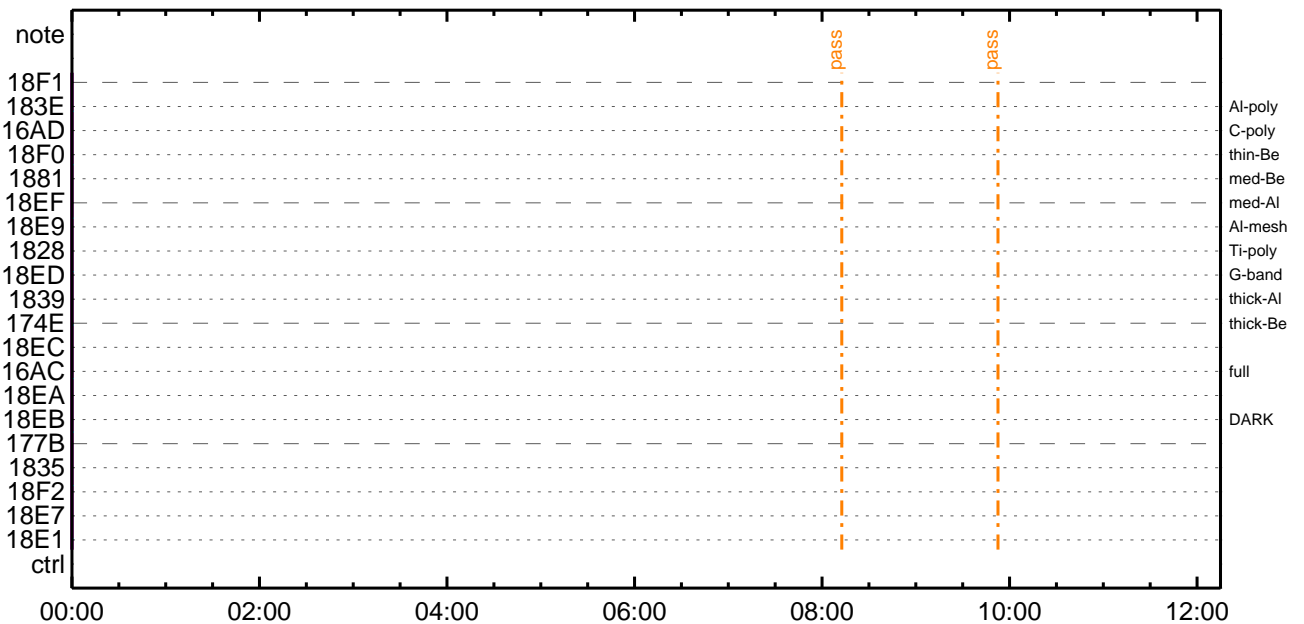
CMDI #0657 2012/06/07



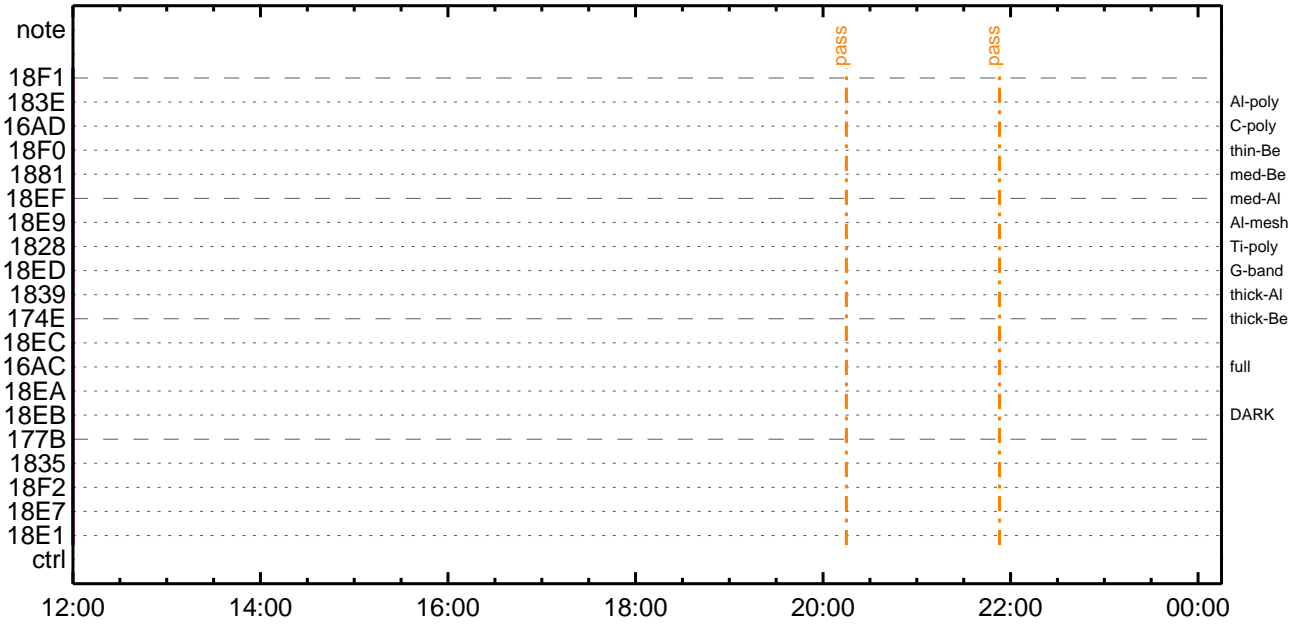
CMDI #0657 2012/06/07



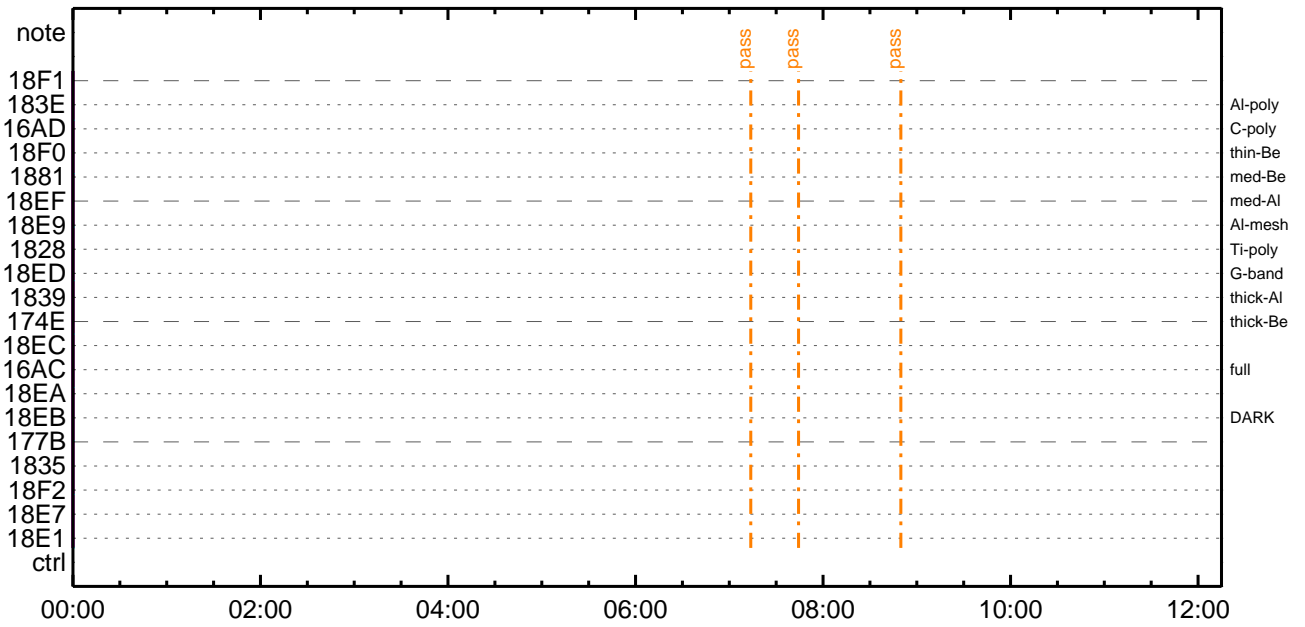
CMDI #0657 2012/06/08



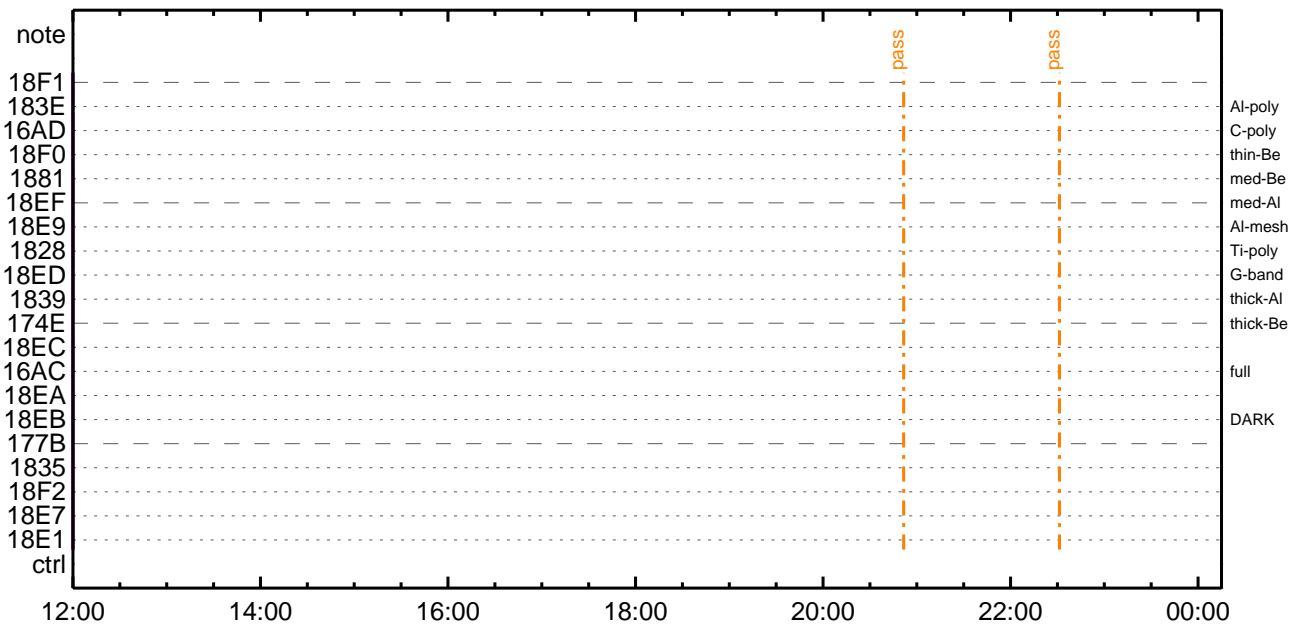
CMDI #0657 2012/06/08



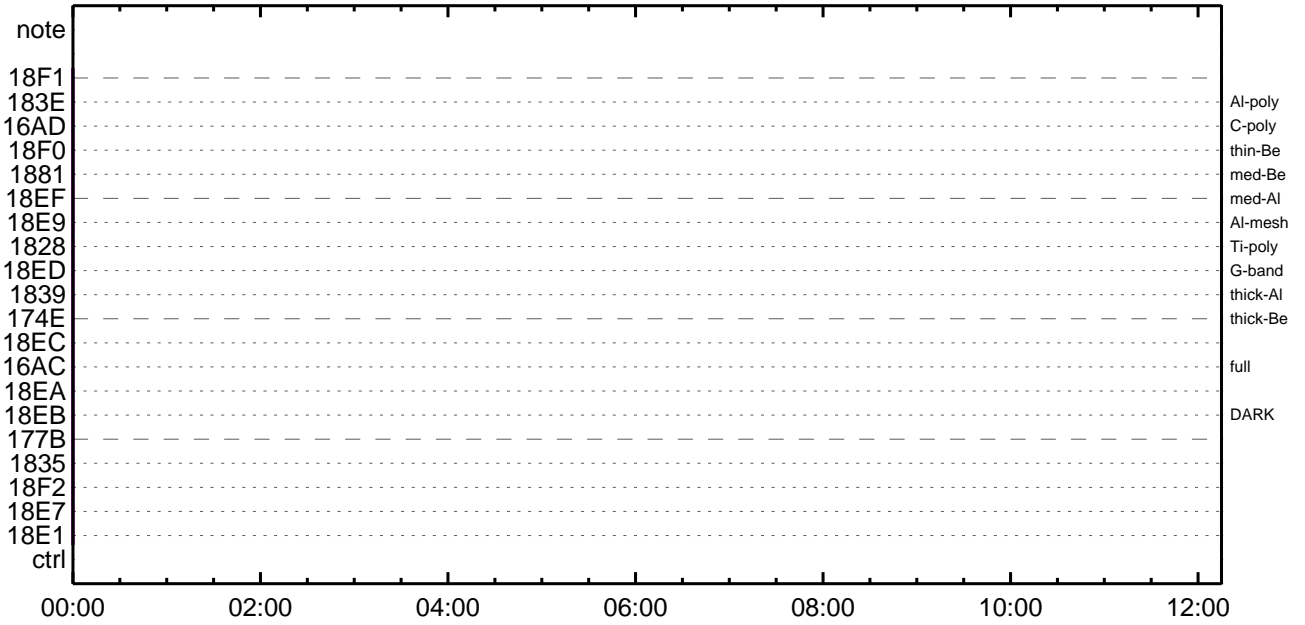
CMDI #0657 2012/06/09



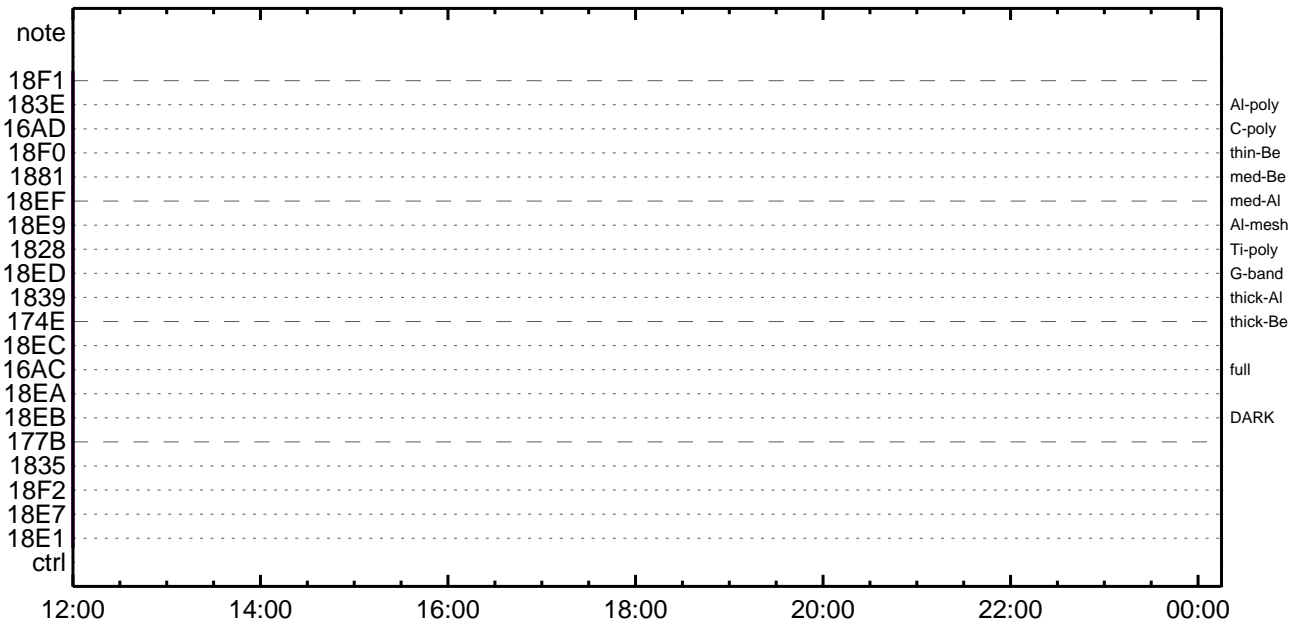
CMDI #0657 2012/06/09



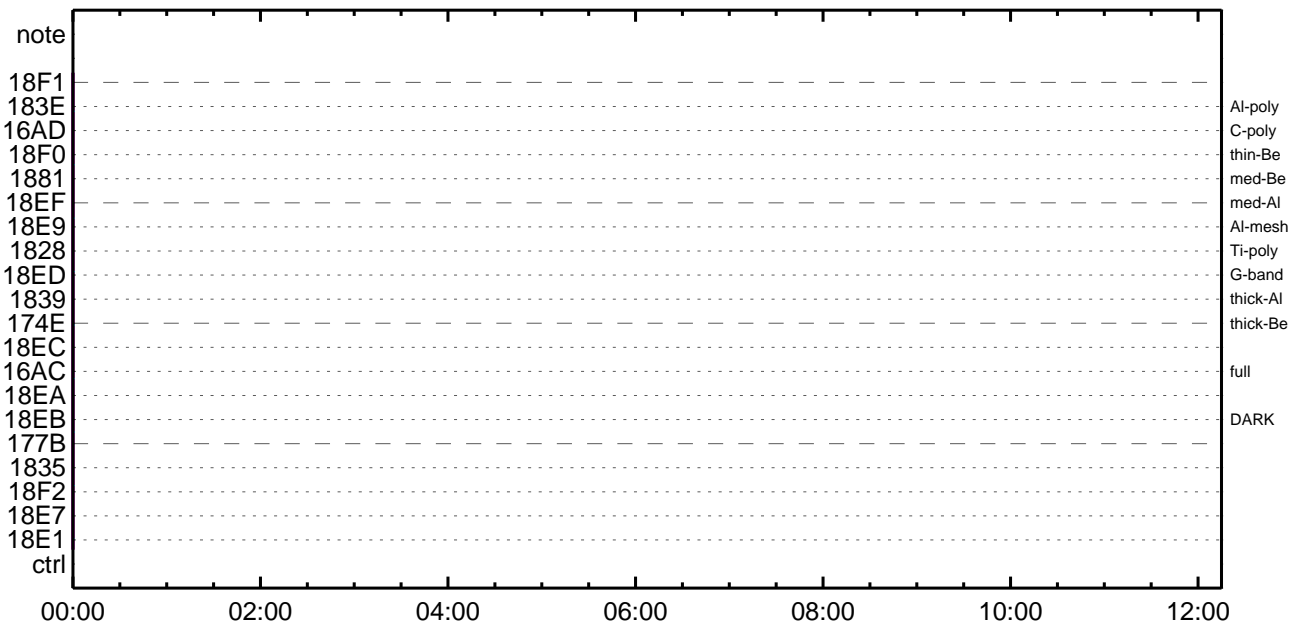
CMDI #0657 2012/06/10



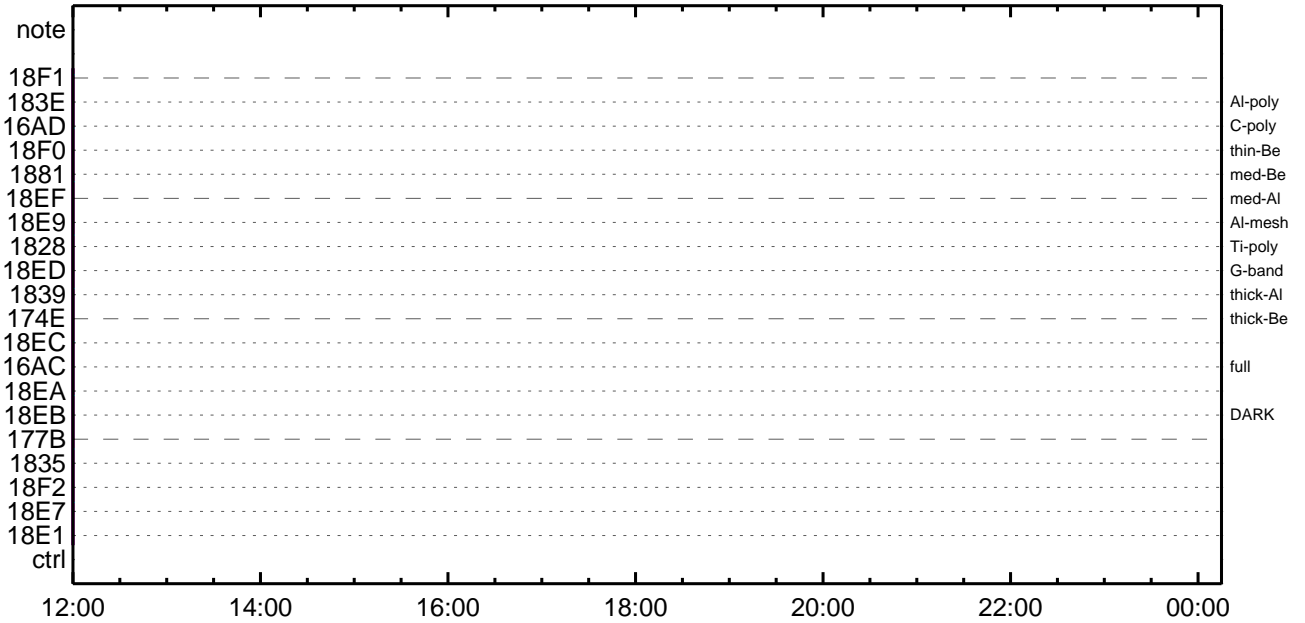
CMDI #0657 2012/06/10



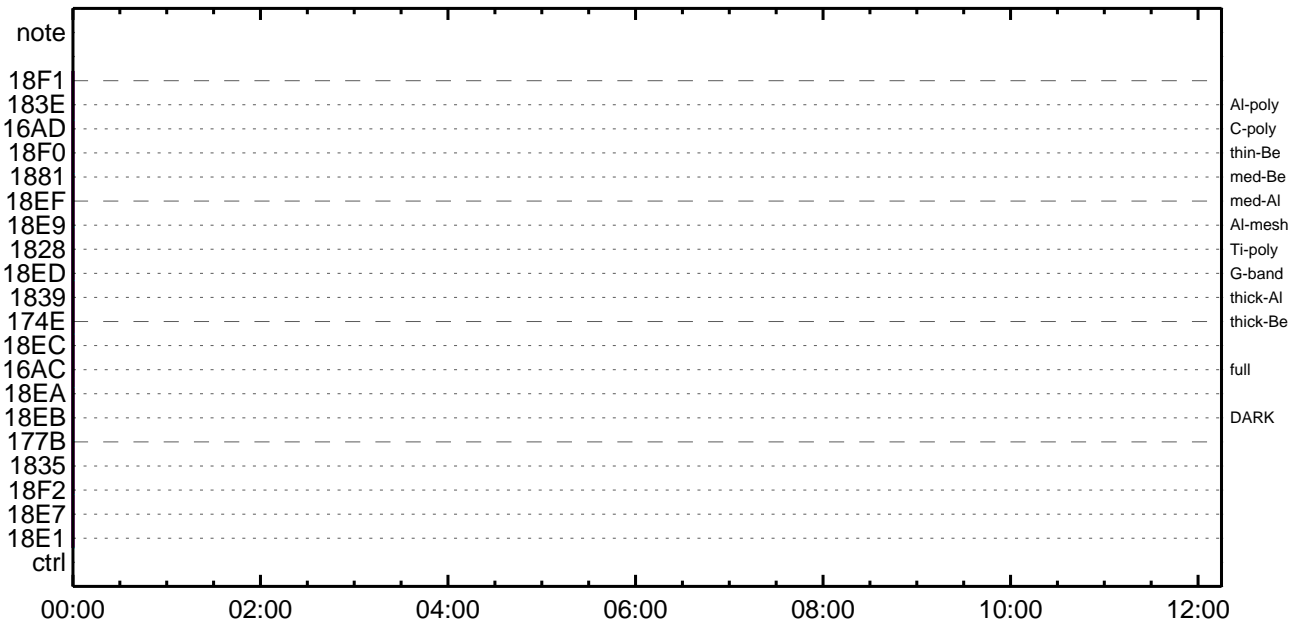
CMDI #0657 2012/06/11



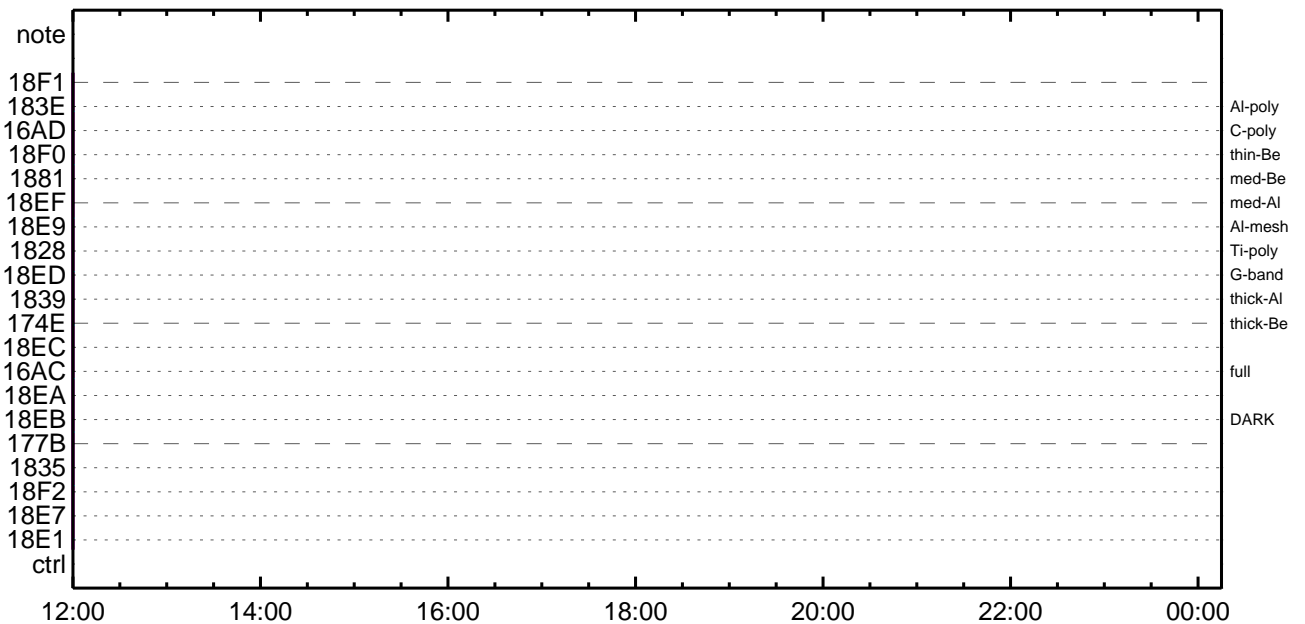
CMDI #0657 2012/06/11



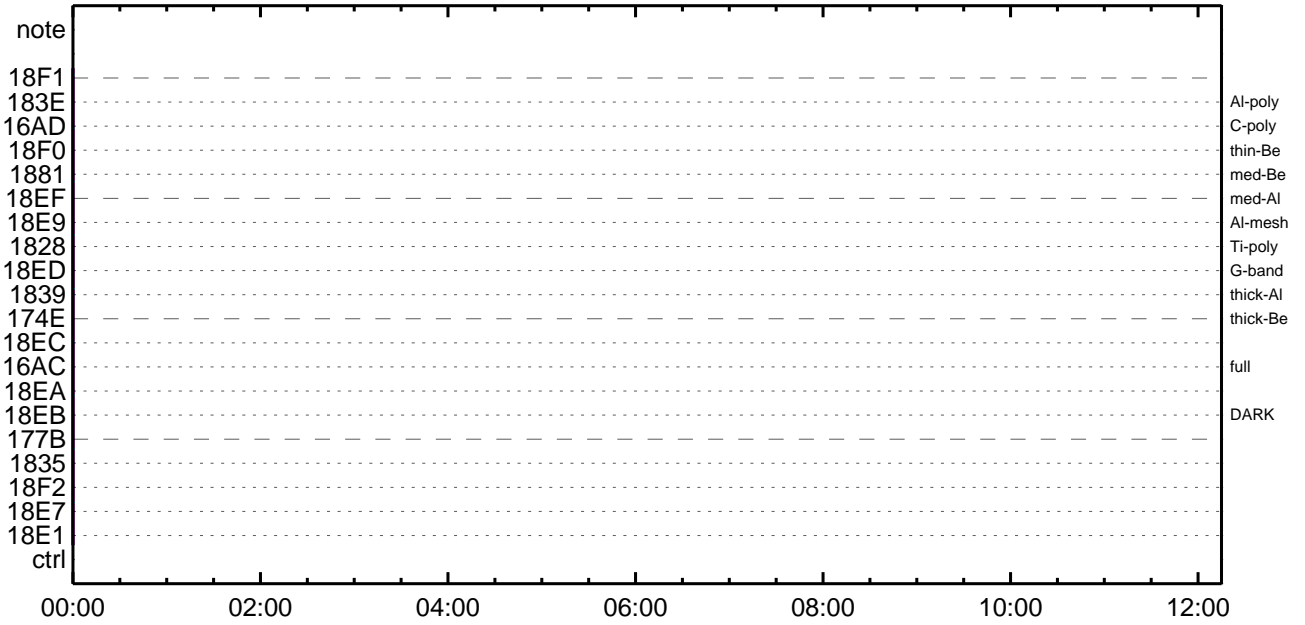
CMDI #0657 2012/06/12



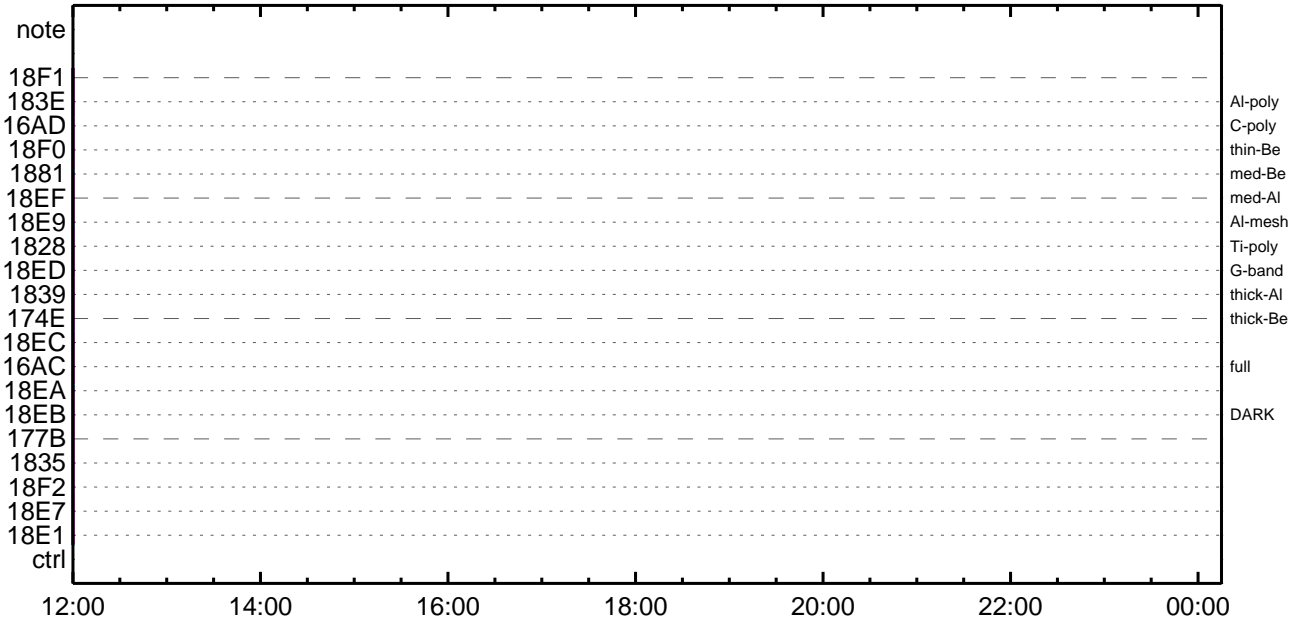
CMDI #0657 2012/06/12



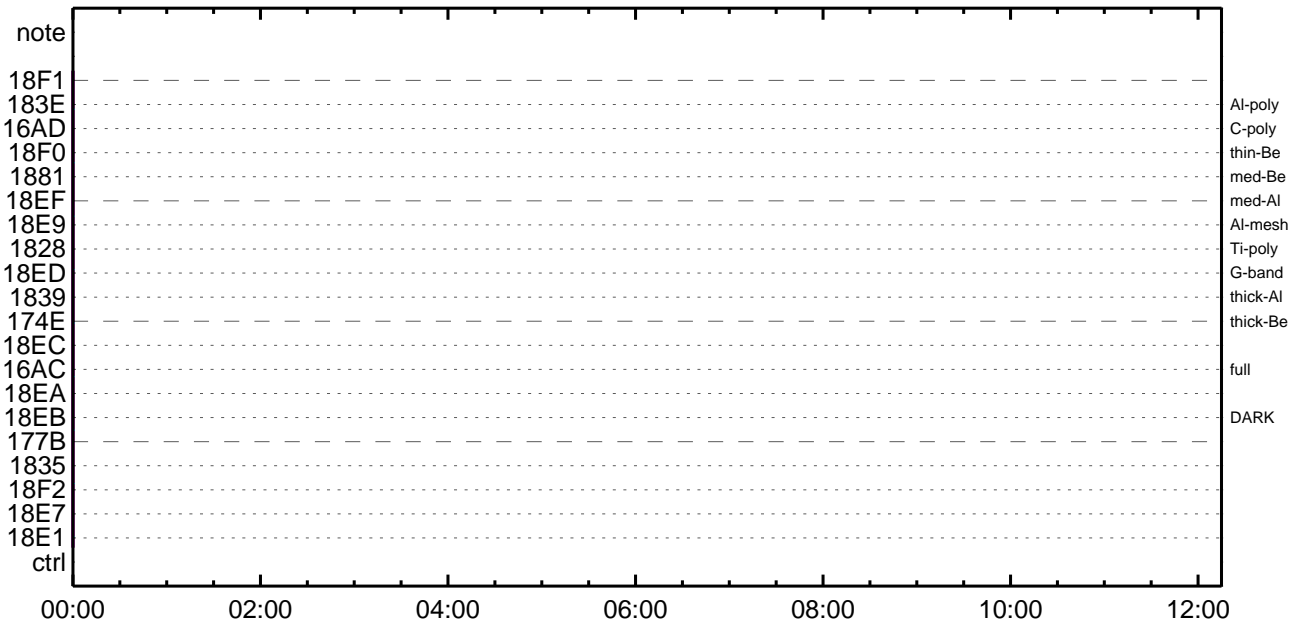
CMDI #0657 2012/06/13



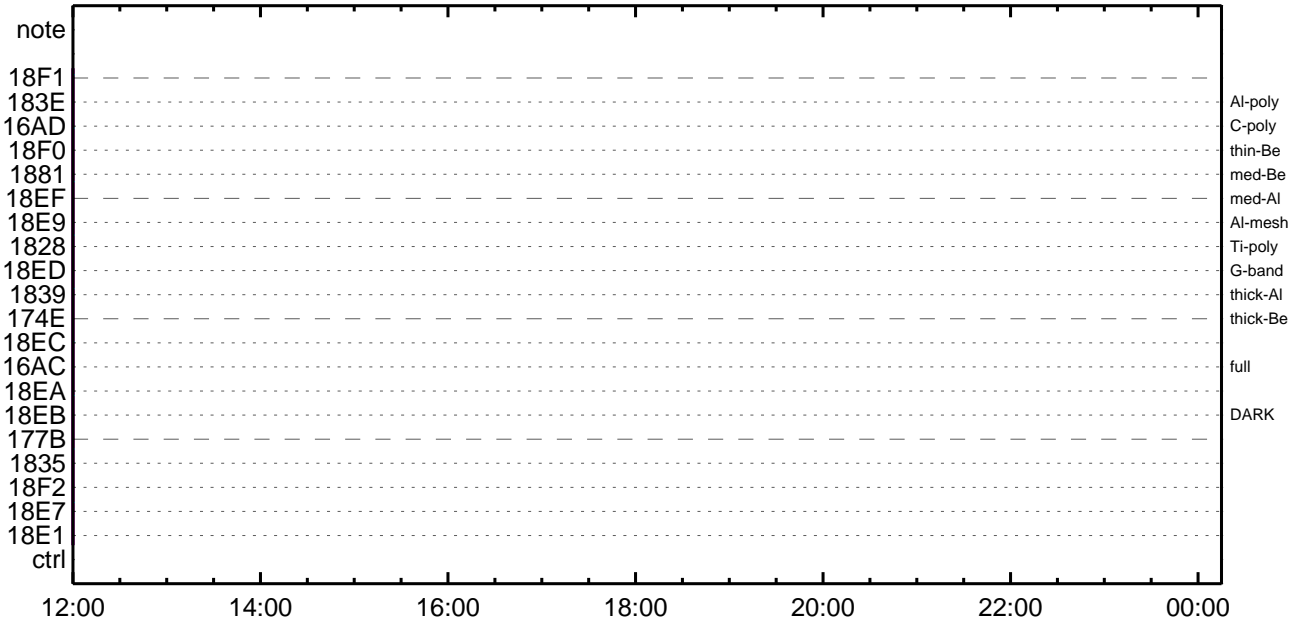
CMDI #0657 2012/06/13



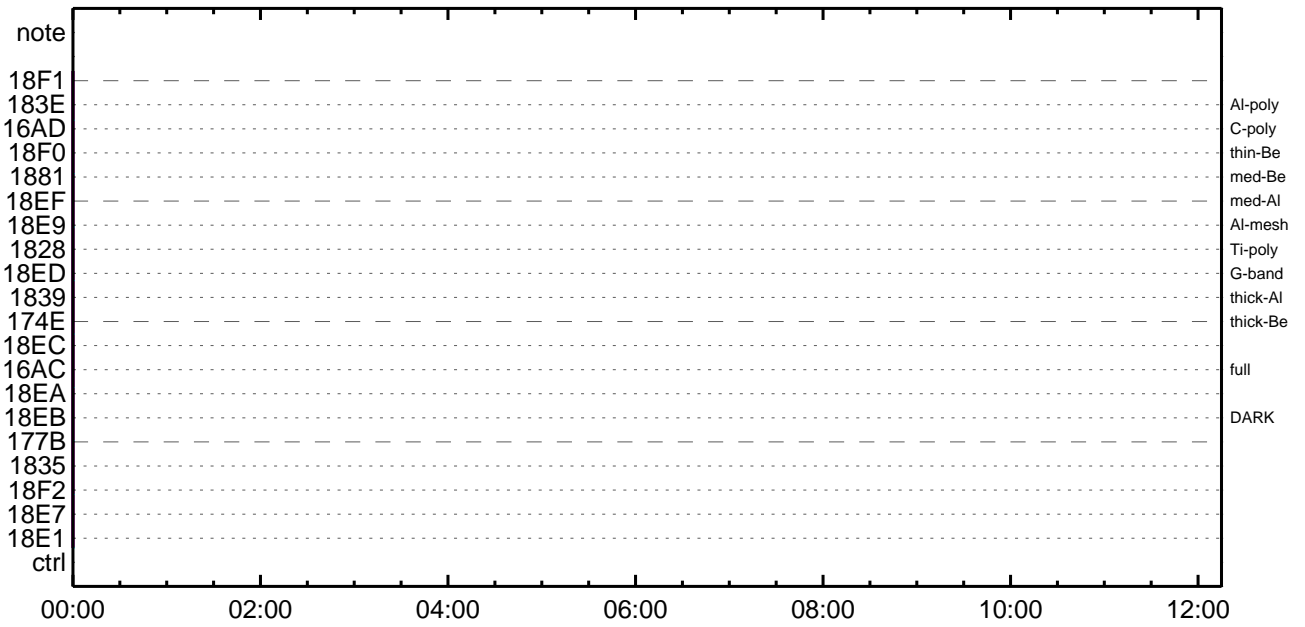
CMDI #0657 2012/06/14



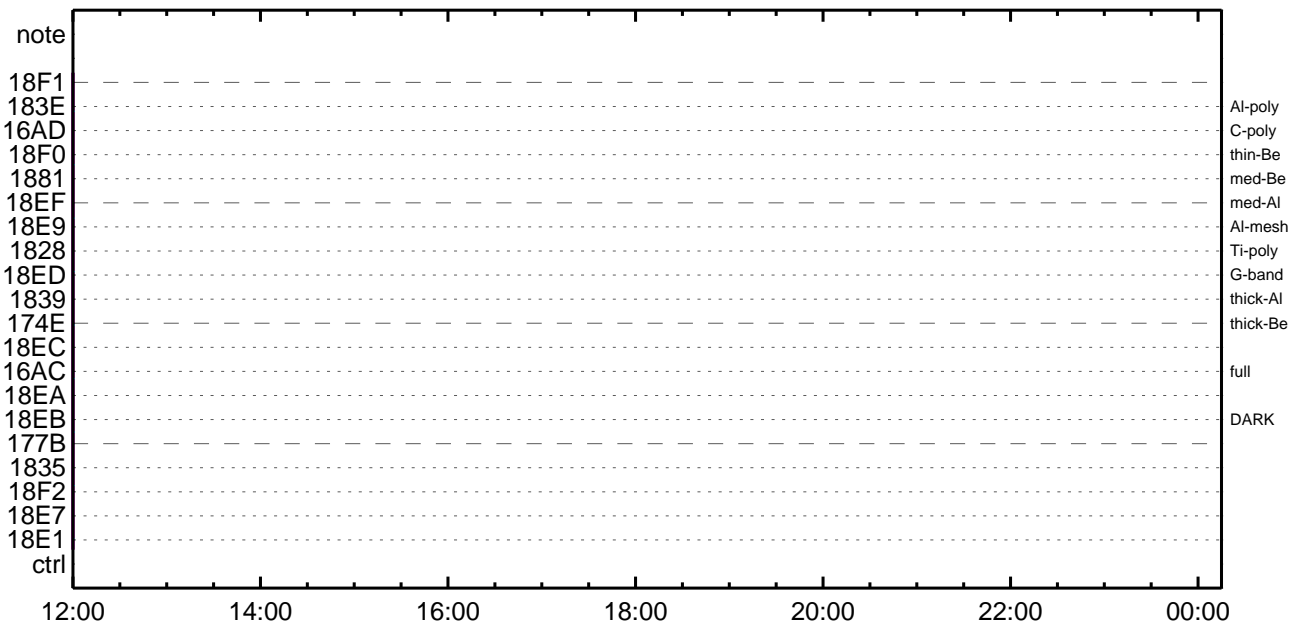
CMDI #0657 2012/06/14



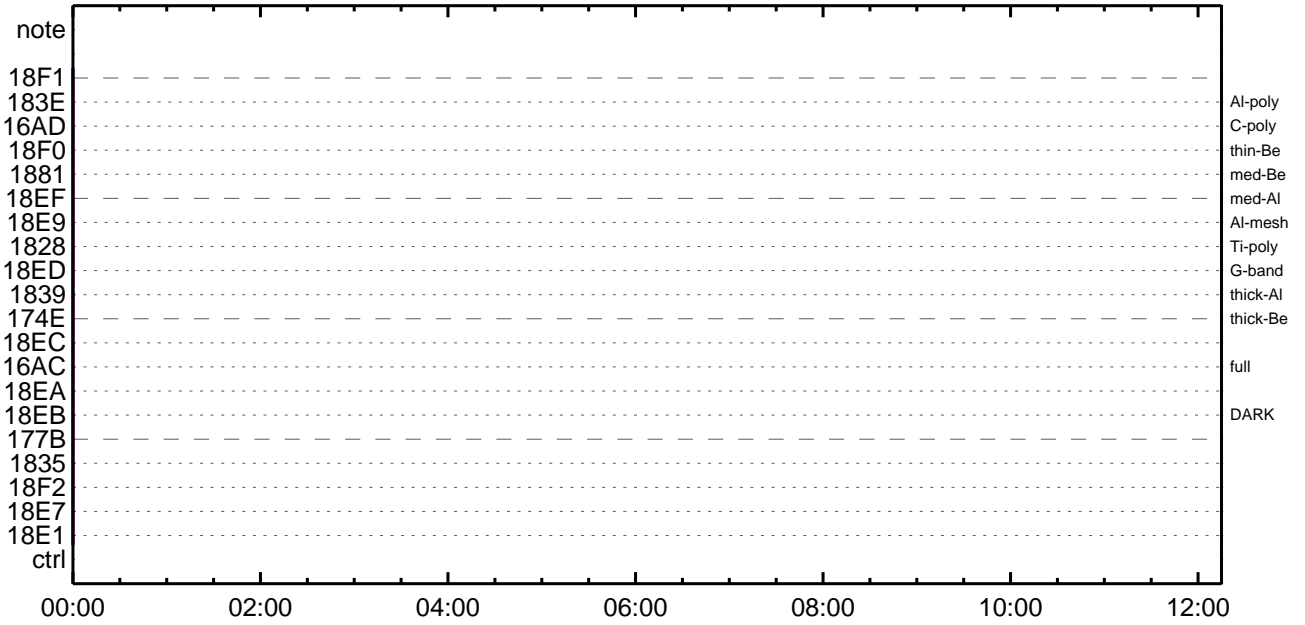
CMDI #0657 2012/06/15



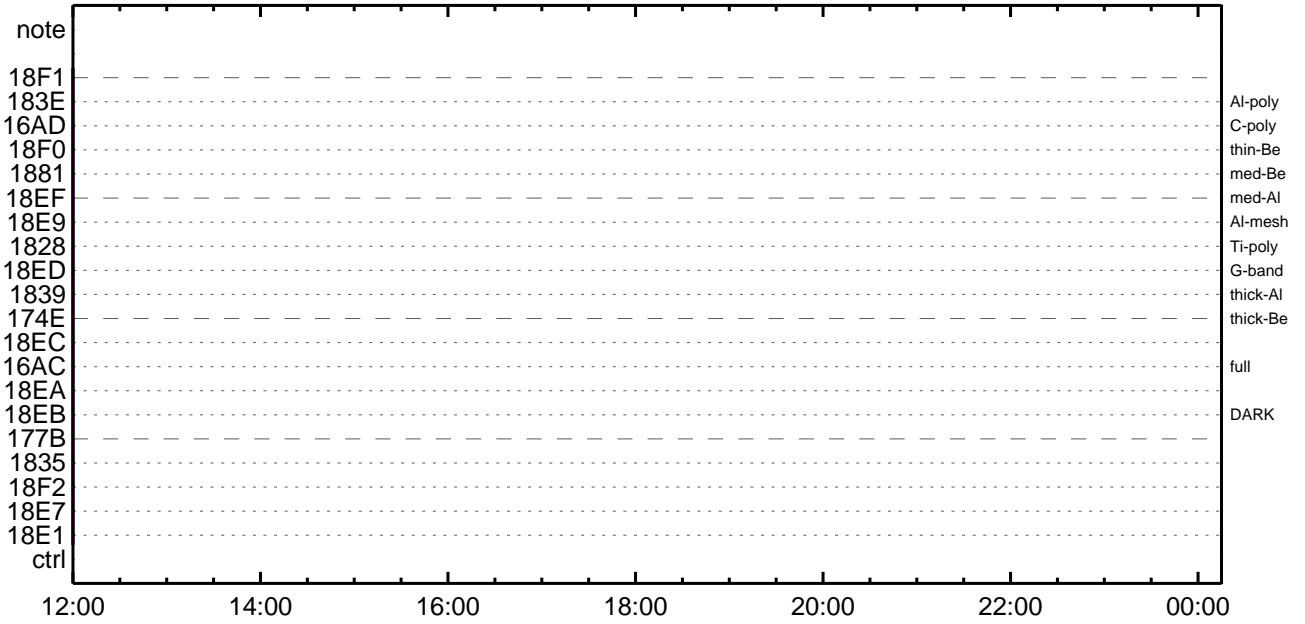
CMDI #0657 2012/06/15



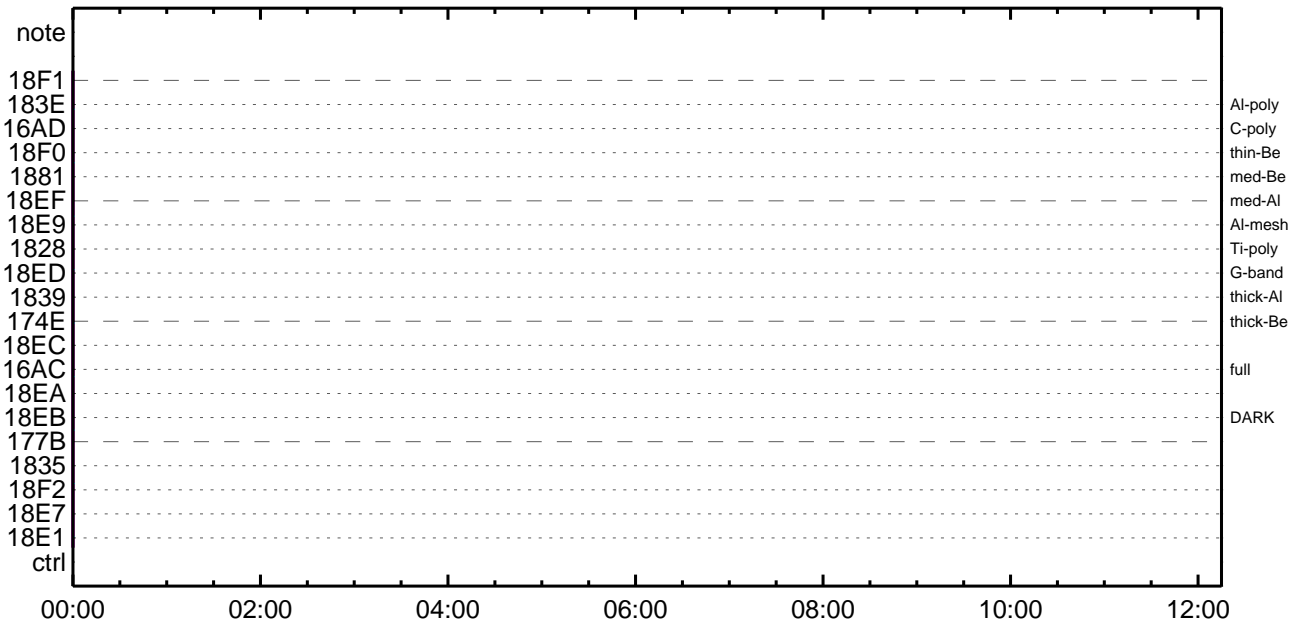
CMDI #0657 2012/06/16



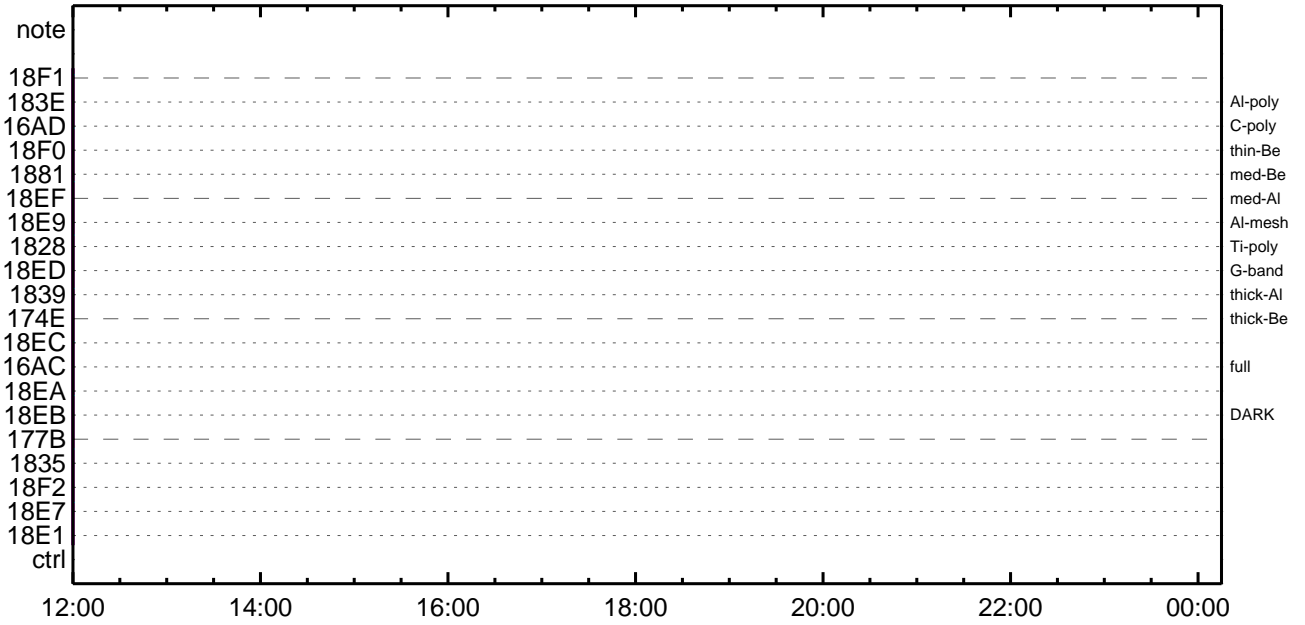
CMDI #0657 2012/06/16



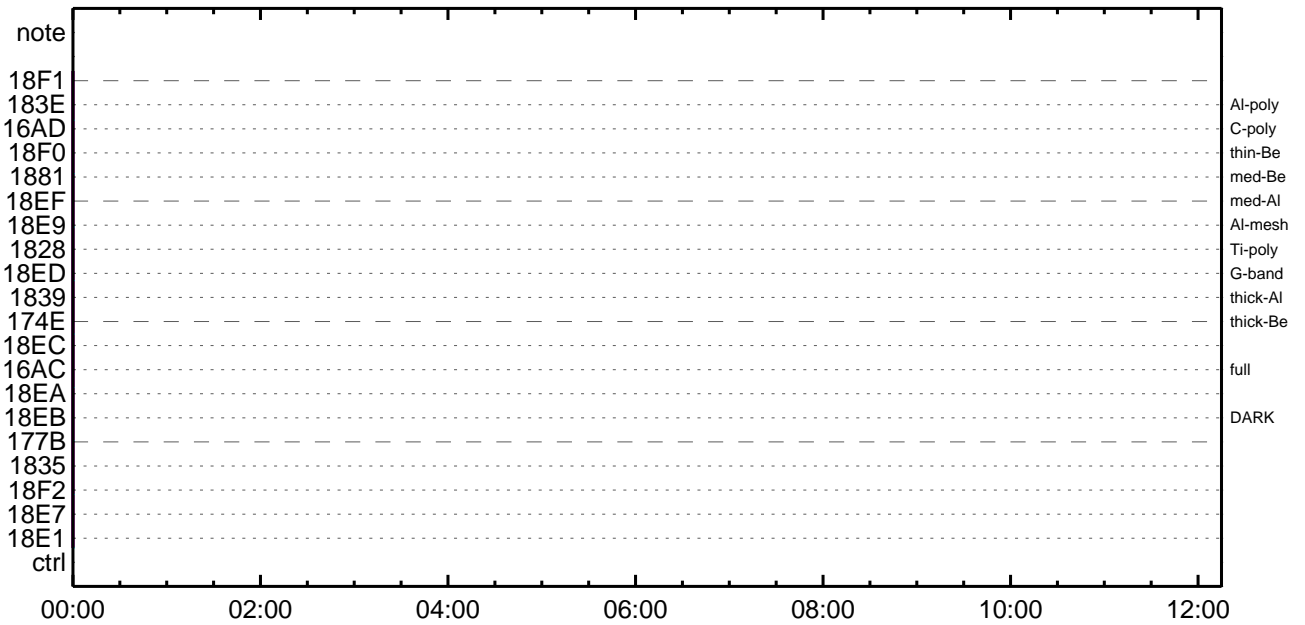
CMDI #0657 2012/06/17



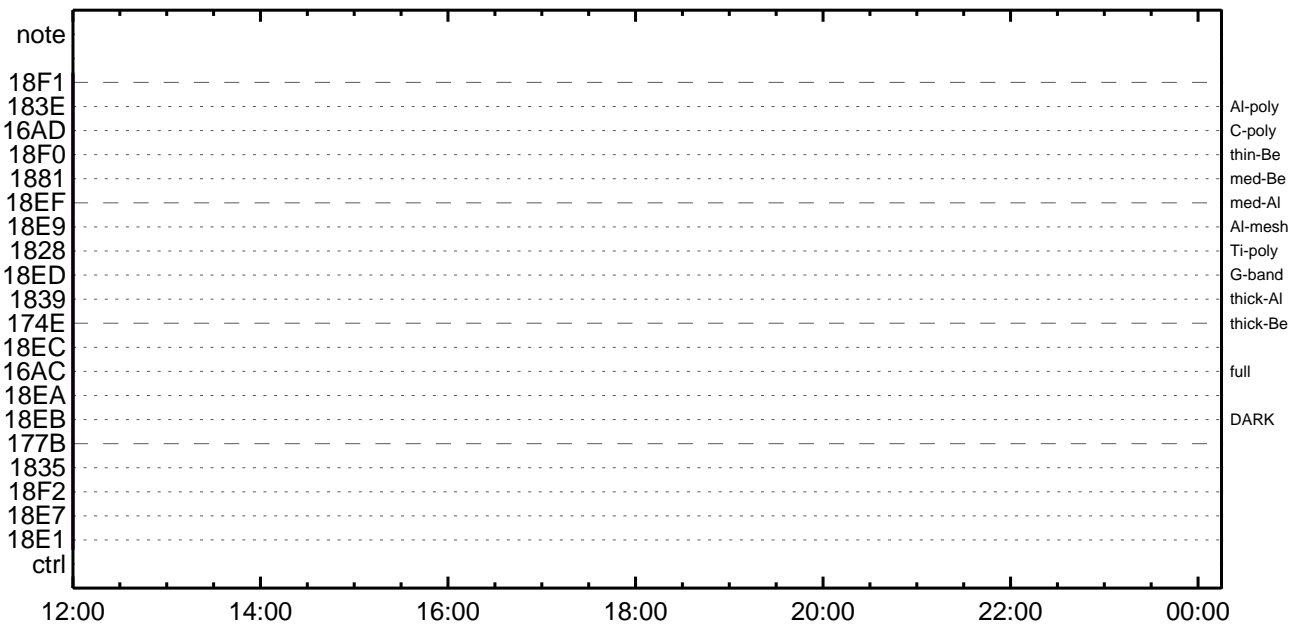
CMDI #0657 2012/06/17



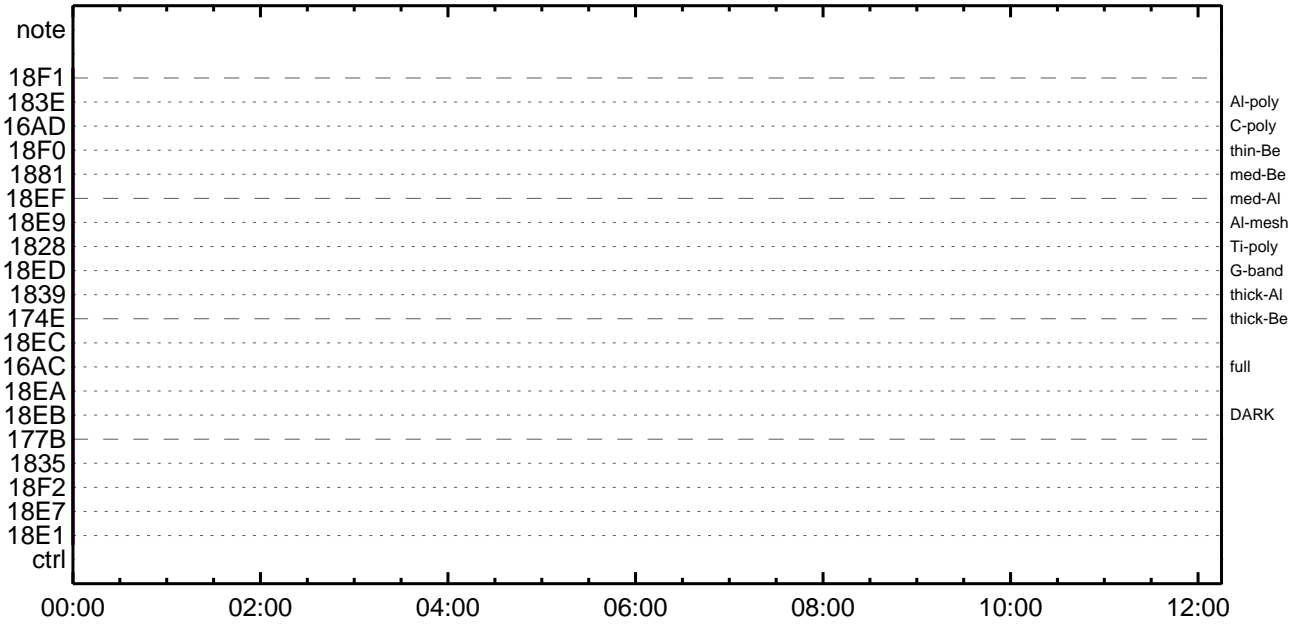
CMDI #0657 2012/06/18



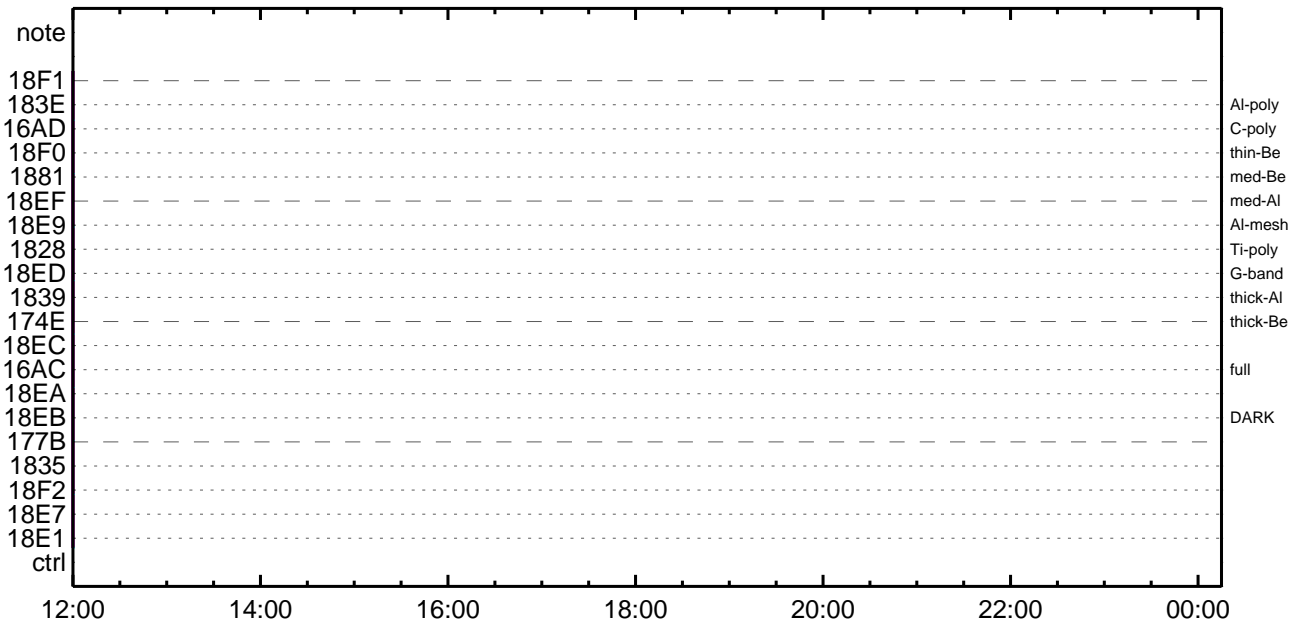
CMDI #0657 2012/06/18



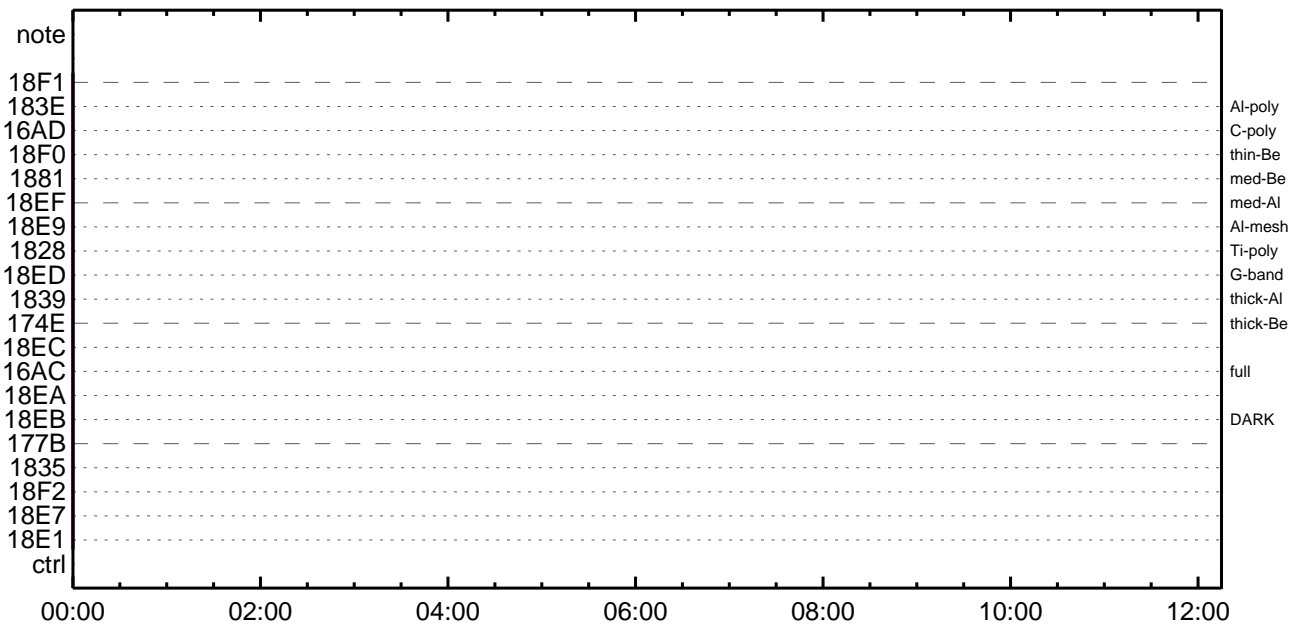
CMDI #0657 2012/06/19



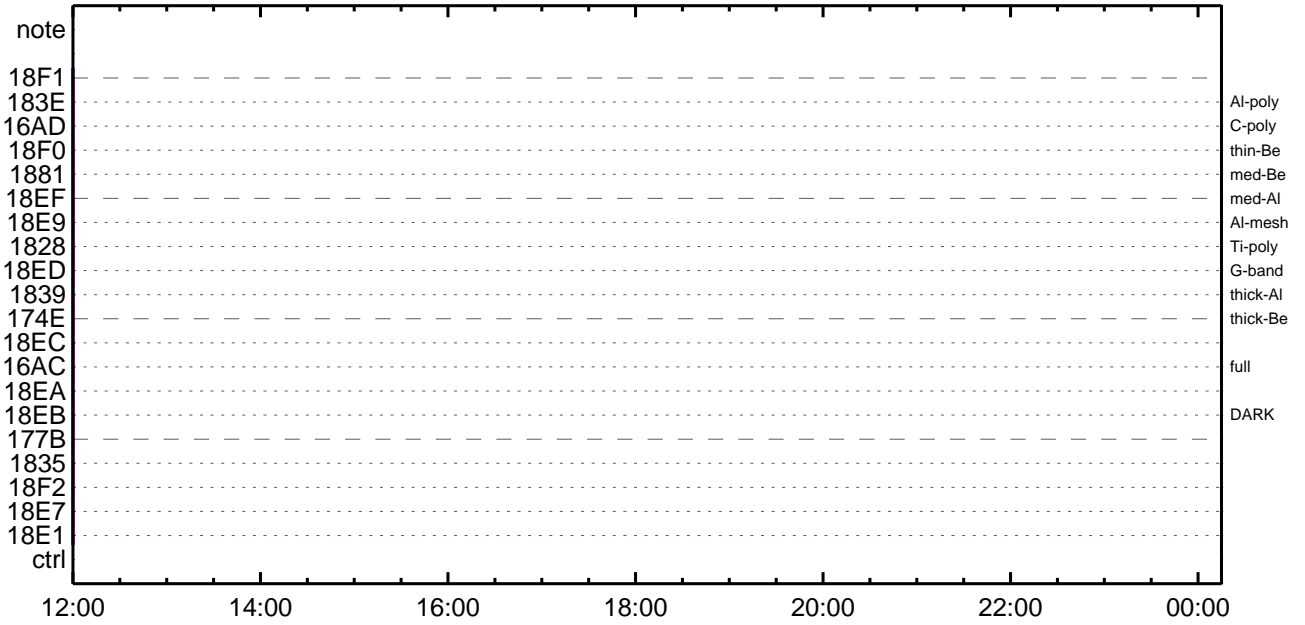
CMDI #0657 2012/06/19



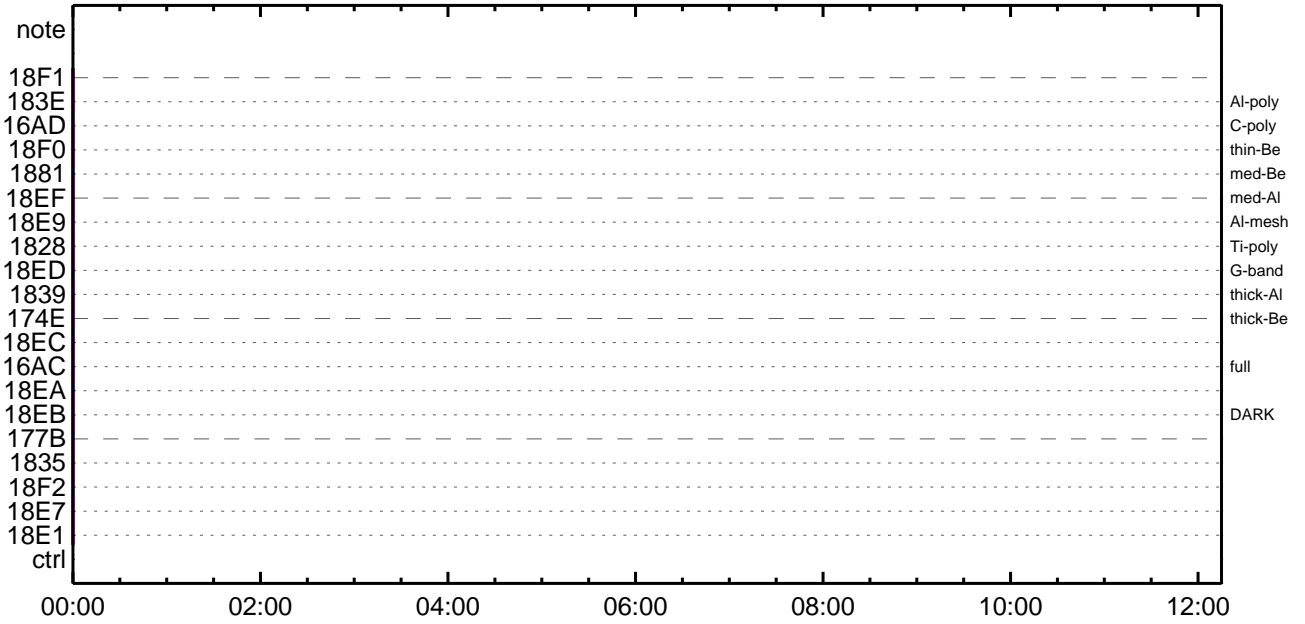
CMDI #0657 2012/06/20



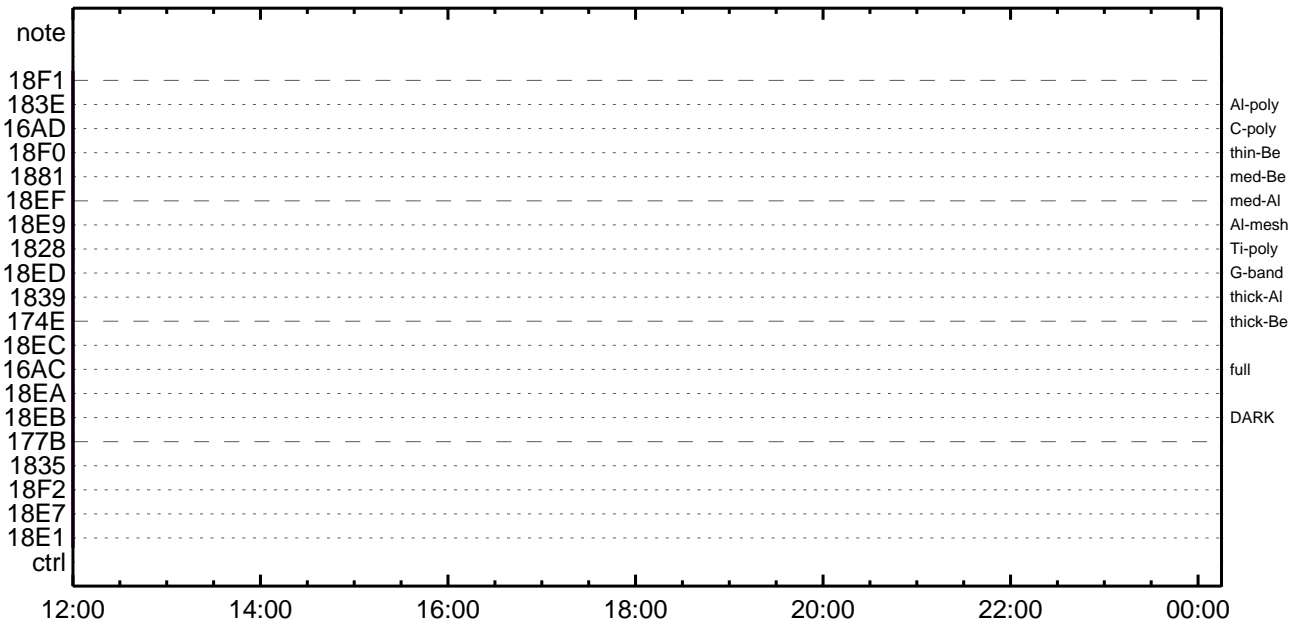
CMDI #0657 2012/06/20



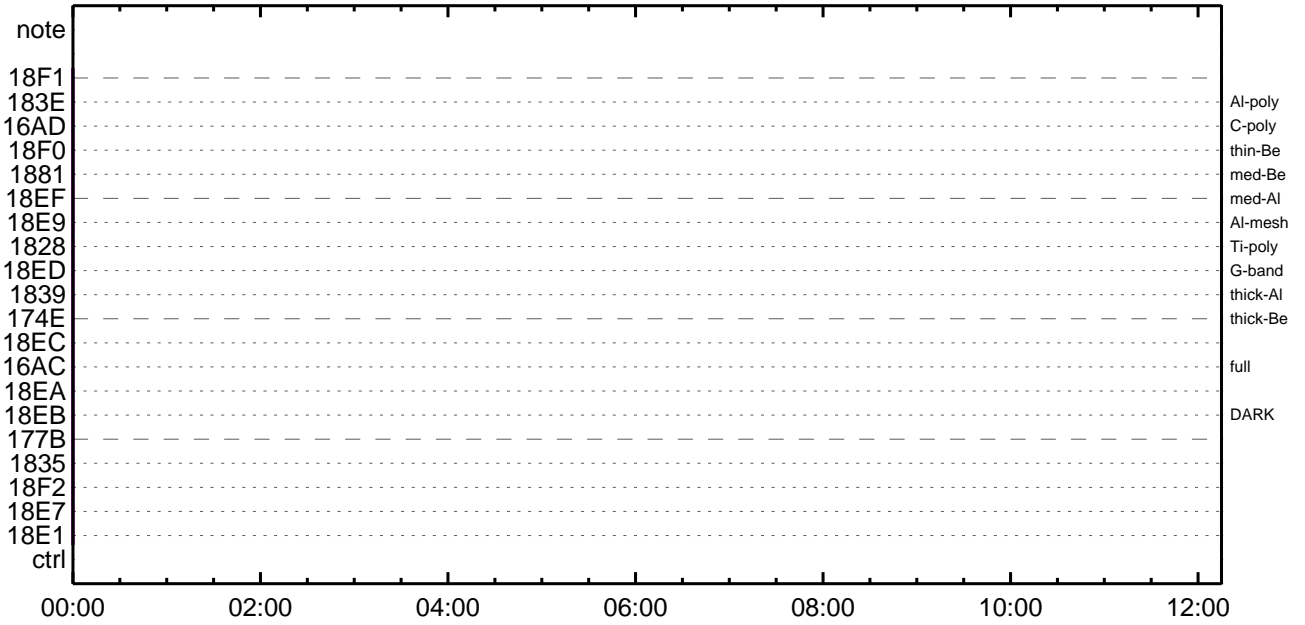
CMDI #0657 2012/06/21



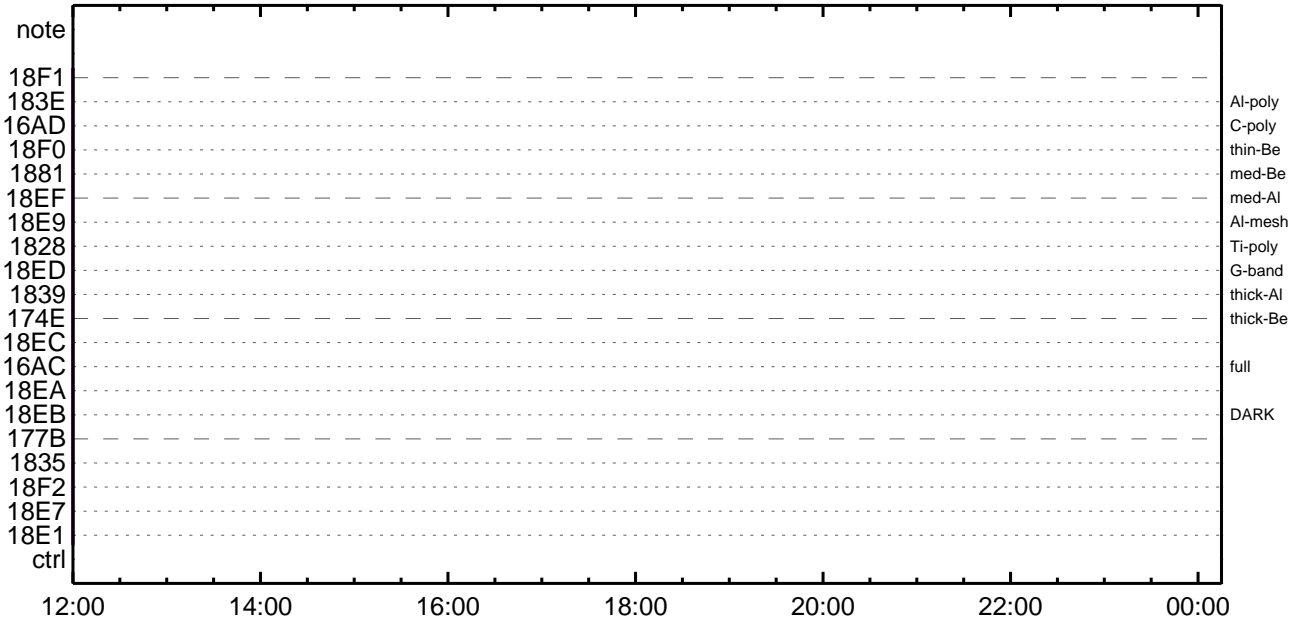
CMDI #0657 2012/06/21



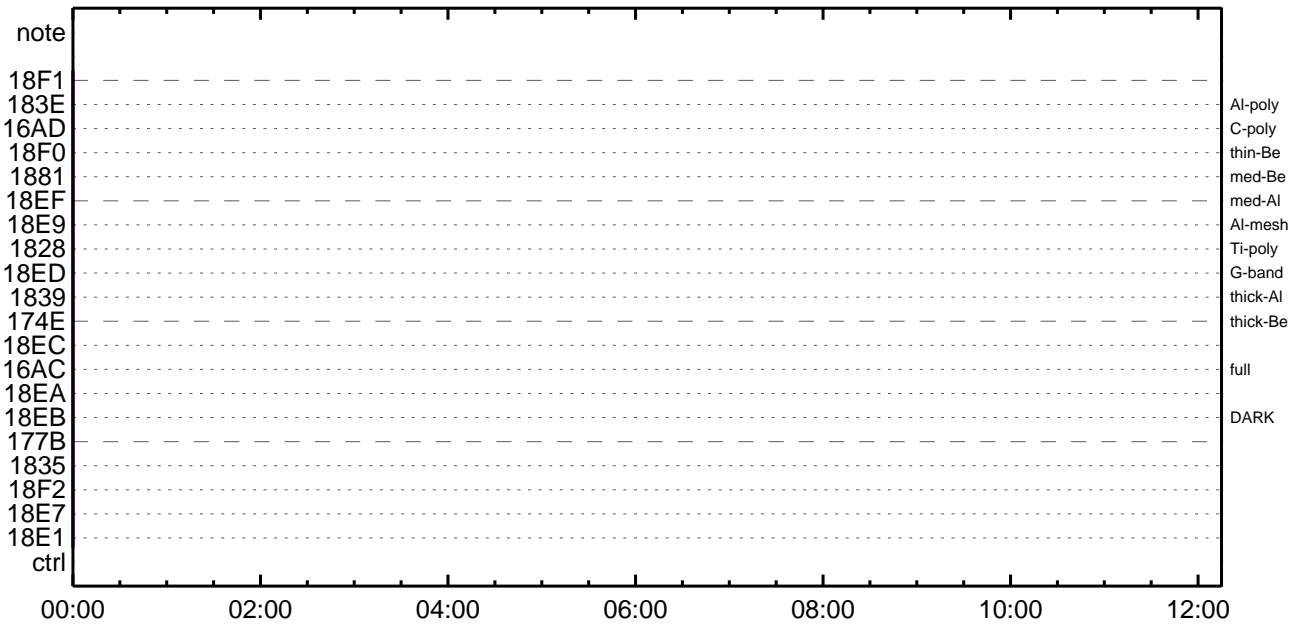
CMDI #0657 2012/06/22



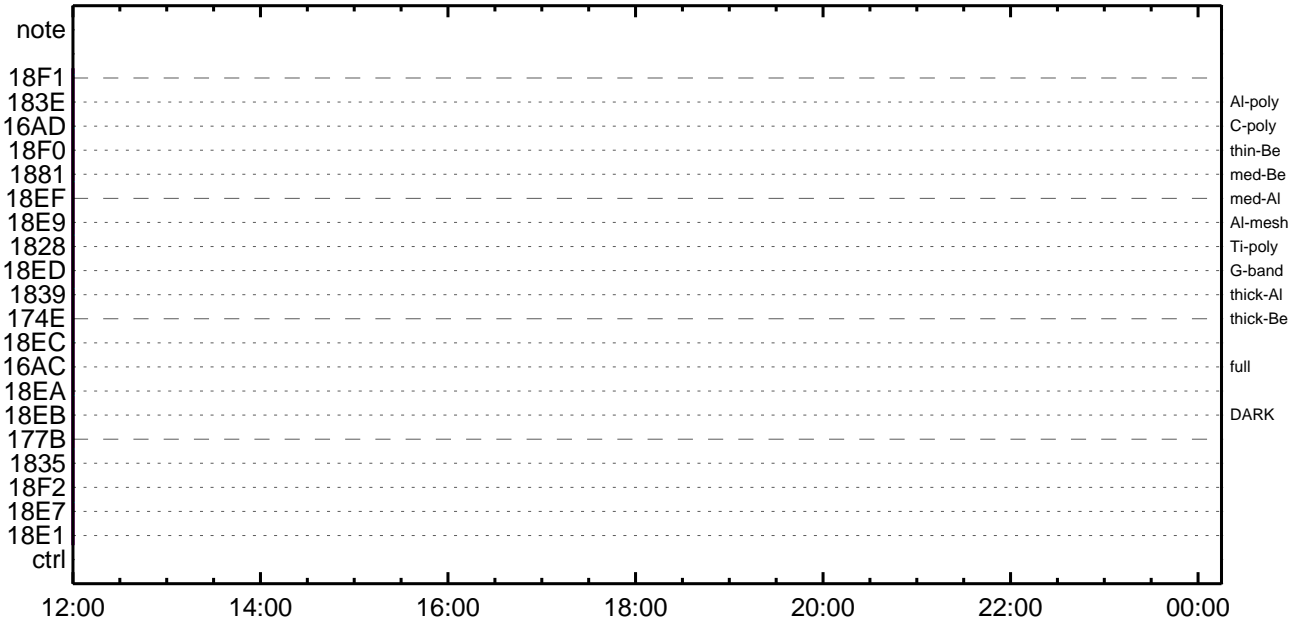
CMDI #0657 2012/06/22



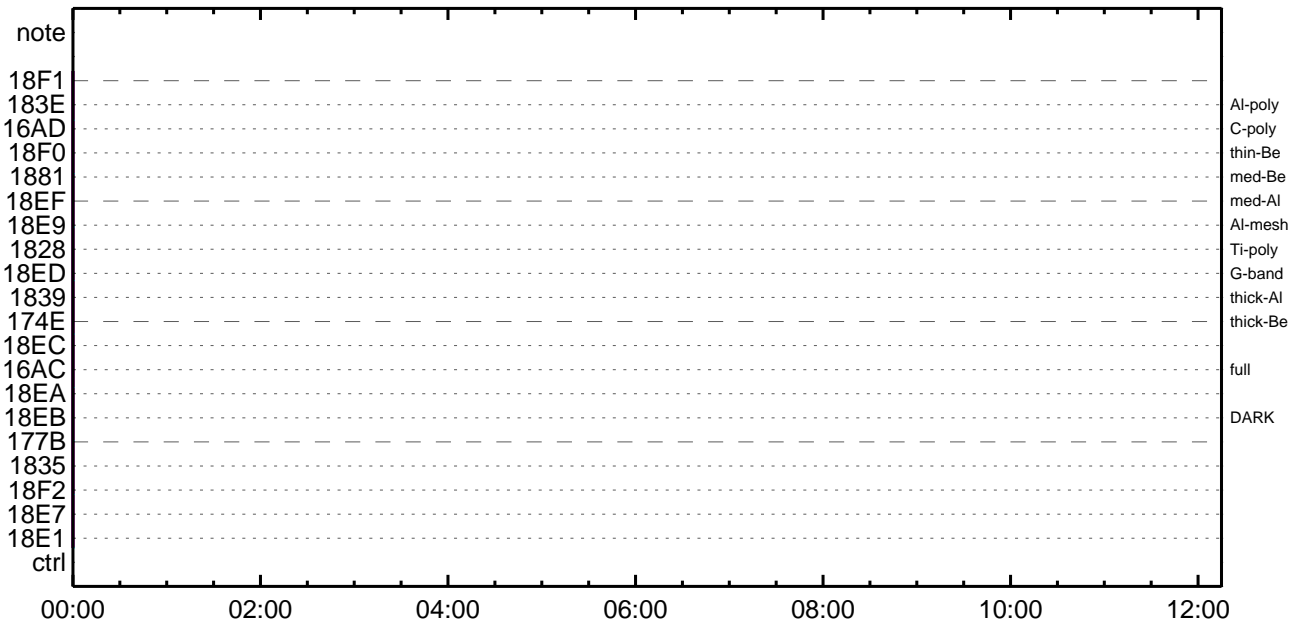
CMDI #0657 2012/06/23



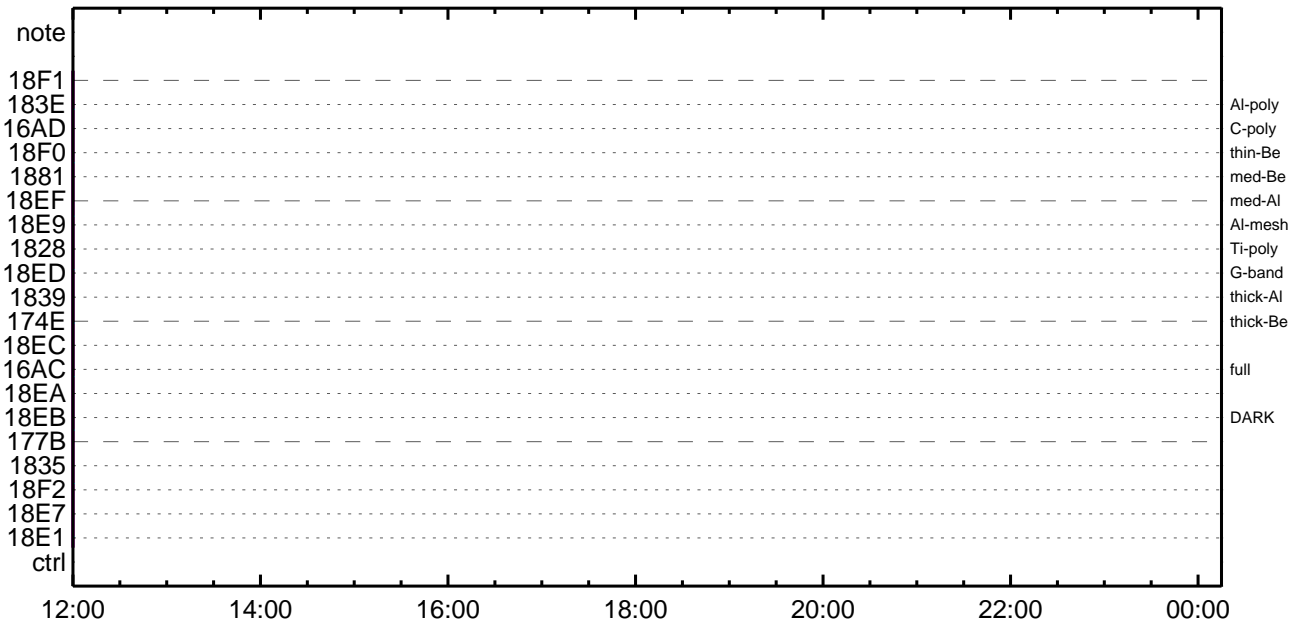
CMDI #0657 2012/06/23



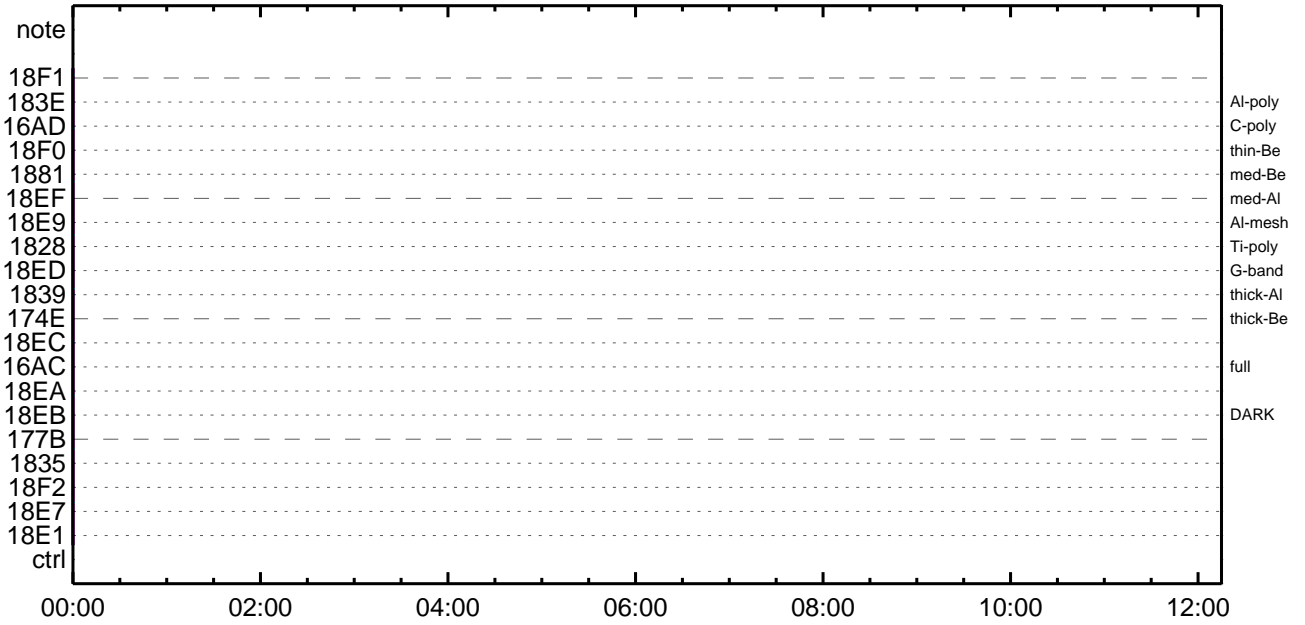
CMDI #0657 2012/06/24



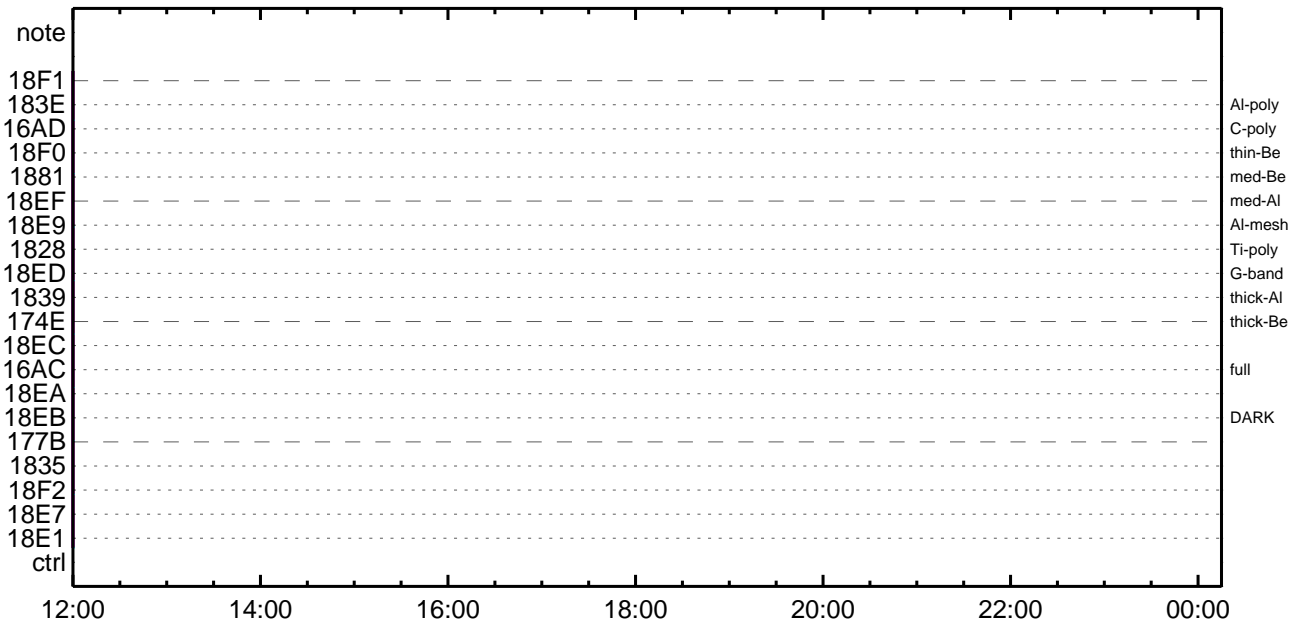
CMDI #0657 2012/06/24



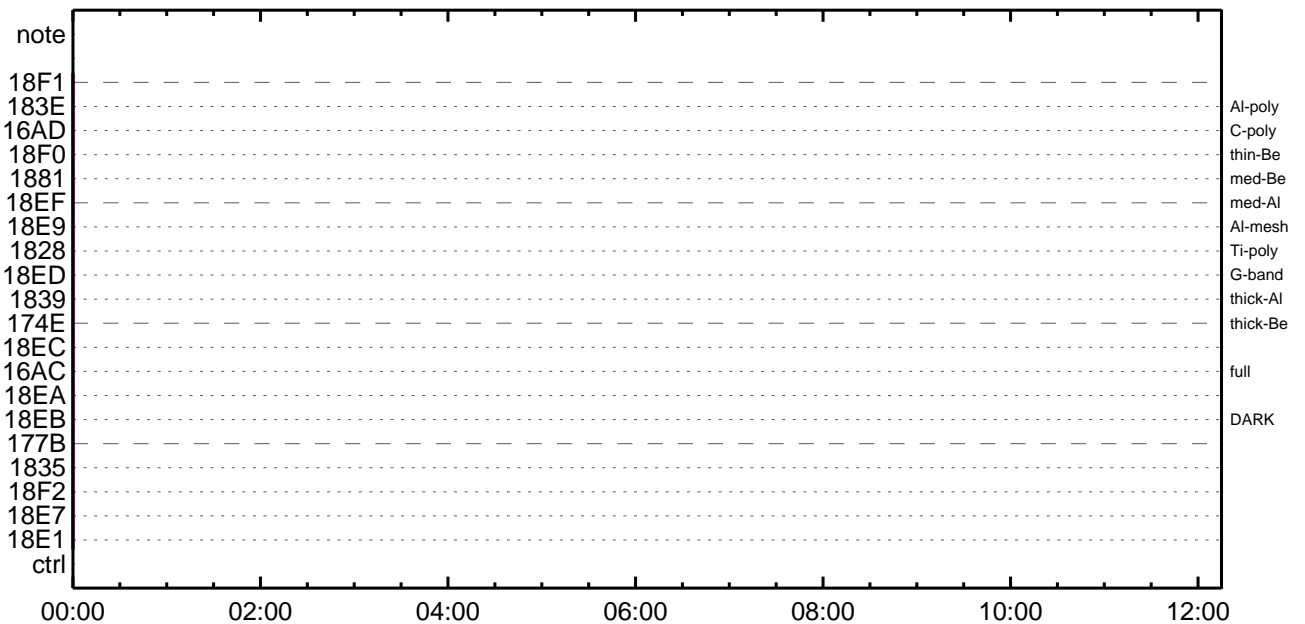
CMDI #0657 2012/06/25



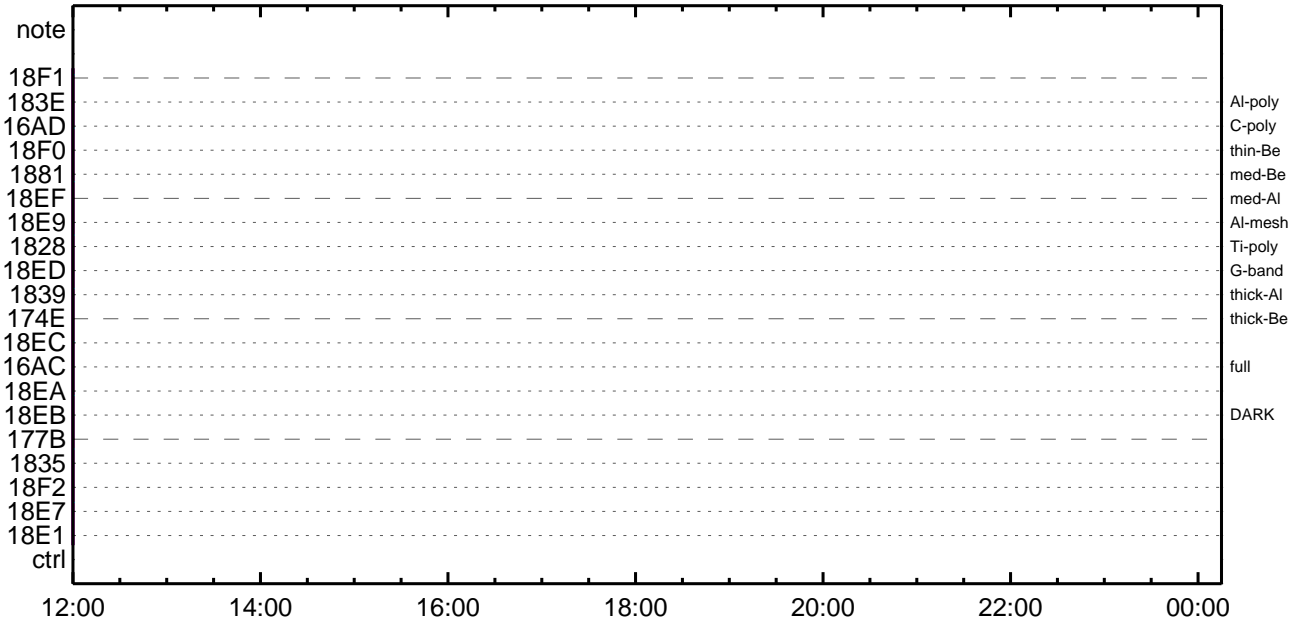
CMDI #0657 2012/06/25



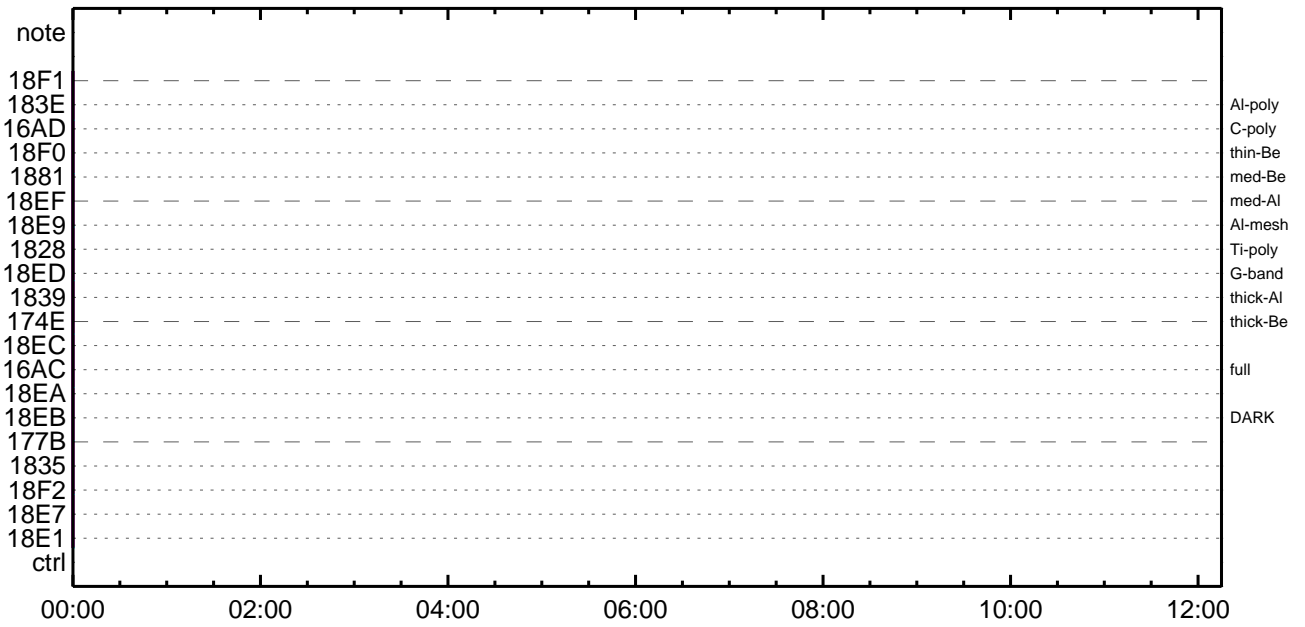
CMDI #0657 2012/06/26



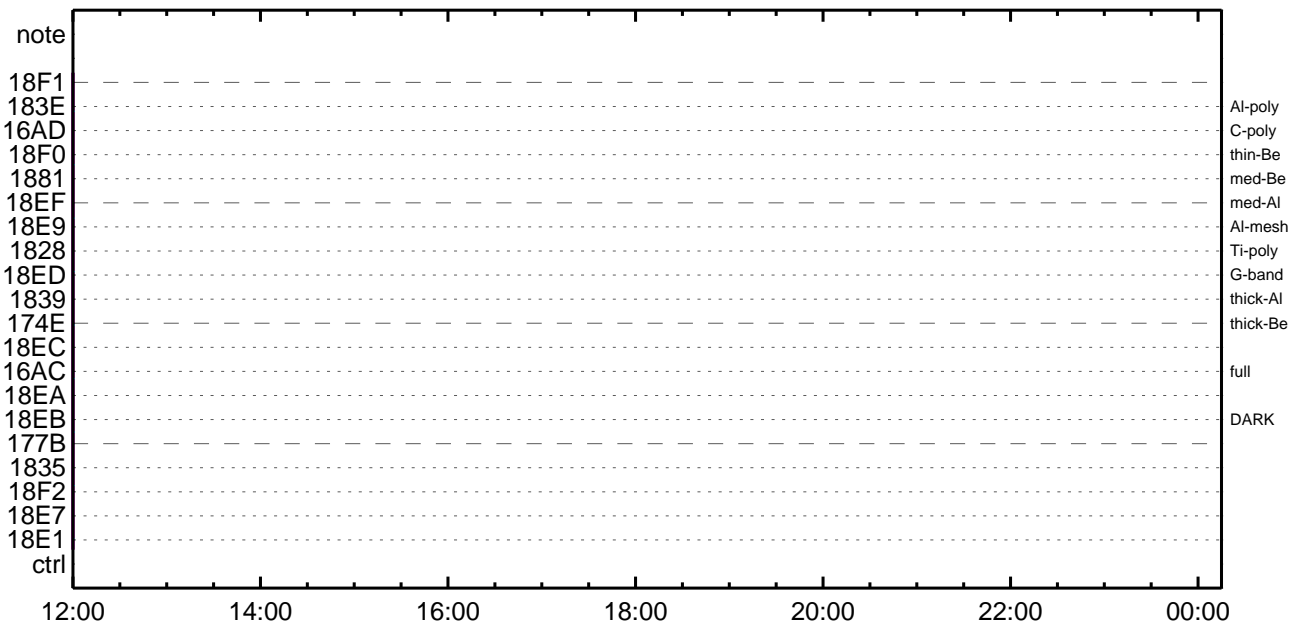
CMDI #0657 2012/06/26



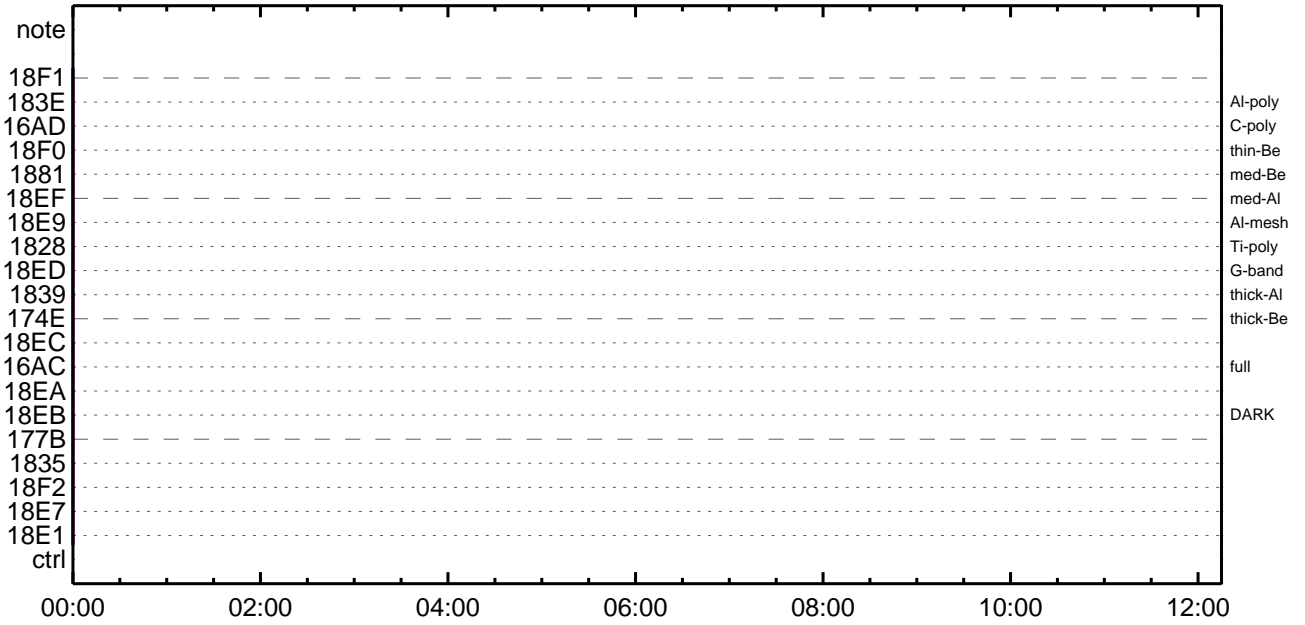
CMDI #0657 2012/06/27



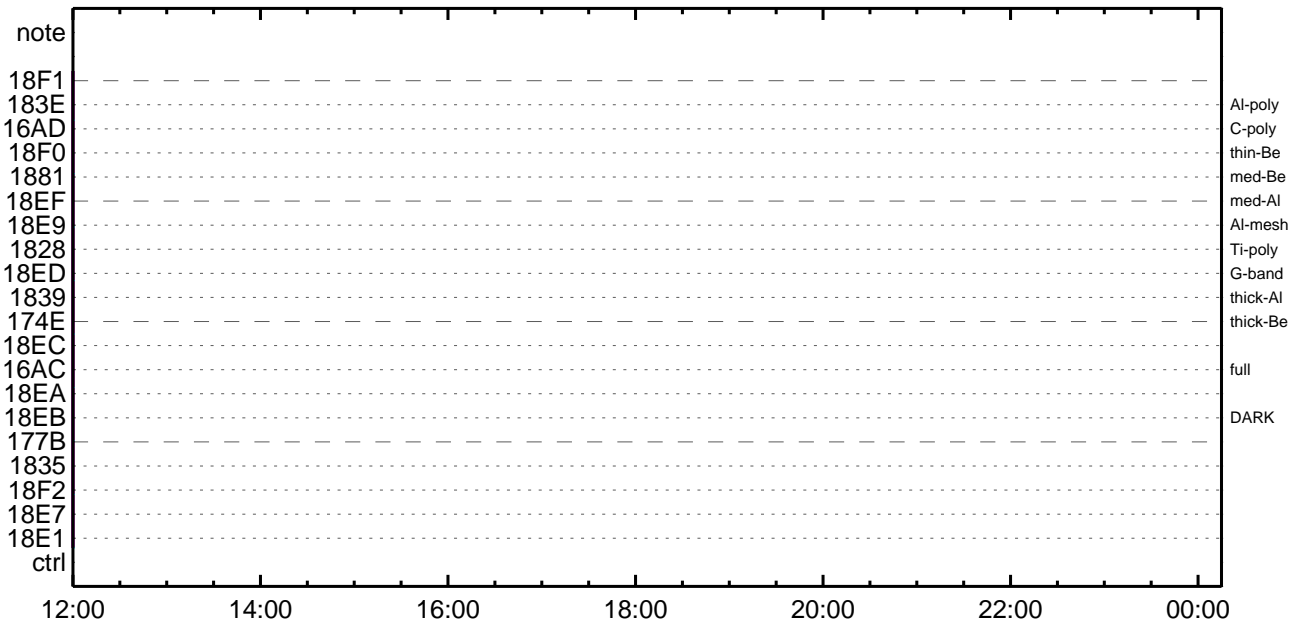
CMDI #0657 2012/06/27



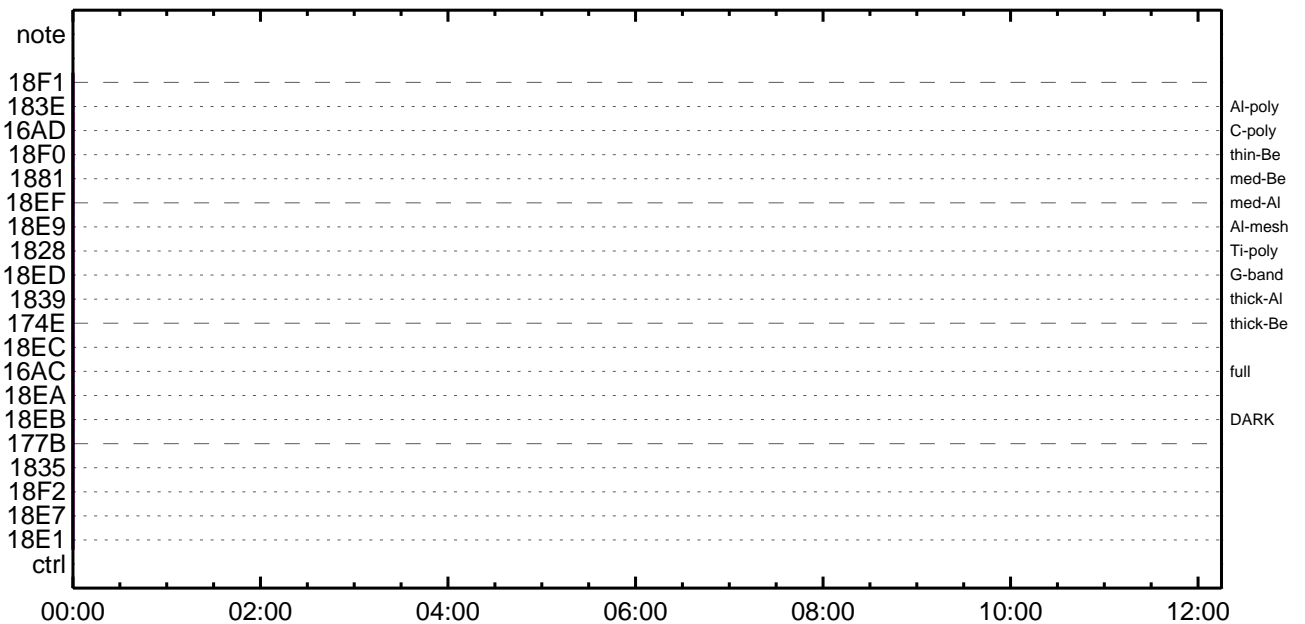
CMDI #0657 2012/06/28



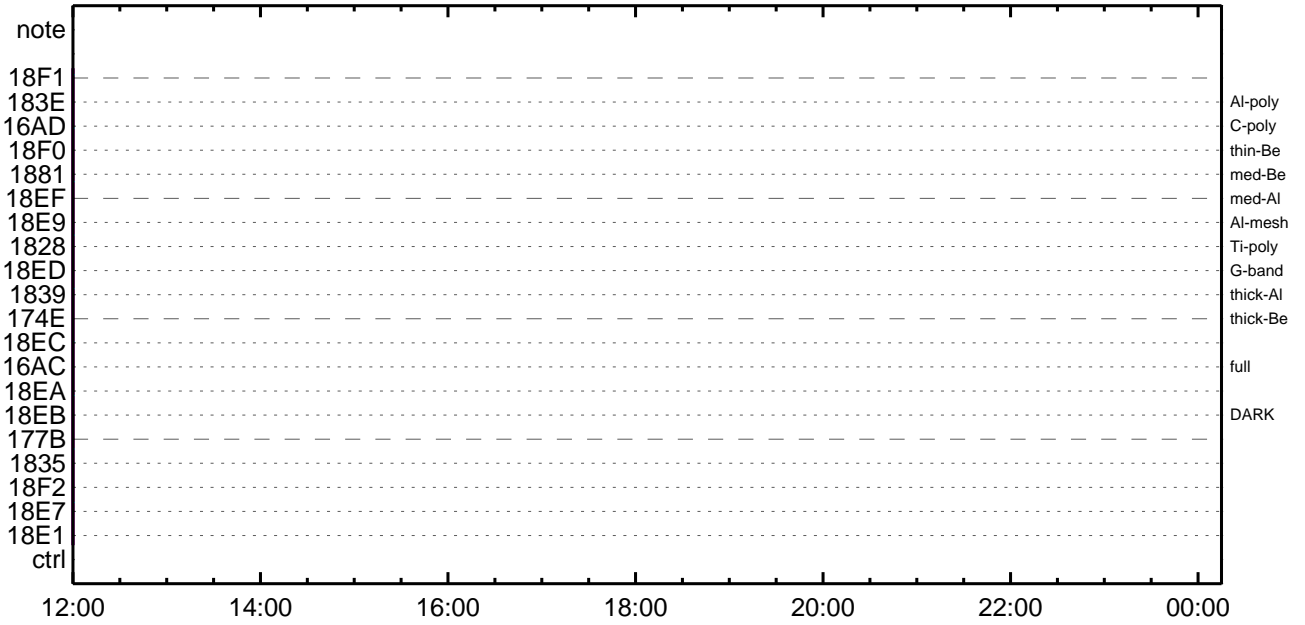
CMDI #0657 2012/06/28



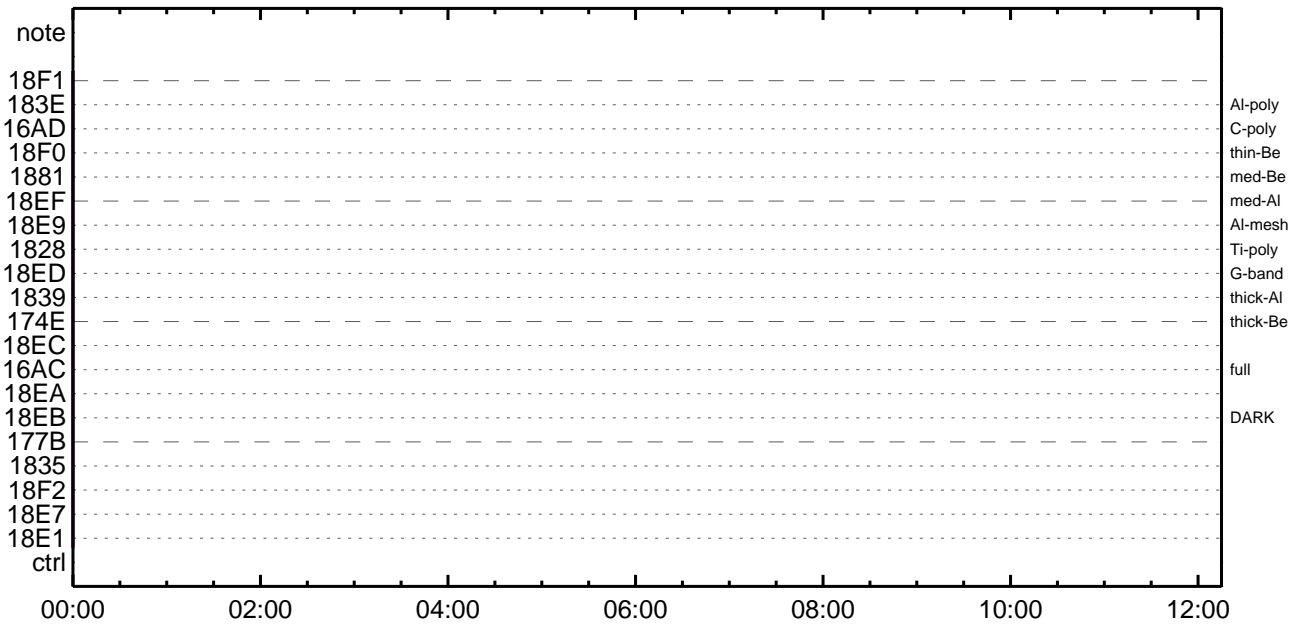
CMDI #0657 2012/06/29



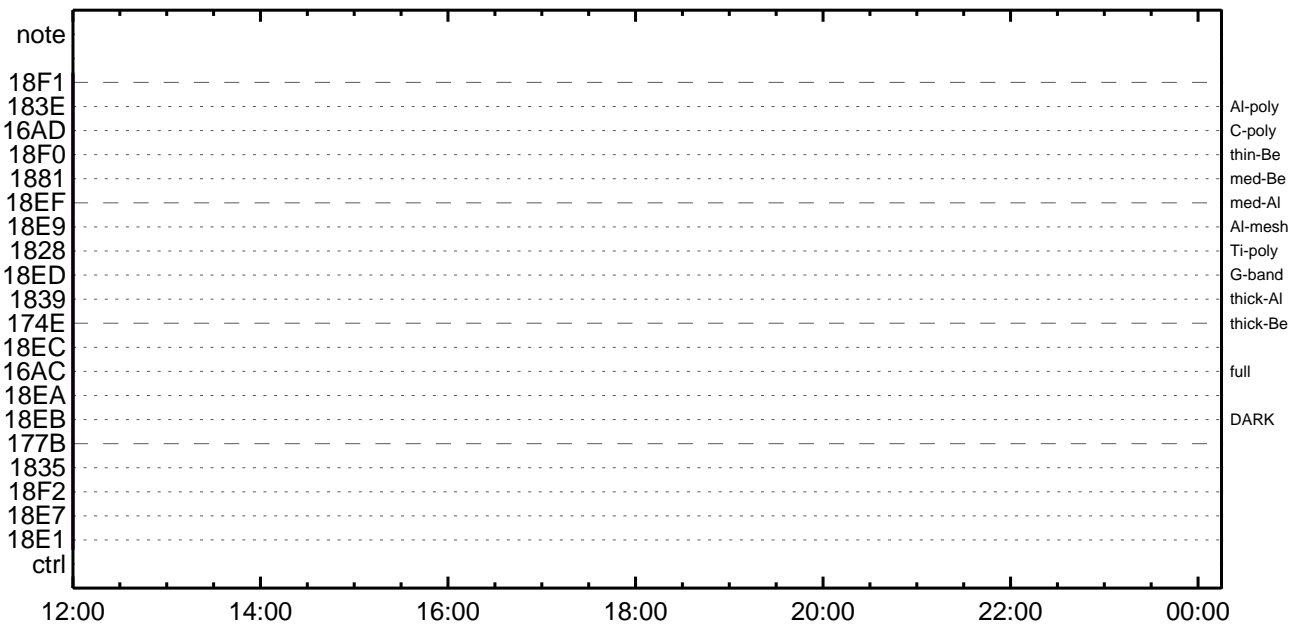
CMDI #0657 2012/06/29



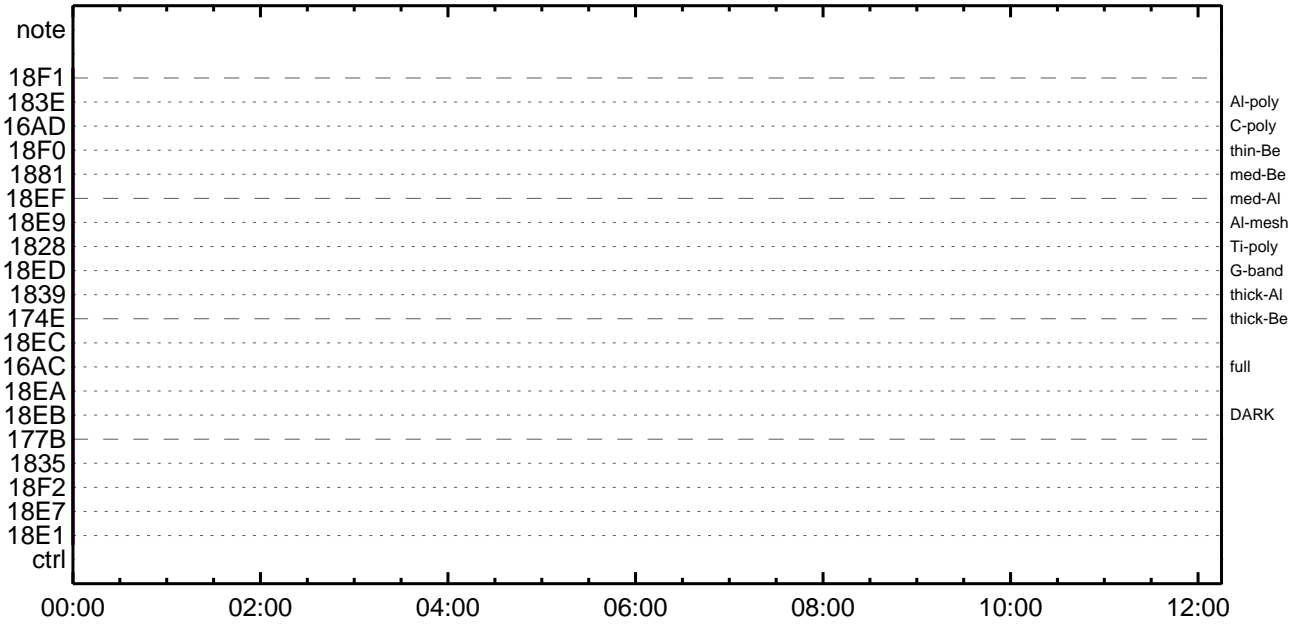
CMDI #0657 2012/06/30



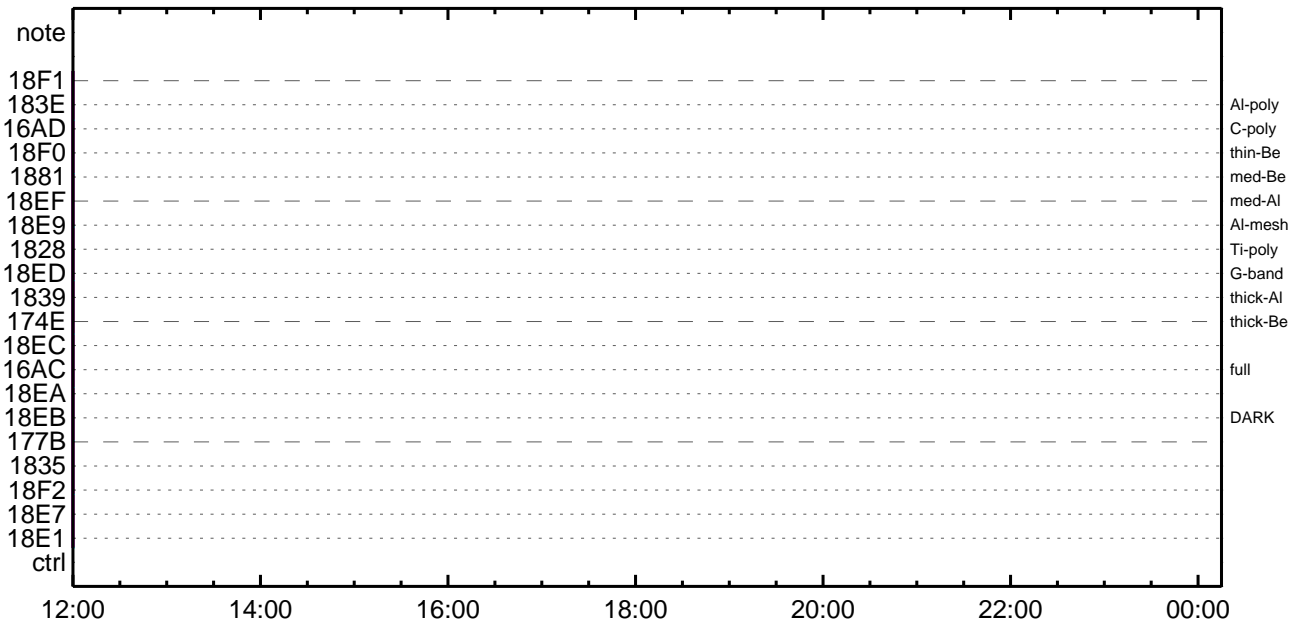
CMDI #0657 2012/06/30



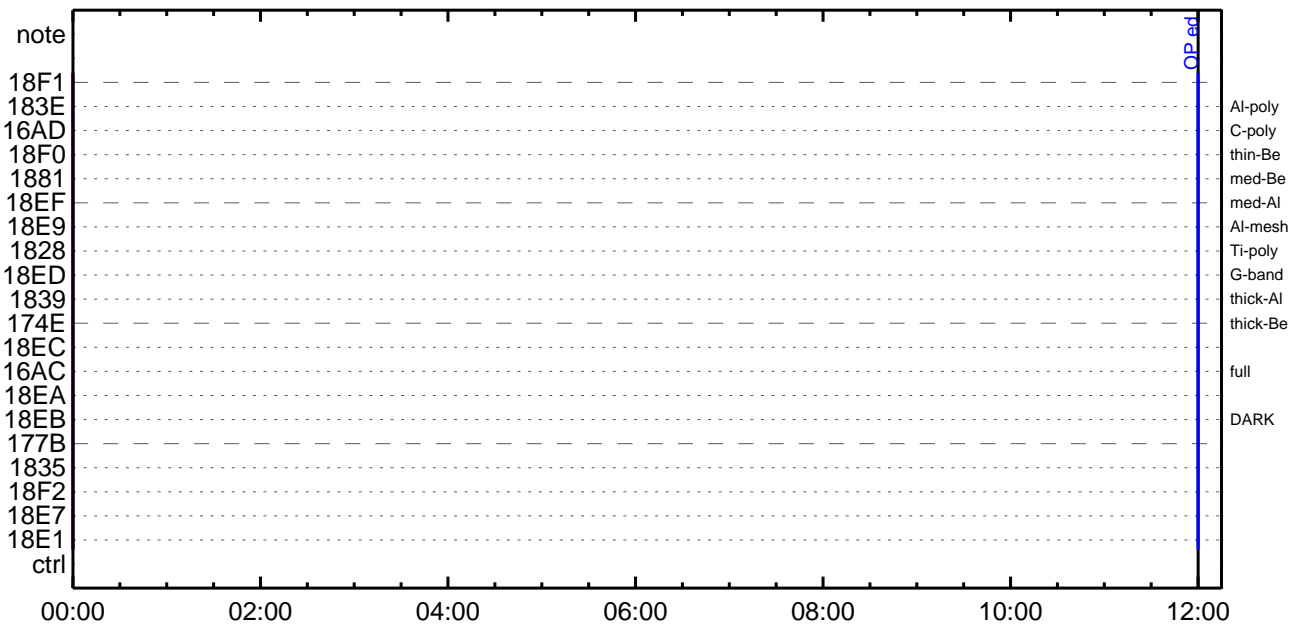
CMDI #0657 2012/07/01

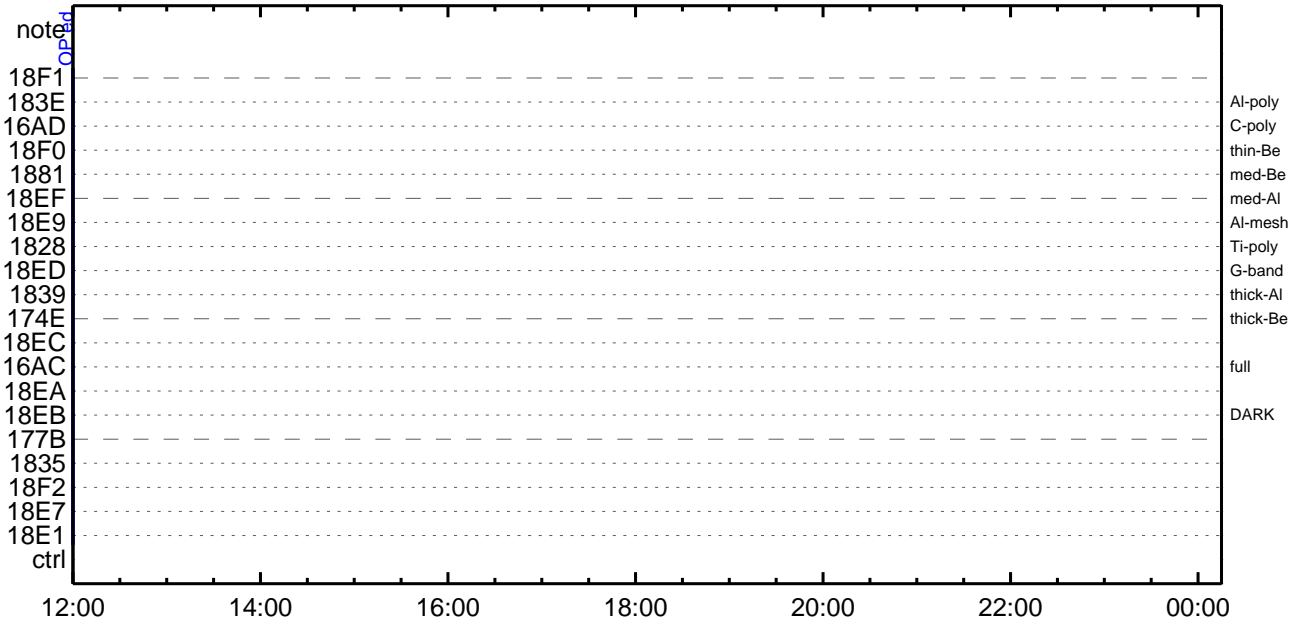


CMDI #0657 2012/07/01



CMDI #0657 2012/07/02





(a) Spacecraft Operation Procedure (real-commands)

```

main-902 2012-05-29 13:22:23 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÁÝÞÝÄÝ-¼Ä»Û;ä
0005 C.
0006 C. ÝÄÝß;¼Ý³ÝÞÝÖÝÉÄ;¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOC : Reload orbital element (send every contact) *****
0010 C. Äí;É¿¿ðÄð•µ°Æ»Í×ÄÇøÍÝÇÝÄÝ×ÝÍ;¼ÝÉ;ÉÈÈ¼µ•ííÉ;ÈðÈ¼°ÇÖð•¿¿¼í¹çøÍ;çÄ®, ù¿¹ðÈðßðçÄ;¿®ð•ðÈðð³ðÈ; £
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ+¿µ;ON
0016 C. *****
0017 C. ç" °ÆÄ, Í×ÈÝðÄLOSðßçøÍ»p´Öøð¹íí, ð•; çÉÖÍ×ðÈXÄÓONðí¹ÖðÈíðÈðð³ðÈ; £
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XÝDÝÖÝÉÝÍÝÄÝ-¾ÖÄÖð~°ÄÄðð•¿¿; ç°È²¼ðí°ÆÄ, ¼È¼çðð¼Ä¹Öð¹ðÈ; £
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÆÄ,
0033 C. *****
0034 C. ç" RESTART; ÈPT1; Èð•¿¿ð¼í¹çøÍ; ç°È²¼ðí°ÆÄ¹Öð»°; çDCBC-150ø¿Èðà; £
0035 C.
0036 . C. ;ãPT1°ÆÄ, ³«»Í; ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãÝçÝÖÝÄÝÈÄÚÄØ; ÈÄ•Ä°²ðÈð; È, áøí°ÆÄ, °Æ³«; ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, ð~¼«Æ°Ää»ßð•¿¿; á; ç°È²¼ð¼Ä¹Öð¹ðÈ; £
0055 C. ÝçÝÖÝÄÝÈÄÚÄØðäÄ•Ä°²ðÈðð~¼áð¼í¹çøÍ´°í»ð¹ðÈðßçÄÖðÄ; £
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÆÄ,
0059 C. *****
0060 C. ç" RESTART; ÈPT2; Èð•¿¿ð¼í¹çøÍ; ç°È²¼ðí°ÆÄ¹Öð»°; çDCBC-151ø¿Èðà; £
0061 C.
0062 . C. ;ãPT2°ÆÄ, ³«»Í; ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãÝçÝÖÝÄÝÈÄÚÄØ; ÈÄ•Ä°²ðÈð; È, áøí°ÆÄ, °Æ³«; ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Ää»ß; çXÄ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÄ, Ää»ß; ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ+¿µ;OFF; ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF

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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ä
0100 C. *****
0101 C.
0102 C. äOP/OGY1;4YE;ä
0103 S. OP op-902:OP
0104 ( )
0105 S. OG og-902:OG
0106 ( )
0107 C.
0108 C. äNMOG&OPfî°èYÄYóYx;ä
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YÄYóYx½ªî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼Ë¹ç•ë²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YÄYóYx½ªî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼Ë¹ç•ë²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YÄYóYx½ªî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼Ë¹ç•ë²îOK²³îÇ§
0165 C.
0166 C. ***** °Ë²¼²î¼Ë¹ç•ë²îOK²³îÇ§ *****
0167 C. DHU²ª;¼YE;Ë¼Y½, Yî;¼YE;Ë²ðîá¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷çîNG²î¼Ë¹ç•ë²¼²îTI-CMDÁ÷ç²î¼Ë¹Ô²•²Ë²³²Ë;ç
0180 C. ²³²ç;çSET²ËDUMP²î¼Ë¹ç•ë²¼²î¼Ë¹²³²Ë;ç
0181 C.
0182 C. TIY³Y²YóYË²ðîÁDîç(UT)
0183 +. TI 2012-05-29 10:13:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-05-29 10:13:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-05-29 10:13:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2012-05-29 10:17:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.         ÷÷[HK1_TI_CMD_NUM]              EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ãîÄè%îíñãîî¥Ä¥§¥Ä¥-¹àîü
0200 C.         ÷÷[HK1_TI_CMD_ENA/DIS]           EQ      ENA
0201 C.         ÷÷[HK1_TI_CMD_NUM]              EQ      4
0202 C.         ÷÷[HK1_NEXT_EXEC_PIM]           EQ      DHU
0203 C.         ÷÷[HK1_NEXT_EXEC_DC]           EQ      0xB3
0204 C.
0205 C. *****
0206 C. Tíîî°è¥Ä¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.         ÷÷[HK1_DMP_TOP_ADRS_1]         EQ      07
0213 C.         ÷÷[HK1_DMP_TOP_ADRS_0]         EQ      2B
0214 C.         ÷÷[HK1_DMP_BLOCK_NUM]          EQ      3
0215 C.         ÷÷[HK1_DMP_REPEAT_NUM]        EQ      0
0216 C.         ÷÷[HK1_DMA_DMP_PIM]           EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.         ÷÷[HK1_PKT_FORM_NO]            EQ      7
0220 C.         ÷÷[HK1_PKT_GEN_TIME]           EQ      0.25 s
0221 C.         ÷÷[HK1_S_TLM_BIT_RATE]        EQ      32k
0222 C.         ÷÷[HK1_X_TLM_BIT_RATE]        EQ      4M
0223 C.         ÷÷[HK1_DMP_CHK_FLG]           EQ      EXEC
0224 C.
0225 C. ¥Ä¥Ö¥×½ªî»ò³îÇ§
0226 C.         ÷÷[HK1_DMP_CHK_FLG]           EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLñîîÈ¹ç·è²îOKò³îÇ§
0229 C.
0230 C. DHU¥ã;¼¥È;È¼¥¼. ¥ì;¼¥È;Èòðîãñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.         ÷÷[HK1_PKT_FORM_NO]            EQ      2
0234 C.         ÷÷[HK1_PKT_GEN_TIME]           EQ      0.5S
0235 C.         ÷÷[HK1_S_TLM_BIT_RATE]        EQ      32K
0236 C.         ÷÷[HK1_X_TLM_BIT_RATE]        EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-05-29 10:17:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-05-29 10:17:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2012-05-29 10:17:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.         [ ] [HK1_TI_CMD_NUM]           EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-05-29 10:17:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.         [ ] [HK1_TI_CMD_NUM]           EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `úÃîãî»ö¼ÝñÈÄðñ¹ñèDCBC·x²è *****
0276 C. (¼á°îî¥Ö¥Ä¥È¥½¥È¥á¥ç¥èè%¼ãñ¼Ä»Ûñ¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹·î Daily±çîñèÈ'Øñ¹ñèDCBC·x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Ä¥§¥Ä¥-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-904 2012-05-29 13:22:23 152 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁã•µ°Æ»Í×ÁÇãÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈÈÈ¼°ÇÔã•ã¿¼í¹çãÍ;çÁ®, ùã¹ãèãÈãÇÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-264:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. YçYÁY×Yí;¼YÉãîÁ°ãÈSTS_CHKãðOFFãÈã¹ãè
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKãðONãÈã¹ãè
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700[ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 C.
0059 C. *****
0060 C. SOT TI command set
0061 C. *****
0062 C. Execute, after the success of TBL upload.
0063 +. TI 2012-05-29 10:17:18.0
0064 DC 07-F0 MDP_SOT_MODE_OBSV
0065 BC (40)
0066 . C. -----
0067 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0068 C. -----
0069 C.
0070 C.
0071 C. ***** XRT START *****
0072 C.
0073 +. DC 07-F0 MDP_XRT_CTRL_MANU
0074 BC (c1)
0075 +. DC 07-F0 MDP_XRT_MODE_STBY
0076 BC (c3)
0077 . C. ----- Success Verify ? OK / NG____
0078 C.
0079 C. XRT Obs. Table Upload
0080 . S. RAM ram-291:MDP_OBS_X
0081 ( )
0082 C.
0083 +. DC 07-F0 MDP_DUMP_XRTTBL
0084 BC (84 07 00 00 00 3a d4)
0085 . C. ----- Comparison Check ? OK / ERR ____
0086 C.
0087 C.
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 01 b1 b1 04 04)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 02 b1 b1 08 08)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 03 b1 b1 08 08)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 04 b1 b1 06 06)
```



```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 85 83 06 06)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 80 80 20 20)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 80 80 20 08)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 08 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0f 80 80 06 06)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 10 80 80 08 08)
0110 + DC 07-F0 MDP_XRT_FLD_ENA
0111 BC (d8)
0112 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0113 BC (c8)
0114 + DC 07-F0 MDP_XRT_AEC_RESET
0115 BC (d0)
0116 + DC 07-F0 MDP_XRT_ARS_DIS
0117 BC (d5)
0118 + DC 07-F0 MDP_XRT_FLD_RESET
0119 BC (da)
0120 + DC 07-F0 MDP_XRT_QT_PROG_SET
0121 BC (c4 13)
0122 + DC 07-F0 MDP_XRT_FL_PROG_SET
0123 BC (c5 0d)
0124 . C. ----- Success Verify ? OK / NG ____
0125 C.
0126 C.
0127 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0128 C.
0129 + DC 07-F0 MDP_XRT_MODE_OBSV
0130 BC (c2)
0131 + TI 2012-05-29 10:17:02.0
0132 DC 07-F0 MDP_XRT_MODE_OBSV
0133 BC (c2)
0134 . C. ----- Success Verify ? OK / NG ____
0135 C.
0136 C. ***** XRT END *****
0137 C.
0138 . C. ***** MDP `úÃîñî»ö¼ÝñÊÃÐñ¹ñèDCBC•x²è *****
0139 C. (%á°îÿÓÿÃÿËÿËÿËÿáÿçÿèñÊ¼ññ¼Ã»Ûñ¹ñè)
0140 . S. DC-BC dcbc-402:DCBC
0141 (MDP_known_event)
0142 C.
0143 C.
0144 . C. ***** ÿDÿ¹•Ï Daily±¿ÎññÊ´Øñ¹ñèDCBC•x²è *****
0145 . S. DC-BC dcbc-153:DCBC
0146 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0147 C.
0148 C.
0149 . C. ;ãLOSÿÁÿ§ÿÃÿ-¼Ã»Û;ã
0150 C.
0151 . C. ***** LOS *****
0152 C.
```

*** OP Sequence for XRT ***

```

2012/05/29 10:27:54.0 XRT_CTRL_MANU_403_OG [0x193]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/05/29 10:27:56.0 XRT_FOCUS_RECALIBRATE_428_OG [0x1ac]
                        XRT_FOCUS_RECAL 2 07-F8 78 00
2012/05/29 10:28:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 01 00 00 00 00
2012/05/29 10:31:56.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2012/05/29 10:32:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/05/29 10:32:18.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/05/29 10:32:20.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/05/29 10:32:22.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/05/29 10:32:24.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/05/29 10:32:26.0 XRT_QT_PROG_SET_434_OG [0x1b2]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 04
2012/05/29 10:32:28.0 XRT_FL_PROG_SET_414_OG [0x19e]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 02
2012/05/29 10:32:30.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/05/29 10:44:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/05/29 10:50:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 ce a8 3f ce
2012/05/29 11:44:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/05/29 11:45:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 01 00 00 00 00
2012/05/29 11:47:26.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2012/05/29 11:47:46.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/05/29 11:47:48.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/05/29 11:47:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/05/29 11:47:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/05/29 11:47:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/05/29 11:47:56.0 XRT_QT_PROG_SET_434_OG [0x1b2]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 04
2012/05/29 11:47:58.0 XRT_FL_PROG_SET_414_OG [0x19e]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 02
2012/05/29 11:48:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/05/29 12:43:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/05/29 12:43:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/05/29 12:43:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/05/29 12:46:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/05/29 13:13:00.0 XRT_Custom_418_OG [0x1a2]
2012/05/29 13:14:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/05/29 14:22:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/05/29 14:22:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/05/29 14:22:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/05/29 14:25:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/05/29 14:59:30.0 XRT_Custom_418_OG [0x1a2]
2012/05/29 15:00:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/05/29 16:00:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/05/29 16:00:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/05/29 16:00:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/05/29 16:03:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/05/29 16:46:30.0 XRT_Custom_418_OG [0x1a2]
2012/05/29 16:47:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/05/29 17:39:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/05/29 17:39:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/05/29 17:39:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/05/29 17:42:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9

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| | | | | | | |
|------------|------------|-----------------------------------|---------------------|---|-------|----------------|
| 2012/05/29 | 18:22:54.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/05/29 | 18:22:56.0 | XRT_FOCUS_POSITION_401_OG [0x191] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 ff aa 00 |
| 2012/05/29 | 18:23:00.0 | AOCS_ORe-point_Start_3_OG [0x099] | AOCU_NM | 5 | 02-76 | 00 00 00 00 00 |
| 2012/05/29 | 18:23:16.0 | XRT_FLD_DIS_402_OG [0x192] | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 |
| 2012/05/29 | 18:23:18.0 | XRT_FLRCTRL_DIS_433_OG [0x1b1] | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 |
| 2012/05/29 | 18:23:20.0 | XRT_ARS_DIS_438_OG [0x1b6] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 |
| 2012/05/29 | 18:25:58.0 | XRT_QT_PROG_SET_423_OG [0x1a7] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 07 |
| 2012/05/29 | 18:26:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/05/29 | 18:42:54.0 | XRT_CTRL_MANU_439_OG [0x1b7] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/05/29 | 18:43:00.0 | AOCS_ORe-point_Start_1_OG [0x097] | AOCU_NM | 5 | 02-76 | 01 00 00 00 00 |
| 2012/05/29 | 18:45:26.0 | XRT_FOCUS_POSITION_409_OG [0x199] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 |
| 2012/05/29 | 18:45:46.0 | XRT_FLD_ENA_411_OG [0x19b] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 |
| 2012/05/29 | 18:45:48.0 | XRT_FLRCTRL_ENA_413_OG [0x19d] | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 |
| 2012/05/29 | 18:45:50.0 | XRT_AEC_RESET_443_OG [0x1bb] | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 |
| 2012/05/29 | 18:45:52.0 | XRT_ARS_DIS_431_OG [0x1af] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 |
| 2012/05/29 | 18:45:54.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/05/29 | 18:45:56.0 | XRT_QT_PROG_SET_415_OG [0x19f] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 13 |
| 2012/05/29 | 18:45:58.0 | XRT_FL_PROG_SET_414_OG [0x19e] | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 02 |
| 2012/05/29 | 18:46:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/05/29 | 19:17:30.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/05/29 | 19:17:32.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/05/29 | 19:17:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/05/29 | 19:20:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/05/29 | 20:00:00.0 | XRT_Custom_418_OG [0x1a2] | | | | |
| 2012/05/29 | 20:01:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/05/29 | 20:56:00.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/05/29 | 20:56:02.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/05/29 | 20:56:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/05/29 | 20:59:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/05/29 | 21:37:00.0 | XRT_Custom_418_OG [0x1a2] | | | | |
| 2012/05/29 | 21:38:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/05/29 | 22:34:30.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/05/29 | 22:34:32.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/05/29 | 22:34:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/05/29 | 22:37:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/05/29 | 23:12:00.0 | XRT_Custom_418_OG [0x1a2] | | | | |
| 2012/05/29 | 23:13:00.5 | XRT_CTRL_AUTO_419_OG [0x1a3] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/05/30 | 00:13:00.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/05/30 | 00:13:02.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/05/30 | 00:13:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/05/30 | 00:16:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/05/30 | 00:42:30.0 | XRT_Custom_418_OG [0x1a2] | | | | |
| 2012/05/30 | 00:43:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2012/05/30 | 01:51:30.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2012/05/30 | 01:51:32.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2012/05/30 | 01:51:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2012/05/30 | 01:54:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2012/05/30 | 02:21:00.0 | XRT_Custom_418_OG [0x1a2] | | | | |
| 2012/05/30 | 02:22:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |

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|------------|------------|------------------------------------|---------------------|---|-------|----------------|--|--|--|--|
| 2012/05/30 | 03:26:00.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/05/30 | 03:26:02.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/05/30 | 03:26:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/05/30 | 03:29:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/05/30 | 03:59:30.0 | XRT_Custom_418_OG [0x1a2] | | | | | | | | |
| 2012/05/30 | 04:00:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/05/30 | 04:58:30.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/05/30 | 04:58:32.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/05/30 | 04:58:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/05/30 | 05:01:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/05/30 | 05:54:54.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/05/30 | 05:54:56.0 | XRT_FOCUS_POSITION_401_OG [0x191] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 ff aa 00 | | | | |
| 2012/05/30 | 05:55:00.0 | AOCS_Or-e-point_Start_3_OG [0x099] | AOCU_NM | 5 | 02-76 | 00 00 00 00 00 | | | | |
| 2012/05/30 | 05:55:16.0 | XRT_FLD_DIS_402_OG [0x192] | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 | | | | |
| 2012/05/30 | 05:55:18.0 | XRT_FLRCTRL_DIS_433_OG [0x1b1] | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 | | | | |
| 2012/05/30 | 05:55:20.0 | XRT_ARS_DIS_438_OG [0x1b6] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2012/05/30 | 05:57:58.0 | XRT_QT_PROG_SET_423_OG [0x1a7] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 07 | | | | |
| 2012/05/30 | 05:58:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/05/30 | 06:04:54.0 | XRT_CTRL_MANU_439_OG [0x1b7] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/05/30 | 06:05:00.0 | AOCS_Or-e-point_Start_1_OG [0x097] | AOCU_NM | 5 | 02-76 | 01 00 00 00 00 | | | | |
| 2012/05/30 | 06:07:26.0 | XRT_FOCUS_POSITION_409_OG [0x199] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 | | | | |
| 2012/05/30 | 06:07:46.0 | XRT_FLD_ENA_411_OG [0x19b] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | | | |
| 2012/05/30 | 06:07:48.0 | XRT_FLRCTRL_ENA_413_OG [0x19d] | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | | | |
| 2012/05/30 | 06:07:50.0 | XRT_AEC_RESET_443_OG [0x1bb] | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | | | |
| 2012/05/30 | 06:07:52.0 | XRT_ARS_DIS_431_OG [0x1af] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2012/05/30 | 06:07:54.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/05/30 | 06:07:56.0 | XRT_QT_PROG_SET_415_OG [0x19f] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 13 | | | | |
| 2012/05/30 | 06:07:58.0 | XRT_FL_PROG_SET_414_OG [0x19e] | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 02 | | | | |
| 2012/05/30 | 06:08:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/05/30 | 06:39:00.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/05/30 | 06:39:02.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/05/30 | 06:39:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/05/30 | 06:42:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/05/30 | 07:16:30.0 | XRT_Custom_418_OG [0x1a2] | | | | | | | | |
| 2012/05/30 | 07:17:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/05/30 | 08:19:00.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/05/30 | 08:19:02.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/05/30 | 08:19:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/05/30 | 08:22:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/05/30 | 08:55:00.0 | XRT_Custom_418_OG [0x1a2] | | | | | | | | |
| 2012/05/30 | 08:56:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2012/05/30 | 10:00:00.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2012/05/30 | 10:00:02.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2012/05/30 | 10:00:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2012/05/30 | 10:03:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2012/05/30 | 10:20:00.0 | AOCS_Or-e-point_Start_4_OG [0x09a] | AOCU_NM | 5 | 02-76 | 00 00 00 ac cd | | | | |
| 2012/05/30 | 10:49:30.0 | AOCS_Or-e-point_Start_5_OG [0x09b] | AOCU_NM | 5 | 02-76 | 00 00 00 d6 67 | | | | |
| 2012/05/30 | 11:05:24.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |

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|------------|------------|---|---|-------|----|----|----|-------|
| 2012/05/30 | 11:05:26.0 | XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION | 4 | 07-F8 | 22 | ff | aa | 00 |
| 2012/05/30 | 11:05:30.0 | AOCs_OrE-point_Start_3_OG [0x099] AOCU_NM | 5 | 02-76 | 00 | 00 | 00 | 00 |
| 2012/05/30 | 11:05:46.0 | XRT_FLD_DIS_402_OG [0x192] MDP_XRT_FLD_DIS | 1 | 07-F0 | | d9 | | |
| 2012/05/30 | 11:05:48.0 | XRT_FLRCTRL_DIS_433_OG [0x1b1] MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | | c9 | | |
| 2012/05/30 | 11:05:50.0 | XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS | 1 | 07-F0 | | d5 | | |
| 2012/05/30 | 11:08:28.0 | XRT_QT_PROG_SET_427_OG [0x1ab] MDP_XRT_QT_PROG_SET | 2 | 07-F0 | | c4 | 03 | |
| 2012/05/30 | 11:08:30.0 | XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO | 1 | 07-F0 | | c0 | | |
| 2012/05/30 | 11:20:10.5 | XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU | 1 | 07-F0 | | c1 | | |
| 2012/05/30 | 11:21:30.0 | AOCs_OrE-point_Start_6_OG [0x09c] AOCU_NM | 5 | 02-76 | 00 | 00 | 00 | 29 99 |
| 2012/05/30 | 12:12:00.0 | AOCs_OrE-point_Start_7_OG [0x09d] AOCU_NM | 5 | 02-76 | 00 | 00 | 00 | 53 33 |
| 2012/05/30 | 12:28:00.0 | AOCs_OrE-point_Start_8_OG [0x09e] AOCU_NM | 5 | 02-76 | 00 | d6 | 36 | b7 8e |
| 2012/05/30 | 12:38:00.0 | AOCs_OrE-point_Start_9_OG [0x09f] AOCU_NM | 5 | 02-76 | 00 | b4 | b5 | db 75 |
| 2012/05/30 | 12:54:00.0 | AOCs_OrE-point_Start_10_OG [0x0a0] AOCU_NM | 5 | 02-76 | 00 | ac | 5b | 00 00 |
| 2012/05/30 | 13:50:30.0 | AOCs_OrE-point_Start_11_OG [0x0a1] AOCU_NM | 5 | 02-76 | 00 | b4 | b5 | 24 8b |
| 2012/05/30 | 14:05:30.0 | AOCs_OrE-point_Start_12_OG [0x0a2] AOCU_NM | 5 | 02-76 | 00 | d6 | 36 | 48 72 |
| 2012/05/30 | 14:16:30.0 | AOCs_OrE-point_Start_13_OG [0x0a3] AOCU_NM | 5 | 02-76 | 00 | 29 | ca | b7 8e |
| 2012/05/30 | 14:26:30.0 | AOCs_OrE-point_Start_14_OG [0x0a4] AOCU_NM | 5 | 02-76 | 00 | 4b | 4b | db 75 |
| 2012/05/30 | 14:42:30.0 | AOCs_OrE-point_Start_15_OG [0x0a5] AOCU_NM | 5 | 02-76 | 00 | 53 | a5 | 00 00 |
| 2012/05/30 | 15:52:30.0 | AOCs_OrE-point_Start_16_OG [0x0a6] AOCU_NM | 5 | 02-76 | 00 | 4b | 4b | 24 8b |
| 2012/05/30 | 16:08:30.0 | AOCs_OrE-point_Start_17_OG [0x0a7] AOCU_NM | 5 | 02-76 | 00 | 29 | db | 48 72 |
| 2012/05/30 | 16:19:54.0 | XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU | 1 | 07-F0 | | c1 | | |
| 2012/05/30 | 16:20:00.0 | AOCs_OrE-point_Start_1_OG [0x097] AOCU_NM | 5 | 02-76 | 01 | 00 | 00 | 00 00 |
| 2012/05/30 | 16:22:26.0 | XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION | 4 | 07-F8 | 22 | fe | 97 | 00 |
| 2012/05/30 | 16:22:46.0 | XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA | 1 | 07-F0 | | d8 | | |
| 2012/05/30 | 16:22:48.0 | XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | | c8 | | |
| 2012/05/30 | 16:22:50.0 | XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET | 1 | 07-F0 | | d0 | | |
| 2012/05/30 | 16:22:52.0 | XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS | 1 | 07-F0 | | d5 | | |
| 2012/05/30 | 16:22:54.0 | XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET | 1 | 07-F0 | | da | | |
| 2012/05/30 | 16:22:56.0 | XRT_QT_PROG_SET_415_OG [0x19f] MDP_XRT_QT_PROG_SET | 2 | 07-F0 | | c4 | 13 | |
| 2012/05/30 | 16:22:58.0 | XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET | 2 | 07-F0 | | c5 | 02 | |
| 2012/05/30 | 16:23:00.0 | XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO | 1 | 07-F0 | | c0 | | |
| 2012/05/30 | 16:38:00.0 | XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU | 1 | 07-F0 | | c1 | | |
| 2012/05/30 | 16:38:02.0 | XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET | 1 | 07-F0 | | da | | |
| 2012/05/30 | 16:38:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT | 1 | 07-F0 | | e8 | | |
| 2012/05/30 | 16:41:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP | 1 | 07-F0 | | e9 | | |
| 2012/05/30 | 17:23:00.0 | XRT_Custom_418_OG [0x1a2] | | | | | | |
| 2012/05/30 | 17:24:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO | 1 | 07-F0 | | c0 | | |
| 2012/05/30 | 17:59:54.0 | XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU | 1 | 07-F0 | | c1 | | |
| 2012/05/30 | 17:59:56.0 | XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION | 4 | 07-F8 | 22 | ff | aa | 00 |
| 2012/05/30 | 18:00:00.0 | AOCs_OrE-point_Start_3_OG [0x099] AOCU_NM | 5 | 02-76 | 00 | 00 | 00 | 00 00 |
| 2012/05/30 | 18:00:16.0 | XRT_FLD_DIS_402_OG [0x192] MDP_XRT_FLD_DIS | 1 | 07-F0 | | d9 | | |
| 2012/05/30 | 18:00:18.0 | XRT_FLRCTRL_DIS_433_OG [0x1b1] MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | | c9 | | |
| 2012/05/30 | 18:00:20.0 | XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS | 1 | 07-F0 | | d5 | | |
| 2012/05/30 | 18:02:58.0 | XRT_QT_PROG_SET_423_OG [0x1a7] MDP_XRT_QT_PROG_SET | 2 | 07-F0 | | c4 | 07 | |
| 2012/05/30 | 18:03:00.0 | XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO | 1 | 07-F0 | | c0 | | |
| 2012/05/30 | 18:09:54.0 | XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU | 1 | 07-F0 | | c1 | | |
| 2012/05/30 | 18:10:00.0 | AOCs_OrE-point_Start_1_OG [0x097] AOCU_NM | 5 | 02-76 | 01 | 00 | 00 | 00 00 |

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|------------|------------|-----------------------------------|---|-------|----|----|-------|
| 2012/05/30 | 18:12:26.0 | XRT_FOCUS_POSITION_409_OG [0x199] | | | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 | fe | 97 00 |
| 2012/05/30 | 18:12:46.0 | XRT_FLD_ENA_411_OG [0x19b] | | | | | |
| | | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | |
| 2012/05/30 | 18:12:48.0 | XRT_FLRCTRL_ENA_413_OG [0x19d] | | | | | |
| | | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | |
| 2012/05/30 | 18:12:50.0 | XRT_AEC_RESET_443_OG [0x1bb] | | | | | |
| | | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | |
| 2012/05/30 | 18:12:52.0 | XRT_ARS_DIS_431_OG [0x1af] | | | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | |
| 2012/05/30 | 18:12:54.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/05/30 | 18:12:56.0 | XRT_QT_PROG_SET_415_OG [0x19f] | | | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 | 13 | |
| 2012/05/30 | 18:12:58.0 | XRT_FL_PROG_SET_414_OG [0x19e] | | | | | |
| | | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 | 02 | |
| 2012/05/30 | 19:00:00.0 | XRT_Custom_418_OG [0x1a2] | | | | | |
| 2012/05/30 | 19:01:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2012/05/30 | 19:55:00.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/05/30 | 19:55:02.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/05/30 | 19:55:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2012/05/30 | 19:58:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2012/05/30 | 20:37:30.0 | XRT_Custom_418_OG [0x1a2] | | | | | |
| 2012/05/30 | 20:38:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2012/05/30 | 21:33:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/05/30 | 21:33:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/05/30 | 21:33:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2012/05/30 | 21:36:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2012/05/30 | 22:13:30.0 | XRT_Custom_418_OG [0x1a2] | | | | | |
| 2012/05/30 | 22:14:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2012/05/30 | 23:12:00.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/05/30 | 23:12:02.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/05/30 | 23:12:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2012/05/30 | 23:15:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2012/05/30 | 23:41:30.0 | XRT_Custom_418_OG [0x1a2] | | | | | |
| 2012/05/30 | 23:42:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2012/05/31 | 00:50:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/05/31 | 00:50:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/05/31 | 00:50:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2012/05/31 | 00:53:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2012/05/31 | 01:20:00.0 | XRT_Custom_418_OG [0x1a2] | | | | | |
| 2012/05/31 | 01:21:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2012/05/31 | 02:28:00.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/05/31 | 02:28:02.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/05/31 | 02:28:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2012/05/31 | 02:31:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2012/05/31 | 02:58:30.0 | XRT_Custom_418_OG [0x1a2] | | | | | |
| 2012/05/31 | 02:59:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2012/05/31 | 03:56:30.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/05/31 | 03:56:32.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/05/31 | 03:56:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2012/05/31 | 03:59:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2012/05/31 | 04:37:00.0 | XRT_Custom_418_OG [0x1a2] | | | | | |
| 2012/05/31 | 04:38:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2012/05/31 | 05:37:00.0 | XRT_CTRL_MANU_408_OG [0x198] | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2012/05/31 | 05:37:02.0 | XRT_FLD_RESET_412_OG [0x19c] | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2012/05/31 | 05:37:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2012/05/31 | 05:40:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |

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|------------|------------|-----------------------------------|---------------------|---|-------|----------------|--|
| 2012/05/31 | 06:20:54.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2012/05/31 | 06:20:56.0 | XRT_FOCUS_POSITION_401_OG [0x191] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 ff aa 00 | |
| 2012/05/31 | 06:21:00.0 | AOCS_ORe-point_Start_3_OG [0x099] | AOCU_NM | 5 | 02-76 | 00 00 00 00 00 | |
| 2012/05/31 | 06:21:16.0 | XRT_FLD_DIS_402_OG [0x192] | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 | |
| 2012/05/31 | 06:21:18.0 | XRT_FLRCTRL_DIS_433_OG [0x1b1] | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 | |
| 2012/05/31 | 06:21:20.0 | XRT_ARS_DIS_438_OG [0x1b6] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | |
| 2012/05/31 | 06:23:58.0 | XRT_QT_PROG_SET_423_OG [0x1a7] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 07 | |
| 2012/05/31 | 06:24:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2012/05/31 | 06:30:54.0 | XRT_CTRL_MANU_439_OG [0x1b7] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2012/05/31 | 06:31:00.0 | AOCS_ORe-point_Start_1_OG [0x097] | AOCU_NM | 5 | 02-76 | 01 00 00 00 00 | |
| 2012/05/31 | 06:33:26.0 | XRT_FOCUS_POSITION_409_OG [0x199] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 | |
| 2012/05/31 | 06:33:46.0 | XRT_FLD_ENA_411_OG [0x19b] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | |
| 2012/05/31 | 06:33:48.0 | XRT_FLRCTRL_ENA_413_OG [0x19d] | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | |
| 2012/05/31 | 06:33:50.0 | XRT_AEC_RESET_443_OG [0x1bb] | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | |
| 2012/05/31 | 06:33:52.0 | XRT_ARS_DIS_431_OG [0x1af] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | |
| 2012/05/31 | 06:33:54.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |
| 2012/05/31 | 06:33:56.0 | XRT_QT_PROG_SET_415_OG [0x19f] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 13 | |
| 2012/05/31 | 06:33:58.0 | XRT_FL_PROG_SET_414_OG [0x19e] | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 02 | |
| 2012/05/31 | 06:34:00.0 | XRT_CTRL_AUTO_406_OG [0x196] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2012/05/31 | 07:17:30.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2012/05/31 | 07:17:32.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |
| 2012/05/31 | 07:17:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | |
| 2012/05/31 | 07:20:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | |
| 2012/05/31 | 07:54:00.0 | XRT_Custom_418_OG [0x1a2] | | | | | |
| 2012/05/31 | 07:55:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2012/05/31 | 08:57:00.0 | XRT_CTRL_MANU_408_OG [0x198] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2012/05/31 | 08:57:02.0 | XRT_FLD_RESET_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |
| 2012/05/31 | 08:57:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | |
| 2012/05/31 | 09:00:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | |
| 2012/05/31 | 11:10:00.0 | AOCS_ORe-point_Start_3_OG [0x099] | AOCU_NM | 5 | 02-76 | 00 00 00 00 00 | |