

XRT Timeline to be uploaded on 2012/05/31

Period: 2012/05/31 11:00:00 - 2012/07/05 12:00:00

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Normal mode

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XOB #183E: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad													
Term	Pointing (x, y)					Comment							
05/31 11:13:00 - 05/31 17:15:30	Track (13.6, -253.0) <small>@ 05/31 11:10:00</small>					# OP start + 10min, track AR 11492							
06/01 10:11:00 - 06/01 16:14:30	Track (583.7, -235.8) <small>@ 06/01 09:57:00</small>					Resume tracking AR 11490.							
06/01 18:37:30 - 06/02 05:13:00	Track (635.1, -236.7) <small>@ 06/01 17:48:30</small>					# Cont.							
06/02 06:13:00 - 06/02 08:33:00	Track (708.6, -238.0) <small>@ 06/02 06:10:00</small>					# Cont.							
PROG= 19 Inf.-time(s)													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 41 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 16 30-time(s) 80.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	20.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	20.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	20.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18F3: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(24)													
Term	Pointing (x, y)					Comment							
05/31 18:03:00 - 05/31 18:09:54	Fixed (0.0, 0.0)					synoptic							
06/01 05:50:00 - 06/01 05:56:54	Fixed (0.0, 0.0)					synoptic, shifted -13.0 min							
06/01 17:41:30 - 06/01 17:48:24	Fixed (0.0, 0.0)					synoptic, shifted -21.5 min							
06/02 06:03:00 - 06/02 06:09:54	Fixed (0.0, 0.0)					synoptic							
PROG= 05 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 7 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 27 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1835: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 40sec-cad													
Term	Pointing (x, y)					Comment							
05/31 18:13:00 - 06/01 05:46:54	Track (470.4, -234.0) <small>@ 05/31 18:10:00</small>					# Track AR 11490							
PROG= 04 Inf.-time(s)													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 41 4-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 52 45-time(s) 24.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT												
Term	Pointing (x, y)					Comment						
06/01 06:12:00 - 06/01 07:56:54	Fixed (0.0, 945.0)					# Coalignment offset, N.						
PROG= 08 1-time(s)												

Subr= 1	1-time(s)	360.0sec										
└─ Seqn= 21	24-time(s)	300.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
06/01 08:12:00 - 06/01 09:56:54	Fixed (-945.0, 0.0)	* Co-alignment offset, E.

PROG= 18	1-time(s)											
└─ Subr= 1	1-time(s)	360.0sec										
└─ Seqn= 22	15-time(s)	480.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #18E7: Flare obs. dynamics - Ti_poly high cadence + context (thick-AI-384x384)-15 loops

Term	Pointing (x, y)	Comment
05/31 11:13:00 - 05/31 17:15:30	Track (13.6, -253.0) ^{Ⓜ 05/31 11:10:00}	# OP start + 10min, track AR 11492
05/31 18:13:00 - 06/01 05:46:54	Track (470.4, -234.0) ^{Ⓜ 05/31 18:10:00}	# Track AR 11490
06/01 10:11:00 - 06/01 16:14:30	Track (583.7, -235.8) ^{Ⓜ 06/01 09:57:00}	Resume tracking AR 11490.
06/01 18:37:30 - 06/02 05:13:00	Track (635.1, -236.7) ^{Ⓜ 06/01 17:48:30}	# Cont.
06/02 06:13:00 - 06/02 08:33:00	Track (708.6, -238.0) ^{Ⓜ 06/02 06:10:00}	# Cont.

PROG= 02	15-time(s)											
└─ Subr= 1	45-time(s)	10.0sec										
└─ Seqn= 92	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-AI	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
└─ Open/Ti-poly	Open/thick-AI	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
└─ Subr= 2	1-time(s)	10.0sec										
└─ Seqn= 54	1-time(s)	2.0sec										
└─ Open/thick-AI	Open/thick-AI	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
└─ Open/thick-AI	Open/thick-AI	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
└─ Seqn= 79	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

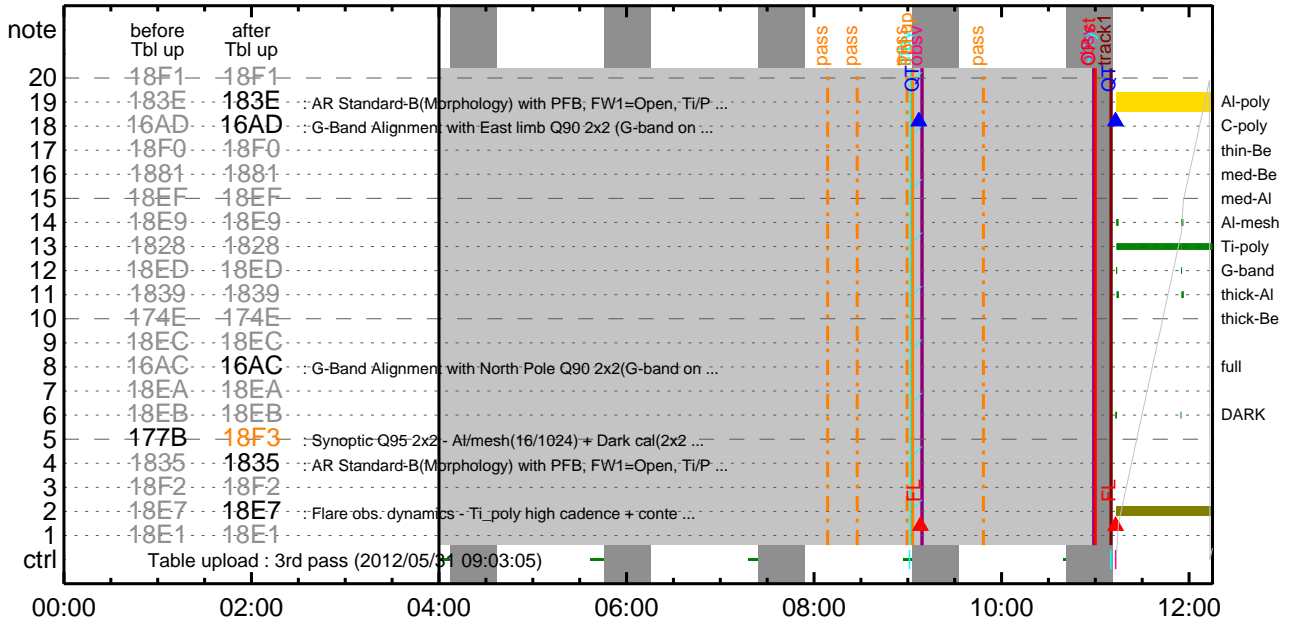
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Flare Detection

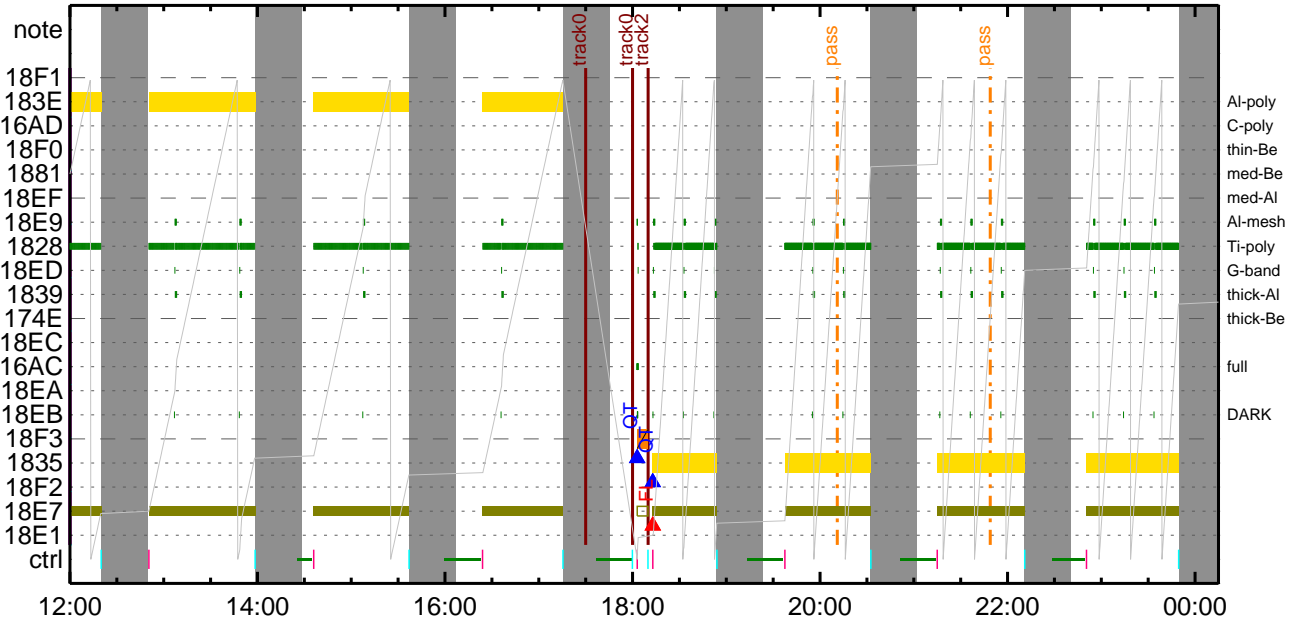
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FLD Patrol												
Term	Pointing (x, y)	Comment										
05/31 18:12:46 - 06/01 05:47:16	Track (470.4, -234.0) ^{Ⓜ 05/31 18:10:00}	# Track AR 11490										
06/01 09:59:46 - 06/01 17:38:46	Track (583.7, -235.8) ^{Ⓜ 06/01 09:57:00}	Resume tracking AR 11490.										
06/01 17:51:16 - 06/02 06:00:16	Track (635.1, -236.7) ^{Ⓜ 06/01 17:48:30}	# Cont.										
06/02 06:12:46 - 07/05 12:00:00	Track (708.6, -238.0) ^{Ⓜ 06/02 06:10:00}	# Cont.										
└─ Open/Ti-poly	Open/thick-AI	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

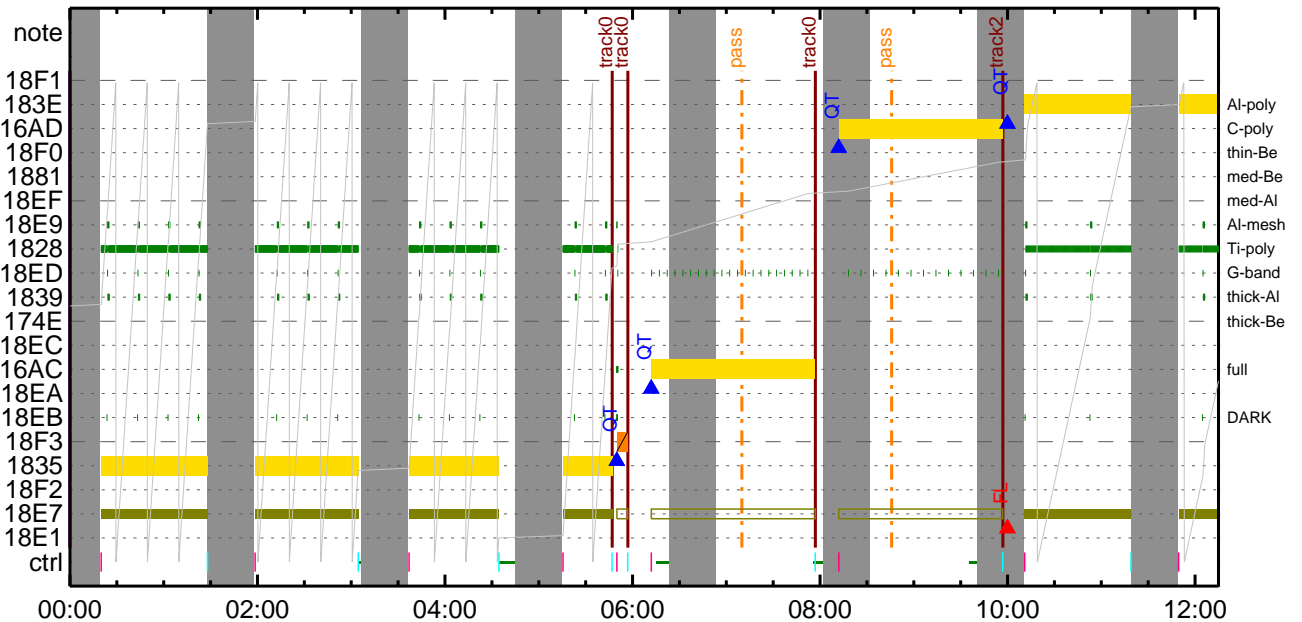
CMDI #0663 2012/05/31



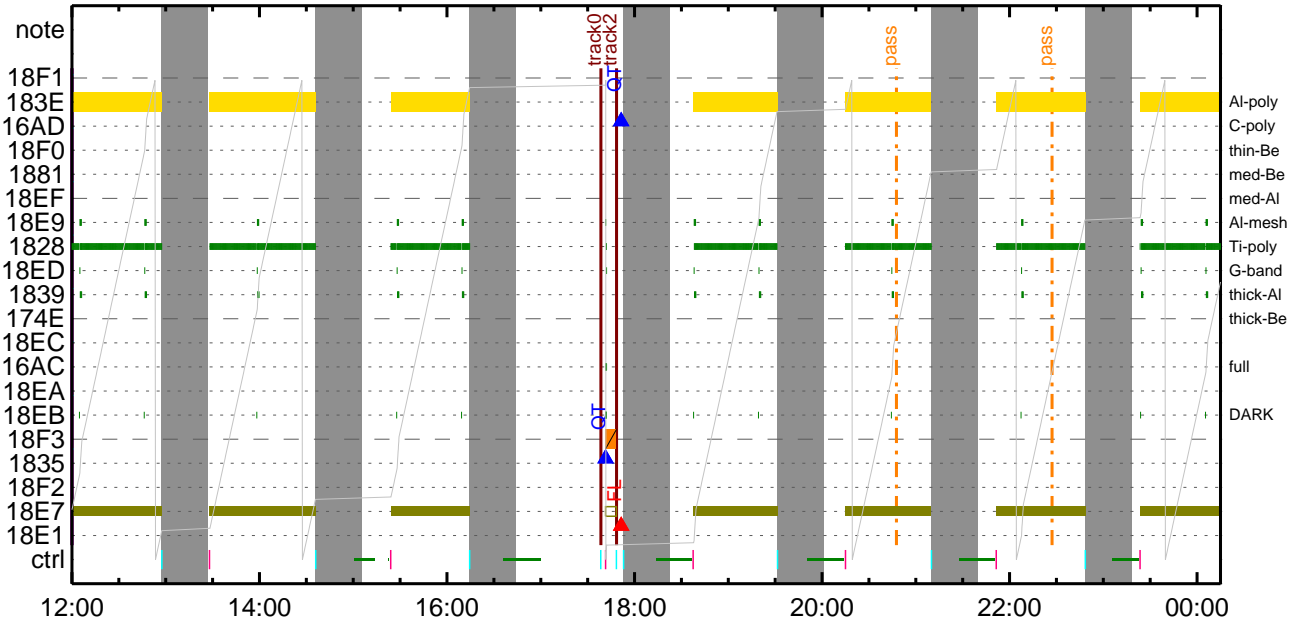
CMDI #0663 2012/05/31



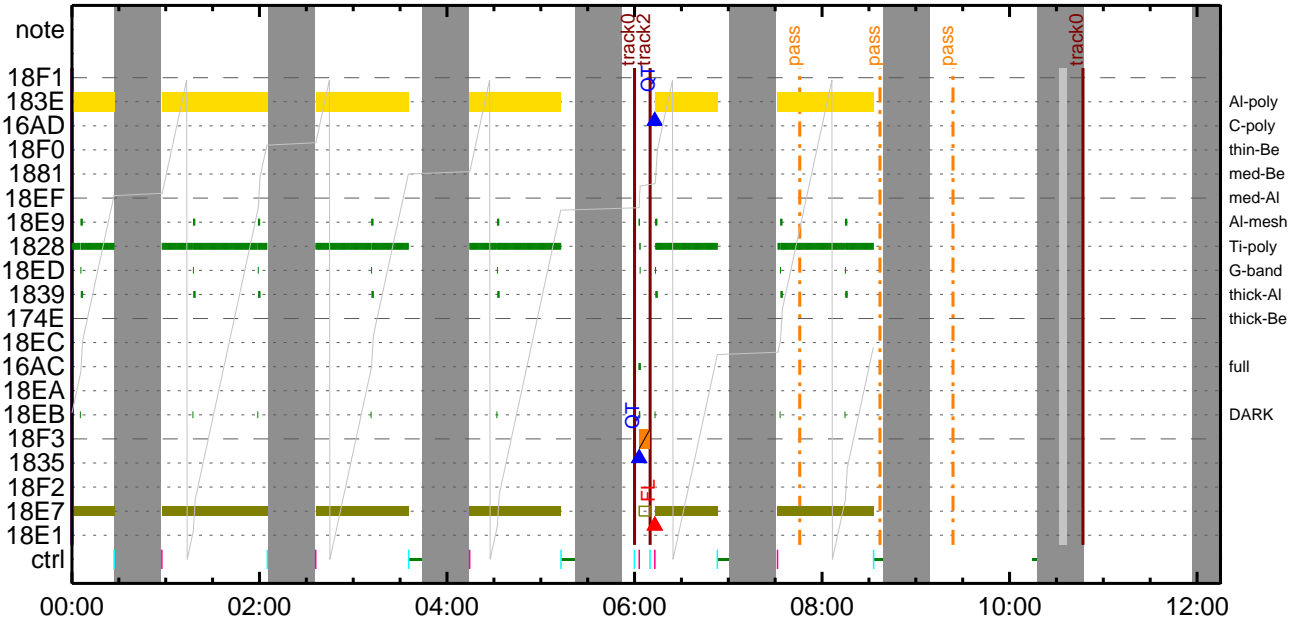
CMDI #0663 2012/06/01



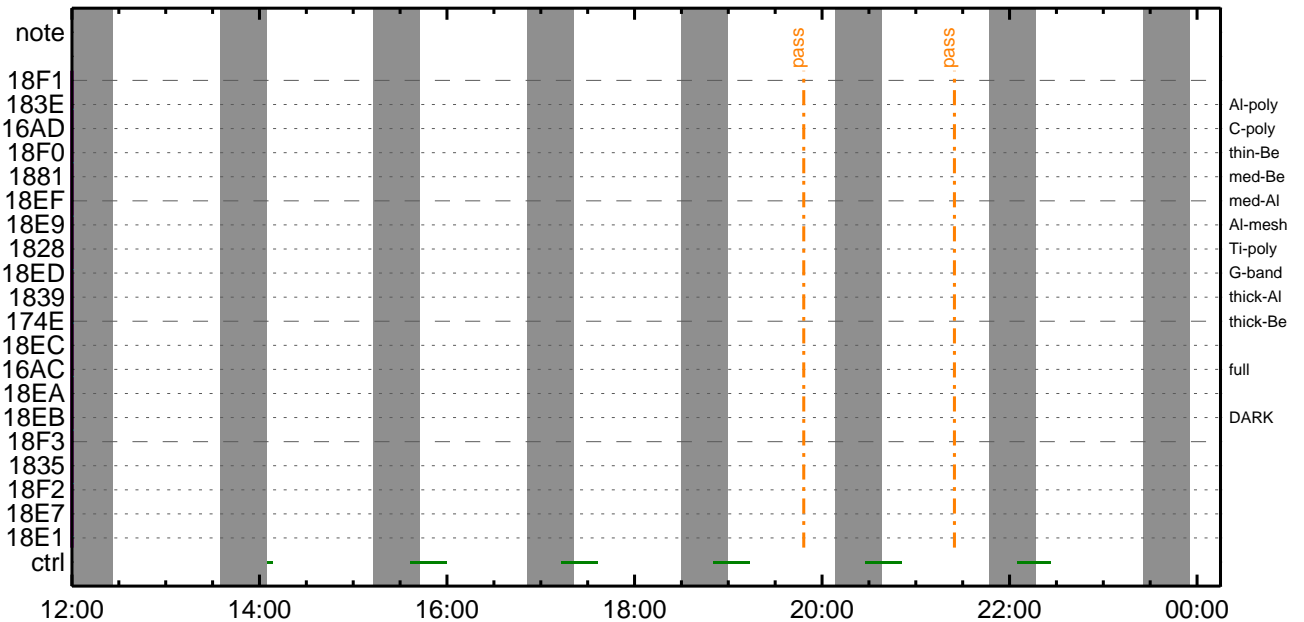
CMDI #0663 2012/06/01



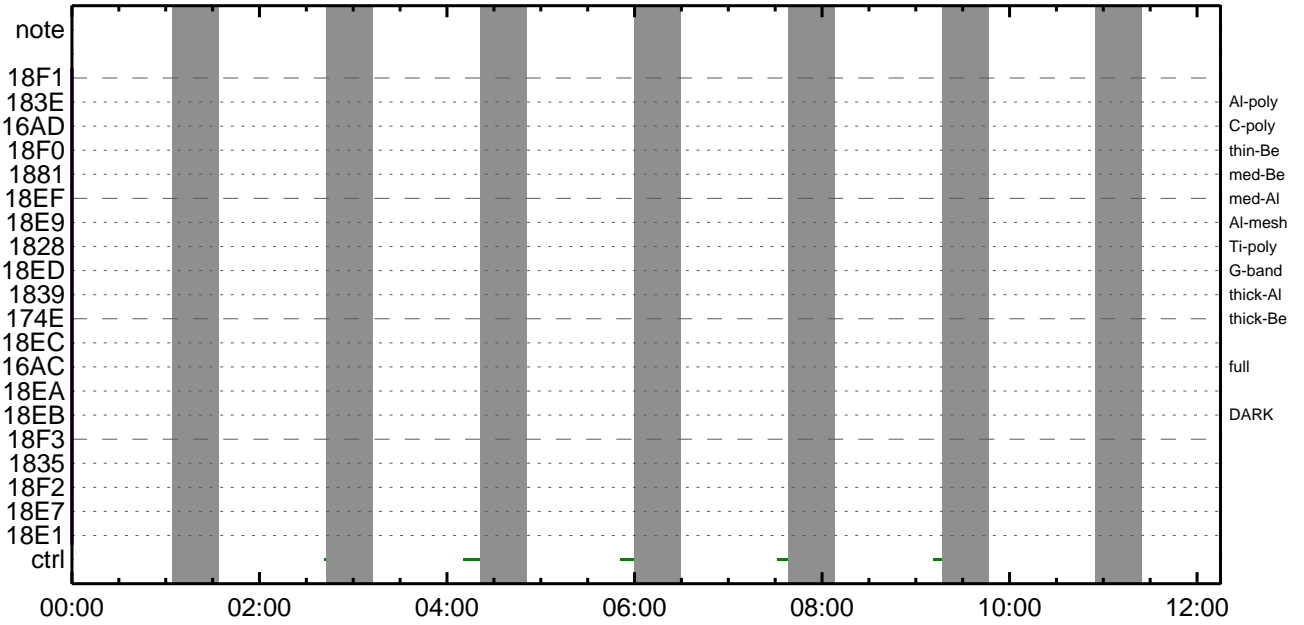
CMDI #0663 2012/06/02



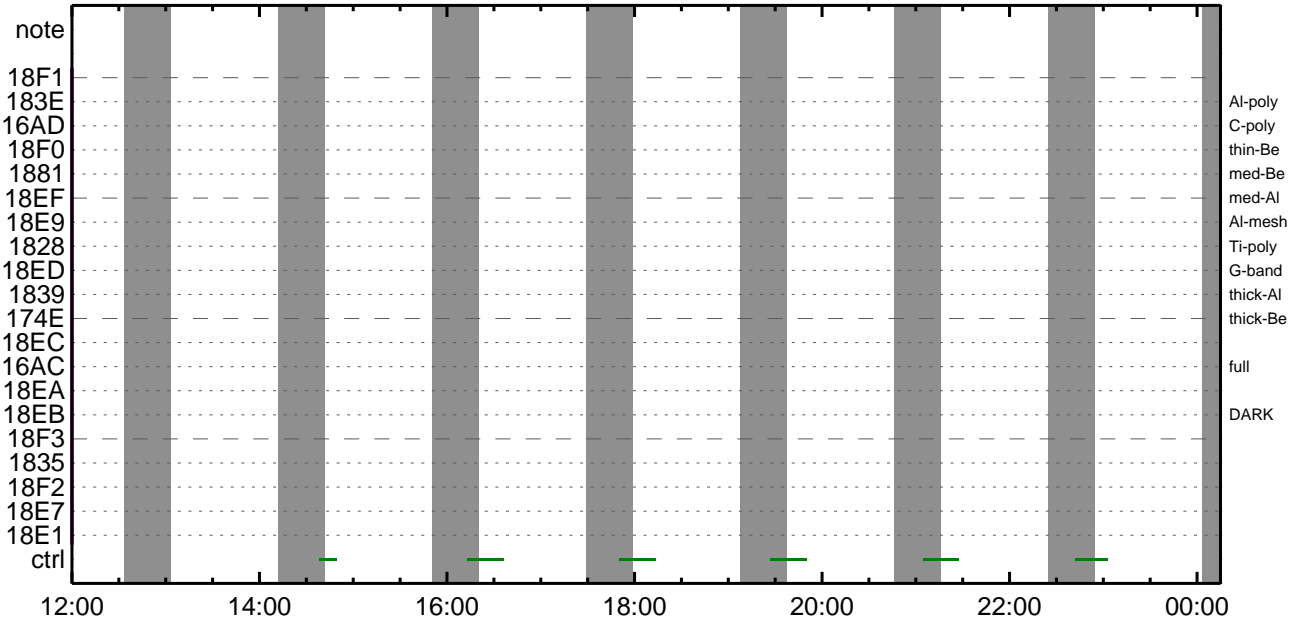
CMDI #0663 2012/06/02



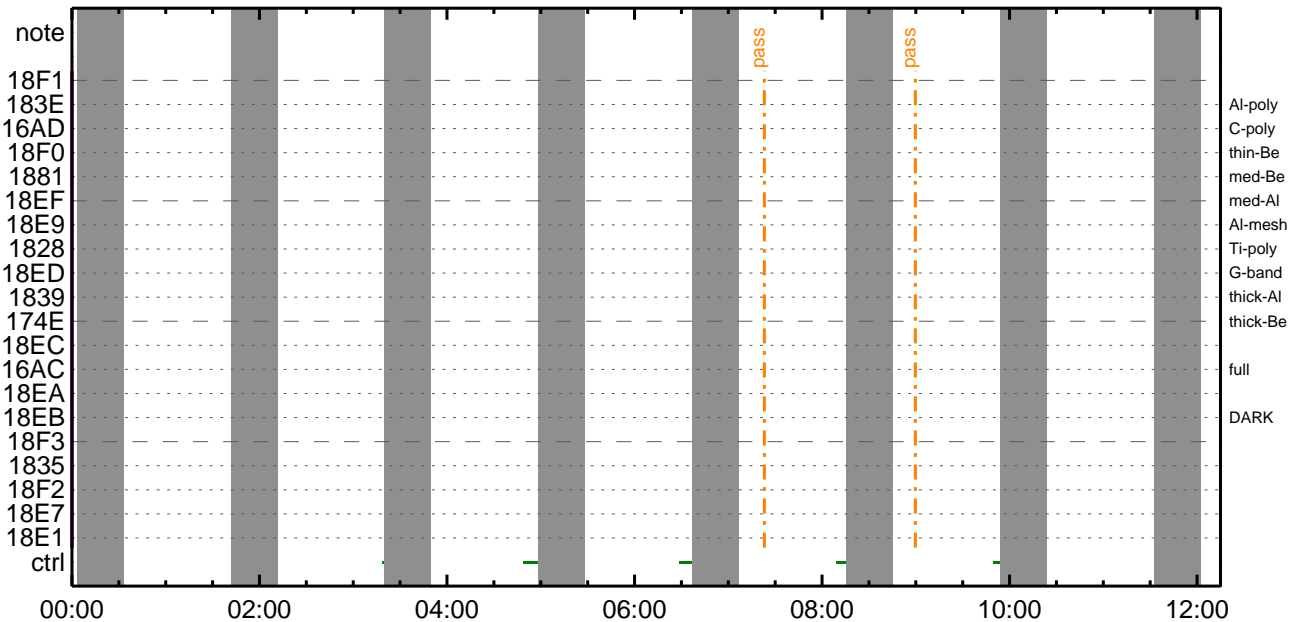
CMDI #0663 2012/06/03



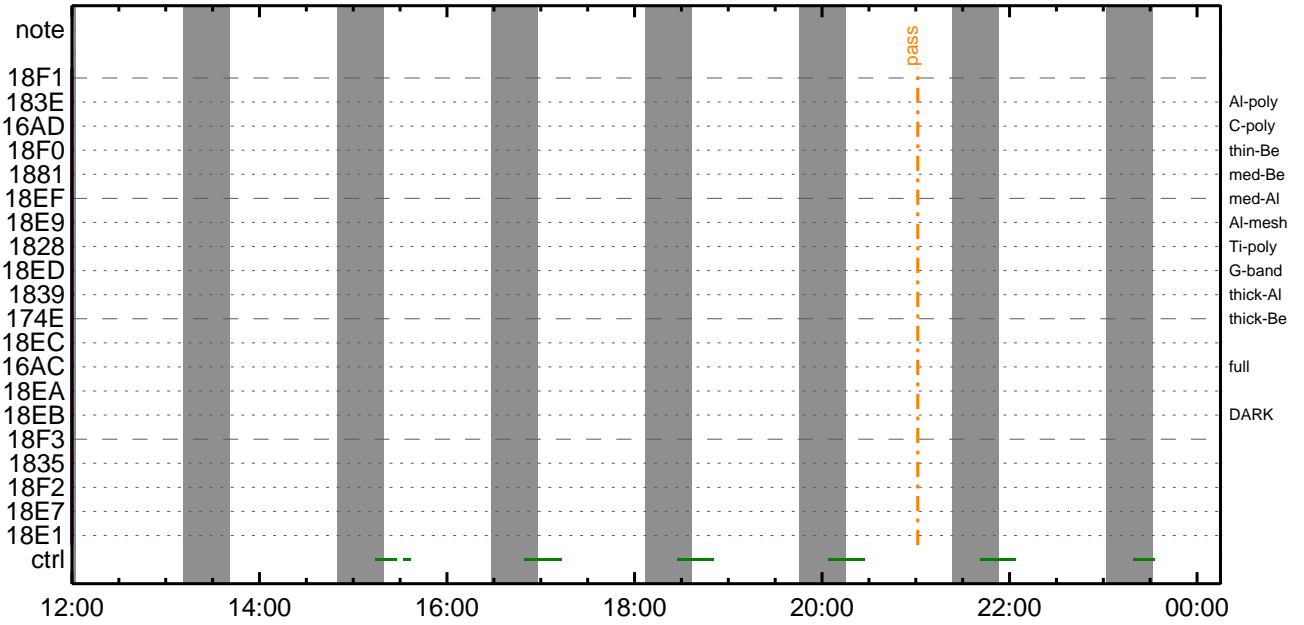
CMDI #0663 2012/06/03



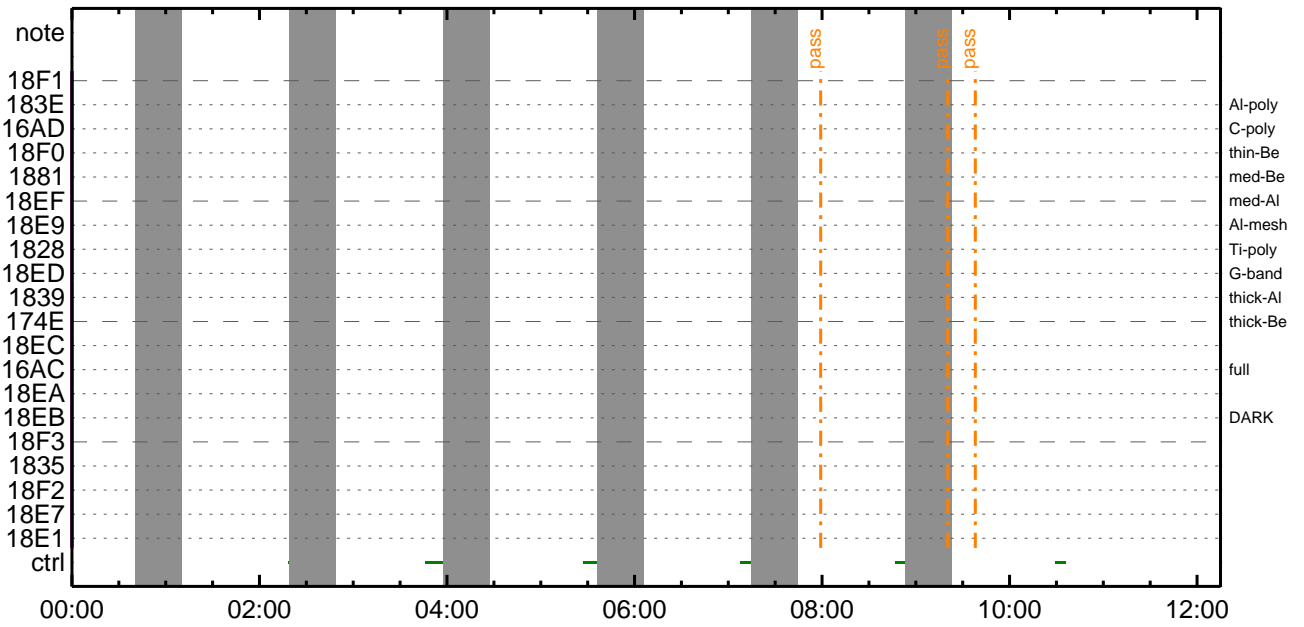
CMDI #0663 2012/06/04



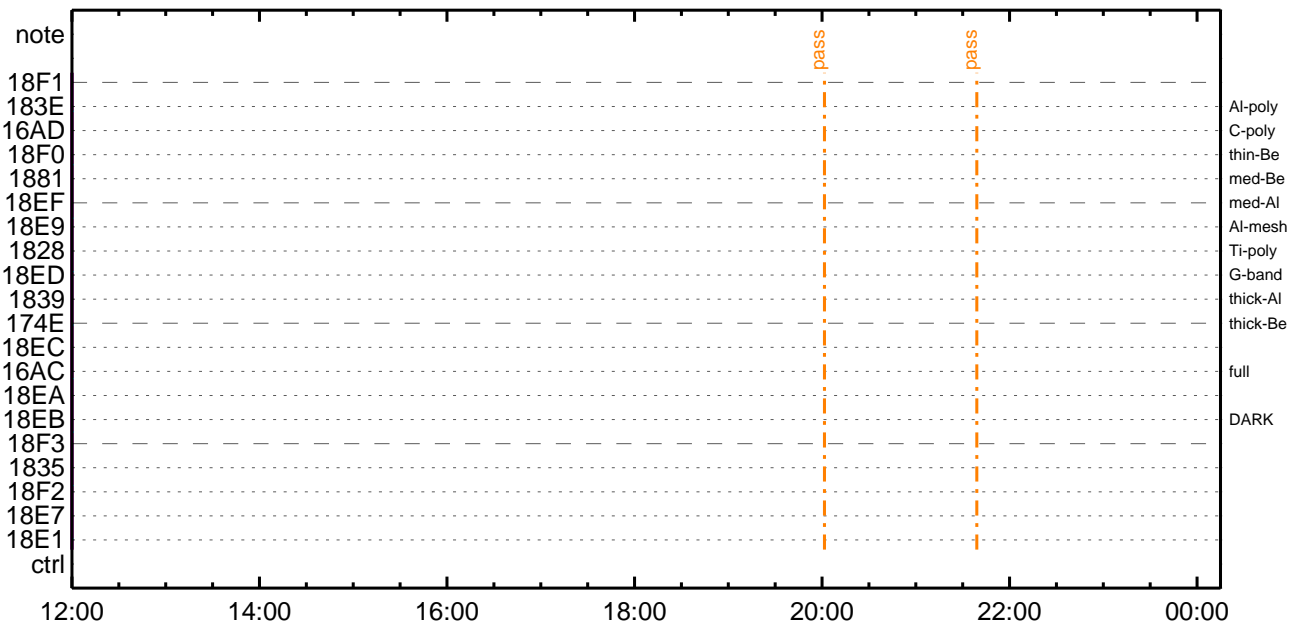
CMDI #0663 2012/06/04



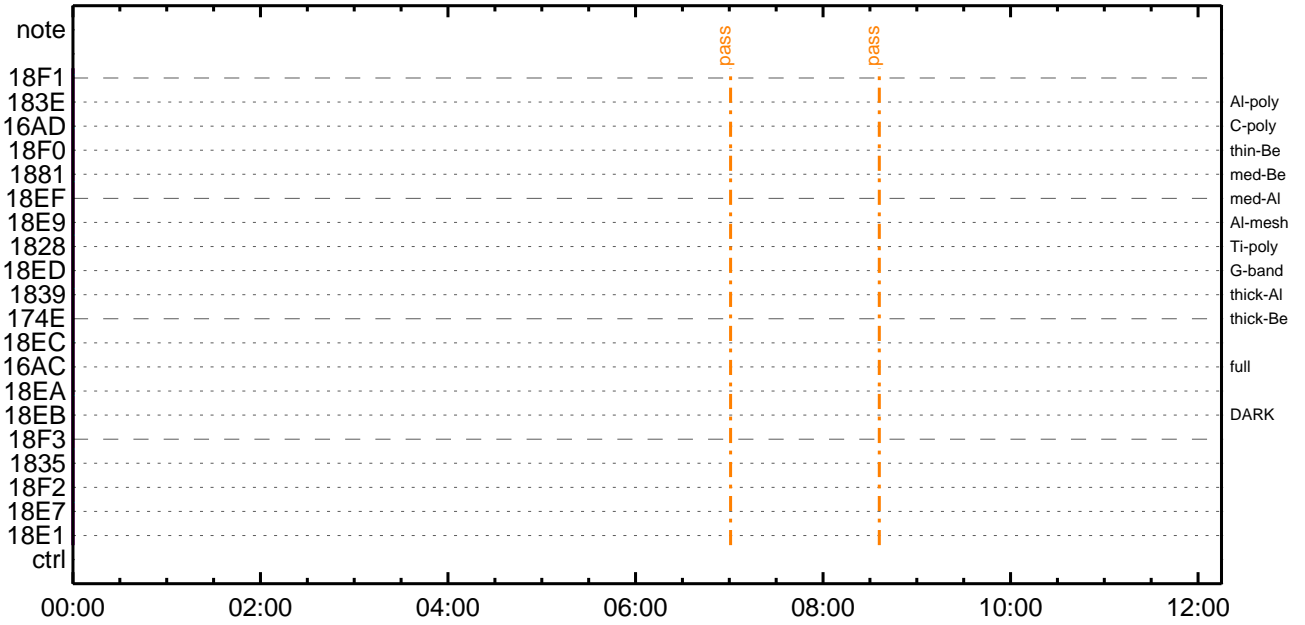
CMDI #0663 2012/06/05



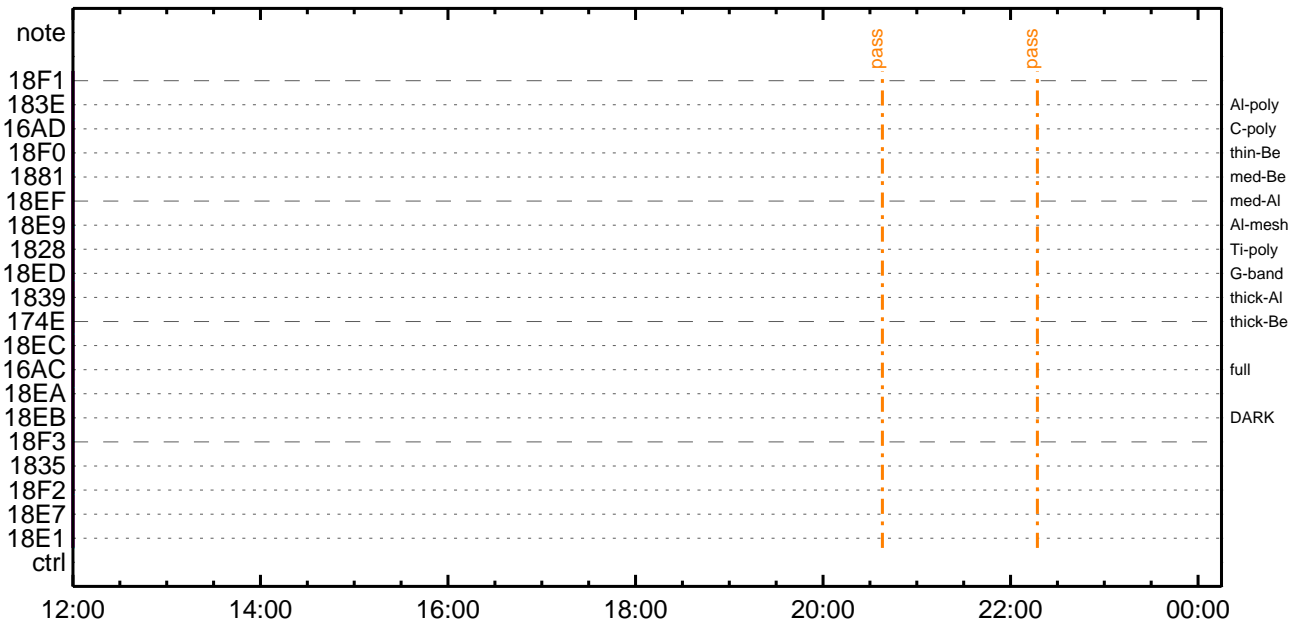
CMDI #0663 2012/06/05



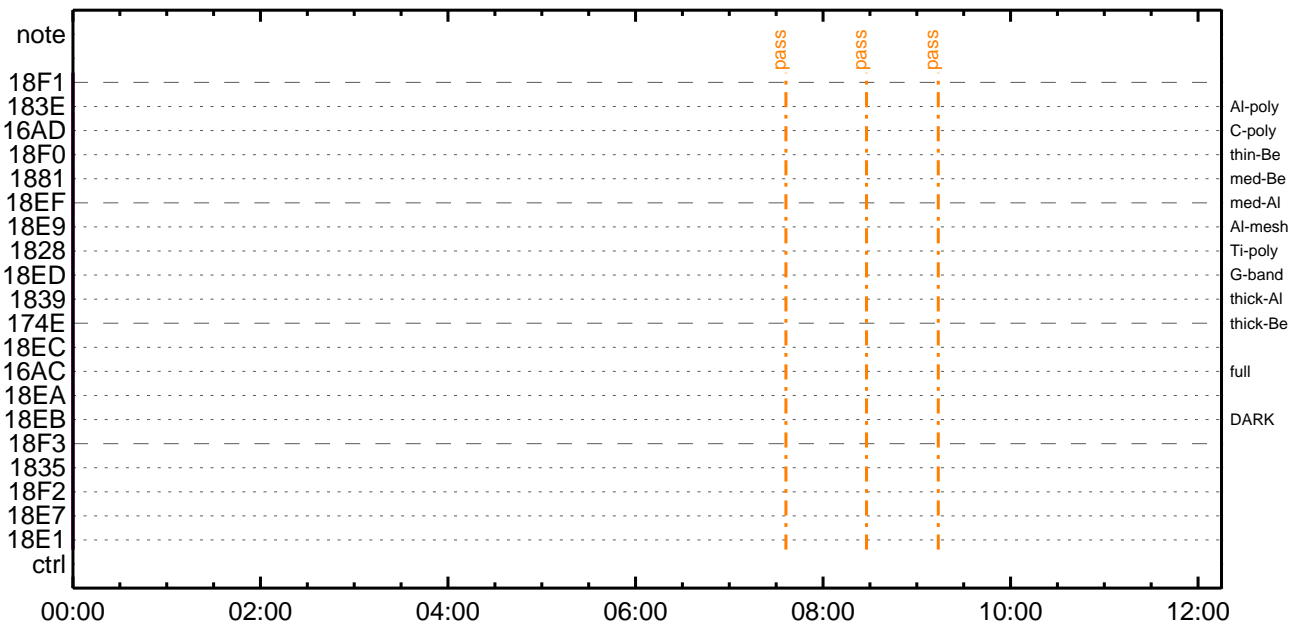
CMDI #0663 2012/06/06



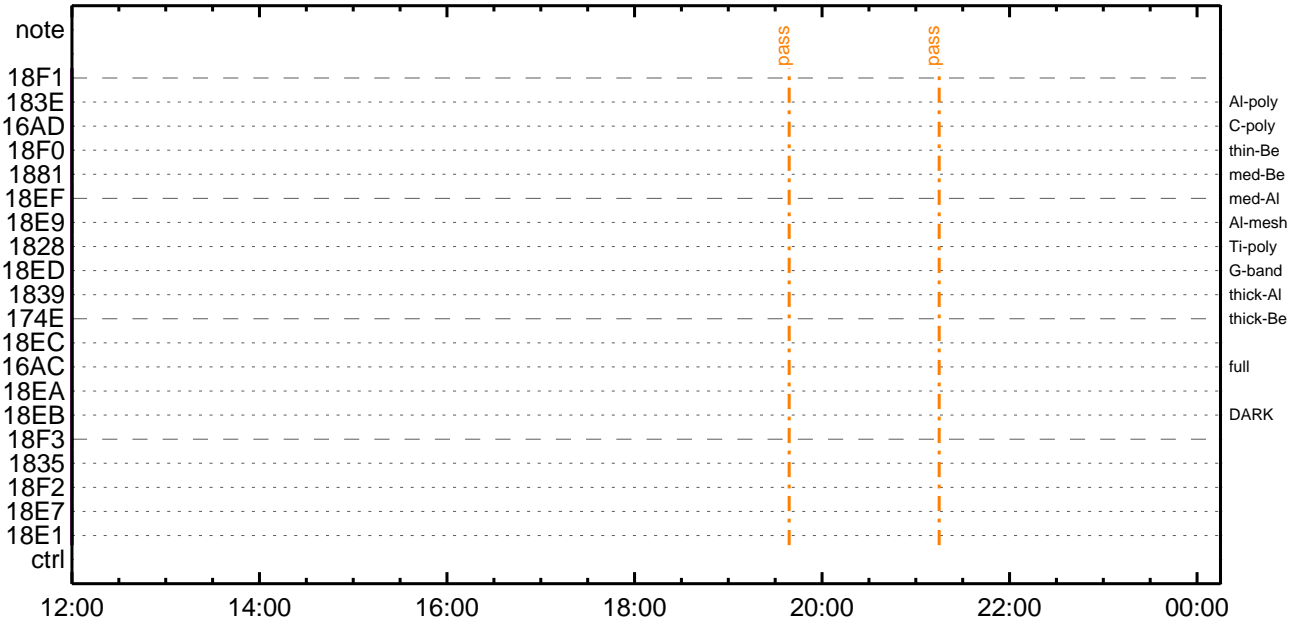
CMDI #0663 2012/06/06



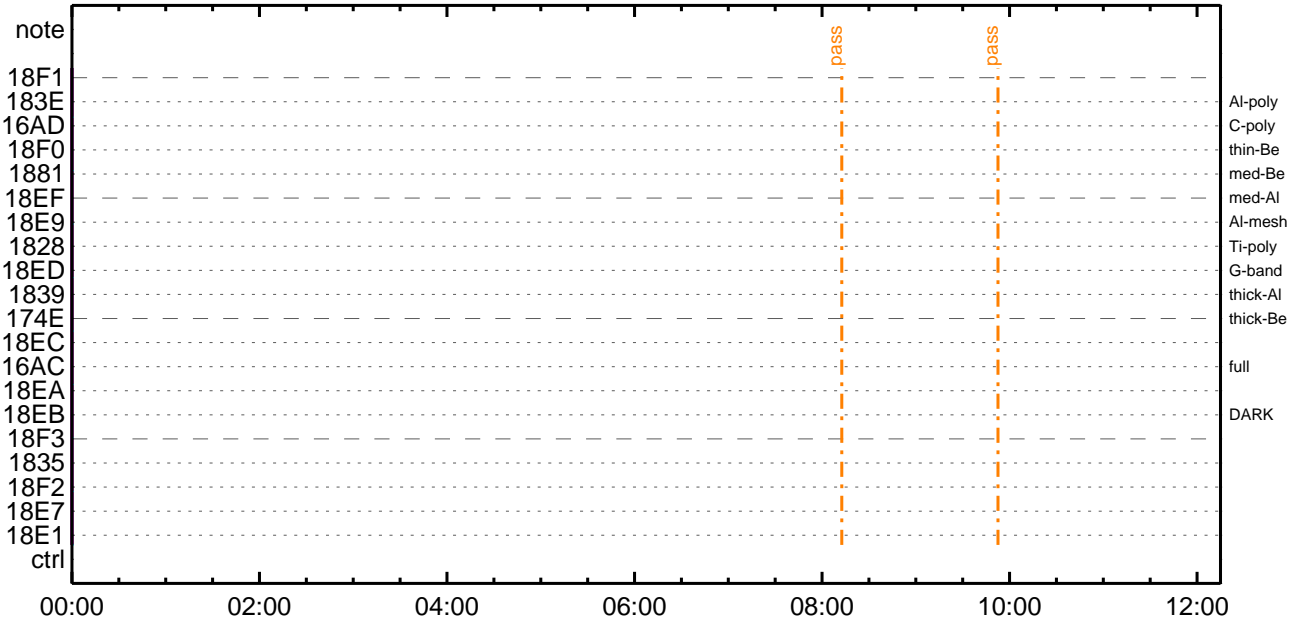
CMDI #0663 2012/06/07



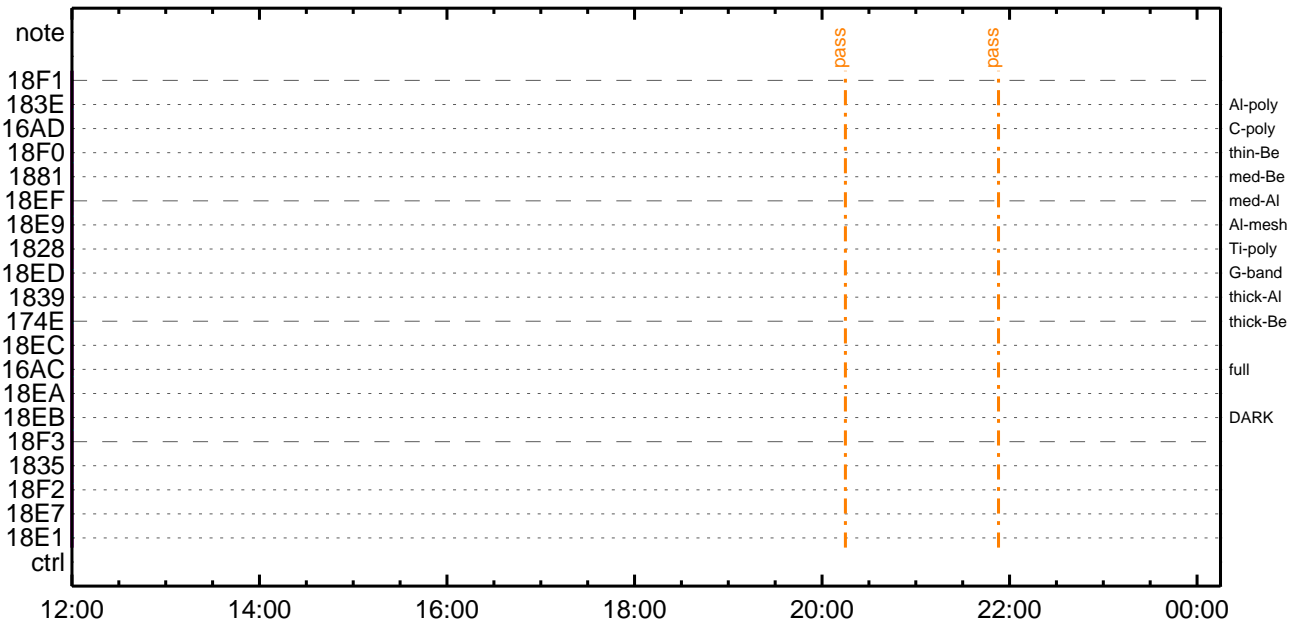
CMDI #0663 2012/06/07



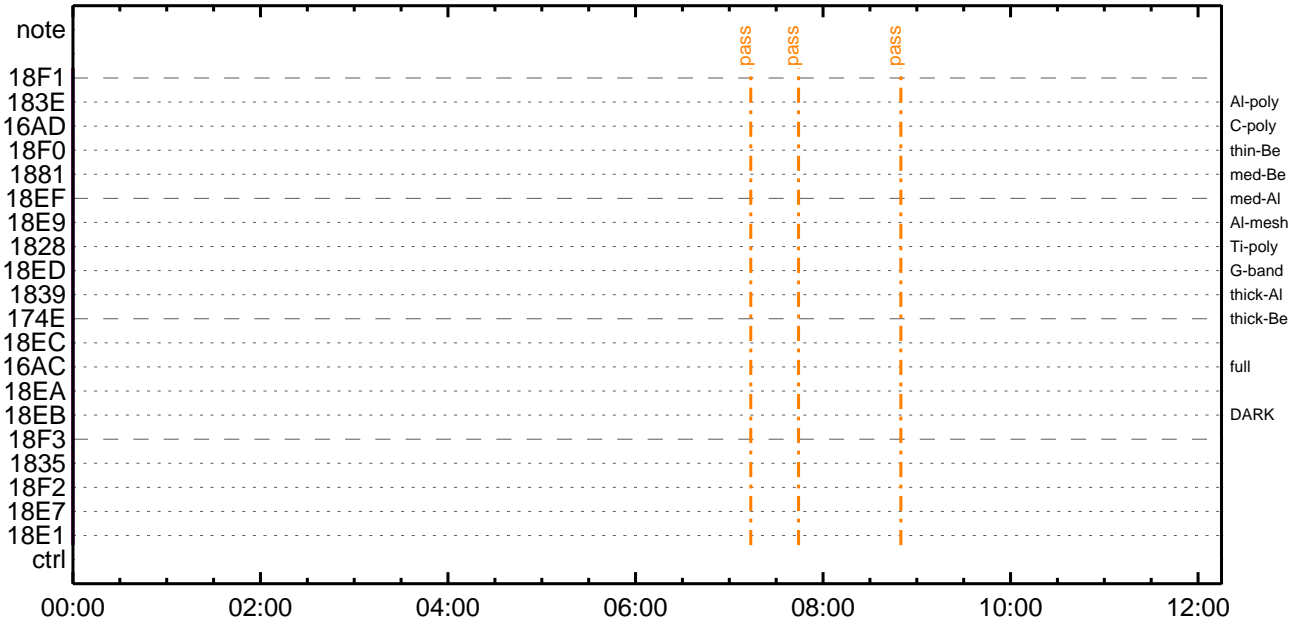
CMDI #0663 2012/06/08



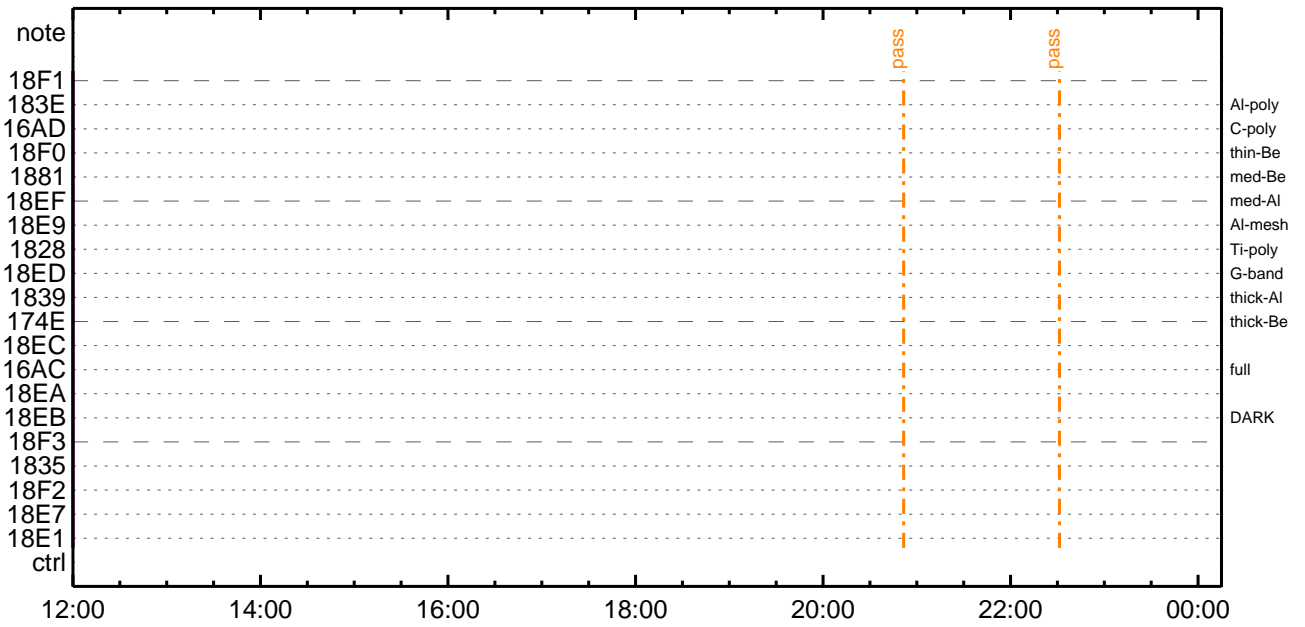
CMDI #0663 2012/06/08



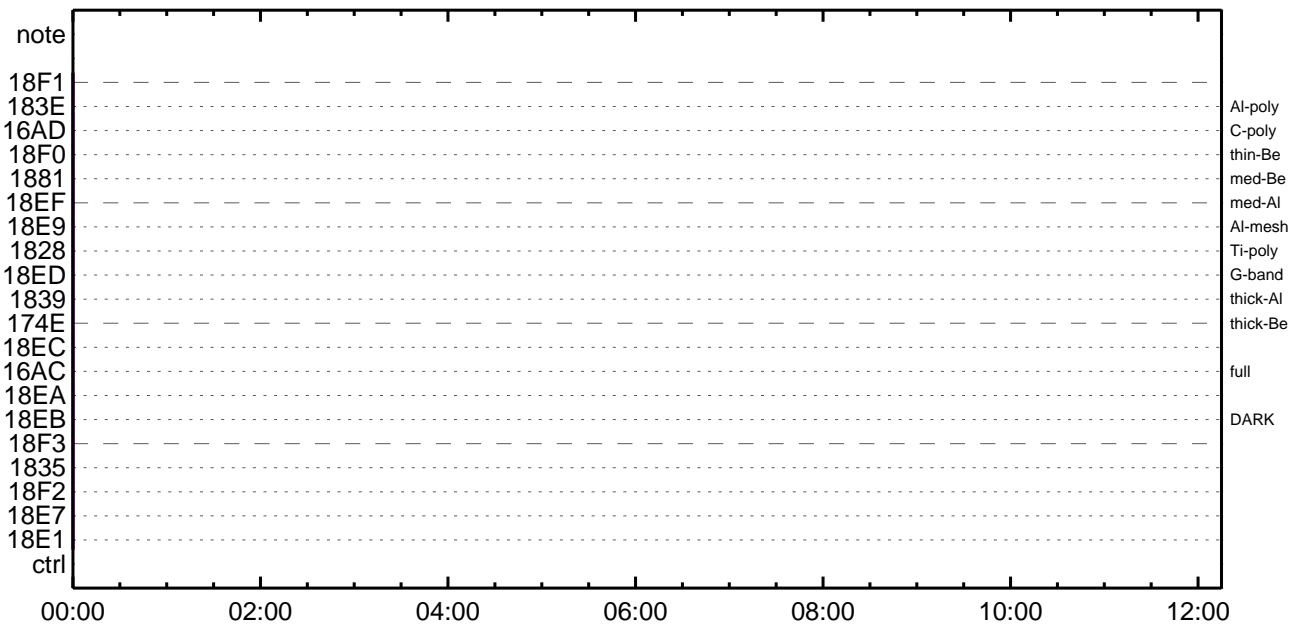
CMDI #0663 2012/06/09



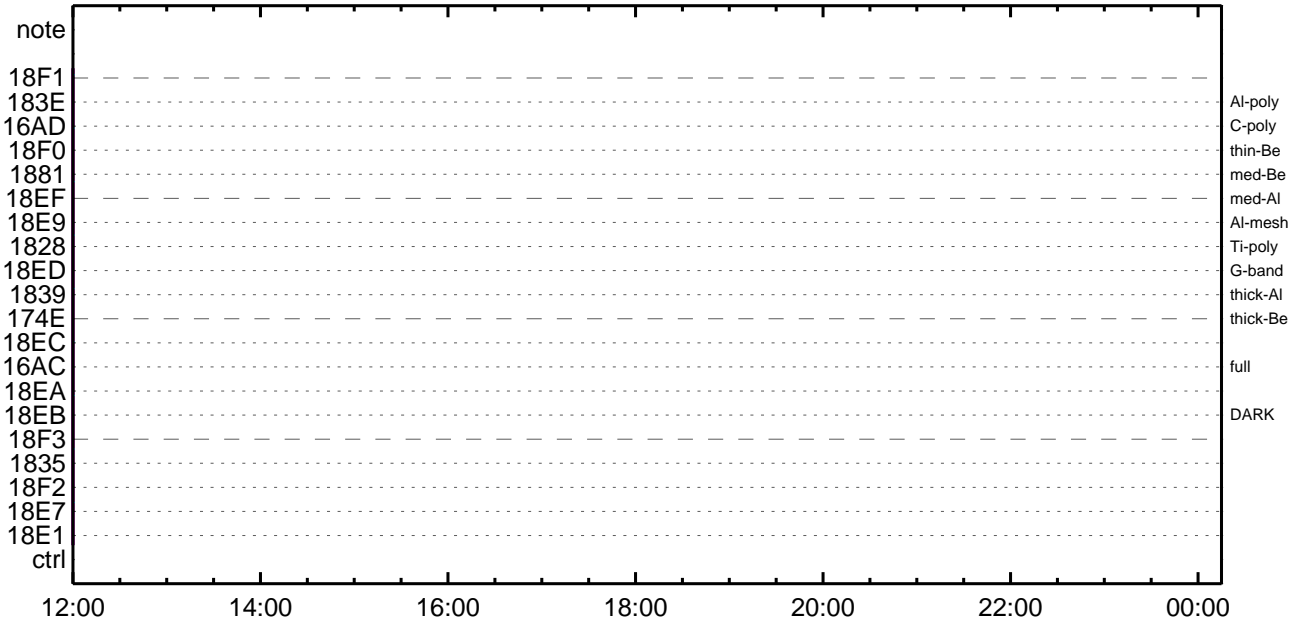
CMDI #0663 2012/06/09



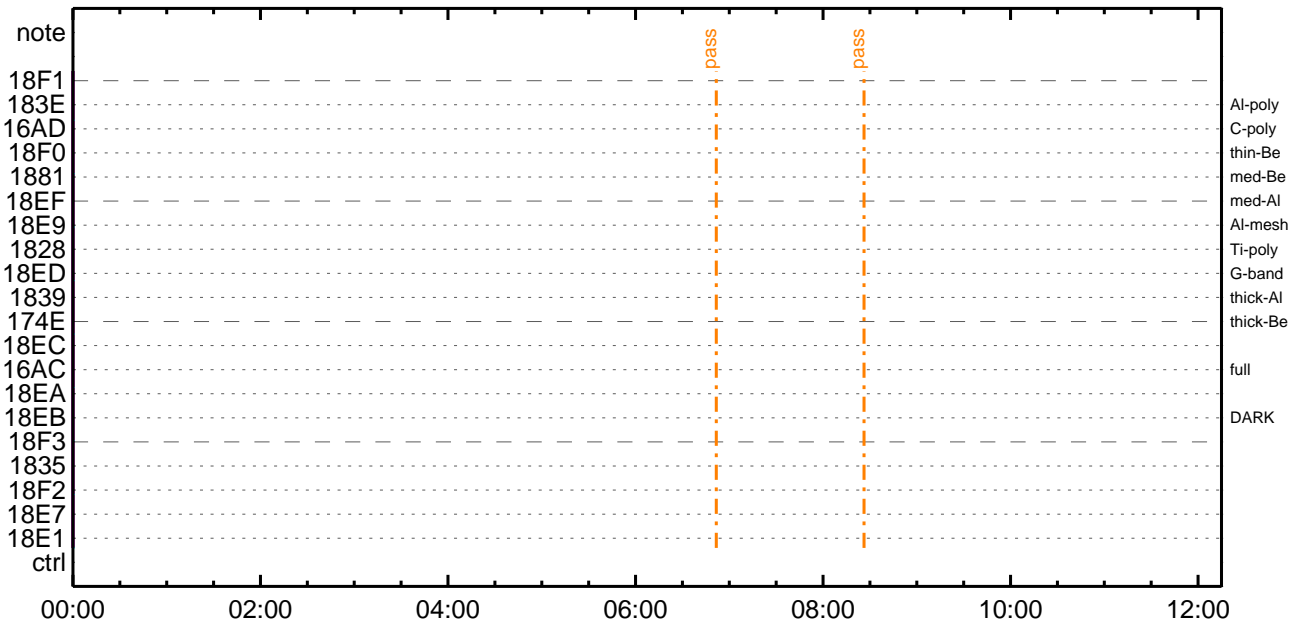
CMDI #0663 2012/06/10



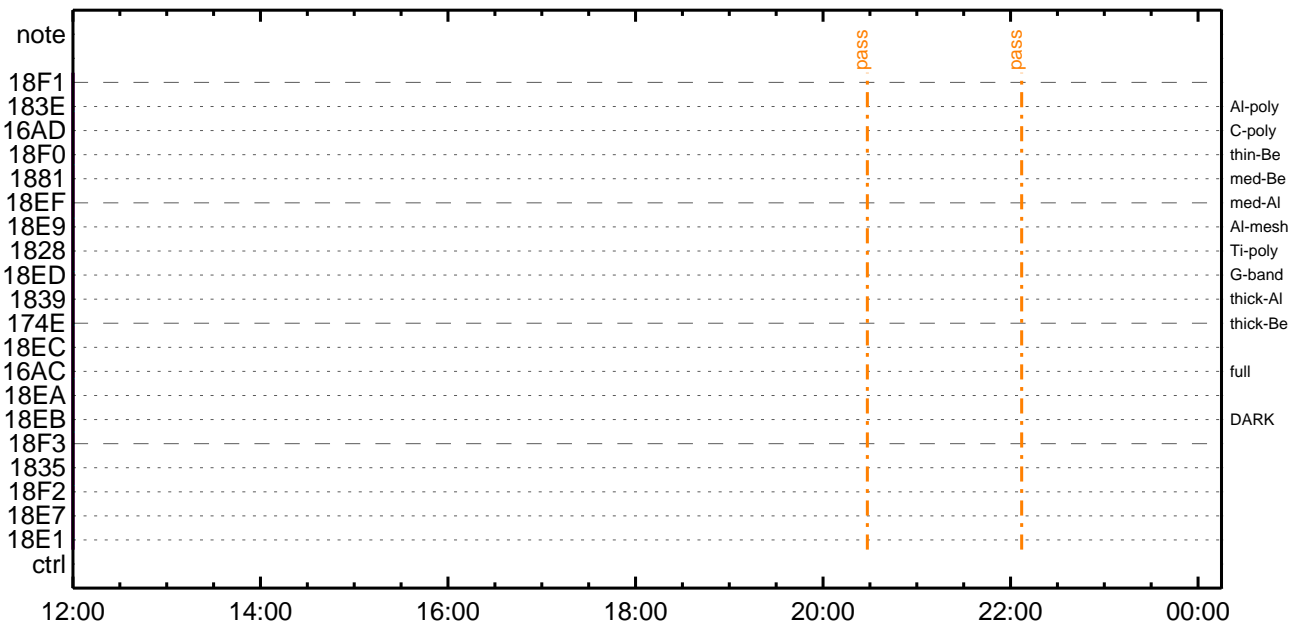
CMDI #0663 2012/06/10



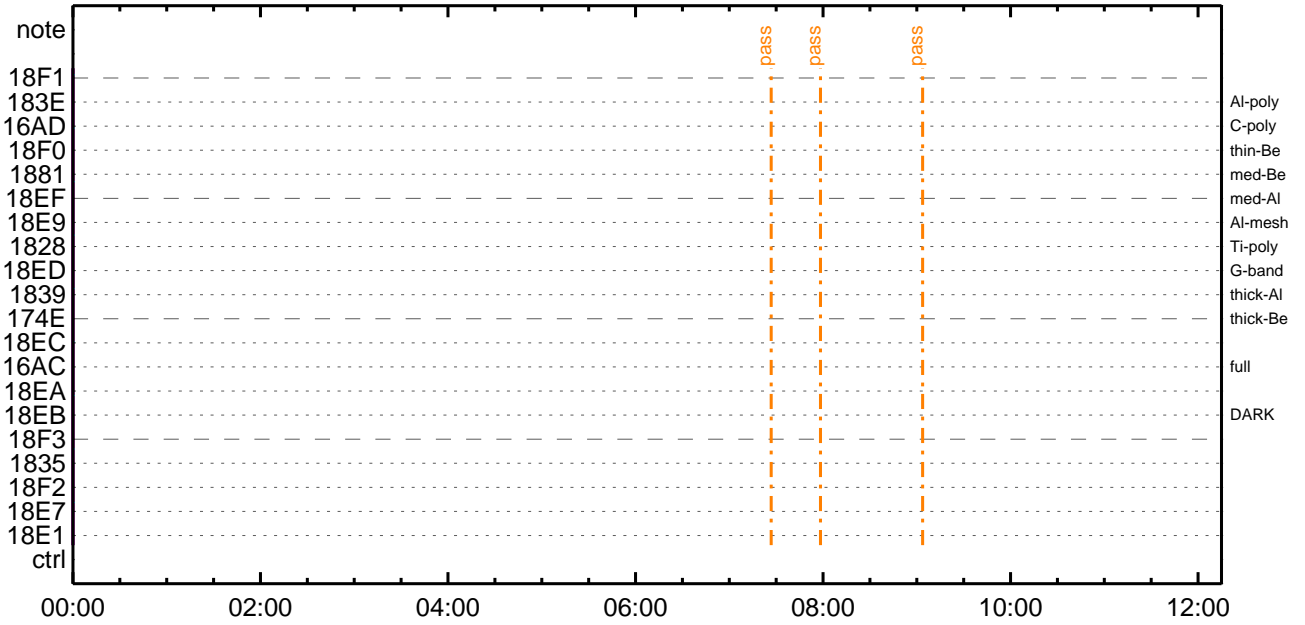
CMDI #0663 2012/06/11



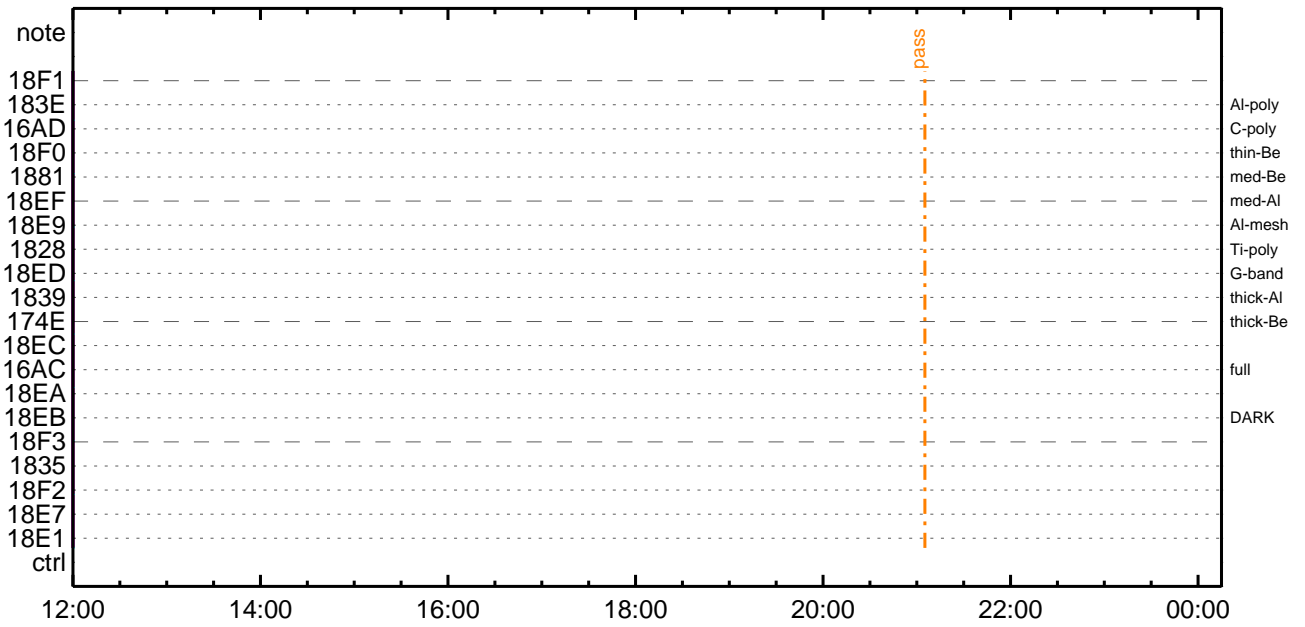
CMDI #0663 2012/06/11



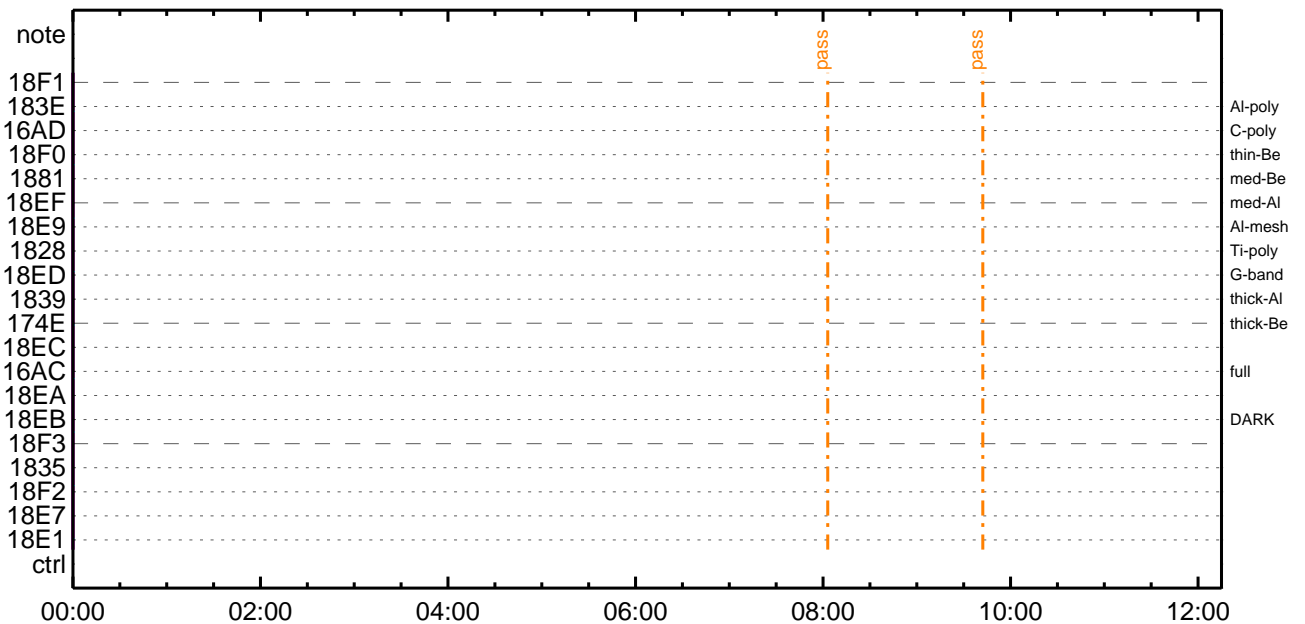
CMDI #0663 2012/06/12



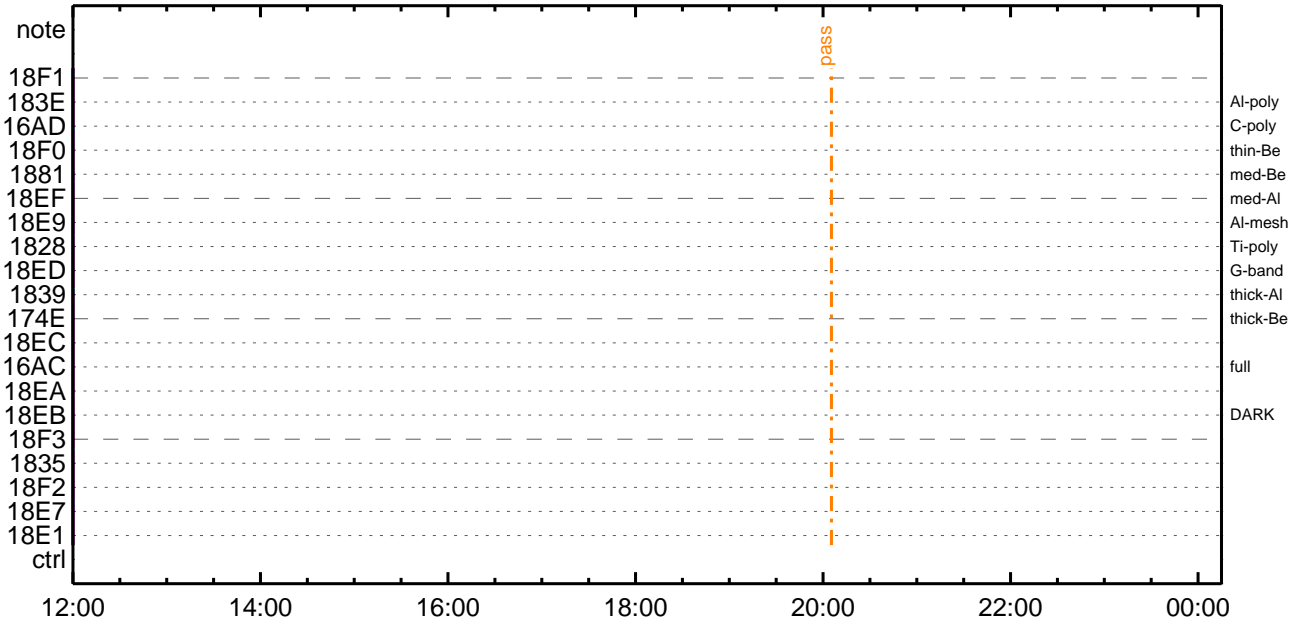
CMDI #0663 2012/06/12



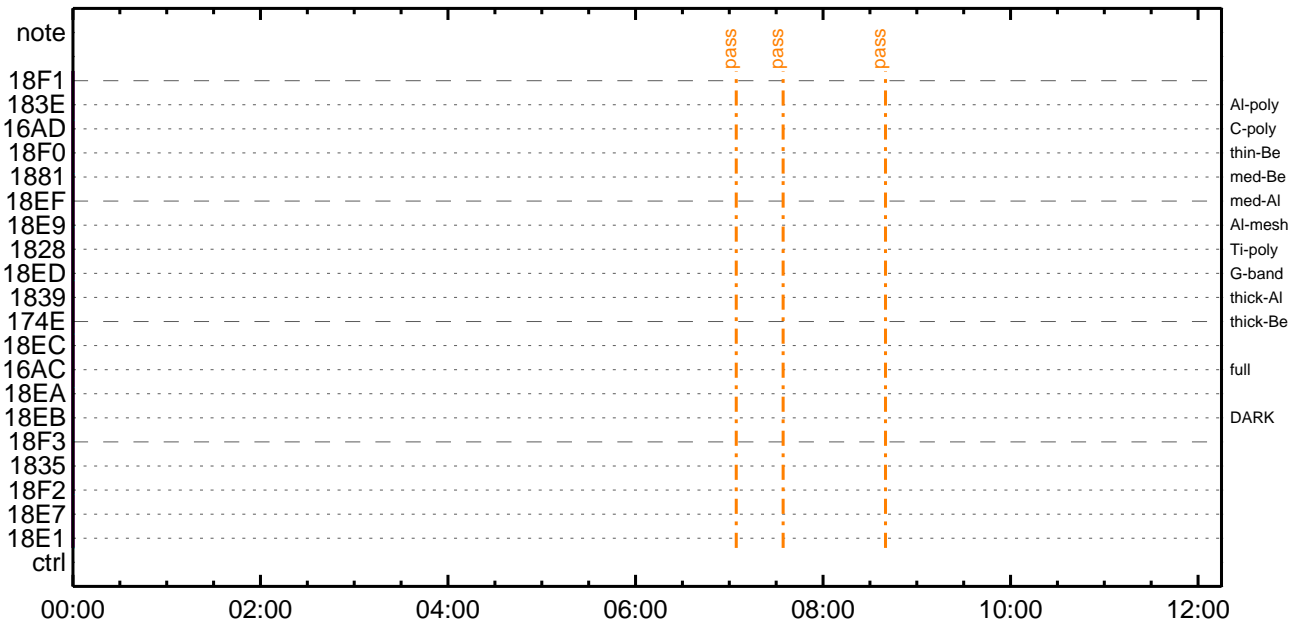
CMDI #0663 2012/06/13



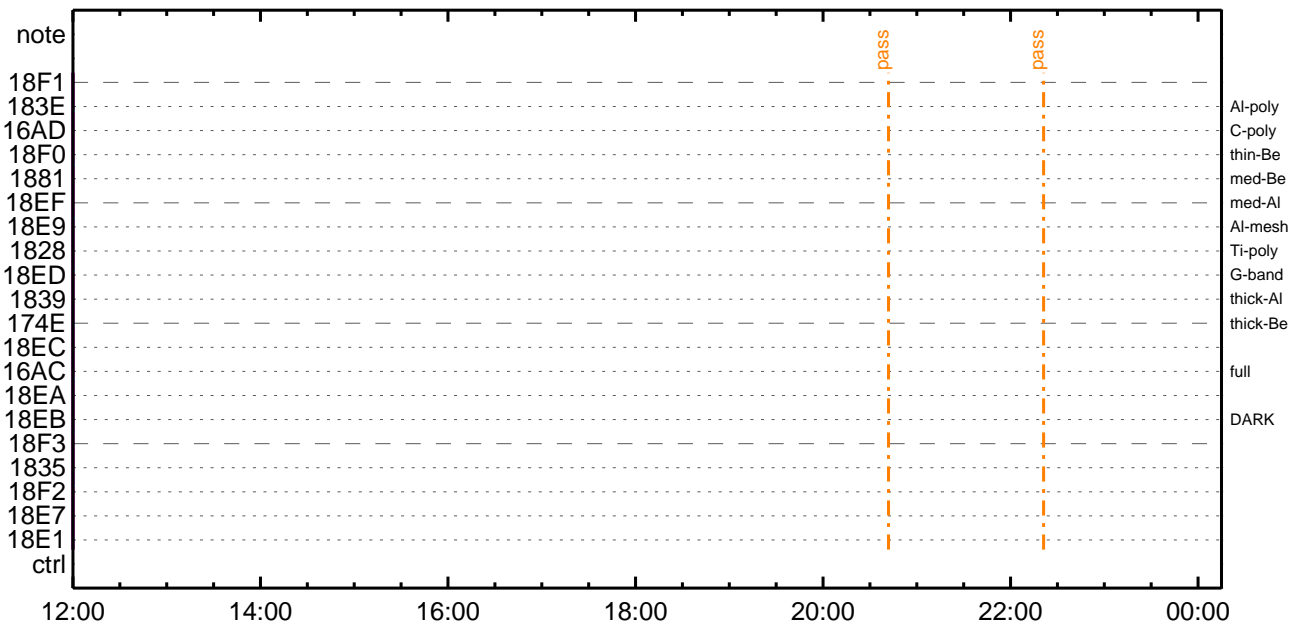
CMDI #0663 2012/06/13



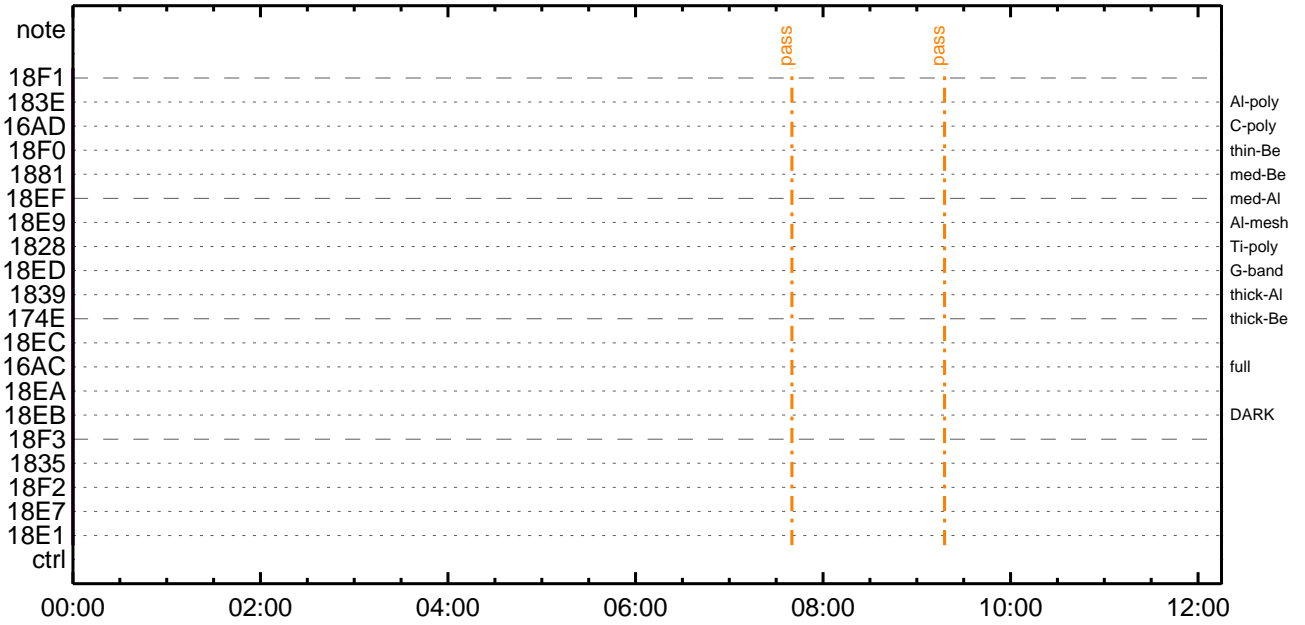
CMDI #0663 2012/06/14



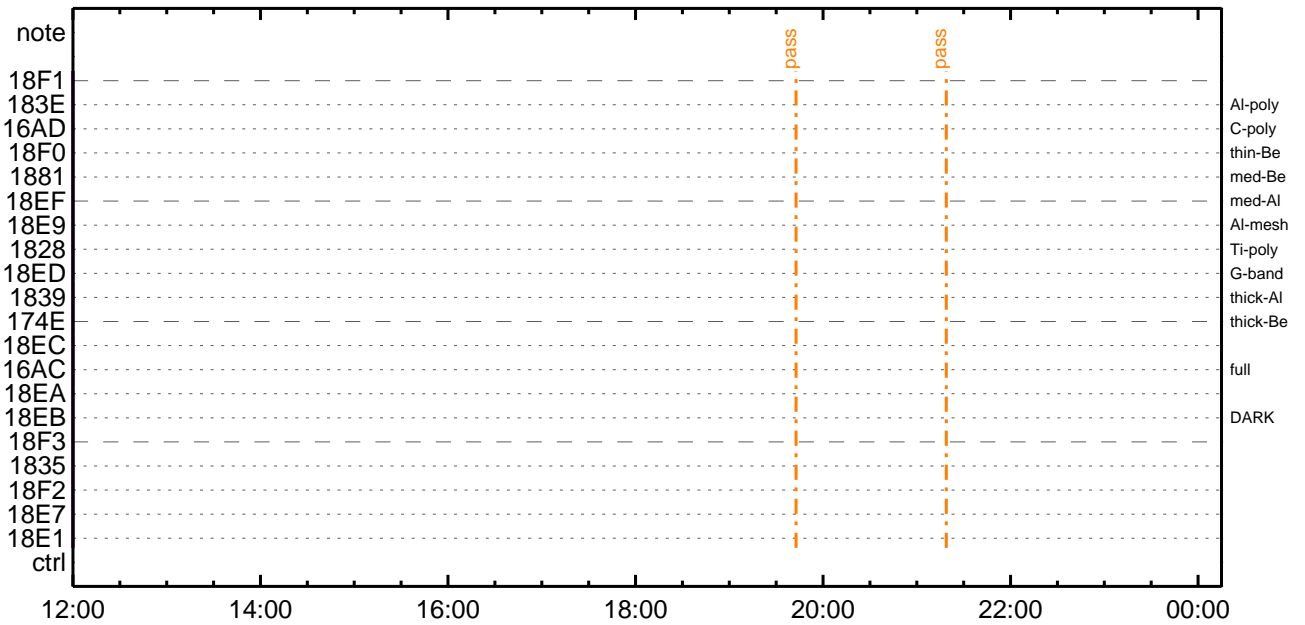
CMDI #0663 2012/06/14



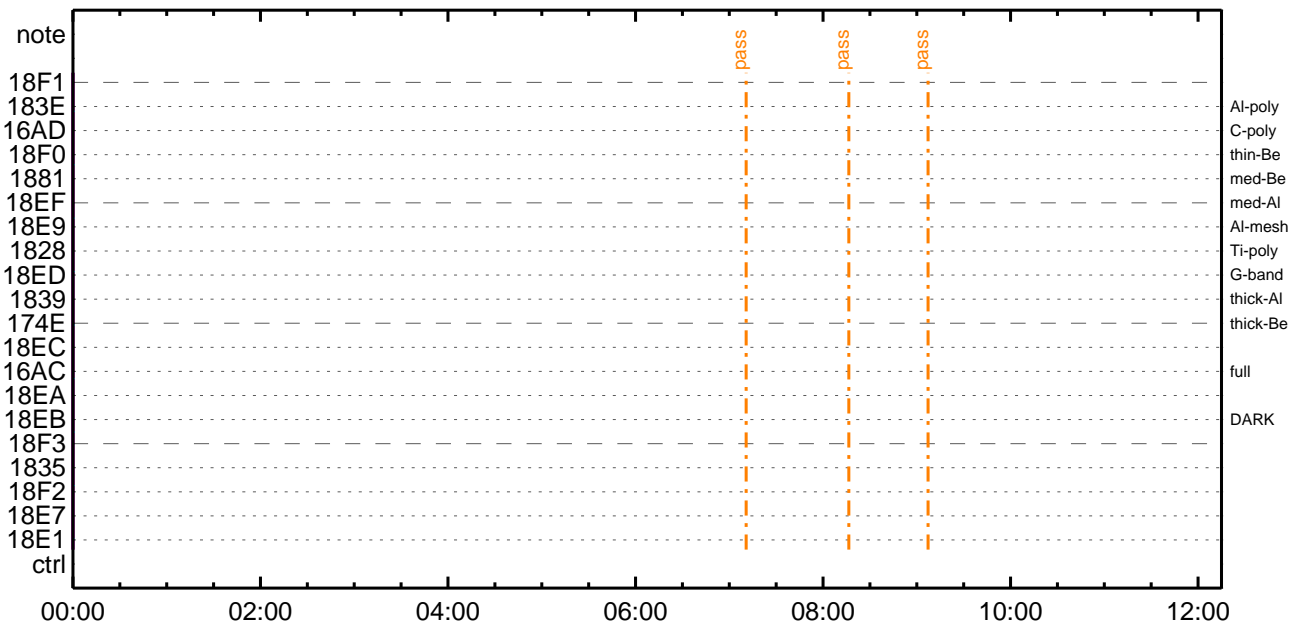
CMDI #0663 2012/06/15



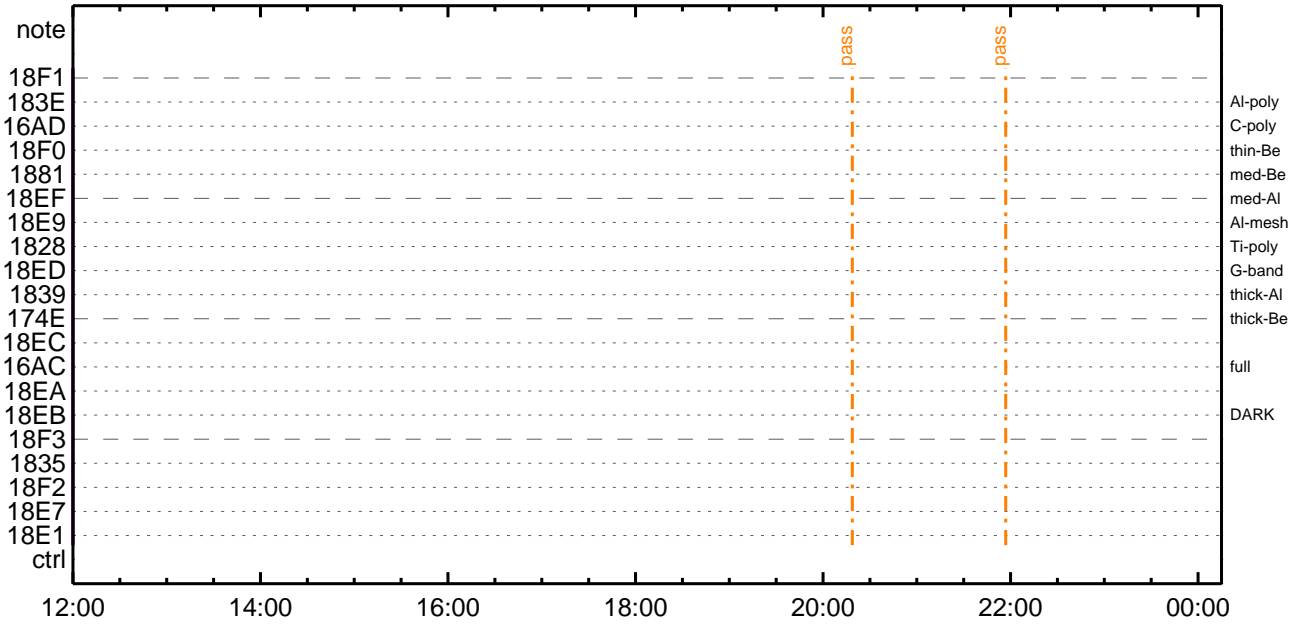
CMDI #0663 2012/06/15



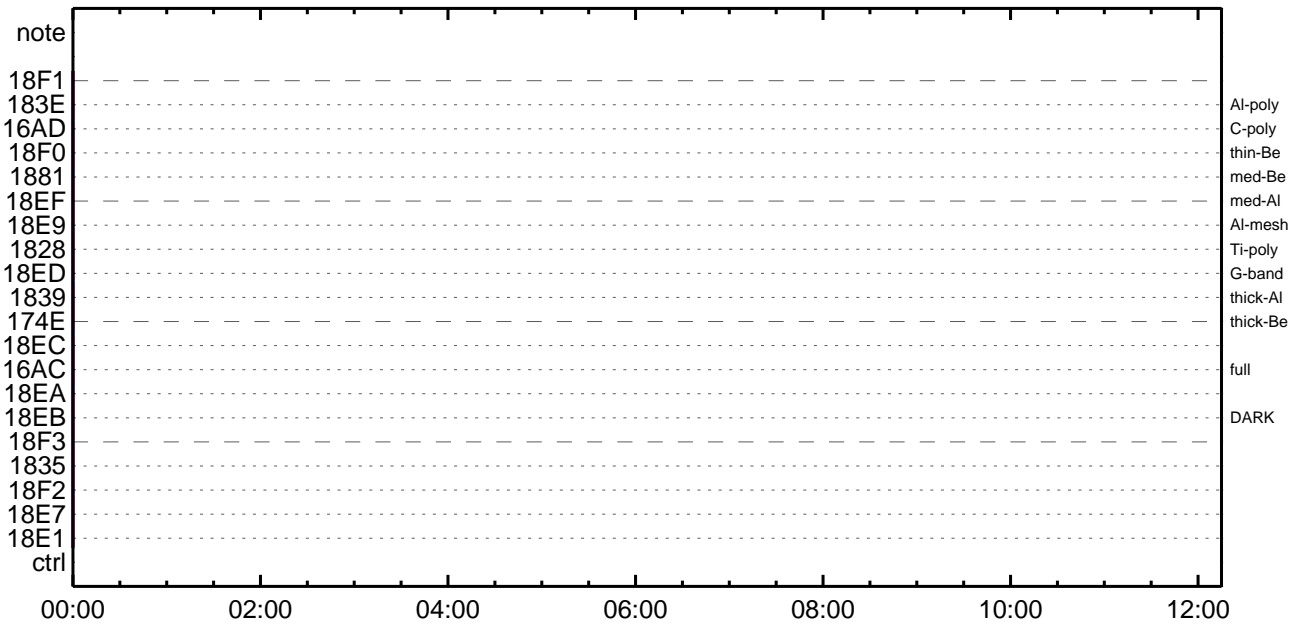
CMDI #0663 2012/06/16



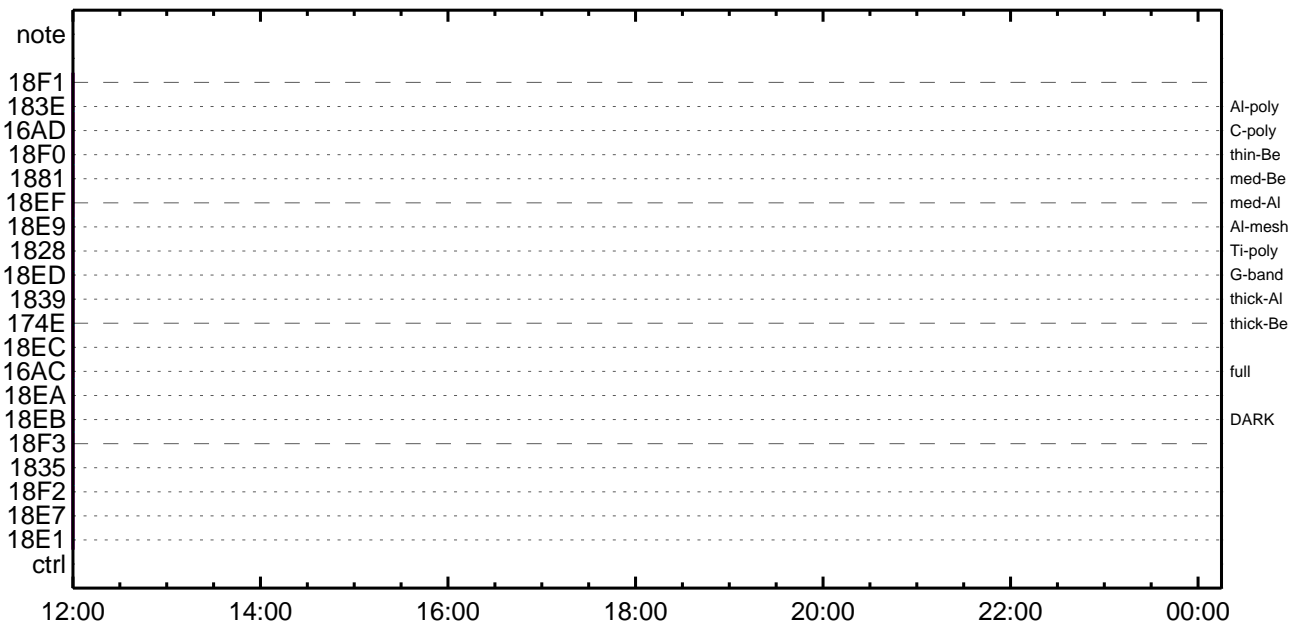
CMDI #0663 2012/06/16



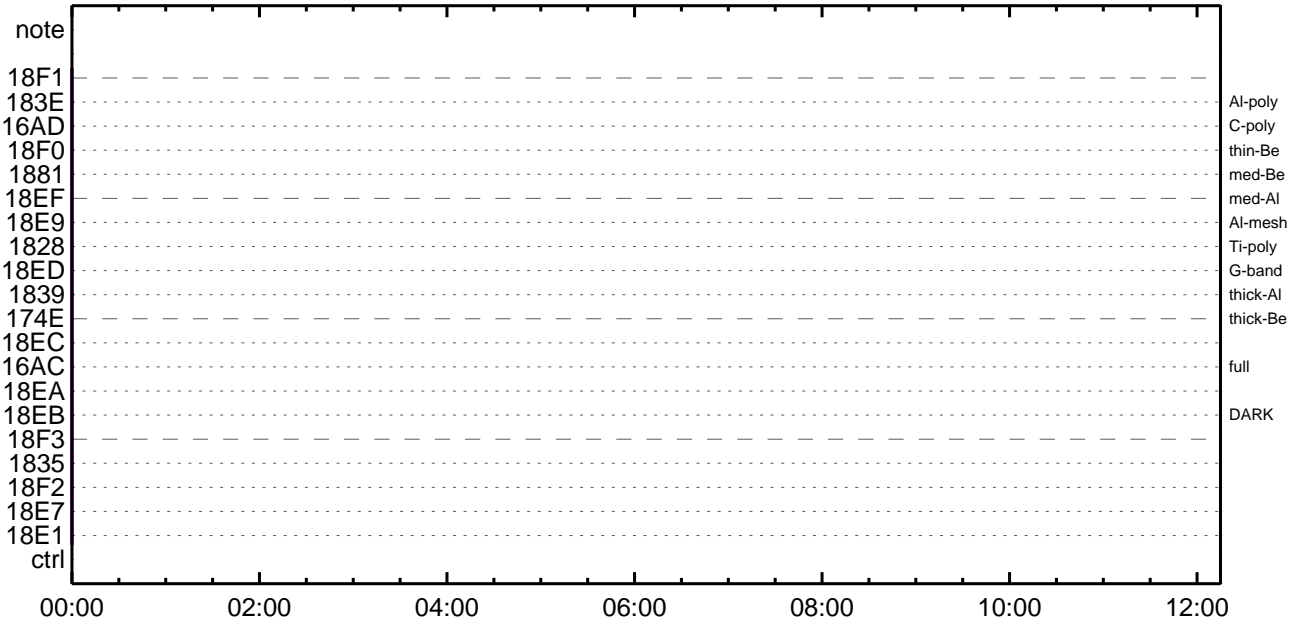
CMDI #0663 2012/06/17



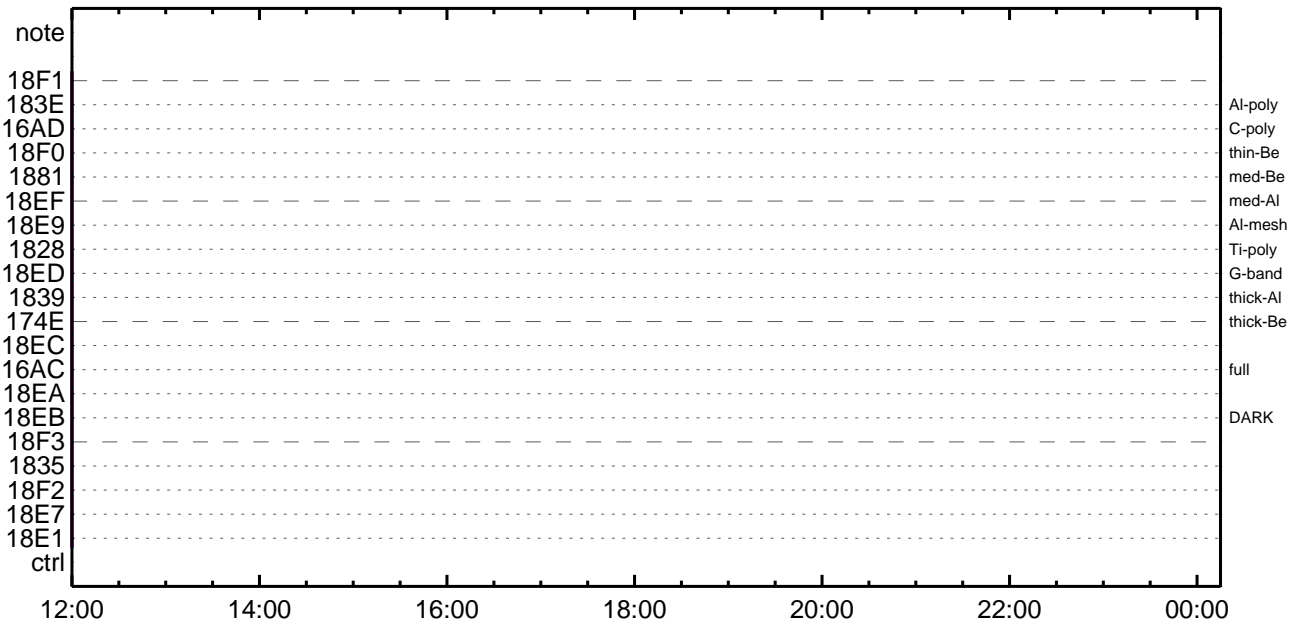
CMDI #0663 2012/06/17



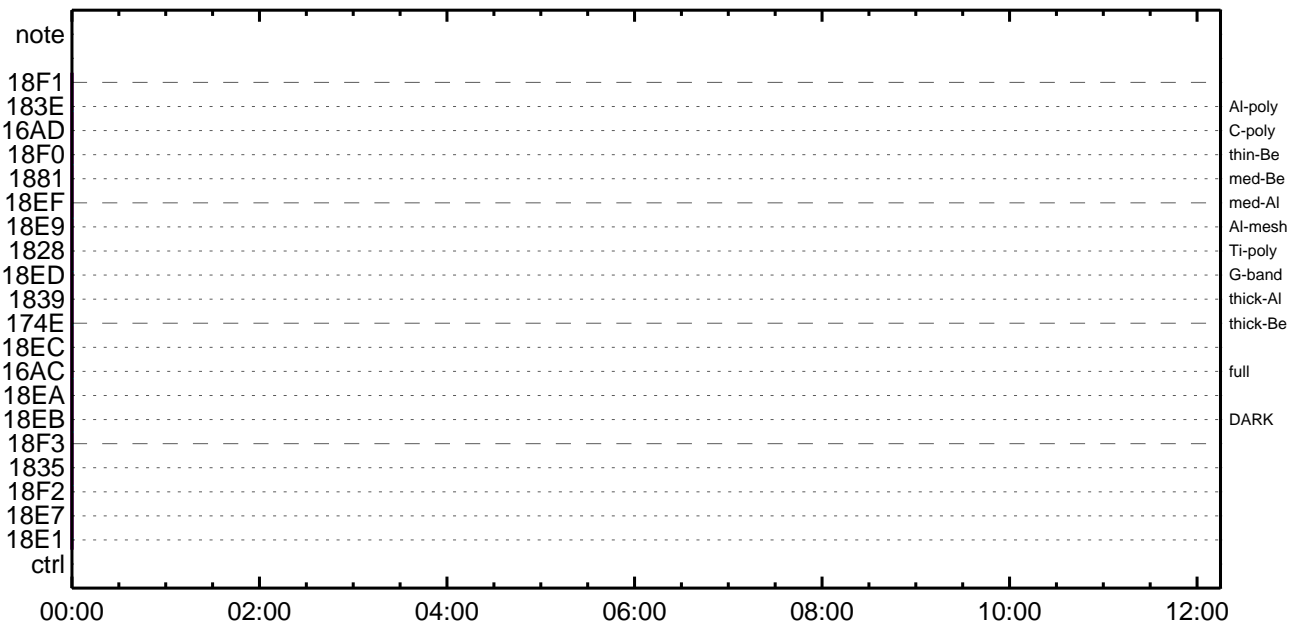
CMDI #0663 2012/06/18



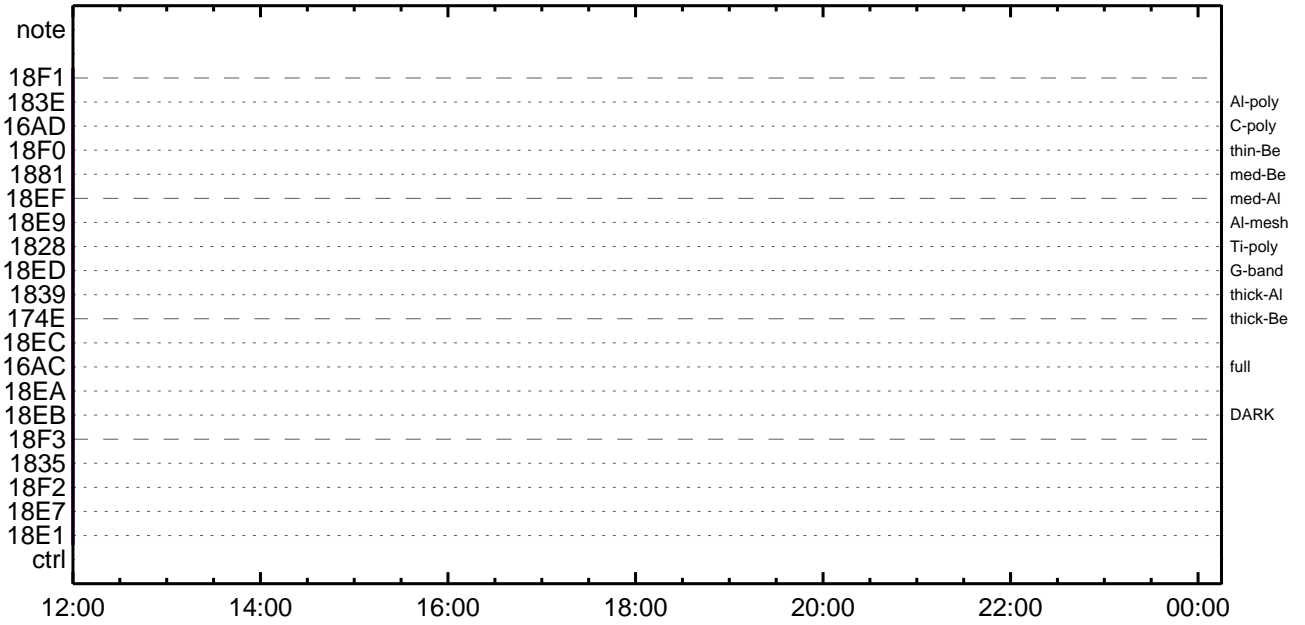
CMDI #0663 2012/06/18



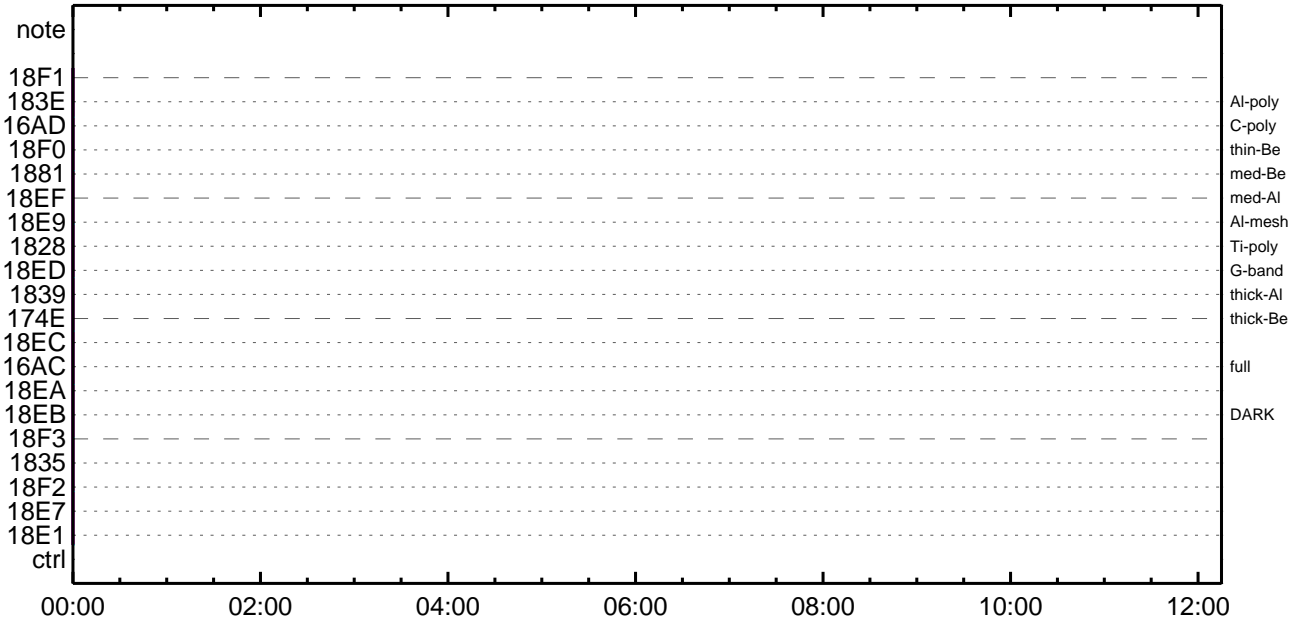
CMDI #0663 2012/06/19



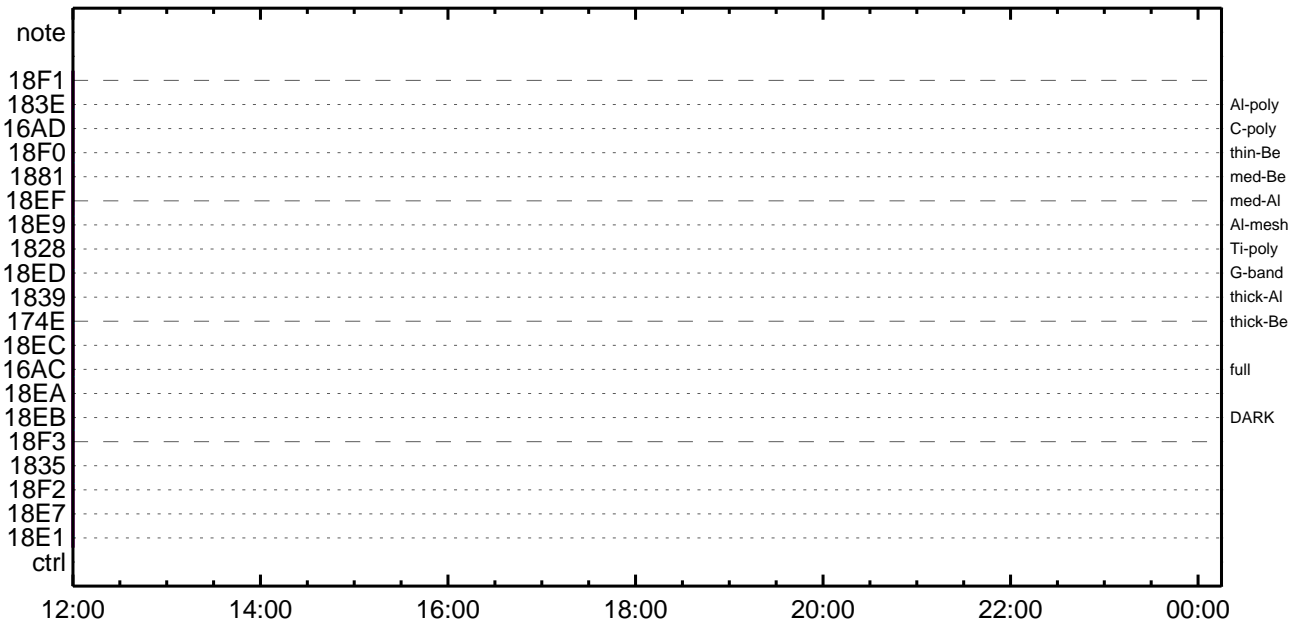
CMDI #0663 2012/06/19



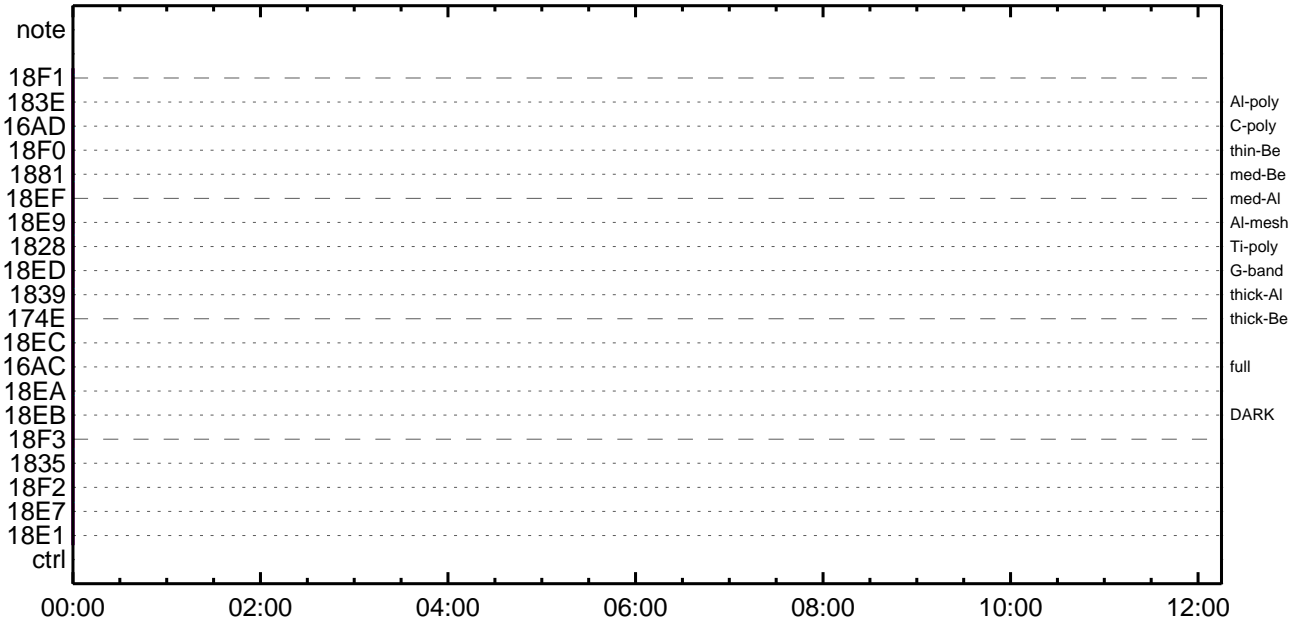
CMDI #0663 2012/06/20



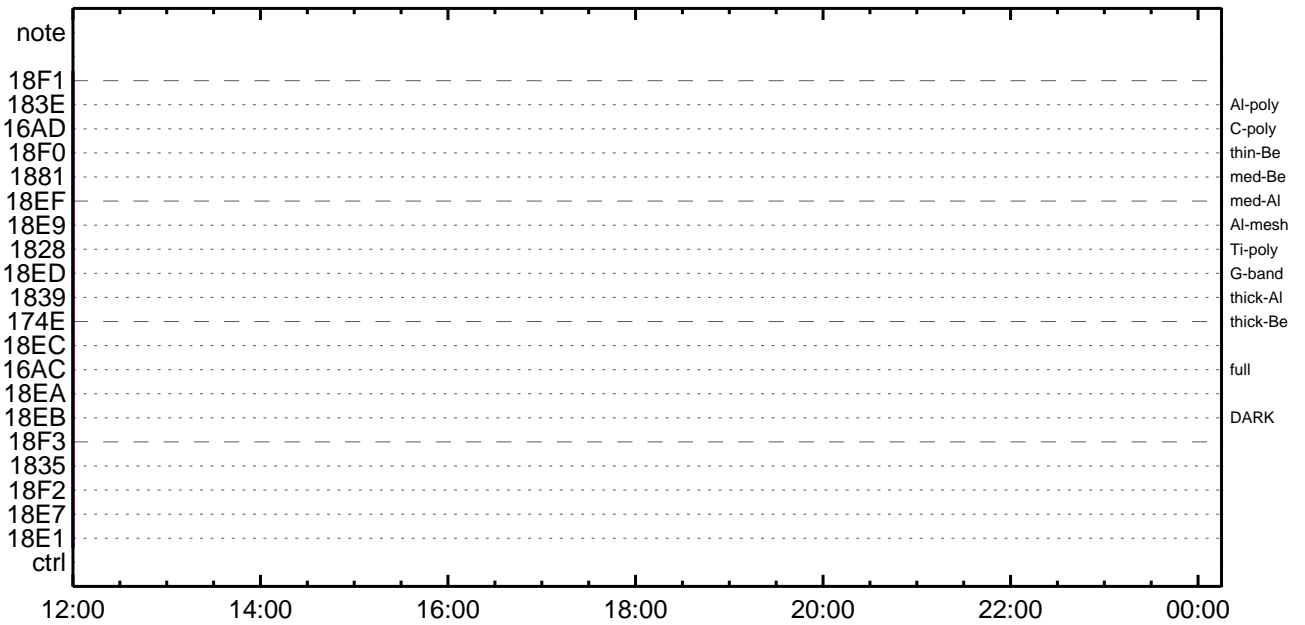
CMDI #0663 2012/06/20



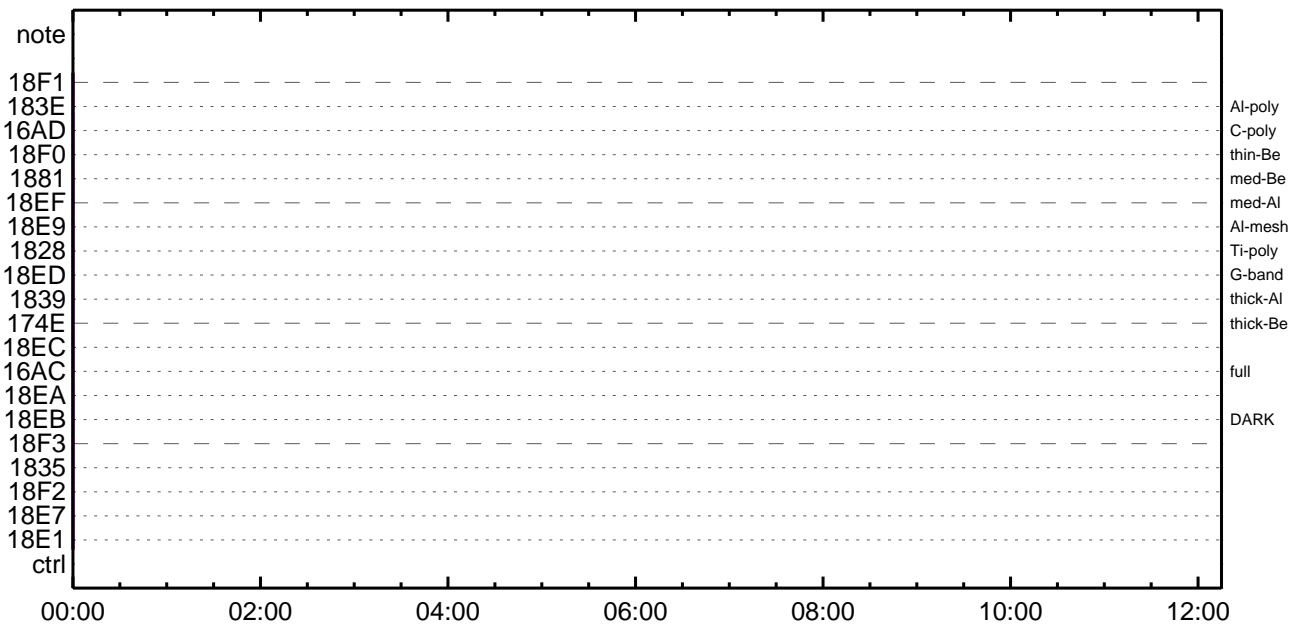
CMDI #0663 2012/06/21



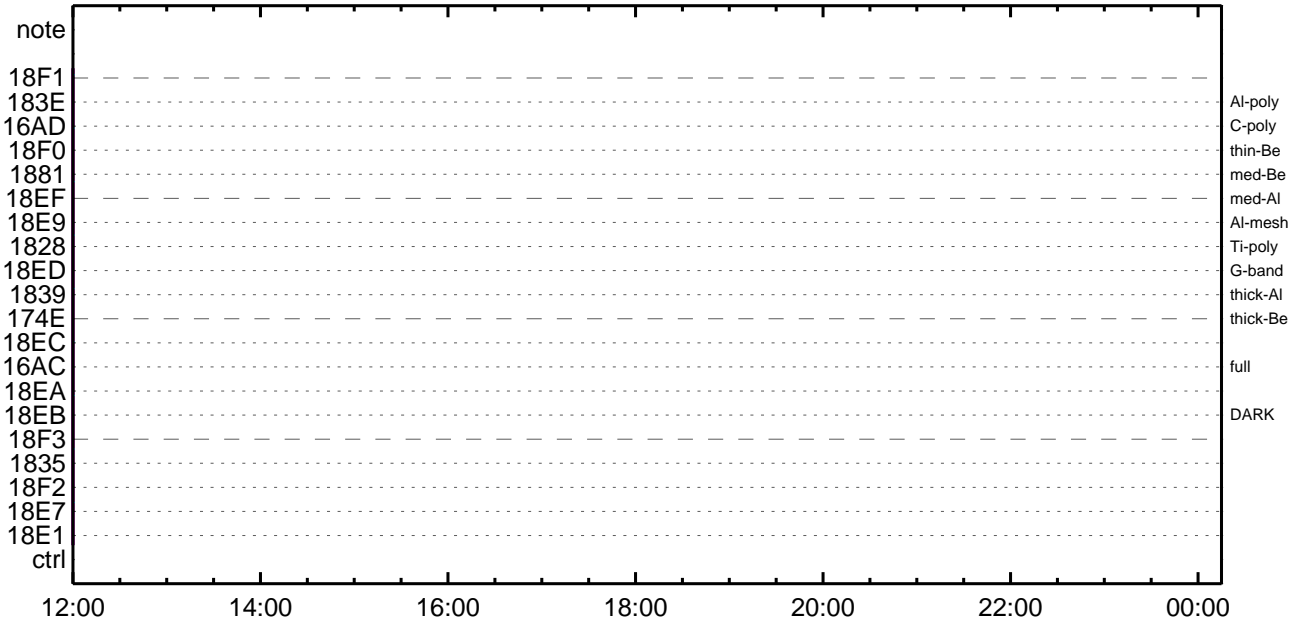
CMDI #0663 2012/06/21



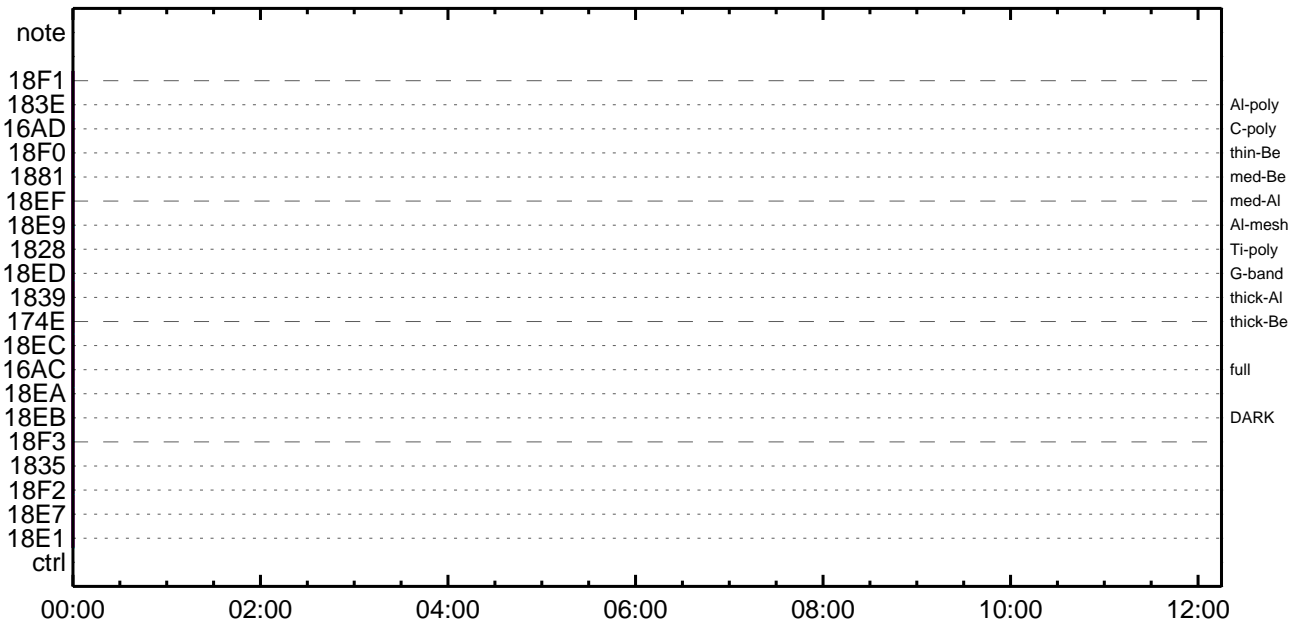
CMDI #0663 2012/06/22



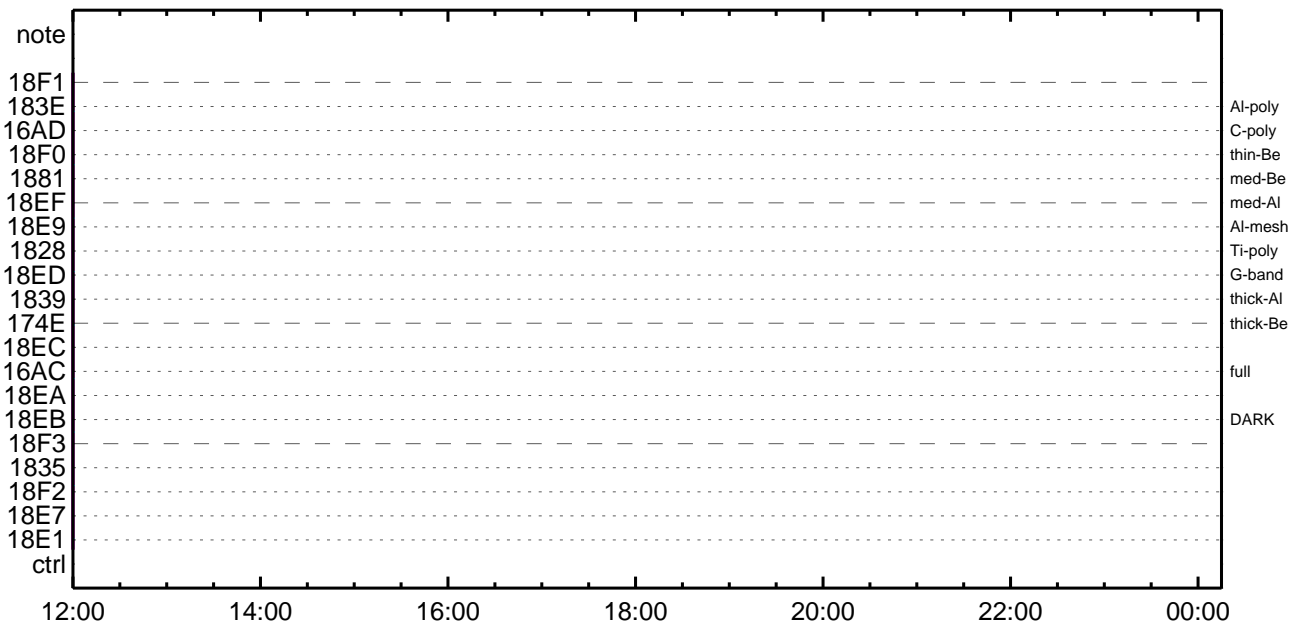
CMDI #0663 2012/06/22



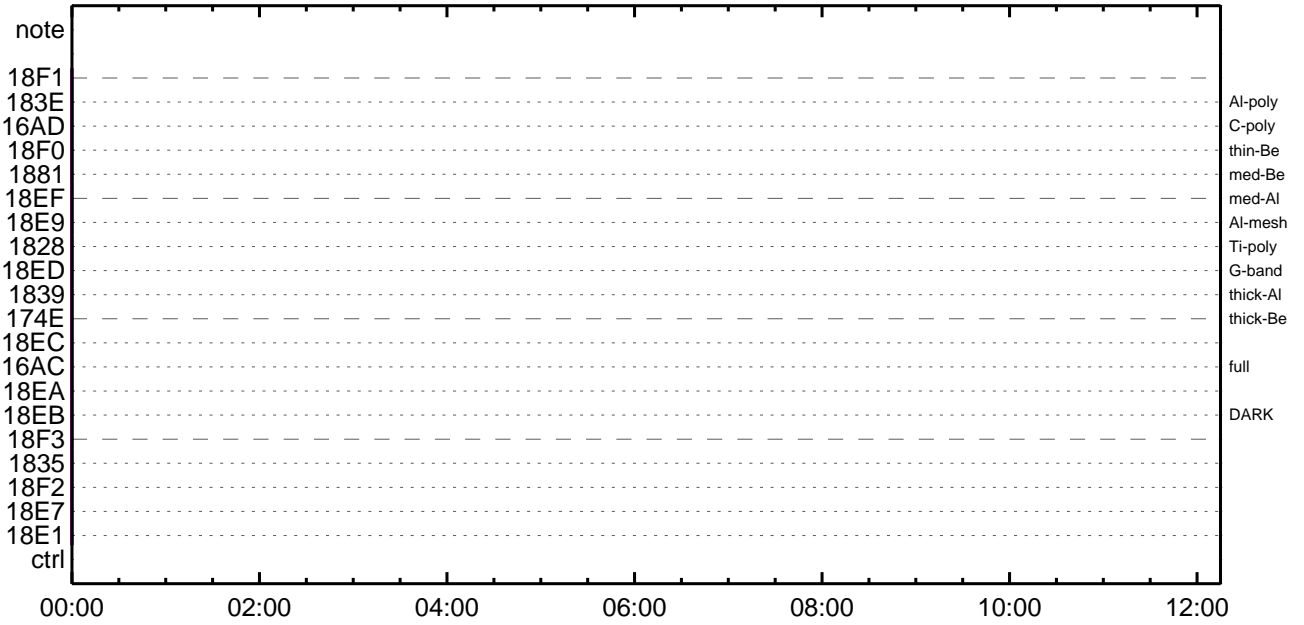
CMDI #0663 2012/06/23



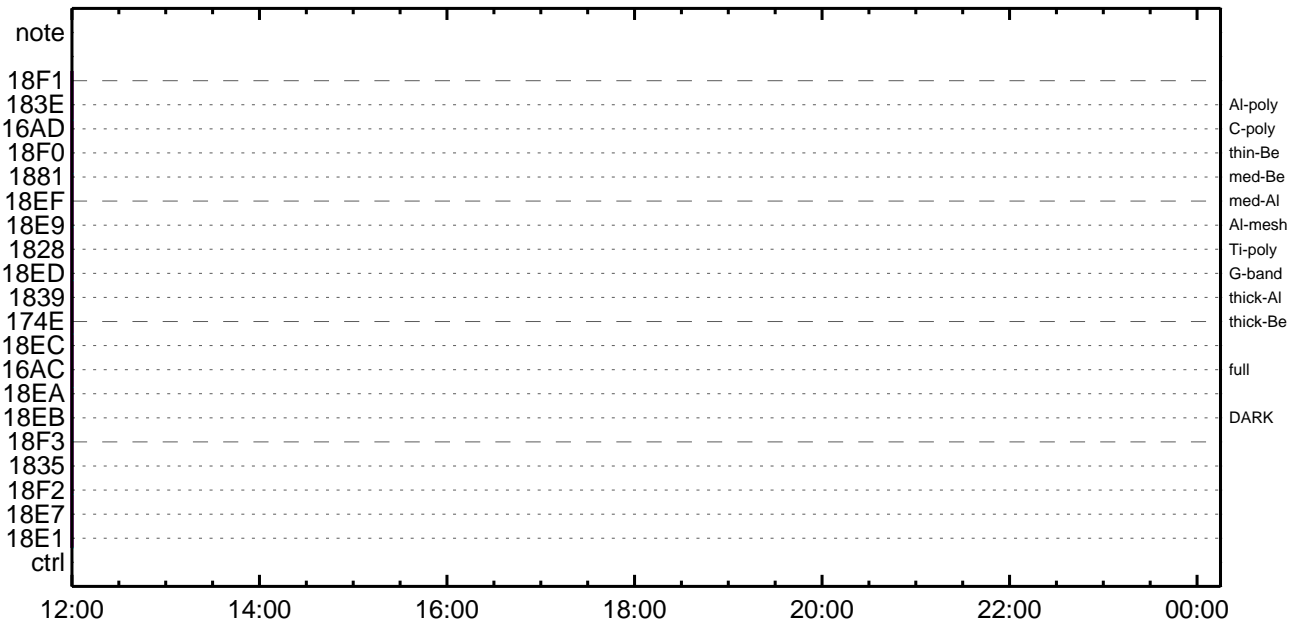
CMDI #0663 2012/06/23



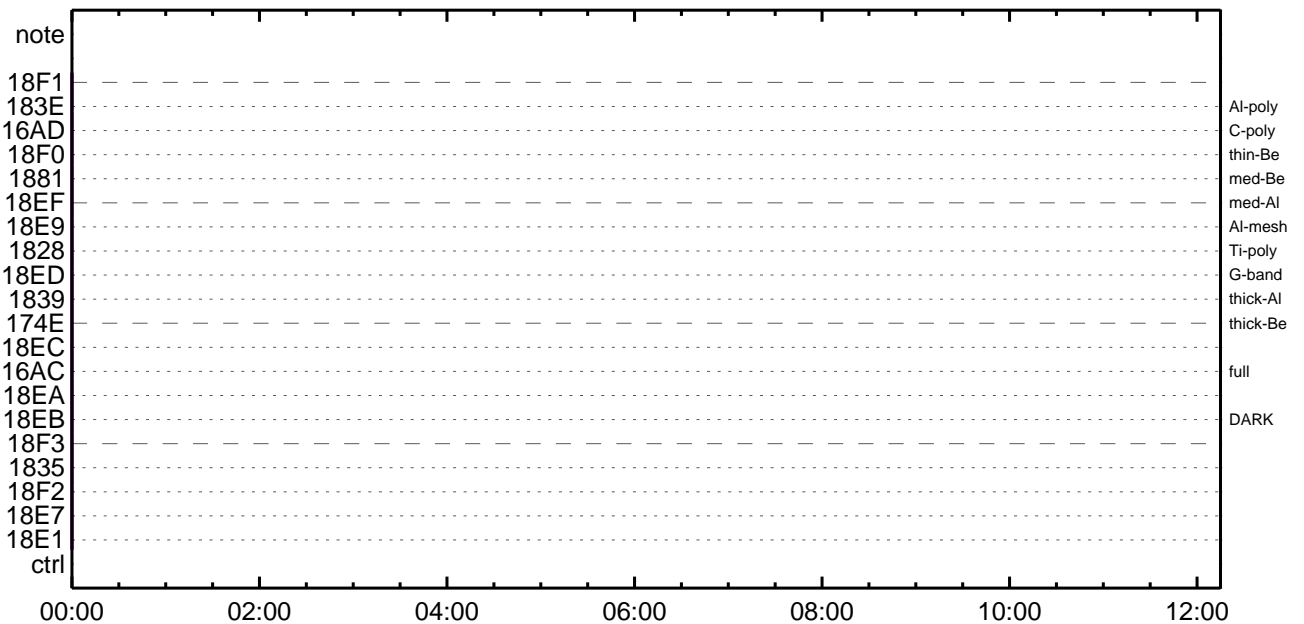
CMDI #0663 2012/06/24



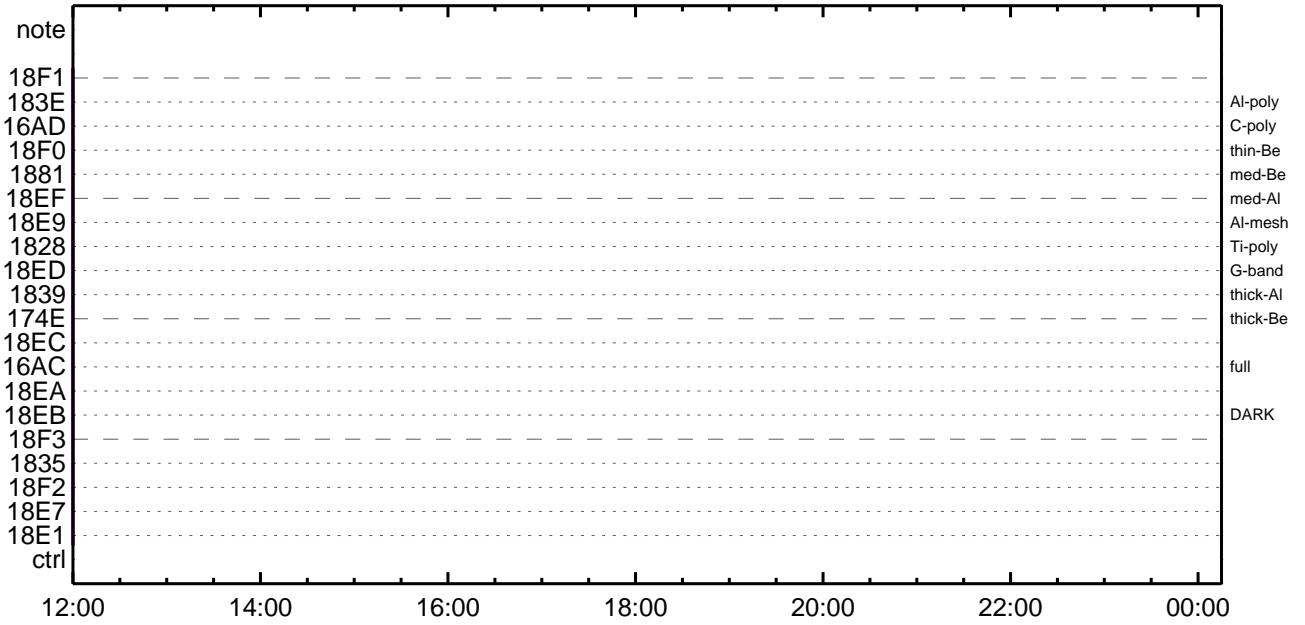
CMDI #0663 2012/06/24



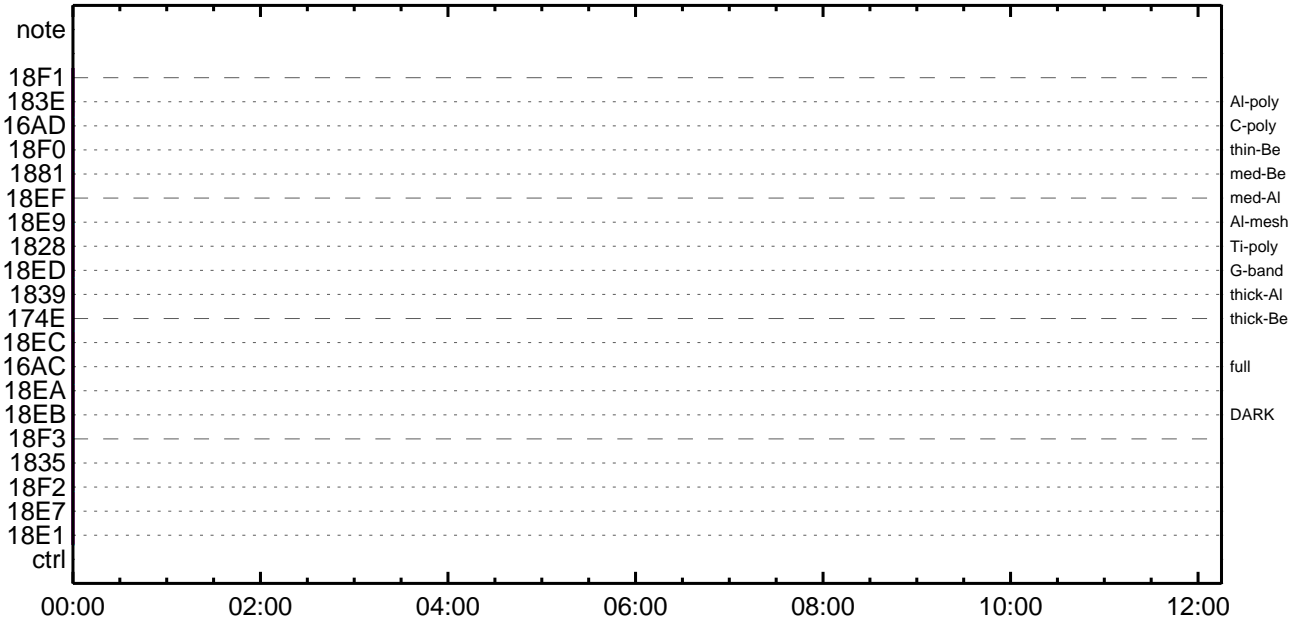
CMDI #0663 2012/06/25



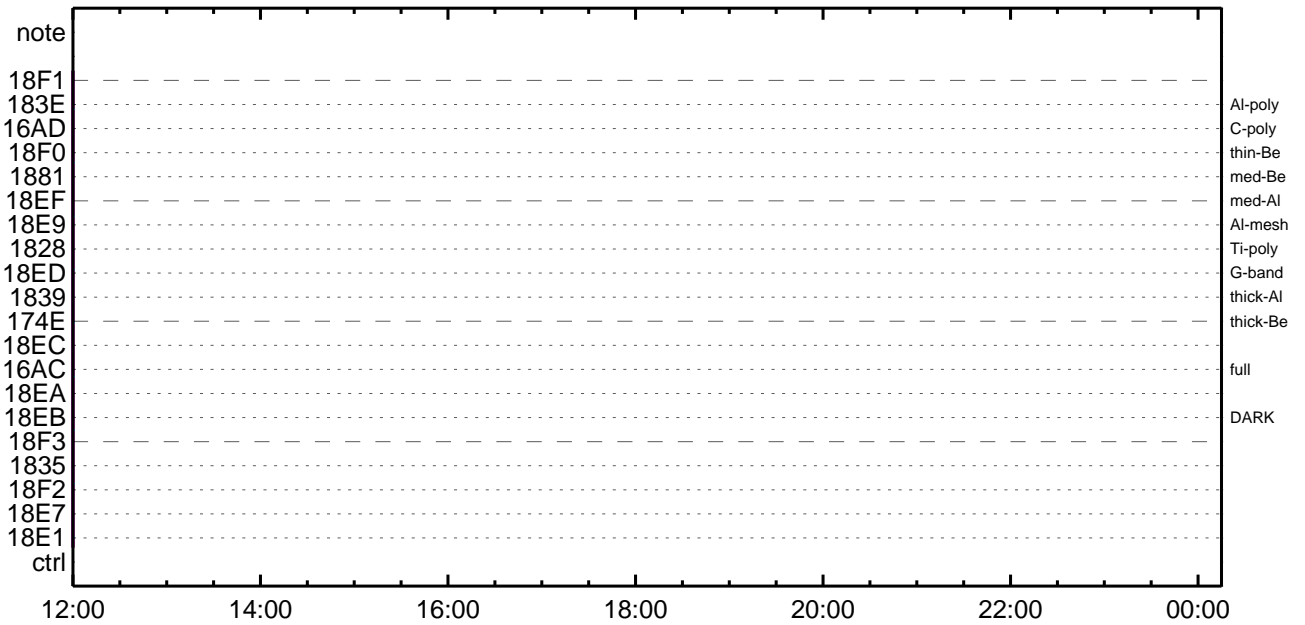
CMDI #0663 2012/06/25



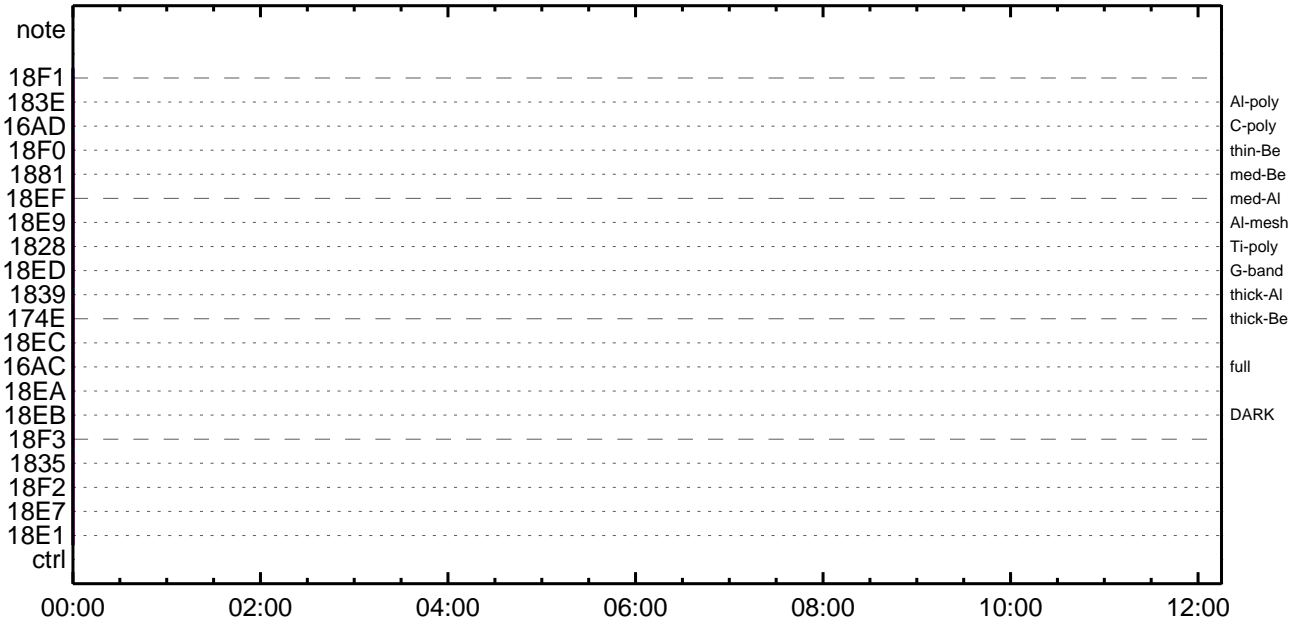
CMDI #0663 2012/06/26



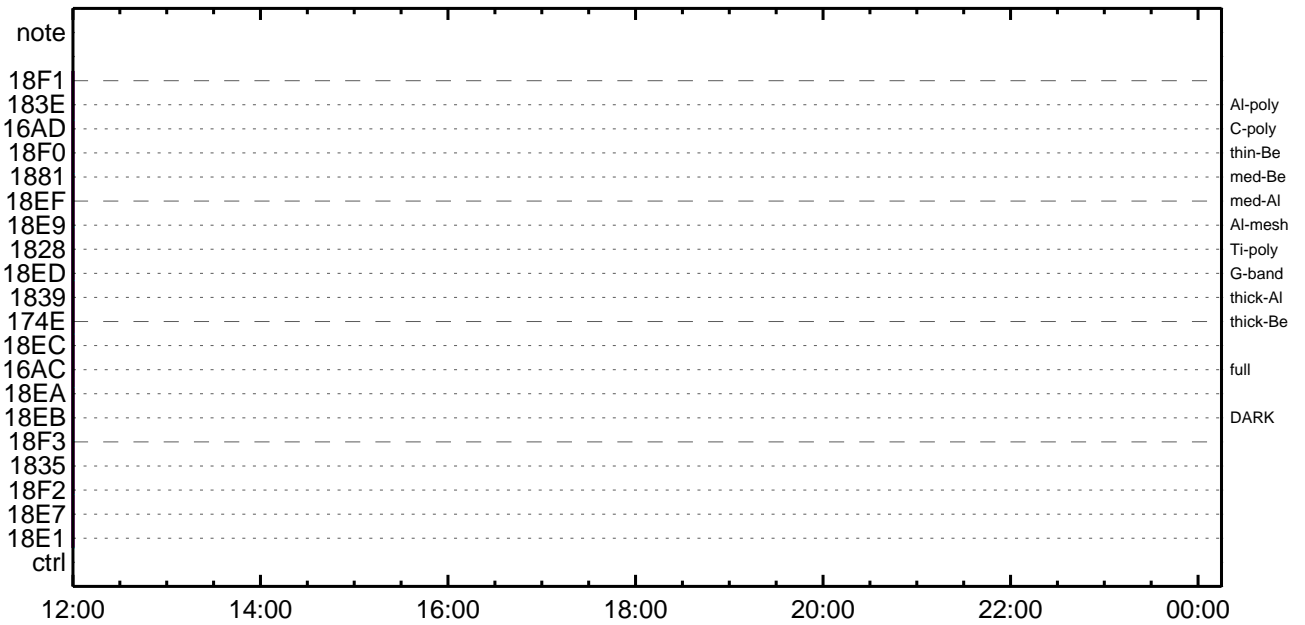
CMDI #0663 2012/06/26



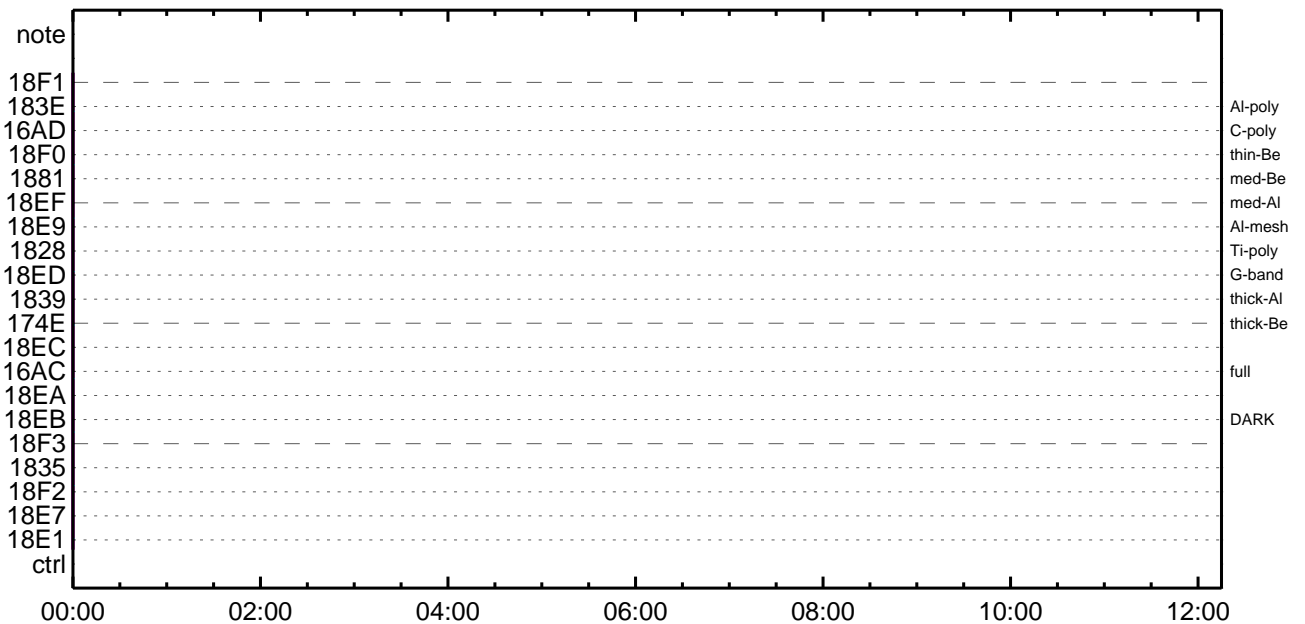
CMDI #0663 2012/06/27



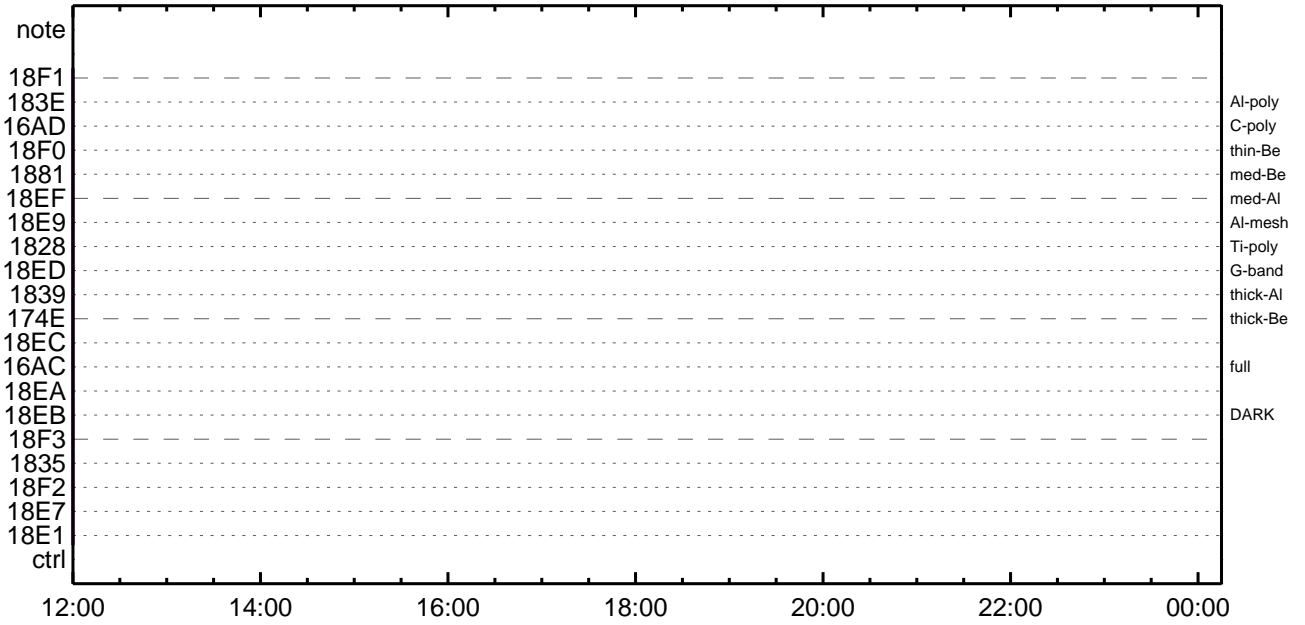
CMDI #0663 2012/06/27



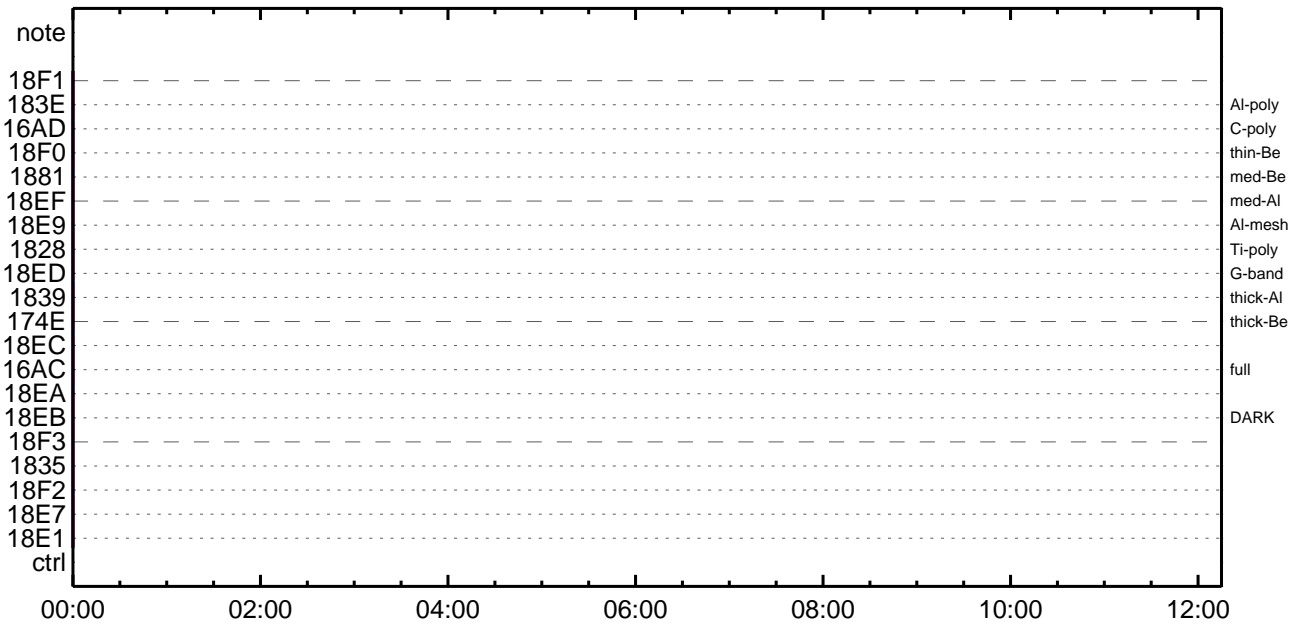
CMDI #0663 2012/06/28



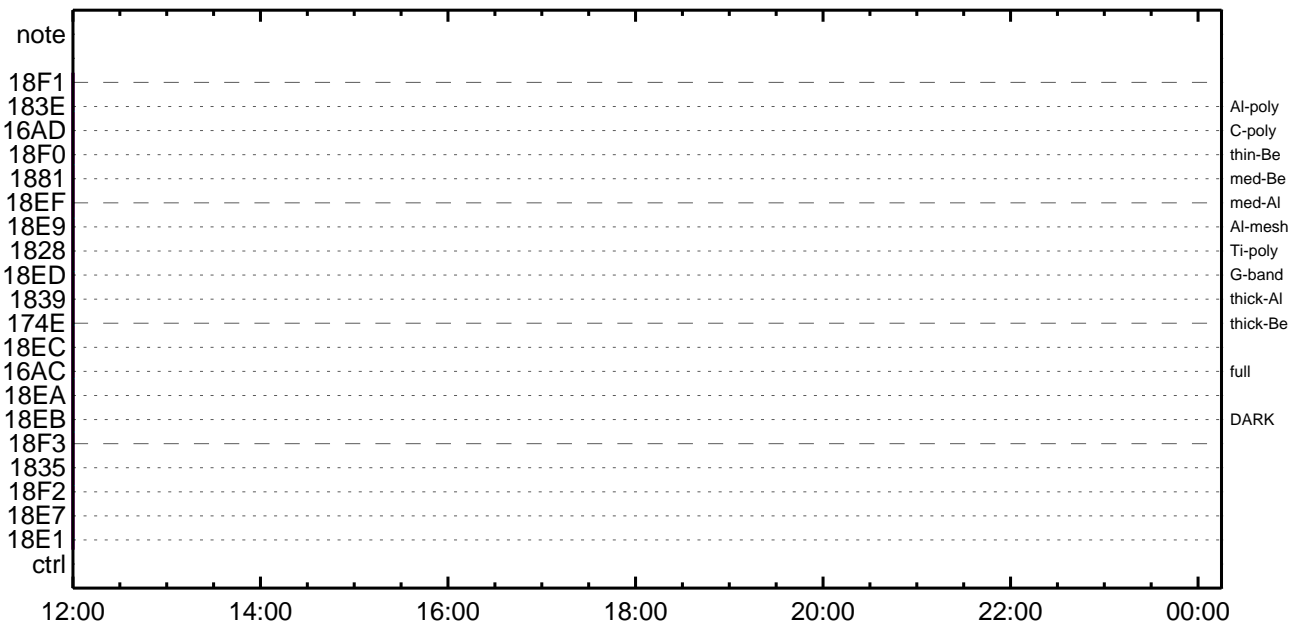
CMDI #0663 2012/06/28



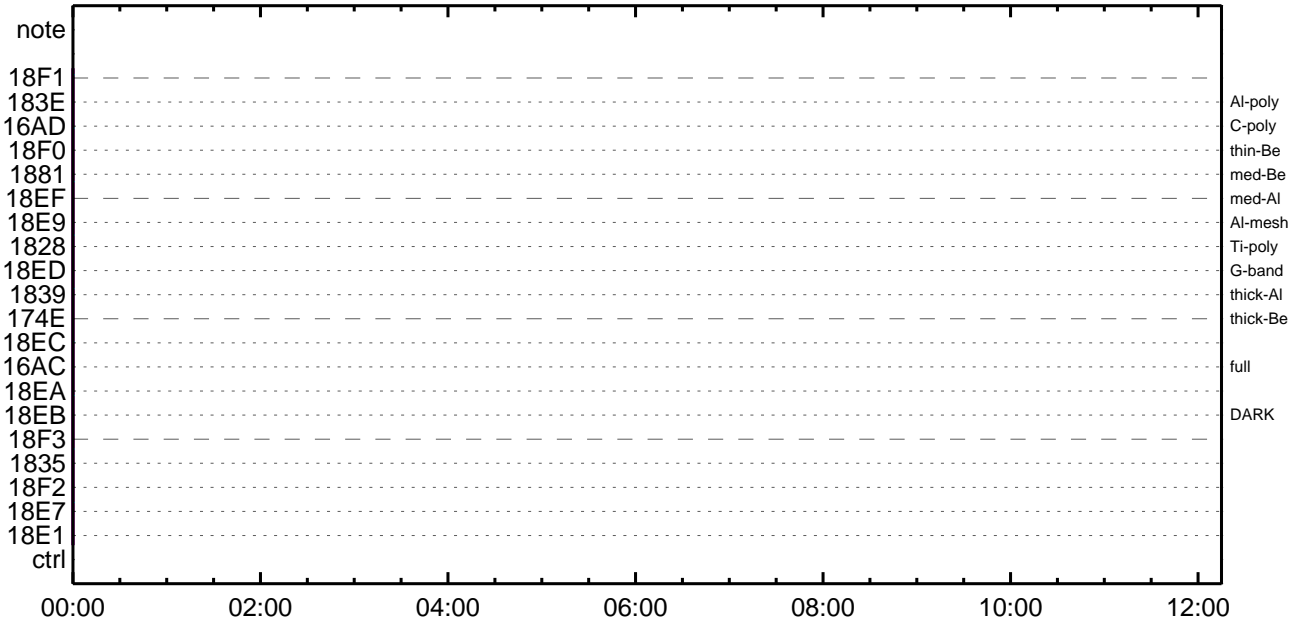
CMDI #0663 2012/06/29



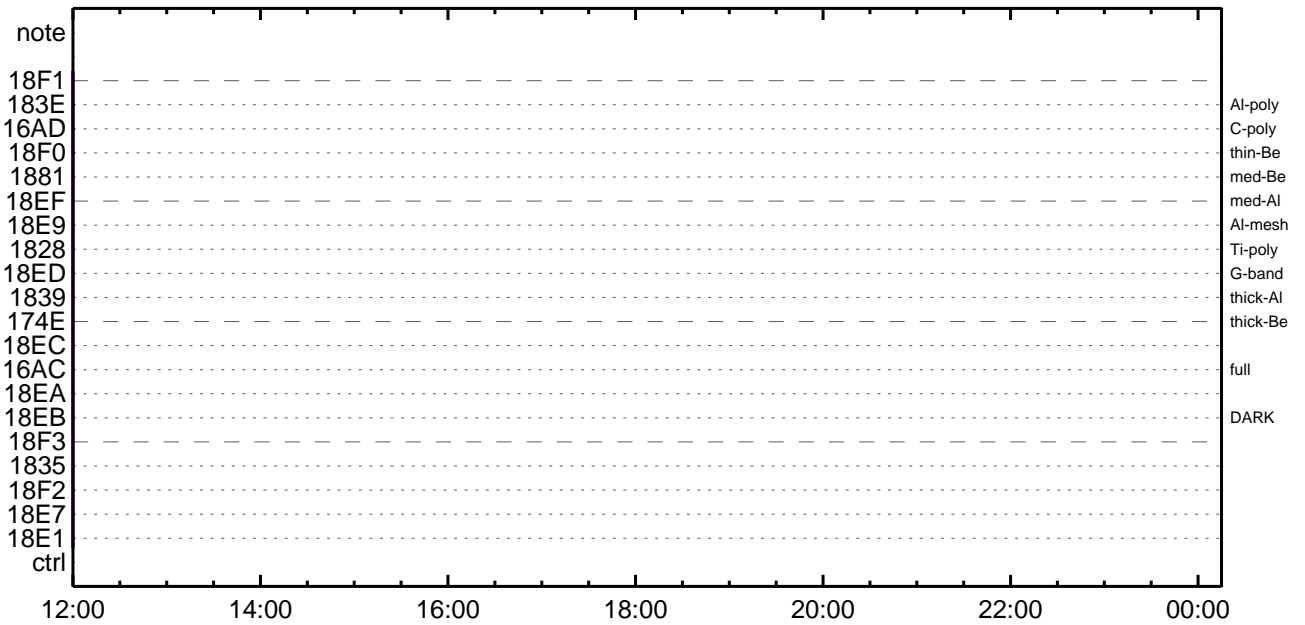
CMDI #0663 2012/06/29



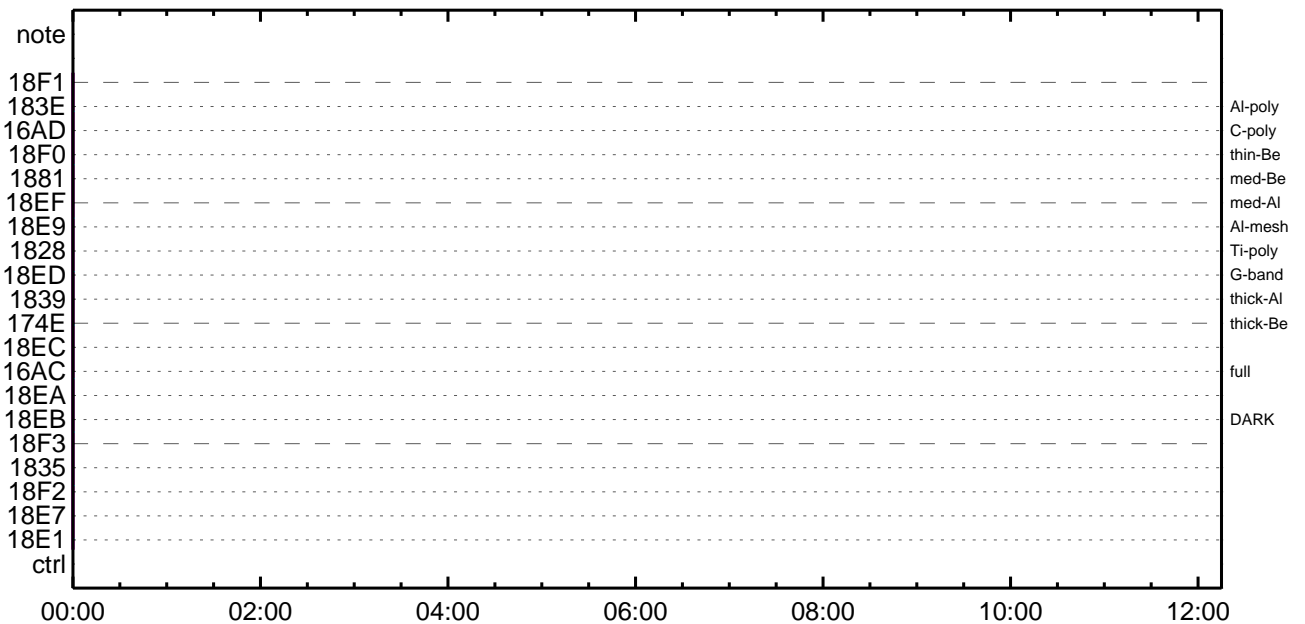
CMDI #0663 2012/06/30



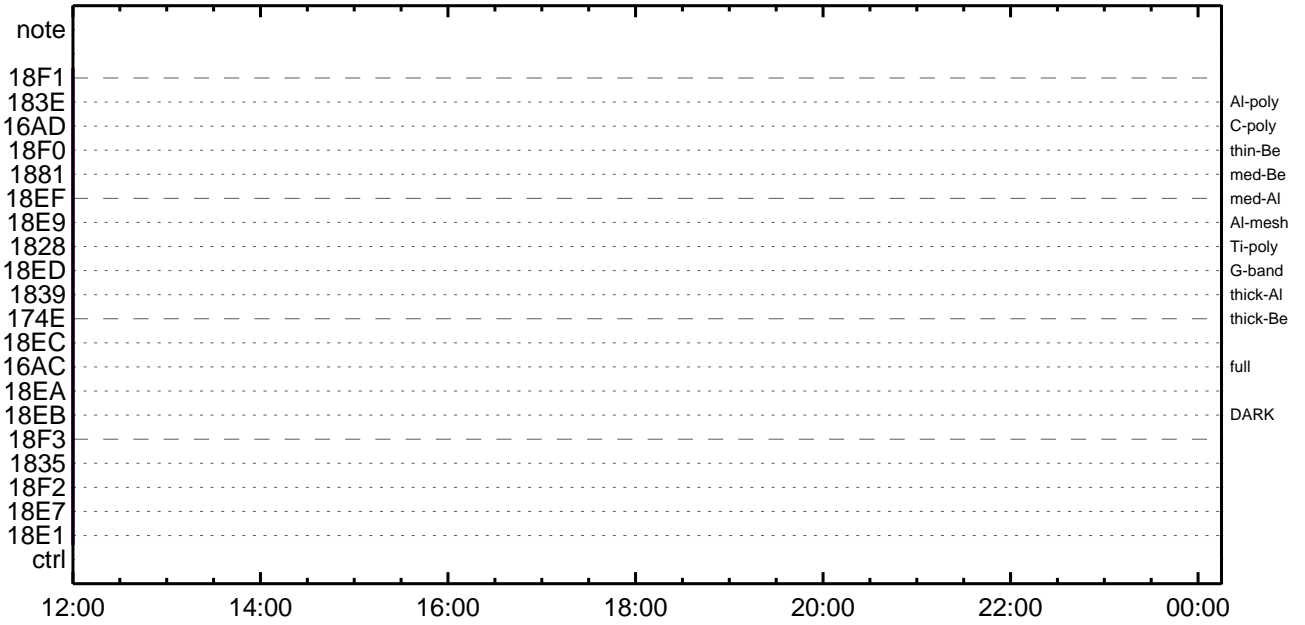
CMDI #0663 2012/06/30



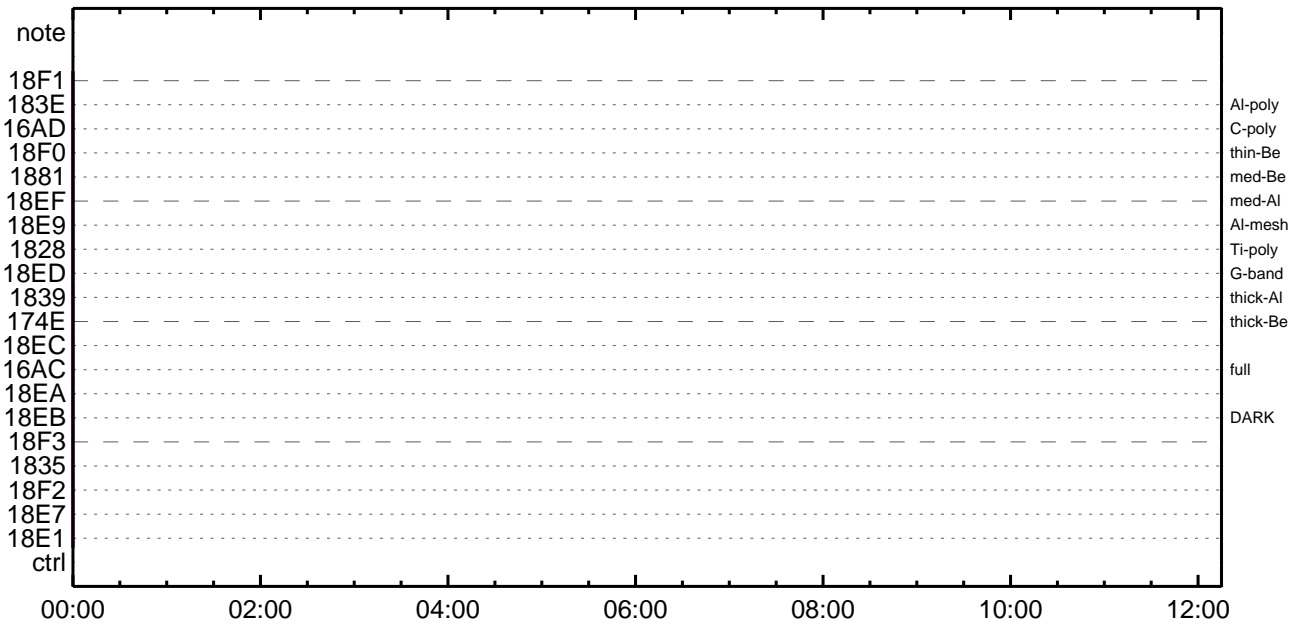
CMDI #0663 2012/07/01



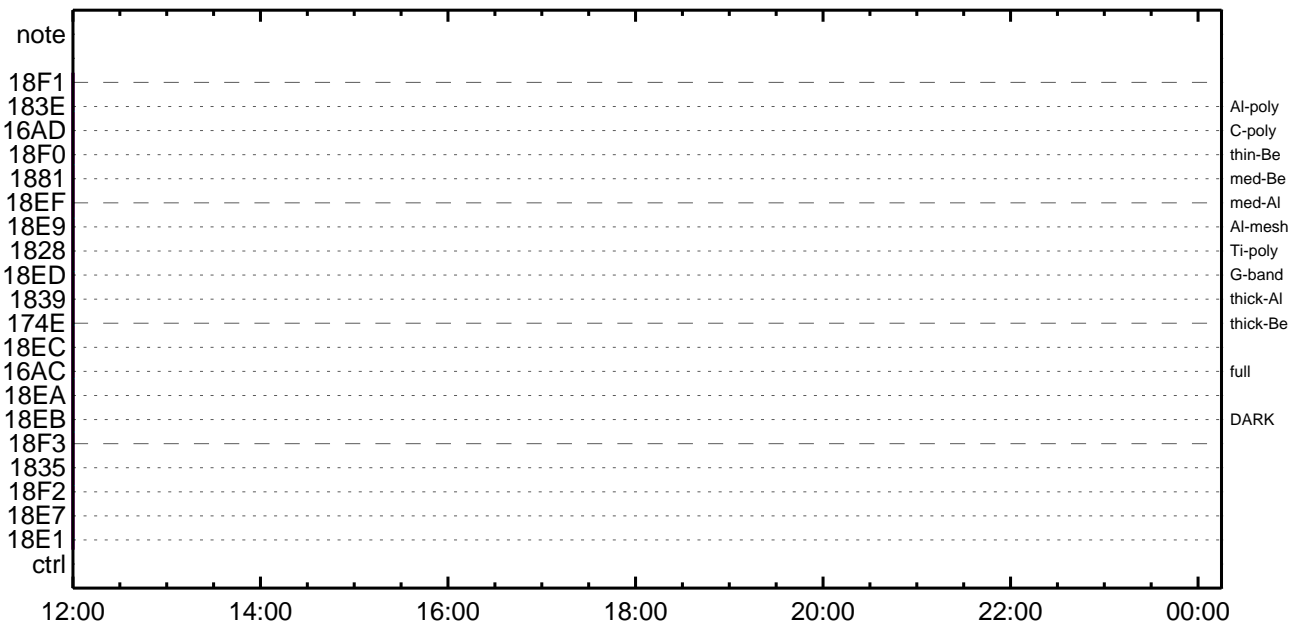
CMDI #0663 2012/07/01



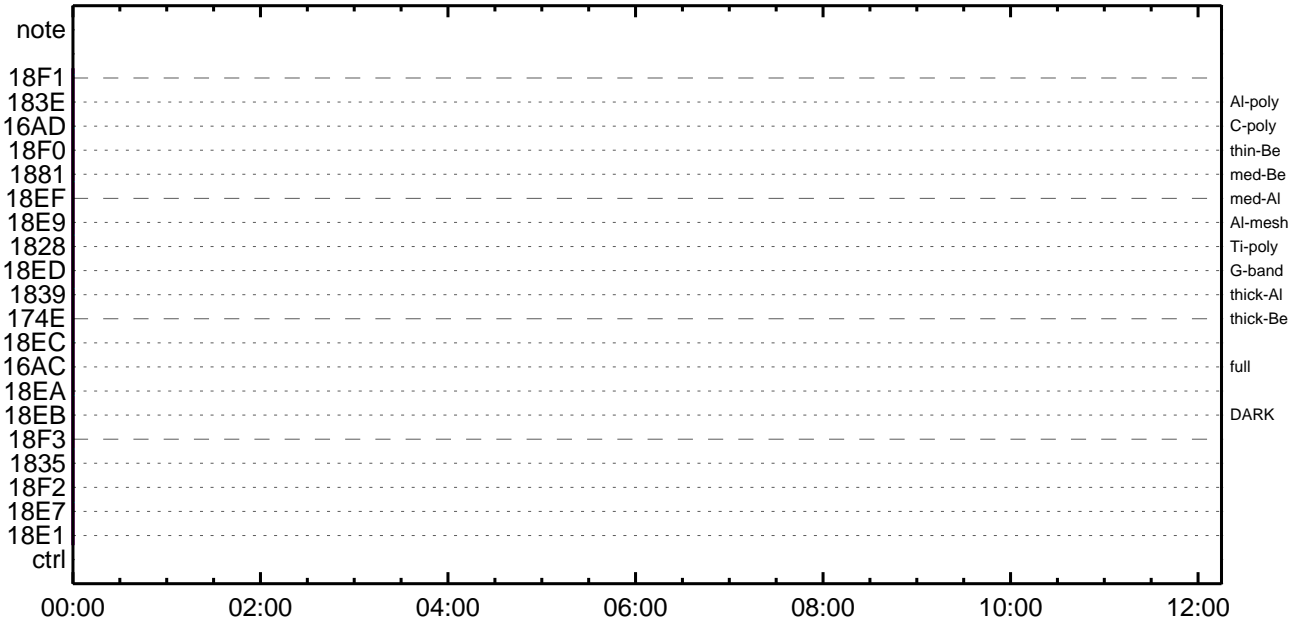
CMDI #0663 2012/07/02



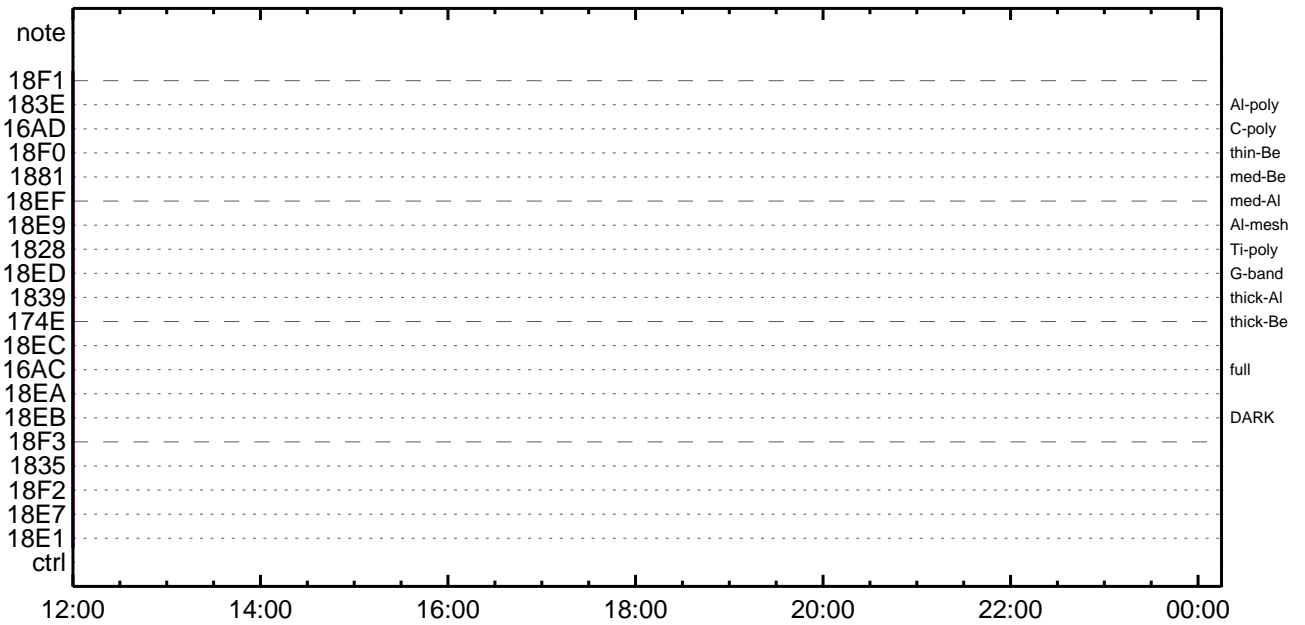
CMDI #0663 2012/07/02



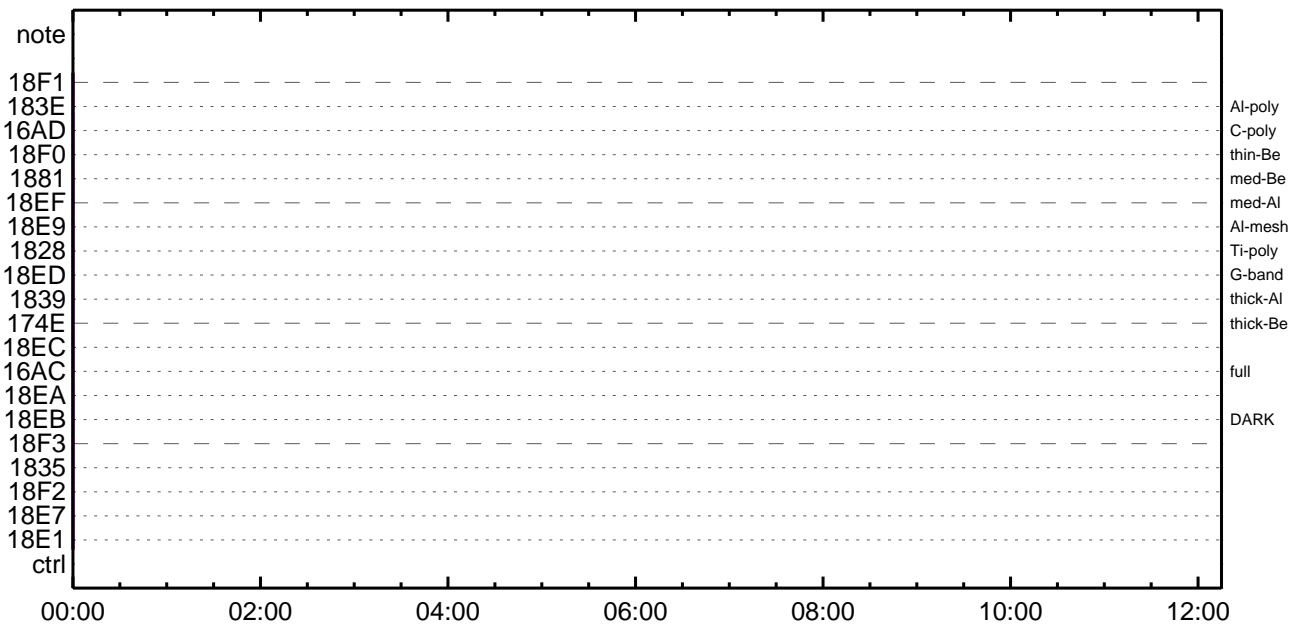
CMDI #0663 2012/07/03



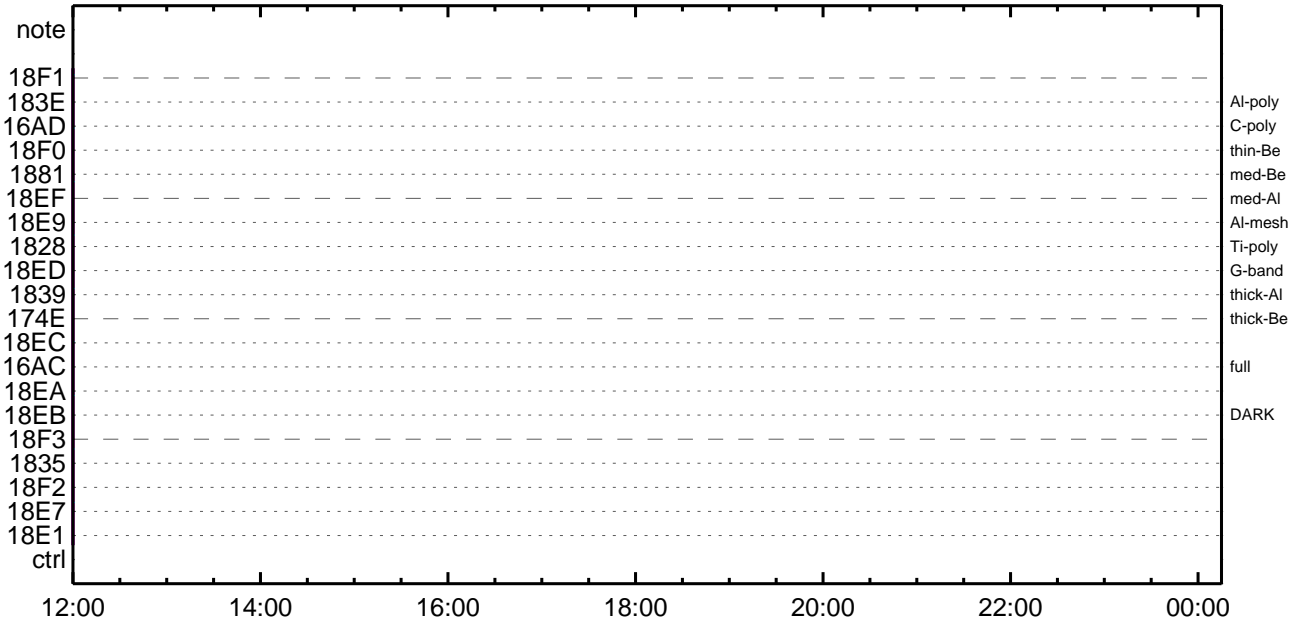
CMDI #0663 2012/07/03



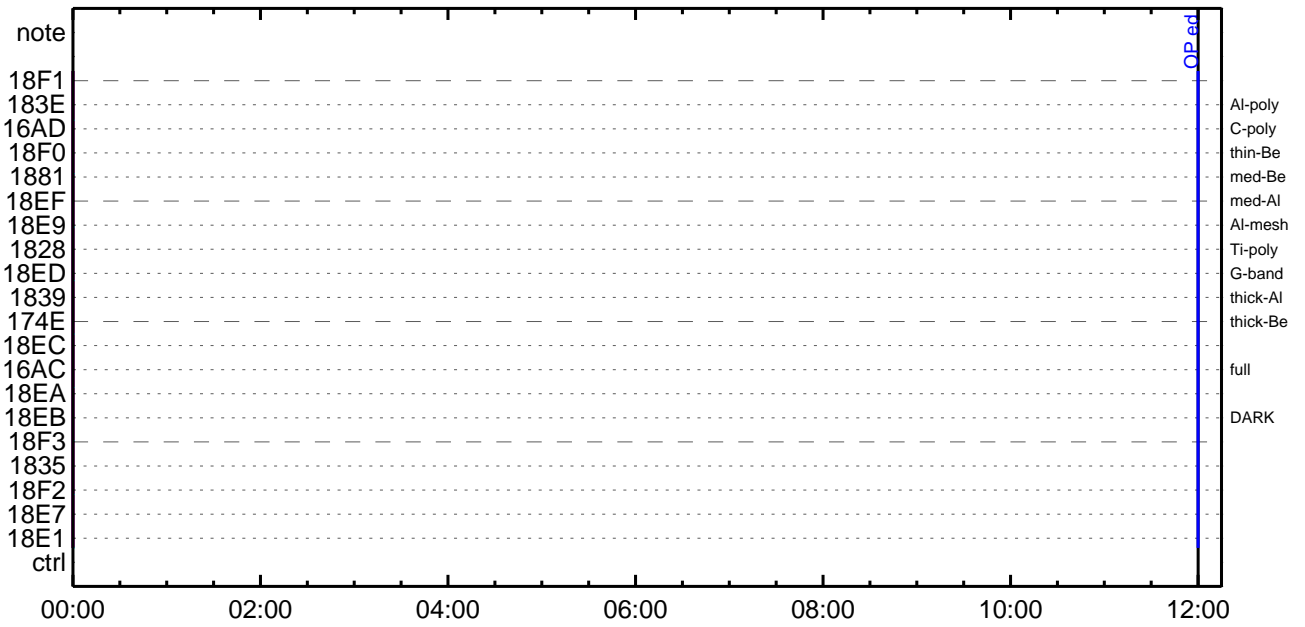
CMDI #0663 2012/07/04



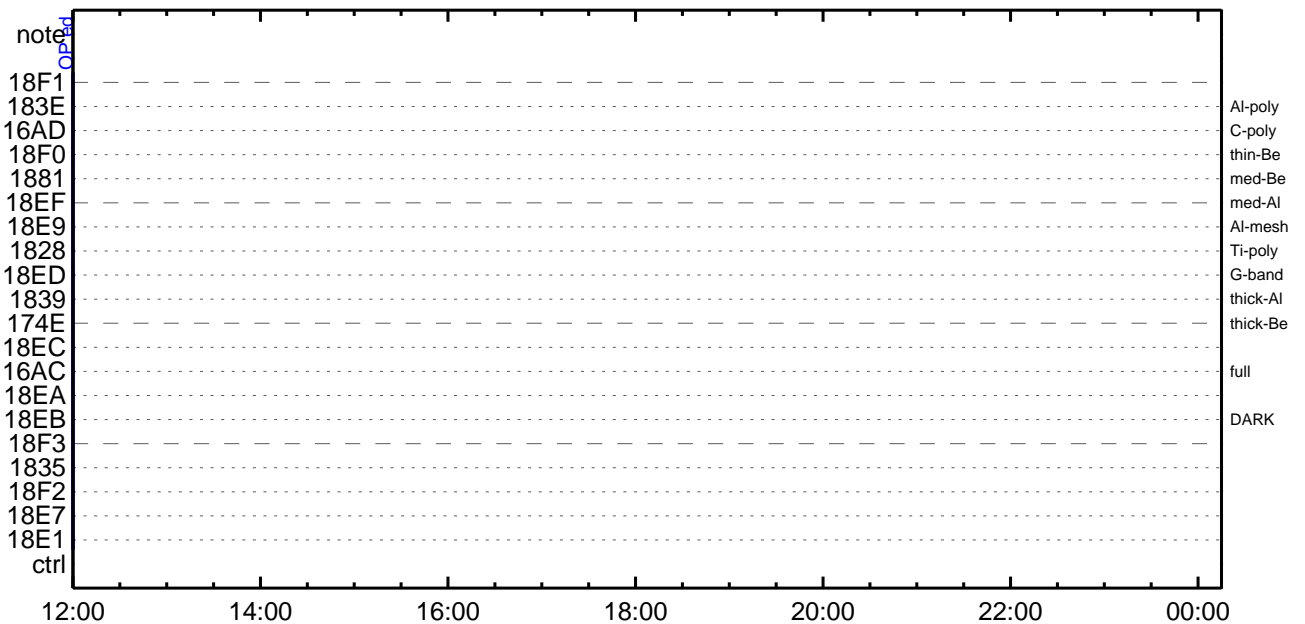
CMDI #0663 2012/07/04



CMDI #0663 2012/07/05



CMDI #0663 2012/07/05



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*** OP Sequence for XRT ***

2012/05/31	11:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/31	11:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/05/31	11:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/05/31	11:12:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/31	11:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/31	11:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/31	11:12:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/31	11:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/31	11:12:56.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/05/31	11:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2012/05/31	11:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/31	12:20:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/31	12:20:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/31	12:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/31	12:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/31	12:49:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/31	12:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/31	13:58:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/31	13:58:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/31	13:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/31	14:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/31	14:35:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/31	14:36:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/31	15:37:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/31	15:37:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/31	15:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/31	15:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/31	16:23:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/31	16:24:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/31	17:15:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/31	17:15:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/31	17:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/31	17:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/31	17:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 cf 34 3f a5				
2012/05/31	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/31	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/31	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/05/31	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/05/31	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/05/31	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/31	18:02:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/05/31	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/31	18:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/31	18:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2012/05/31	18:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/05/31	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/31	18:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				

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2012/05/31	18:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/05/31	18:12:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/05/31	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/31	18:12:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/05/31	18:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2012/05/31	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/31	18:54:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/31	18:54:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/31	18:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/31	18:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/31	19:36:30.0	XRT_Custom_418_OG [0x1a2]			
2012/05/31	19:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/31	20:32:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/31	20:32:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/31	20:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/31	20:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/31	21:14:00.0	XRT_Custom_418_OG [0x1a2]			
2012/05/31	21:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/31	22:11:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/31	22:11:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/31	22:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/31	22:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/31	22:49:30.0	XRT_Custom_418_OG [0x1a2]			
2012/05/31	22:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/31	23:49:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/31	23:49:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/31	23:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/31	23:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/01	00:19:00.0	XRT_Custom_418_OG [0x1a2]			
2012/06/01	00:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/01	01:28:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/01	01:28:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/01	01:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/01	01:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/01	01:57:30.0	XRT_Custom_418_OG [0x1a2]			
2012/06/01	01:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/01	03:04:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/01	03:04:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/01	03:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/01	03:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/01	03:36:00.0	XRT_Custom_418_OG [0x1a2]			
2012/06/01	03:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/01	04:34:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/01	04:34:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/01	04:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/01	04:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/01	05:14:30.0	XRT_Custom_418_OG [0x1a2]			
2012/06/01	05:15:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/01	05:46:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/01	05:46:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00

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2012/06/01	13:27:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/01	13:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
2012/06/01	14:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/01	14:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/01	14:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/01	14:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/01	15:23:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/01	15:24:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
2012/06/01	16:14:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/01	16:14:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/01	16:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/01	16:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/01	17:38:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/01	17:38:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/01	17:38:30.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/06/01	17:38:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00			
2012/06/01	17:38:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/06/01	17:38:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/06/01	17:41:28.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/06/01	17:41:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
2012/06/01	17:48:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/01	17:48:30.0	AOCS_Ore-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/01	17:50:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	02 00 00 00 00			
2012/06/01	17:51:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/06/01	17:51:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/06/01	17:51:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/06/01	17:51:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/06/01	17:51:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/06/01	17:51:26.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/01	17:51:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2012/06/01	17:53:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2012/06/01	17:53:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/01	17:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/01	17:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/01	18:36:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/01	18:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
2012/06/01	19:31:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/01	19:31:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/01	19:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/01	19:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/01	20:14:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/01	20:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
2012/06/01	21:10:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/01	21:10:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/01	21:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/01	21:10:04.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/01	21:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/01	21:50:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/01	21:51:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/01	22:48:30.0	XRT_CTRL_MANU_408_OG [0x198]							

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2012/06/01	22:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/01	22:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/01	22:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/01	23:22:30.5	XRT_Custom_418_OG [0x1a2]						
2012/06/01	23:23:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/02	00:27:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/02	00:27:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/06/02	00:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/02	00:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/02	00:56:30.0	XRT_Custom_418_OG [0x1a2]						
2012/06/02	00:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/02	02:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/02	02:05:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/06/02	02:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/02	02:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/02	02:35:00.0	XRT_Custom_418_OG [0x1a2]						
2012/06/02	02:36:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/02	03:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/02	03:35:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/06/02	03:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/02	03:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/02	04:13:30.0	XRT_Custom_418_OG [0x1a2]						
2012/06/02	04:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/02	05:13:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/02	05:13:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/06/02	05:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/02	05:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/02	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/06/02	05:59:56.0	XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/06/02	06:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
			AOCU_NM	5	02-76	02 00 00 00 00		
2012/06/02	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/06/02	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/06/02	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2012/06/02	06:02:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/02	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/02	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			AOCU_NM	5	02-76	02 00 00 00 00		
2012/06/02	06:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	02 00 00 00 00		
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/06/02	06:12:26.0	XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/06/02	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/06/02	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/06/02	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/06/02	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/02	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2012/06/02	06:12:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2012/06/02	06:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/02	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/02	06:53:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/02	06:53:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		

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2012/06/02	06:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/02	06:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/02	07:30:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/02	07:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/02	08:33:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/02	08:33:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/02	08:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/02	08:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/02	10:47:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				