

# XRT Timeline to be uploaded on 2012/06/02

Period: 2012/06/02 10:37:00 - 2012/07/07 12:00:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

<b>XOB #1835: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 40sec-cad</b>														
Term	Pointing (x, y)		Comment											
06/02 10:50:00 - 06/02 17:53:54	Track (	-693.4, 182.4)	<small>06/02 10:47:00</small>	# OP start + 10min, track AR 11493 in NE.										
06/02 18:07:00 - 06/03 06:34:54	Track (	-648.1, 182.3)	<small>06/02 18:04:00</small>	# Cont.										
06/05 06:24:30 - 06/05 10:29:00	Track (	-173.9, 179.5)	<small>06/05 06:21:30</small>	# Cont.										
<b>PROG= 04 Inf.-time(s)</b>														
└─ Subr= 2 1-time(s) 2.0sec														
└─ Seqn= 19 1-time(s) 2.0sec														
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
└─ Seqn= 41 4-time(s) 2.0sec														
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
└─ Subr= 1 1-time(s) 2.0sec														
└─ Seqn= 52 45-time(s) 24.0sec														
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval		

<b>XOB #18EA: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-band(16)</b>														
Term	Pointing (x, y)		Comment											
06/02 17:57:00 - 06/02 18:03:54	Fixed (	0.0, 0.0)		synoptic, shifted -6.0 min										
06/03 06:38:00 - 06/03 06:44:54	Fixed (	0.0, 0.0)		synoptic, shifted 35.0 min										
06/03 18:16:30 - 06/03 18:23:24	Fixed (	0.0, 0.0)		synoptic, shifted 13.5 min										
06/04 05:58:00 - 06/04 06:04:54	Fixed (	0.0, 0.0)		synoptic, shifted manually, and extended for SOT flat fields.										
06/04 17:55:00 - 06/04 18:01:54	Fixed (	0.0, 0.0)		synoptic, shifted -8.0 min										
06/05 06:14:30 - 06/05 06:21:24	Fixed (	0.0, 0.0)		synoptic, shifted 11.5 min										
<b>PROG= 07 1-time(s)</b>														
└─ Subr= 1 1-time(s) 12.0sec														
└─ Seqn= 46 1-time(s) 4.0sec														
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Seqn= 5 1-time(s) 2.0sec														
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 69 1-time(s) 4.0sec														
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Seqn= 4 1-time(s) 2.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval		

<b>XOB #183E: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad</b>														
Term	Pointing (x, y)		Comment											
06/03 06:48:00 - 06/03 17:29:00	Track (	-561.7, 182.1)	<small>06/03 06:45:00</small>	# Cont.										
06/03 18:26:30 - 06/04 05:54:54	Track (	-474.9, 181.7)	<small>06/03 18:23:30</small>	# Cont.										
06/04 10:25:00 - 06/04 17:51:54	Track (	-347.9, 180.9)	<small>06/04 10:10:00</small>	# Track AR 11493.										
06/04 21:05:00 - 06/05 06:11:24	Track (	-255.5, 180.2)	<small>06/04 21:02:00</small>	* Track AR 11493.										
<b>PROG= 19 Inf.-time(s)</b>														
└─ Subr= 2 1-time(s) 2.0sec														
└─ Seqn= 19 1-time(s) 2.0sec														
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
└─ Seqn= 41 4-time(s) 2.0sec														
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
└─ Subr= 1 1-time(s) 2.0sec														
└─ Seqn= 16 30-time(s) 80.0sec														
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	20.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	20.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	20.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval		

<b>XOB #1892: CME watch - 4x4 - AEC 2 - Ti/poly AI/Mesh</b>												
Term	Pointing (x, y)							Comment				
06/04 06:08:00 - 06/04 09:49:30	Fixed ( 0.0, 0.0)							synoptic, shifted manually, and extended for SOT flat fields.				
06/04 18:05:00 - 06/04 21:01:54	Fixed ( -790.0, 461.0)							# Observe small region on E limb - EIS request.				
<b>PROG= 14 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 120.0sec												
└─ Seqn= 84 1-time(s) 4.0sec												
Open/Ti-poly	Open/thick-AI	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Open/AI-mesh	Open/thick-AI	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \* **Flare mode** \* \* \* \* \*

<b>XOB #18E7: Flare obs. dynamics - Ti_poly high cadence + context (thick-AI-384x384)-15 loops</b>												
Term	Pointing (x, y)							Comment				
06/02 10:50:00 - 06/02 17:53:54	Track ( -693.4, 182.4) @ 06/02 10:47:00							# OP start + 10min, track AR 11493 in NE.				
06/02 18:07:00 - 06/03 06:34:54	Track ( -648.1, 182.3) @ 06/02 18:04:00							# Cont.				
06/03 06:48:00 - 06/03 17:29:00	Track ( -561.7, 182.1) @ 06/03 06:45:00							# Cont.				
06/03 18:26:30 - 06/04 05:54:54	Track ( -474.9, 181.7) @ 06/03 18:23:30							# Cont.				
06/04 06:08:00 - 06/04 09:49:30	Fixed ( 0.0, 0.0)							synoptic, shifted manually, and extended for SOT flat fields.				
06/04 10:25:00 - 06/04 17:51:54	Track ( -347.9, 180.9) @ 06/04 10:10:00							# Track AR 11493.				
06/04 18:05:00 - 06/04 21:01:54	Fixed ( -790.0, 461.0)							# Observe small region on E limb - EIS request.				
06/04 21:05:00 - 06/05 06:11:24	Track ( -255.5, 180.2) @ 06/04 21:02:00							* Track AR 11493.				
06/05 06:24:30 - 06/05 10:29:00	Track ( -173.9, 179.5) @ 06/05 06:21:30							# Cont.				
<b>PROG= 02 15-time(s)</b>												
└─ Subr= 1 45-time(s) 10.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-AI	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
Open/Ti-poly	Open/thick-AI	close	Safe	Norm	4ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
└─ Subr= 2 1-time(s) 10.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
Open/thick-AI	Open/thick-AI	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
└─ Seqn= 79 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

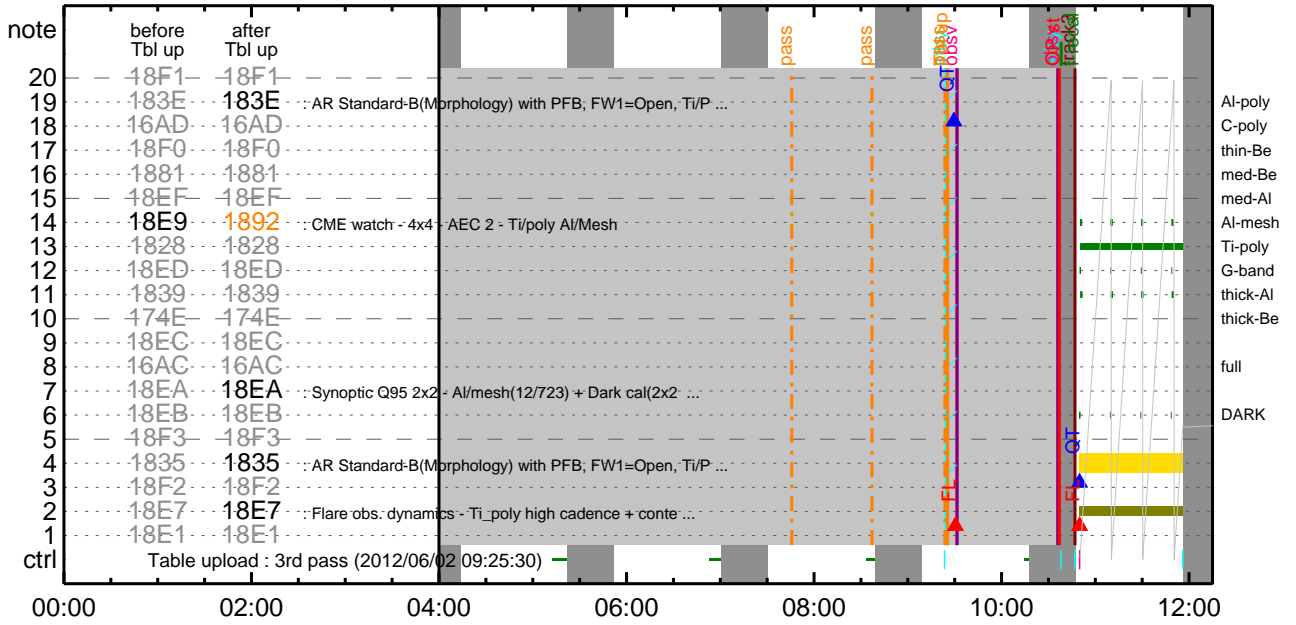
\* \* \* \* \* **Active Region Search** \* \* \* \* \*

NOT USED

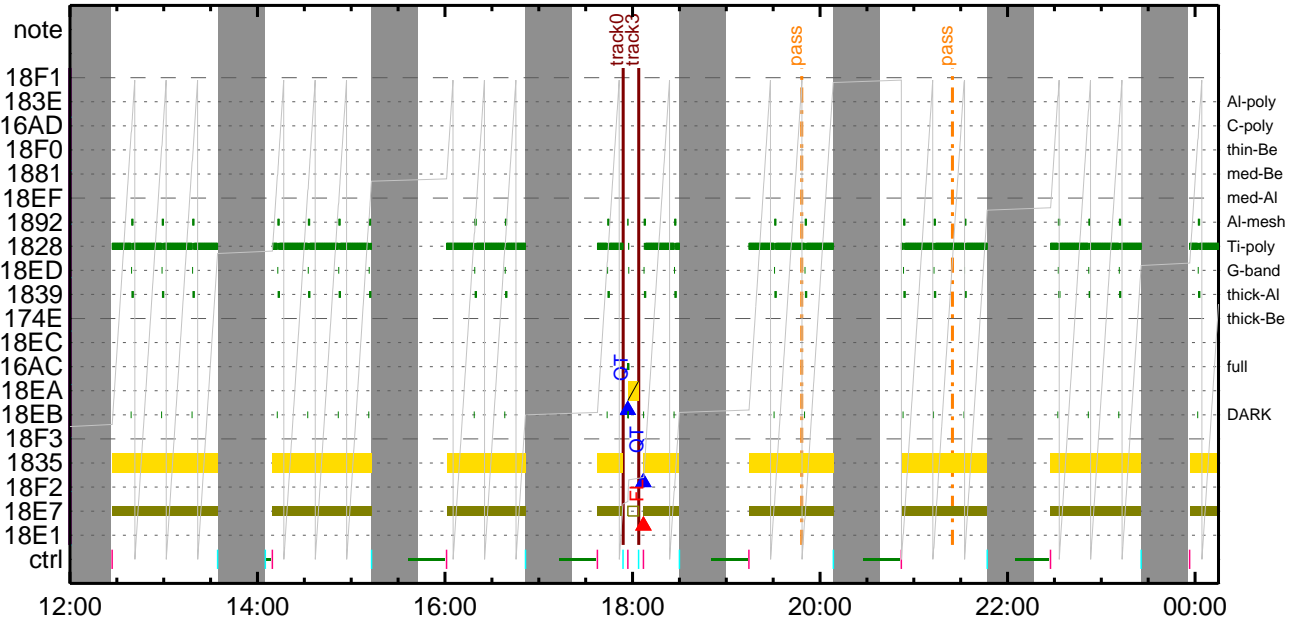
\* \* \* \* \* **Flare Detection** \* \* \* \* \*

<b>FLD Patrol</b>												
Term	Pointing (x, y)							Comment				
06/02 18:06:46 - 06/03 06:35:16	Track ( -648.1, 182.3) @ 06/02 18:04:00							# Cont.				
06/03 06:47:46 - 06/03 18:13:46	Track ( -561.7, 182.1) @ 06/03 06:45:00							# Cont.				
06/03 18:26:16 - 06/04 05:55:16	Track ( -474.9, 181.7) @ 06/03 18:23:30							# Cont.				
06/04 06:07:46 - 06/04 17:52:16	Fixed ( 0.0, 0.0)							synoptic, shifted manually, and extended for SOT flat fields.				
06/04 18:04:46 - 06/05 06:11:46	Fixed ( -790.0, 461.0)							# Observe small region on E limb - EIS request.				
06/05 06:24:16 - 07/07 12:00:00	Track ( -173.9, 179.5) @ 06/05 06:21:30							# Cont.				
Open/Ti-poly	Open/thick-AI	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

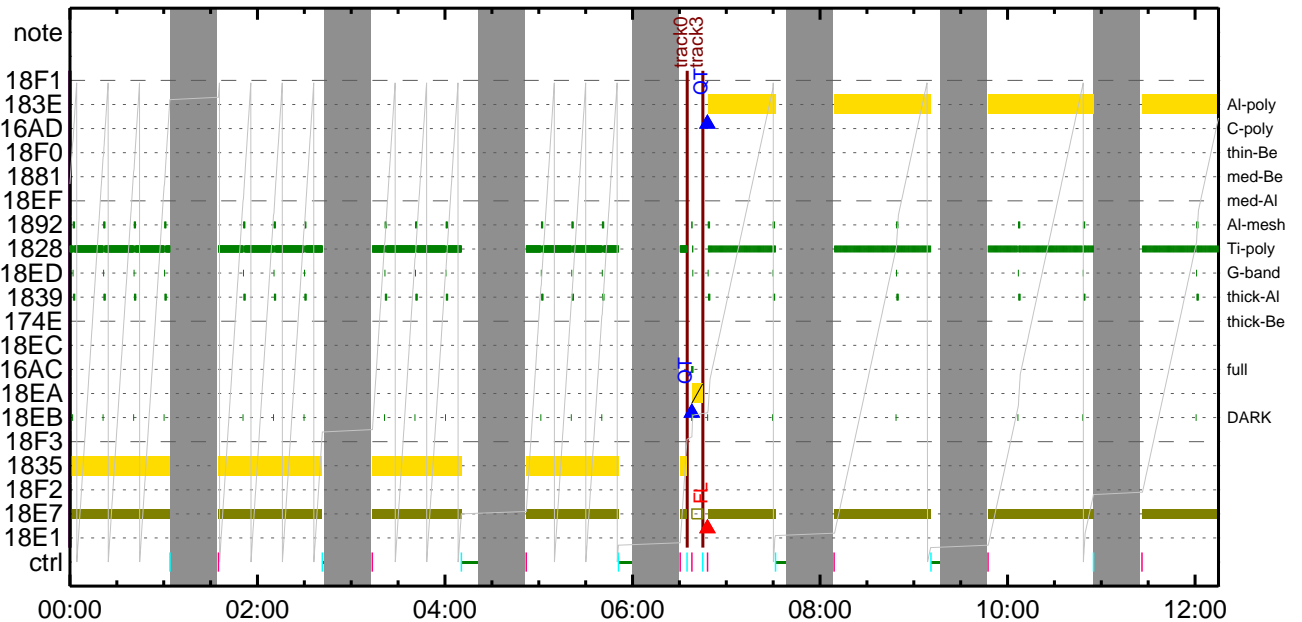
### CMDI #0668 2012/06/02



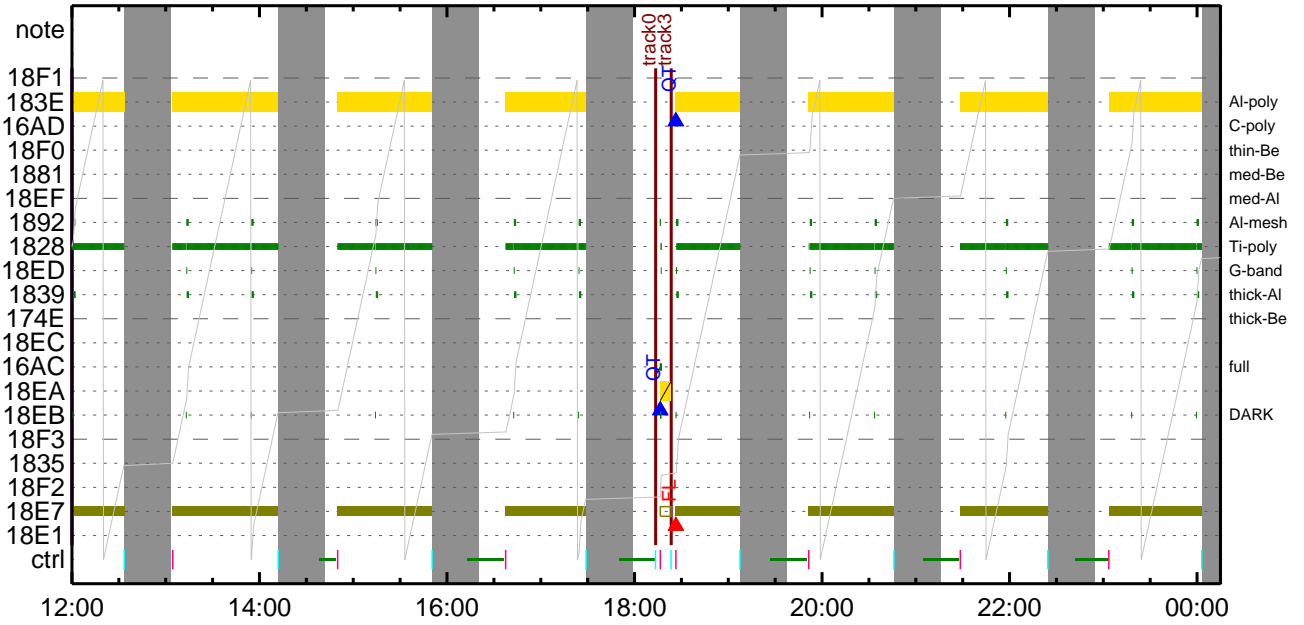
### CMDI #0668 2012/06/02



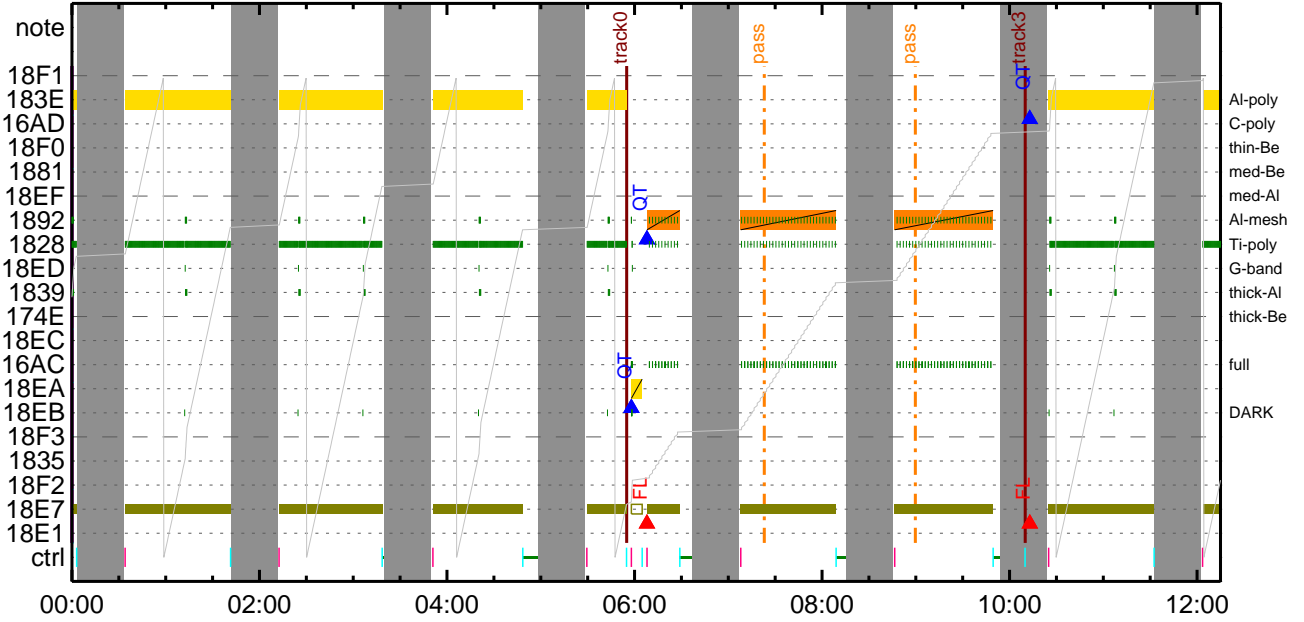
### CMDI #0668 2012/06/03



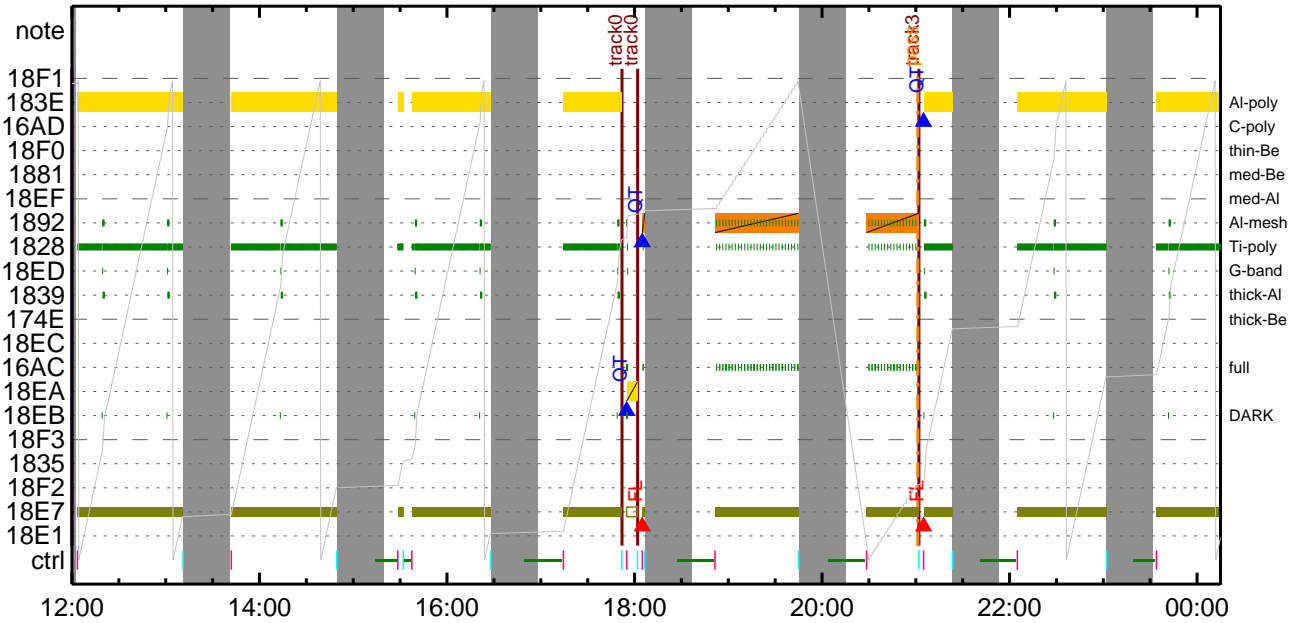
CMDI #0668 2012/06/03



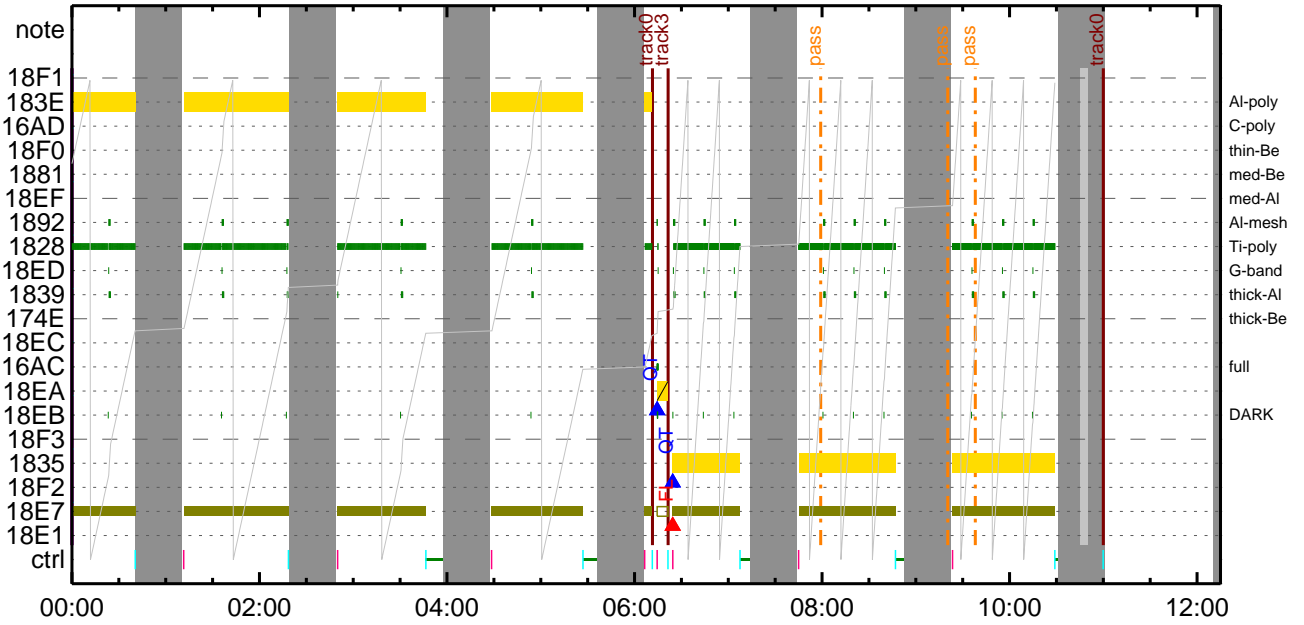
CMDI #0668 2012/06/04



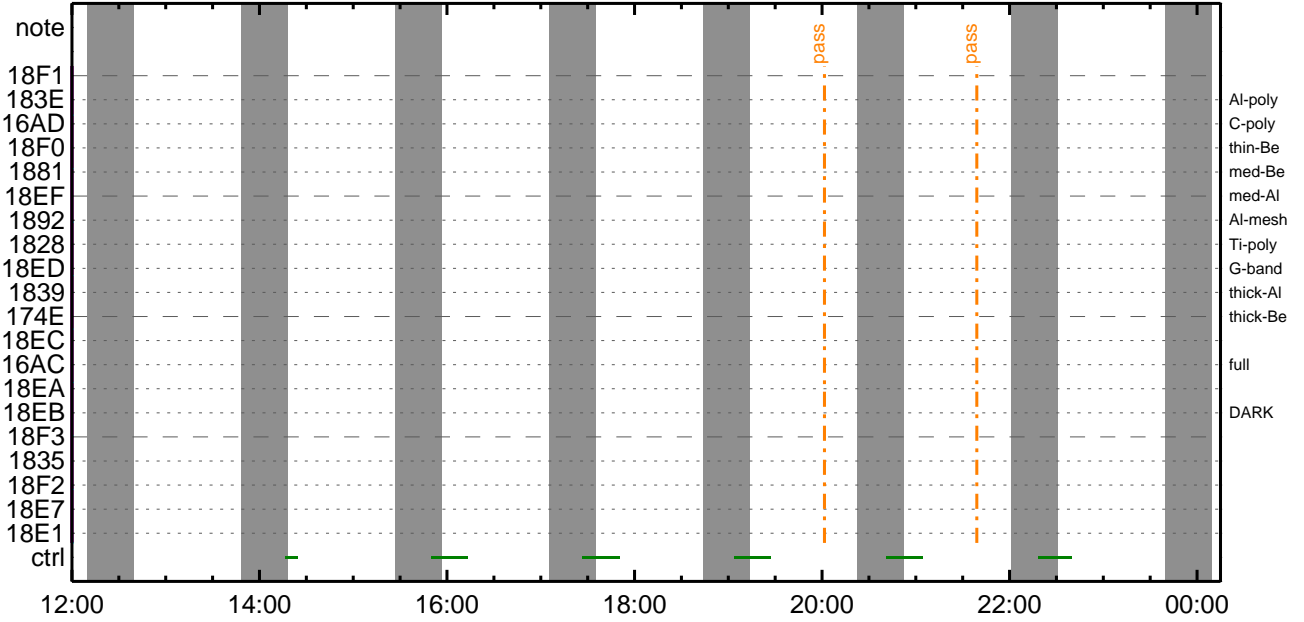
CMDI #0668 2012/06/04



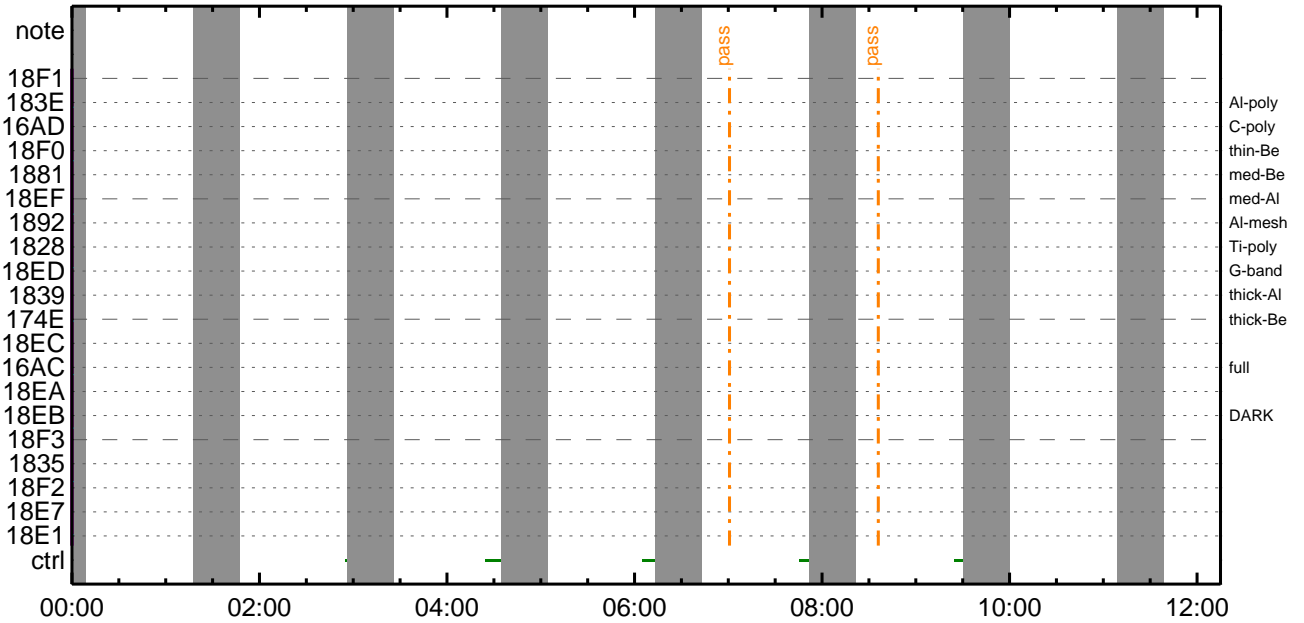
CMDI #0668 2012/06/05



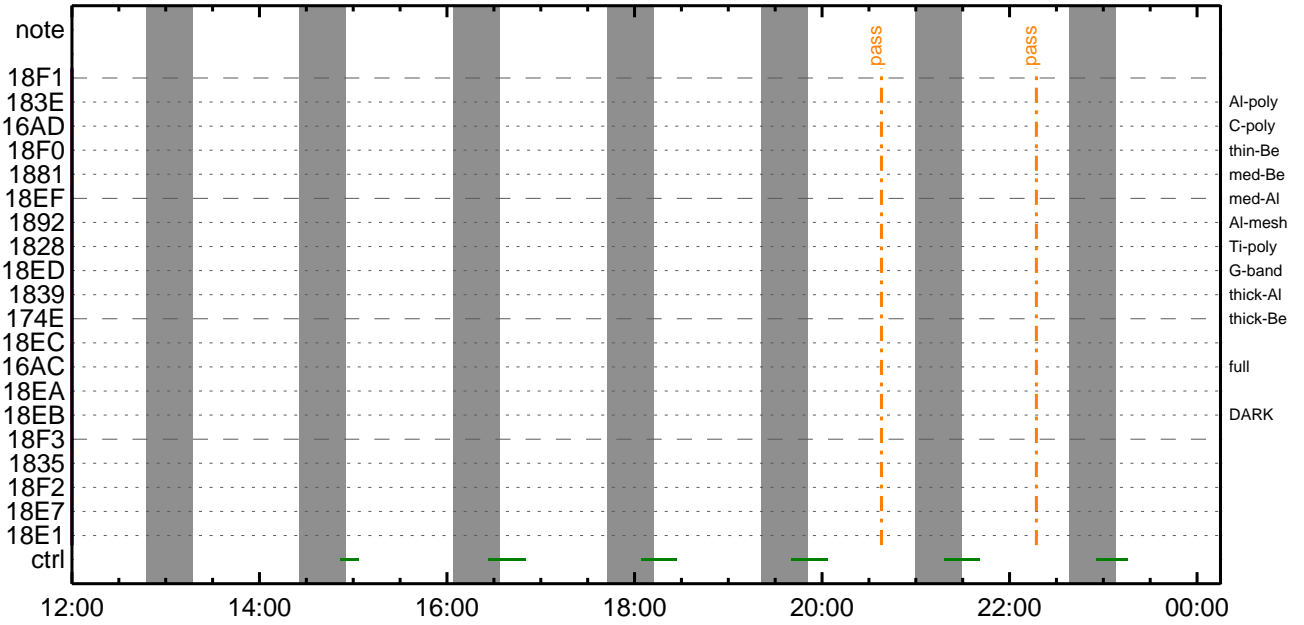
CMDI #0668 2012/06/05



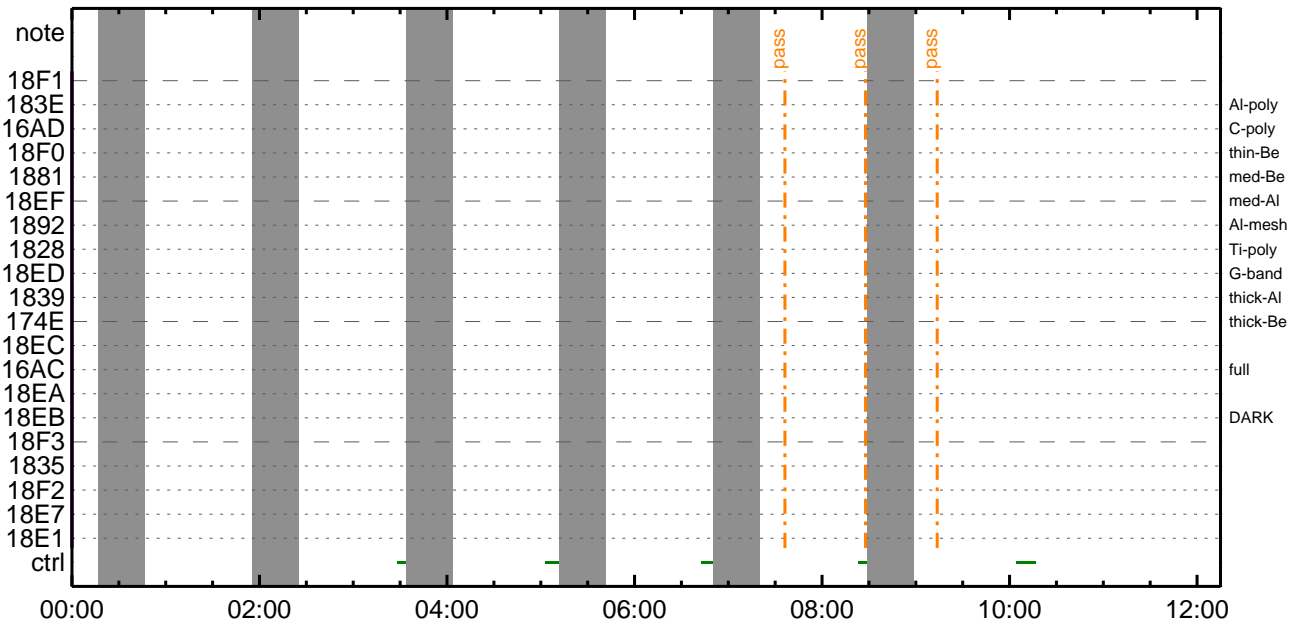
CMDI #0668 2012/06/06



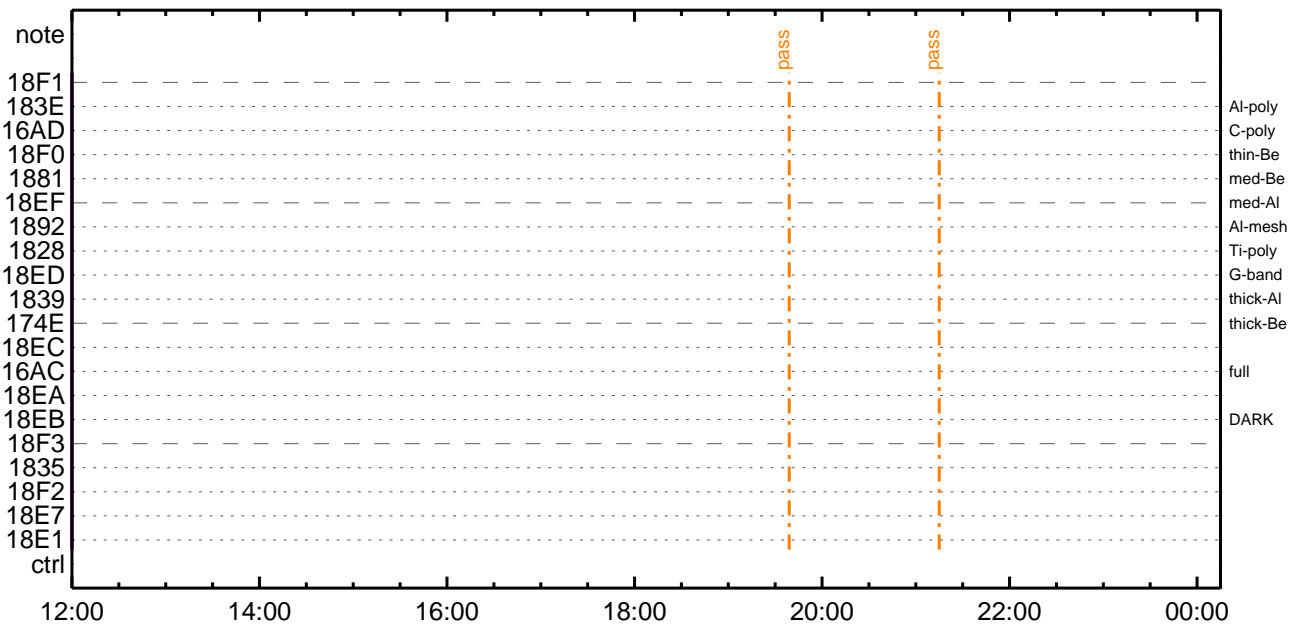
CMDI #0668 2012/06/06



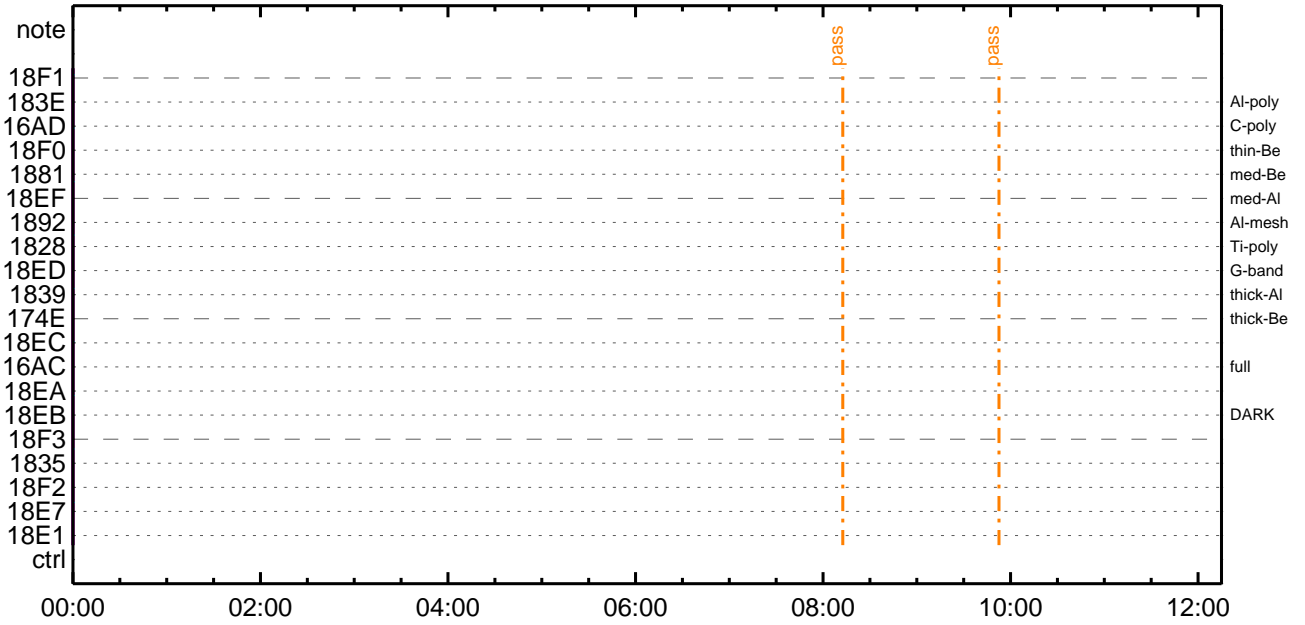
CMDI #0668 2012/06/07



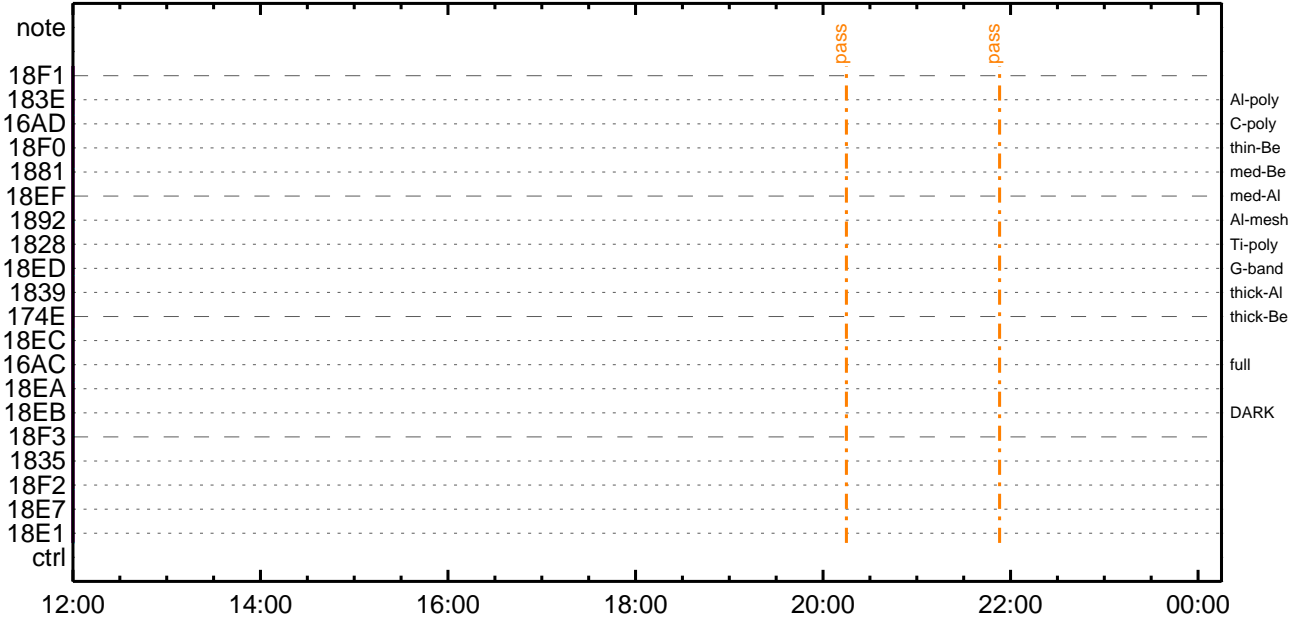
CMDI #0668 2012/06/07



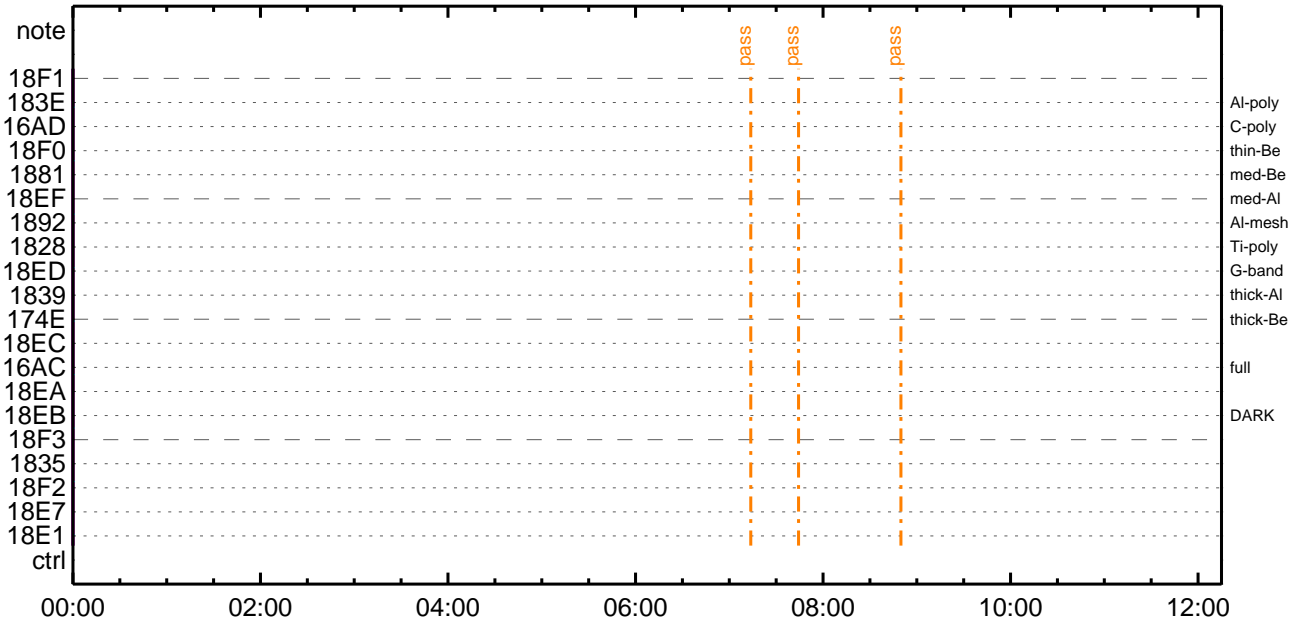
CMDI #0668 2012/06/08



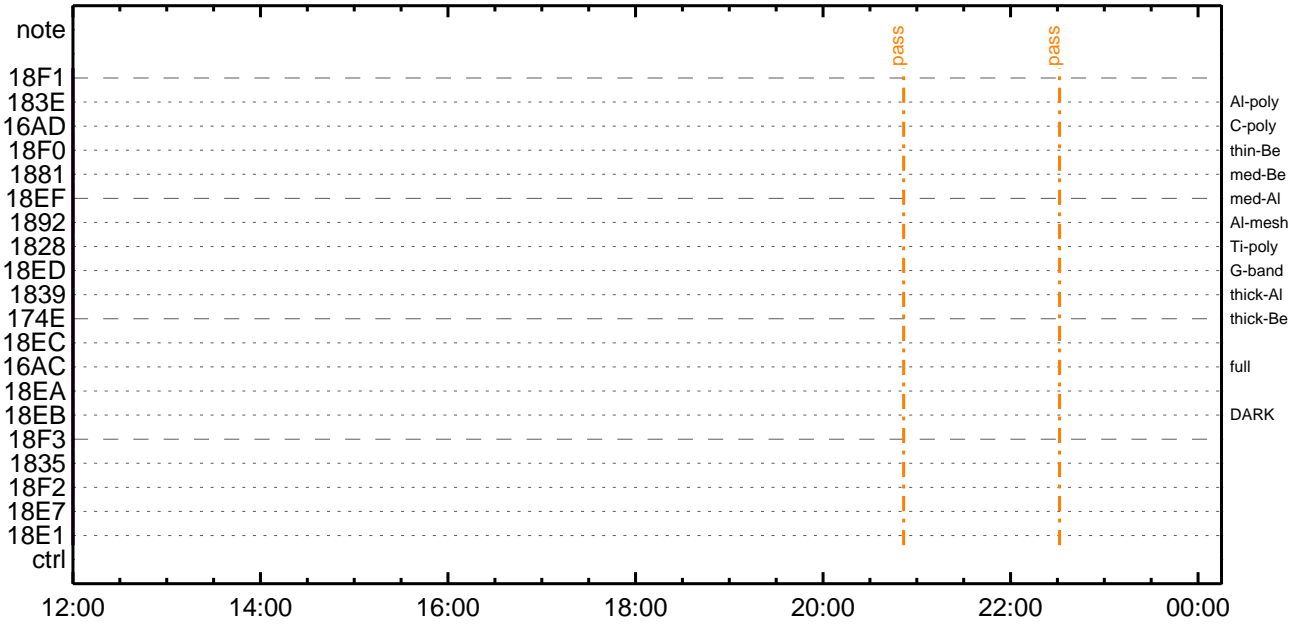
CMDI #0668 2012/06/08



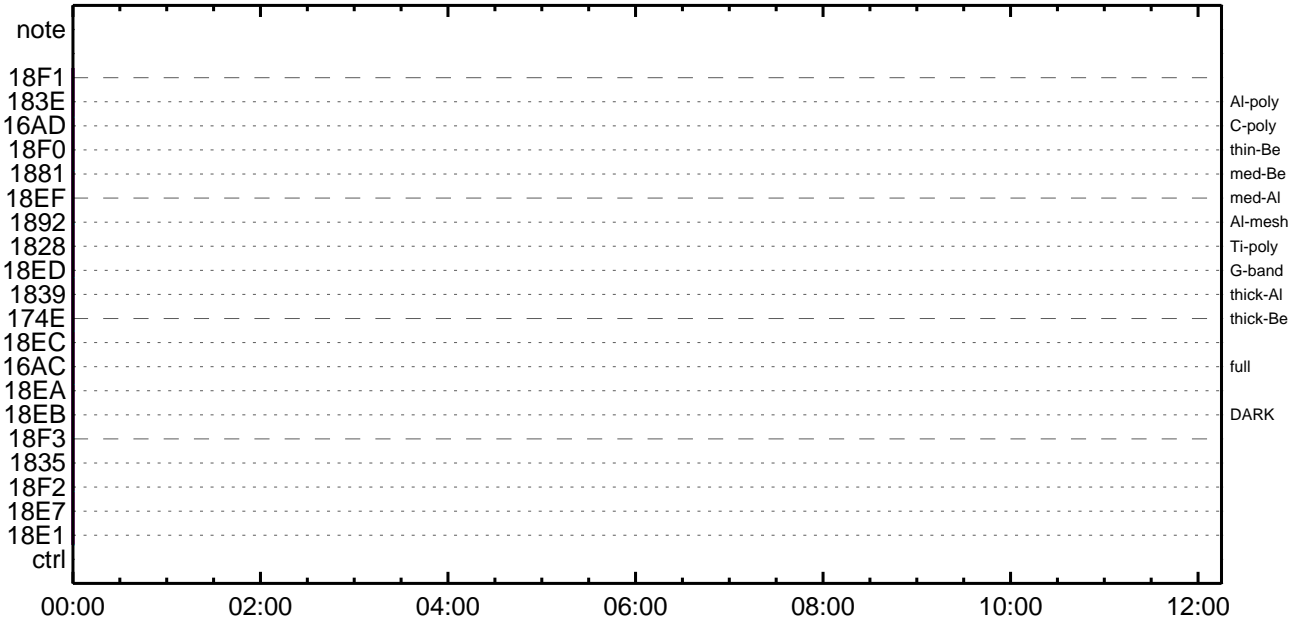
CMDI #0668 2012/06/09



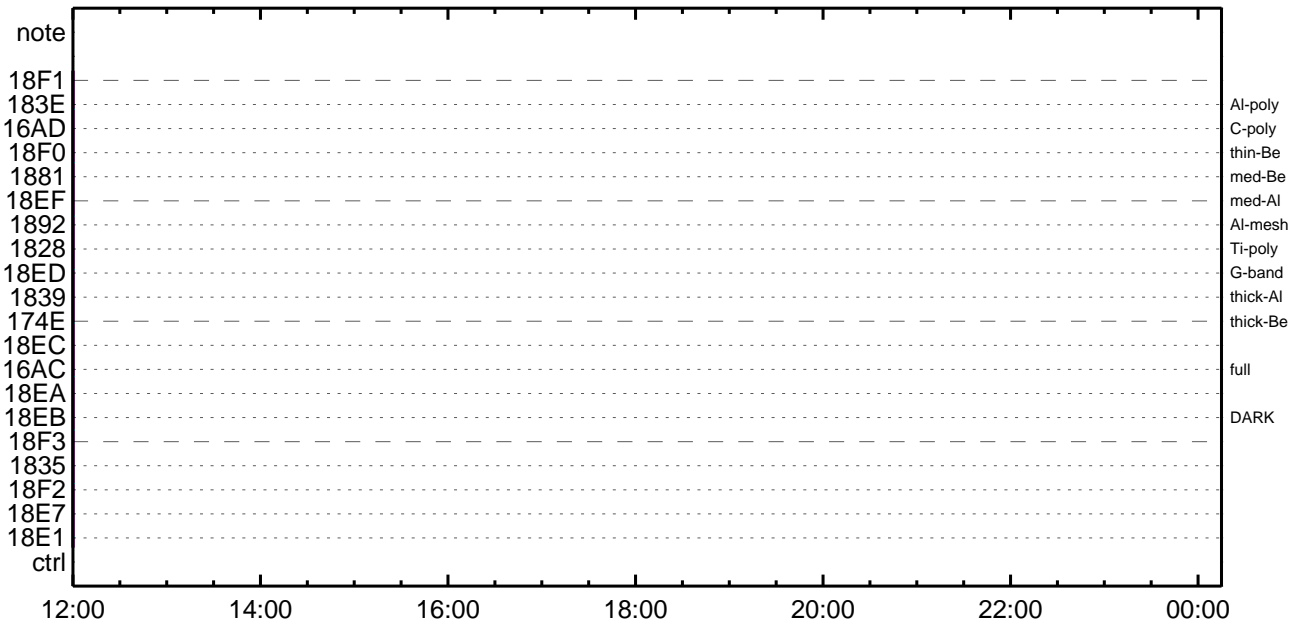
CMDI #0668 2012/06/09



CMDI #0668 2012/06/10

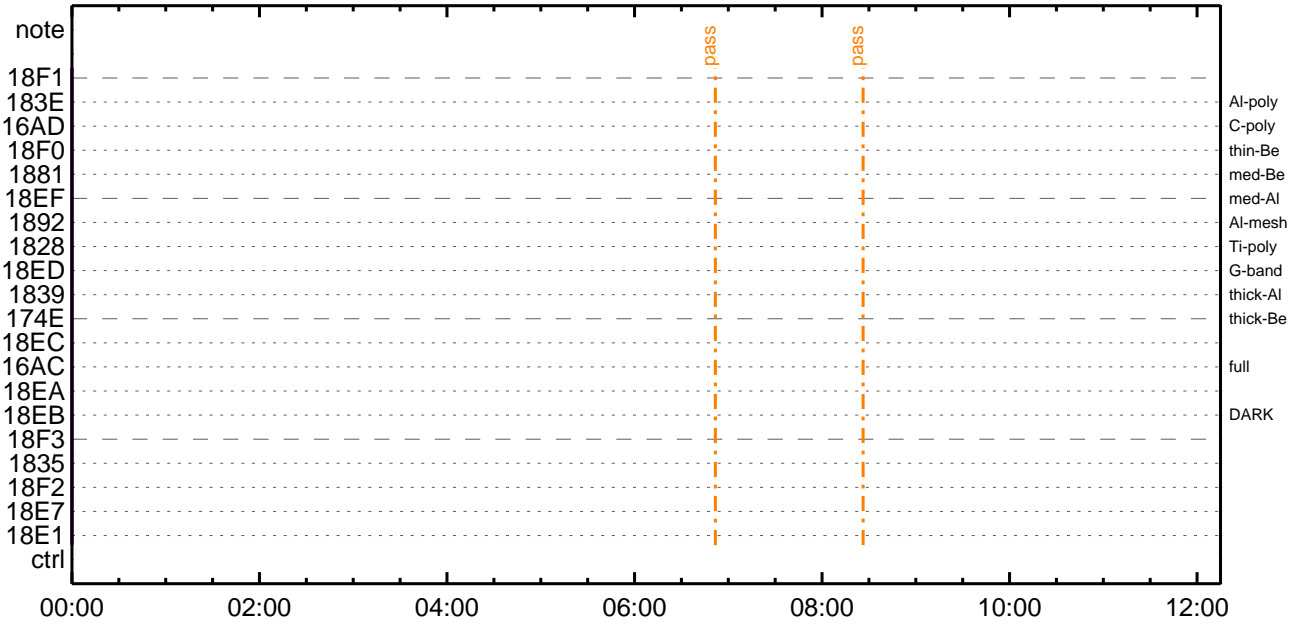


CMDI #0668 2012/06/10

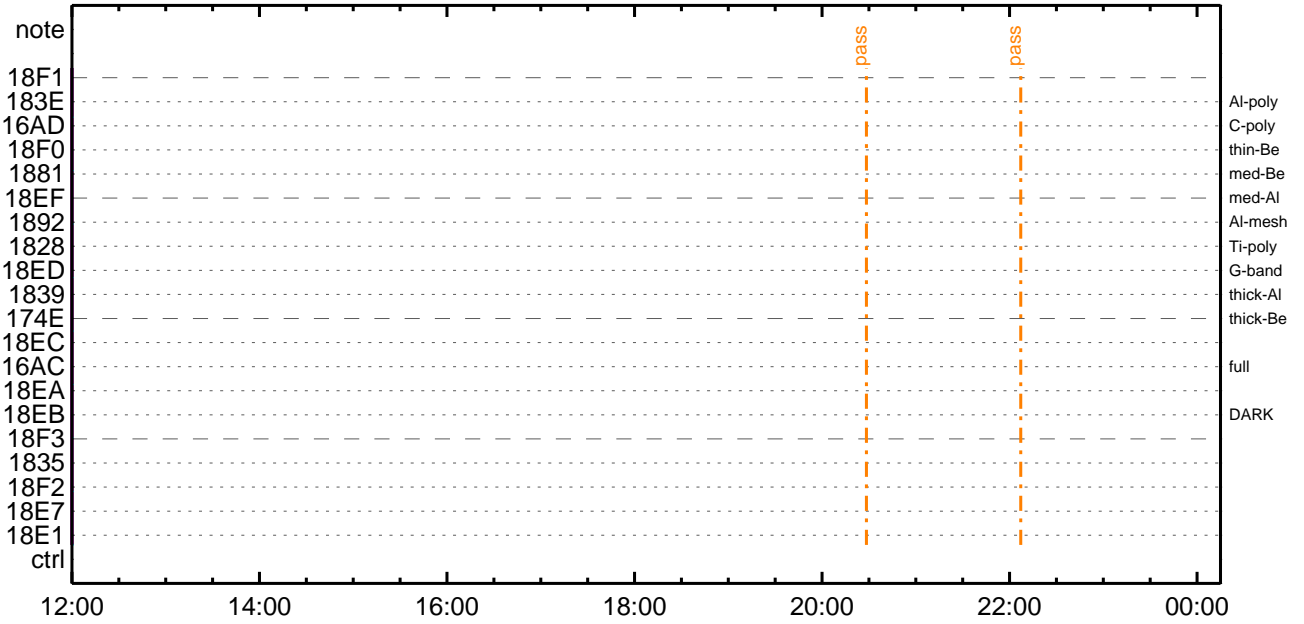




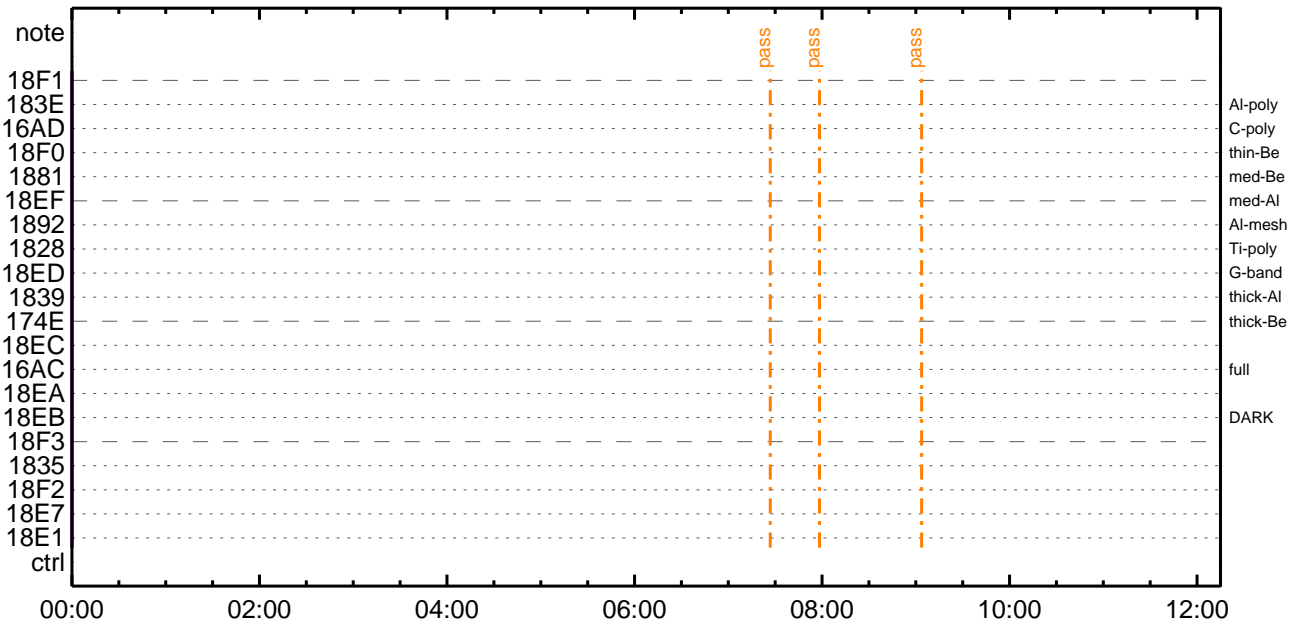
CMDI #0668 2012/06/11



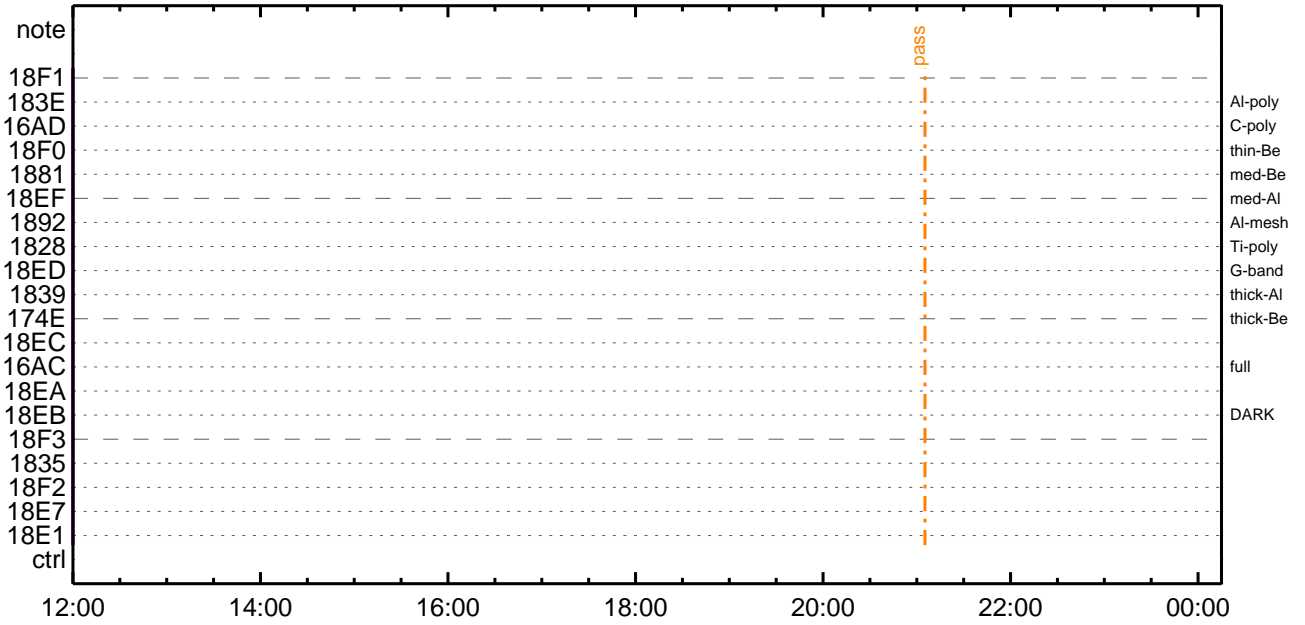
CMDI #0668 2012/06/11



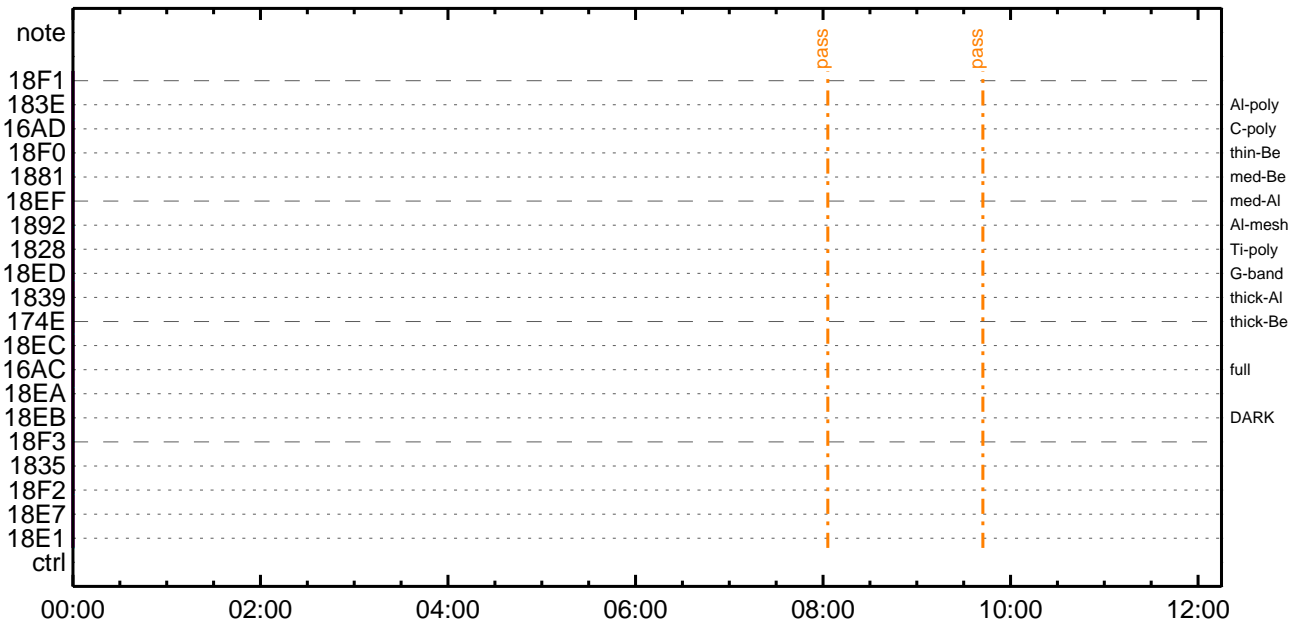
CMDI #0668 2012/06/12



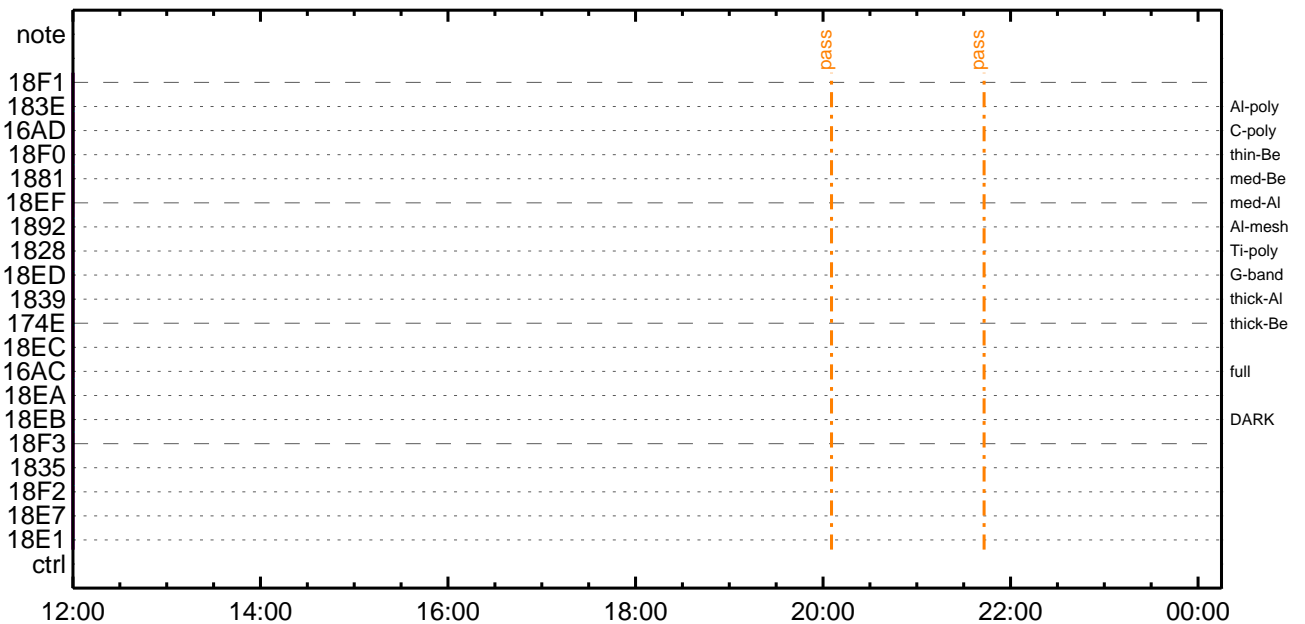
CMDI #0668 2012/06/12



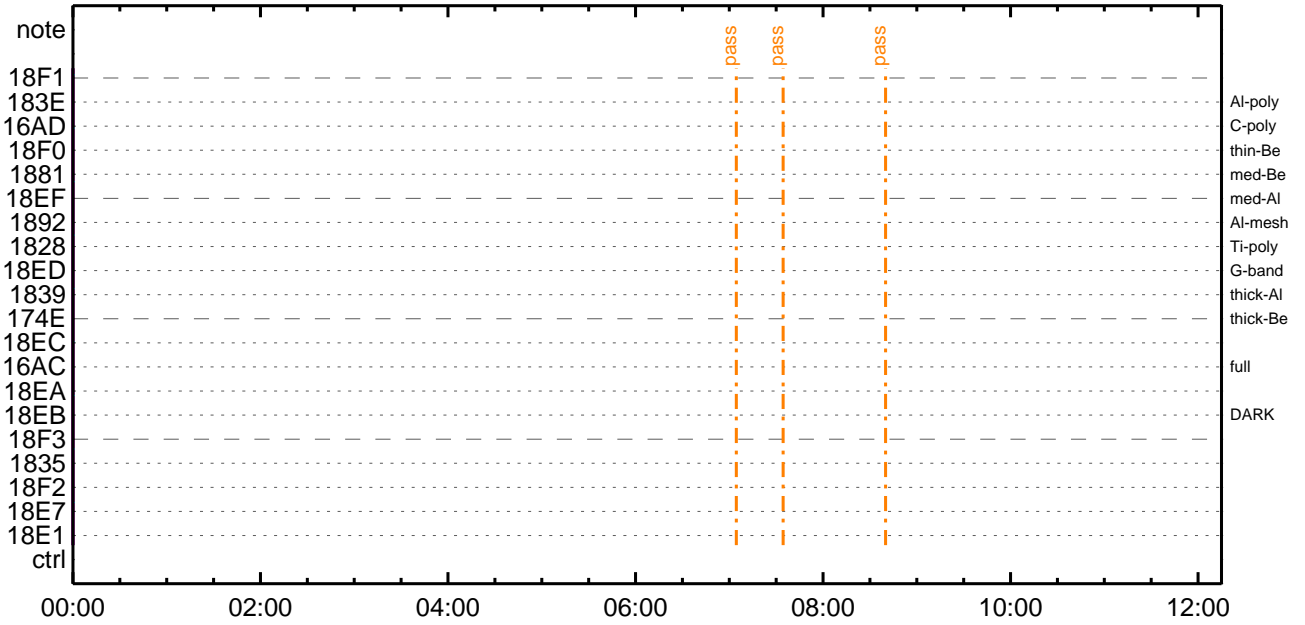
CMDI #0668 2012/06/13



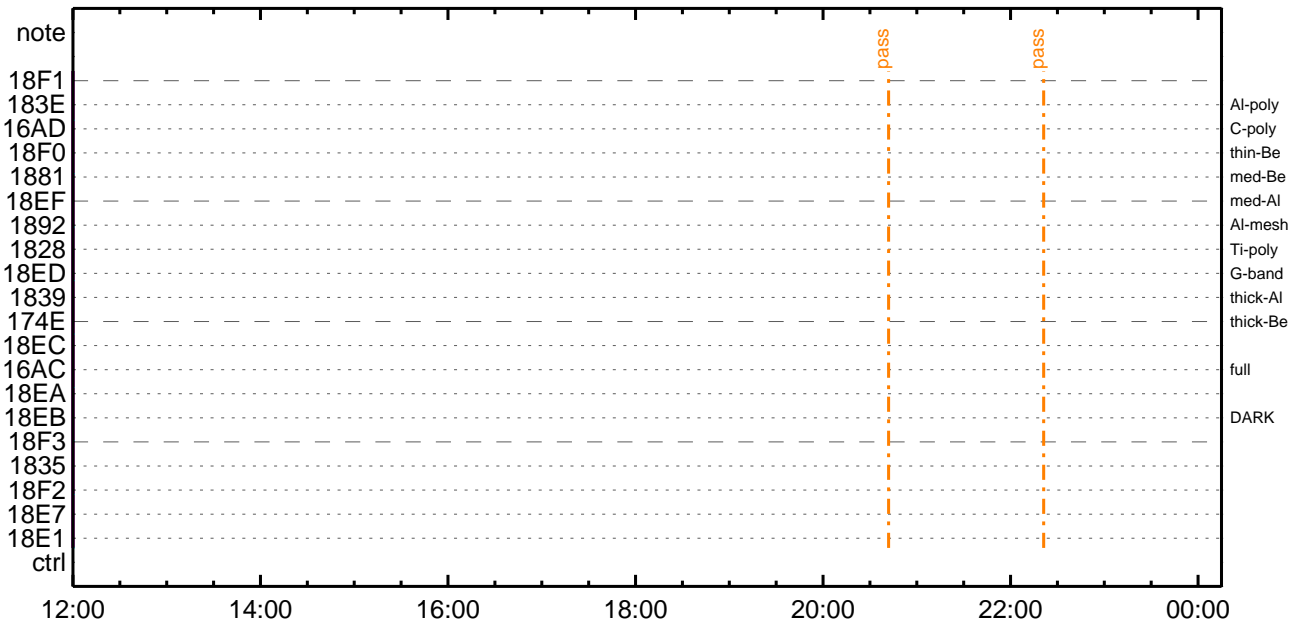
CMDI #0668 2012/06/13



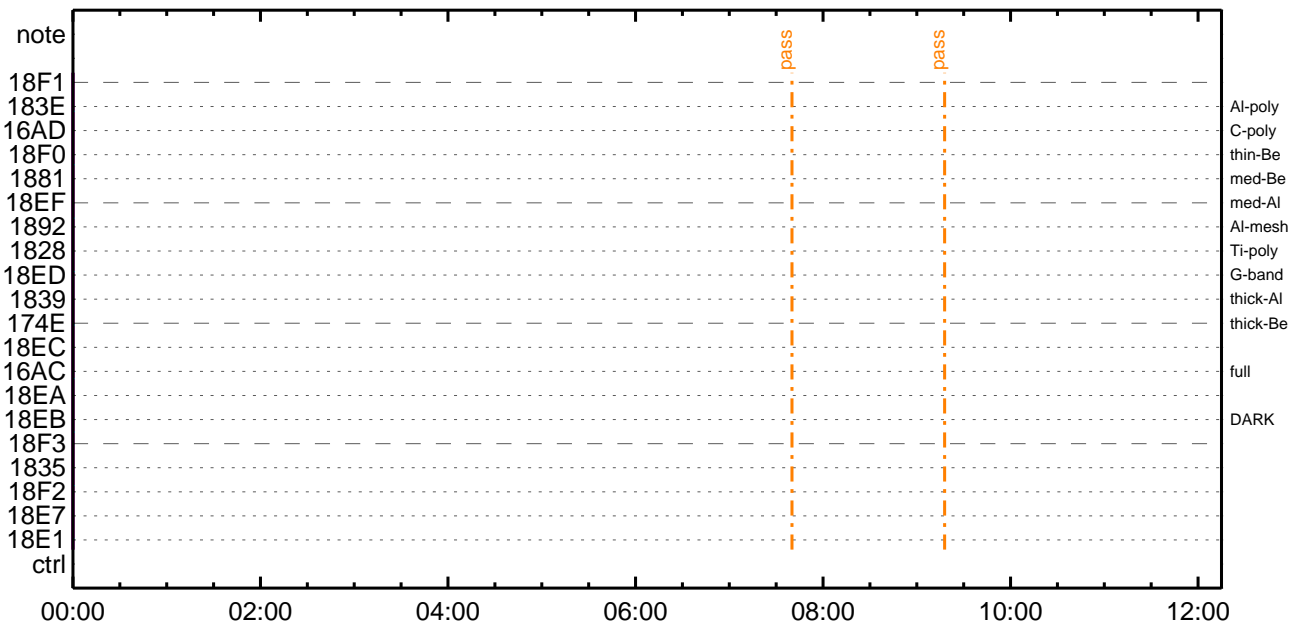
CMDI #0668 2012/06/14



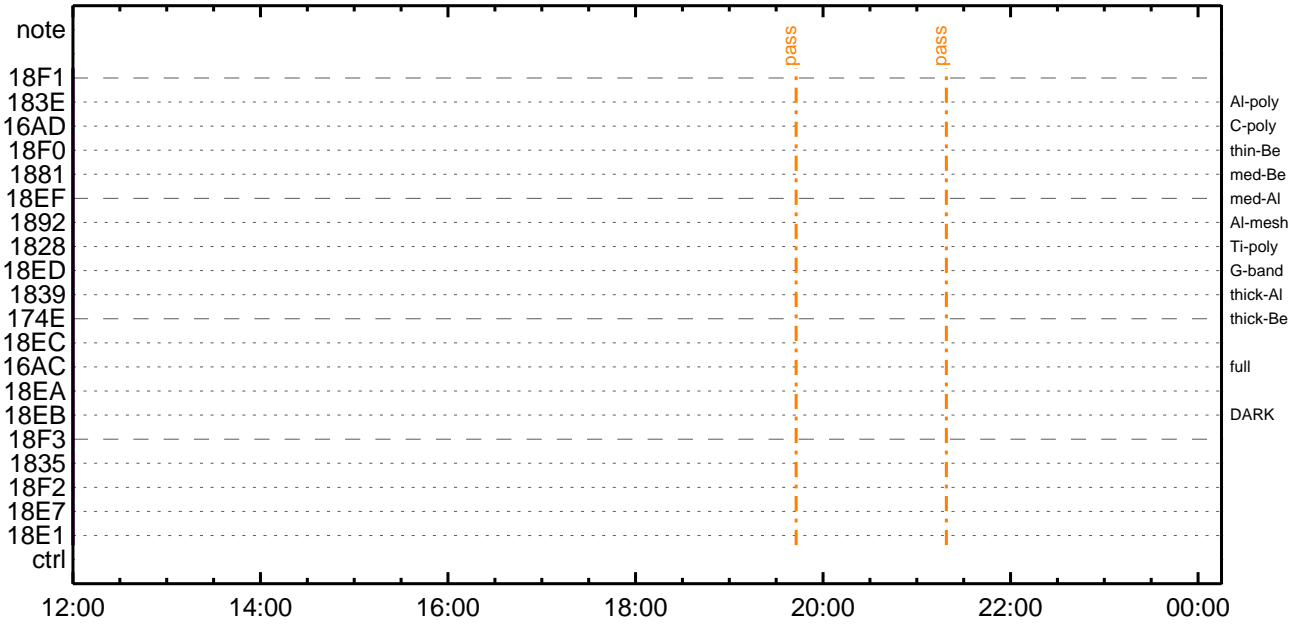
CMDI #0668 2012/06/14



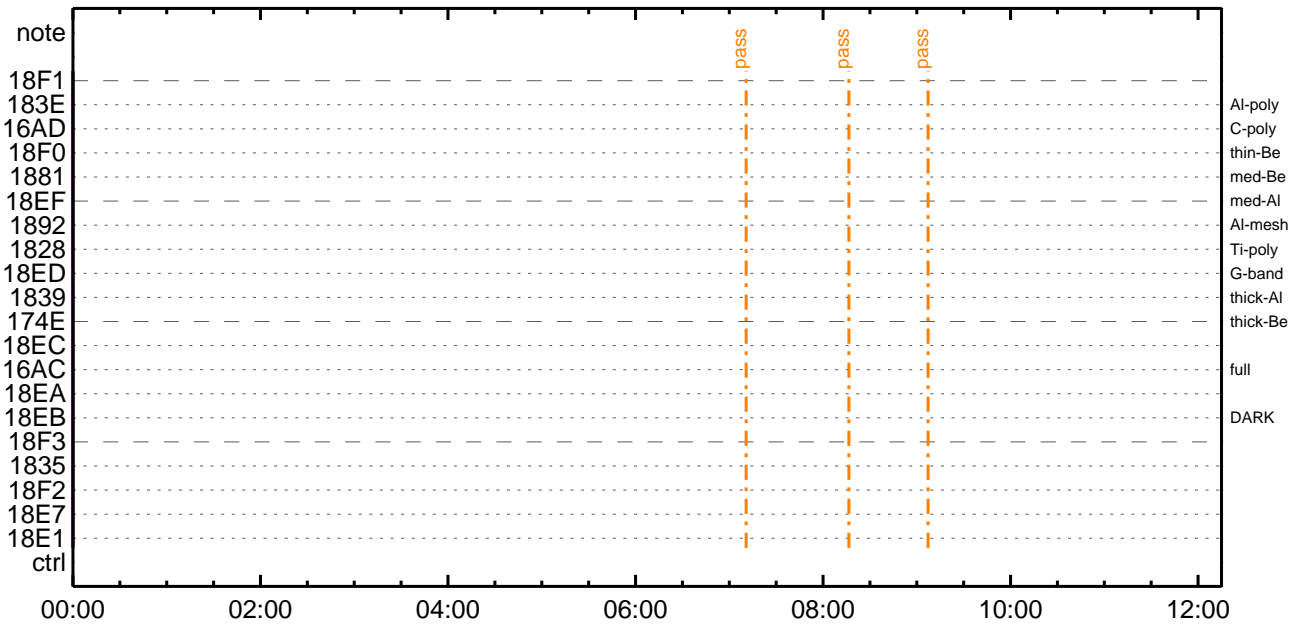
CMDI #0668 2012/06/15



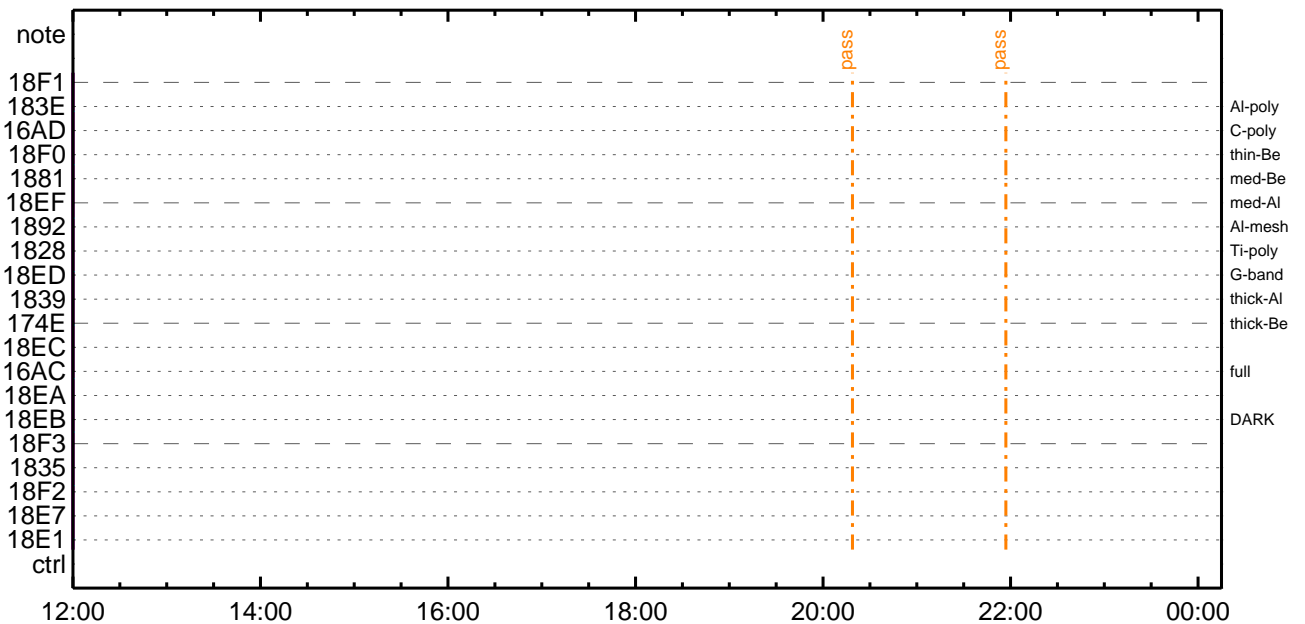
CMDI #0668 2012/06/15



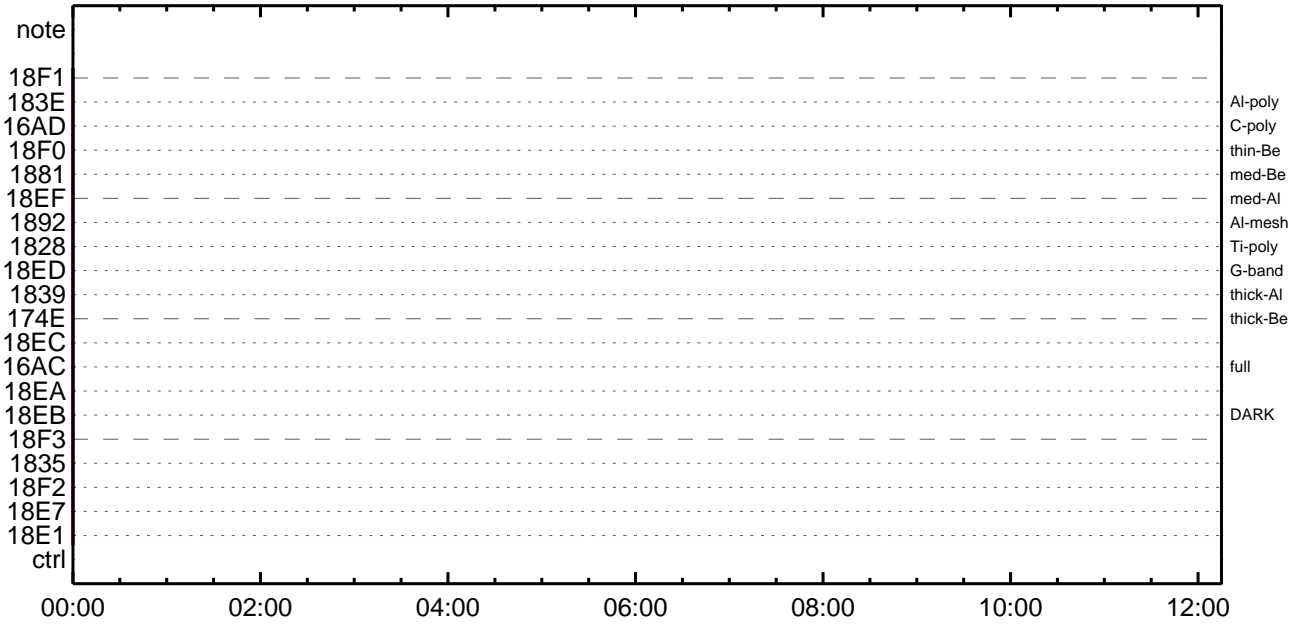
CMDI #0668 2012/06/16



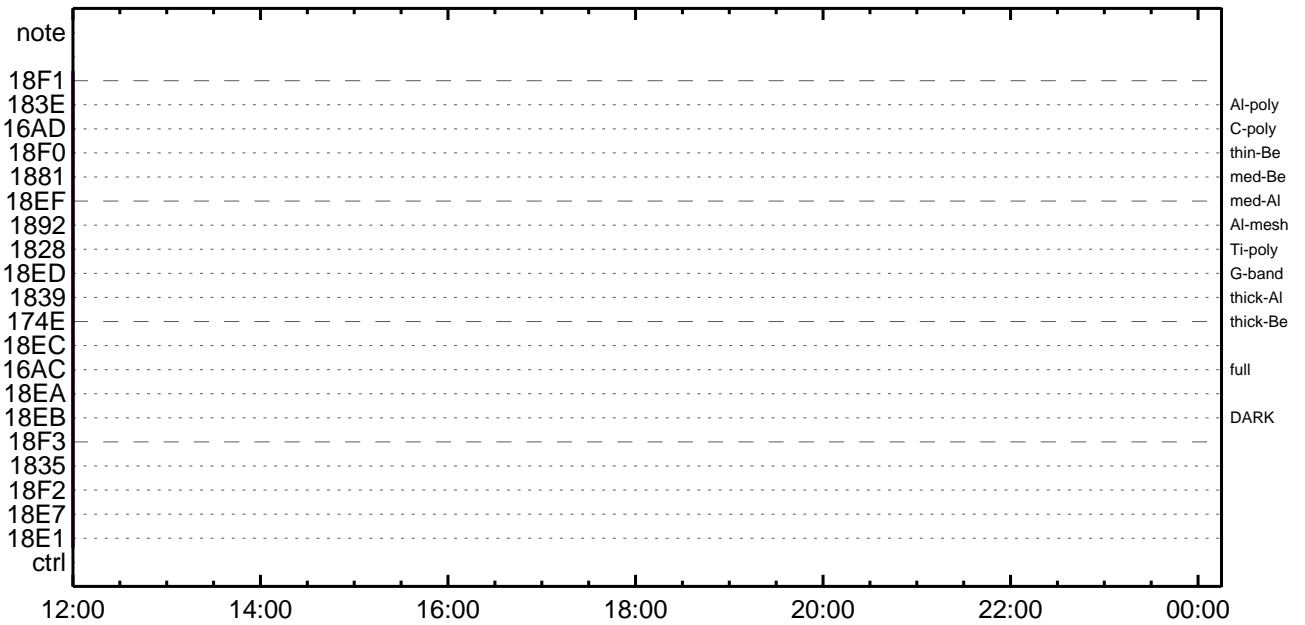
CMDI #0668 2012/06/16



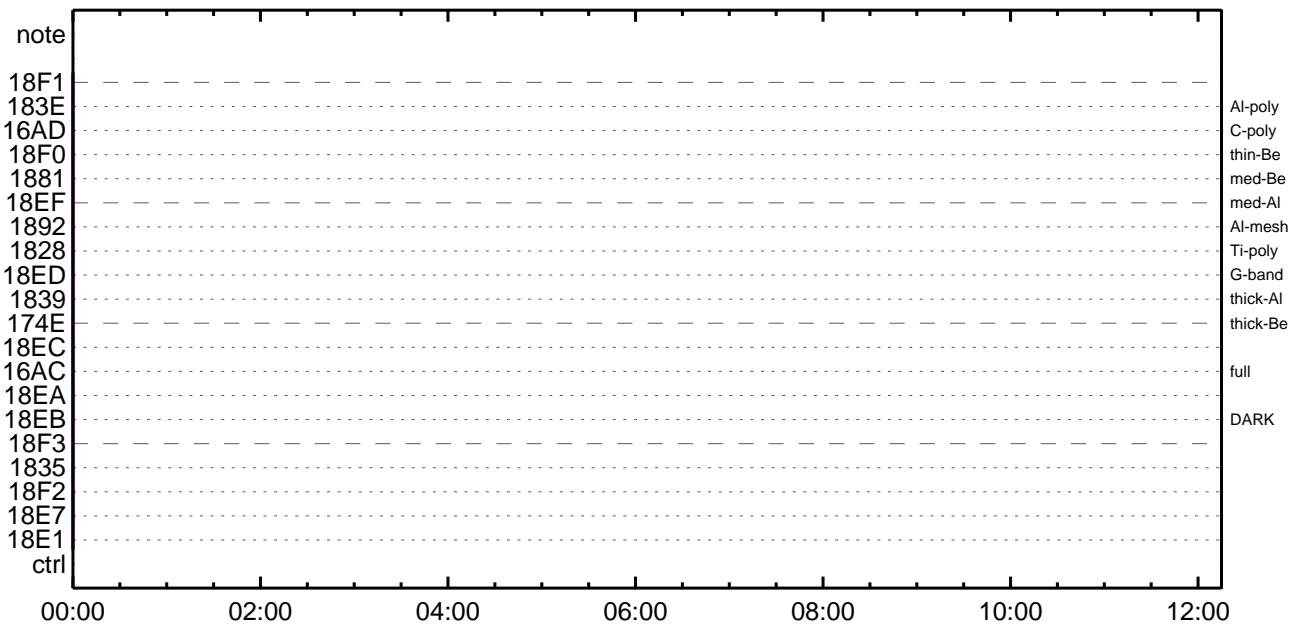
CMDI #0668 2012/06/17



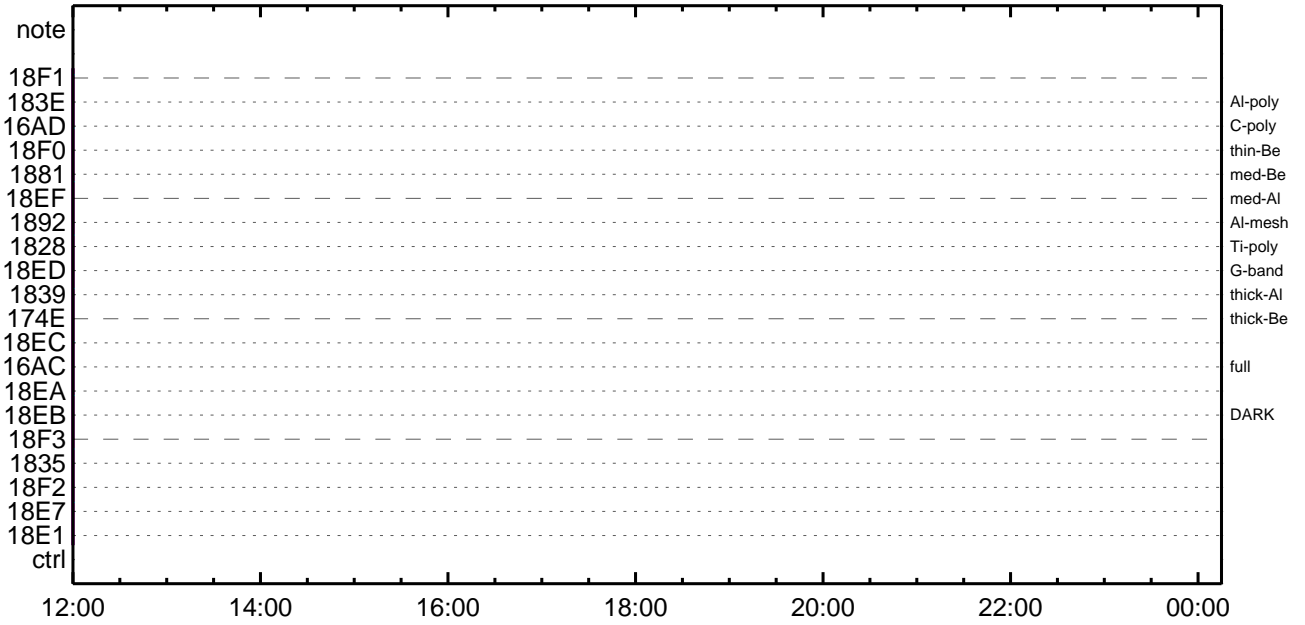
CMDI #0668 2012/06/17



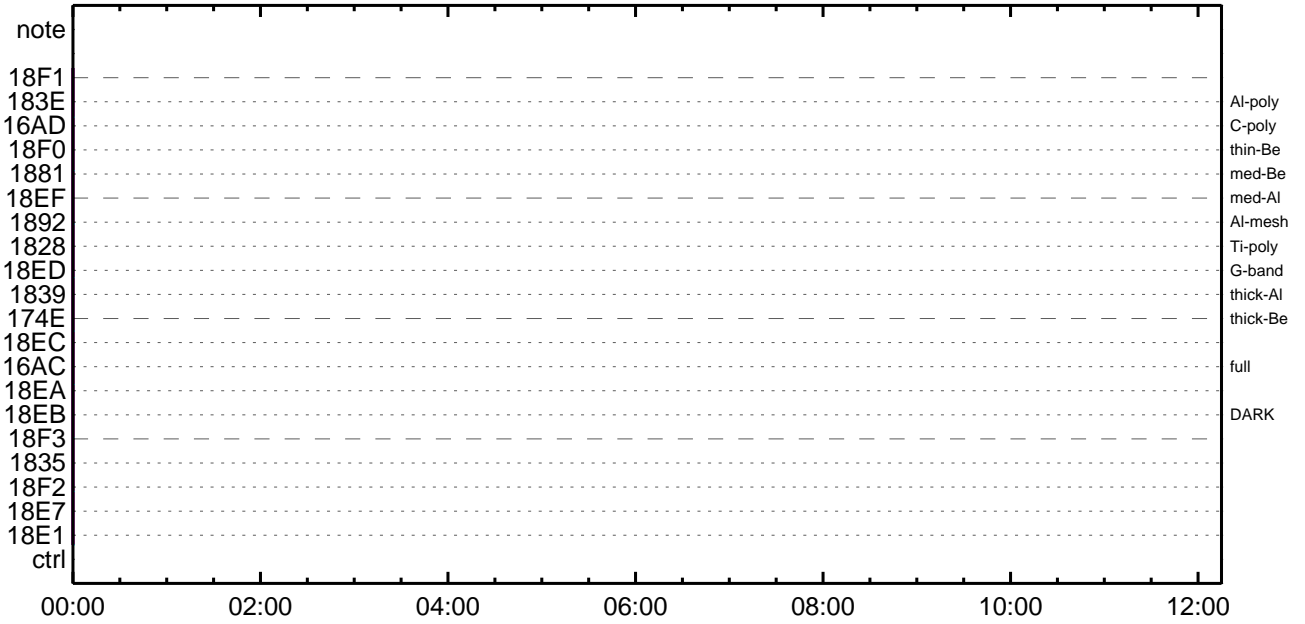
CMDI #0668 2012/06/18



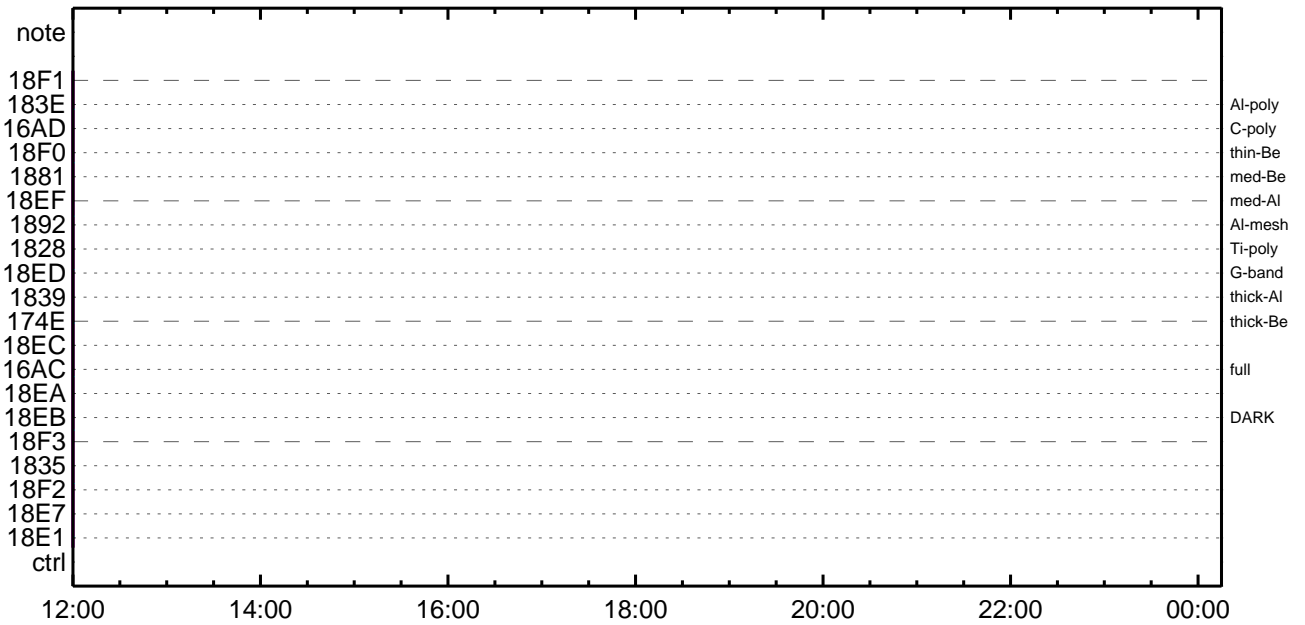
CMDI #0668 2012/06/18



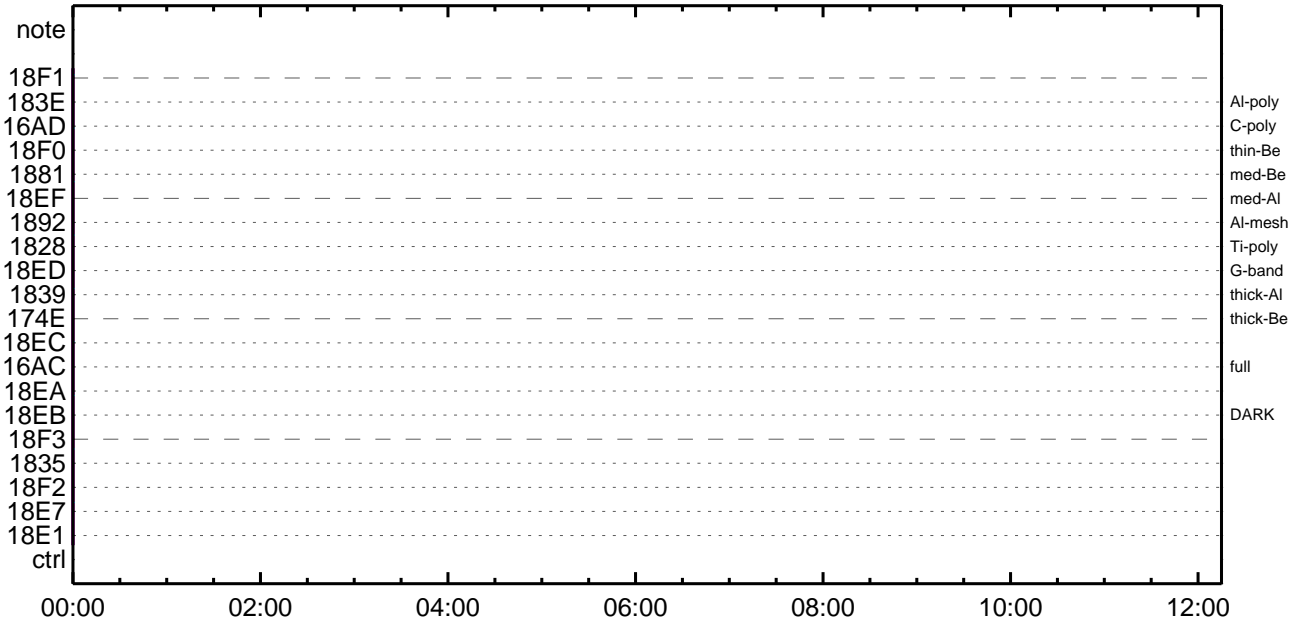
CMDI #0668 2012/06/19



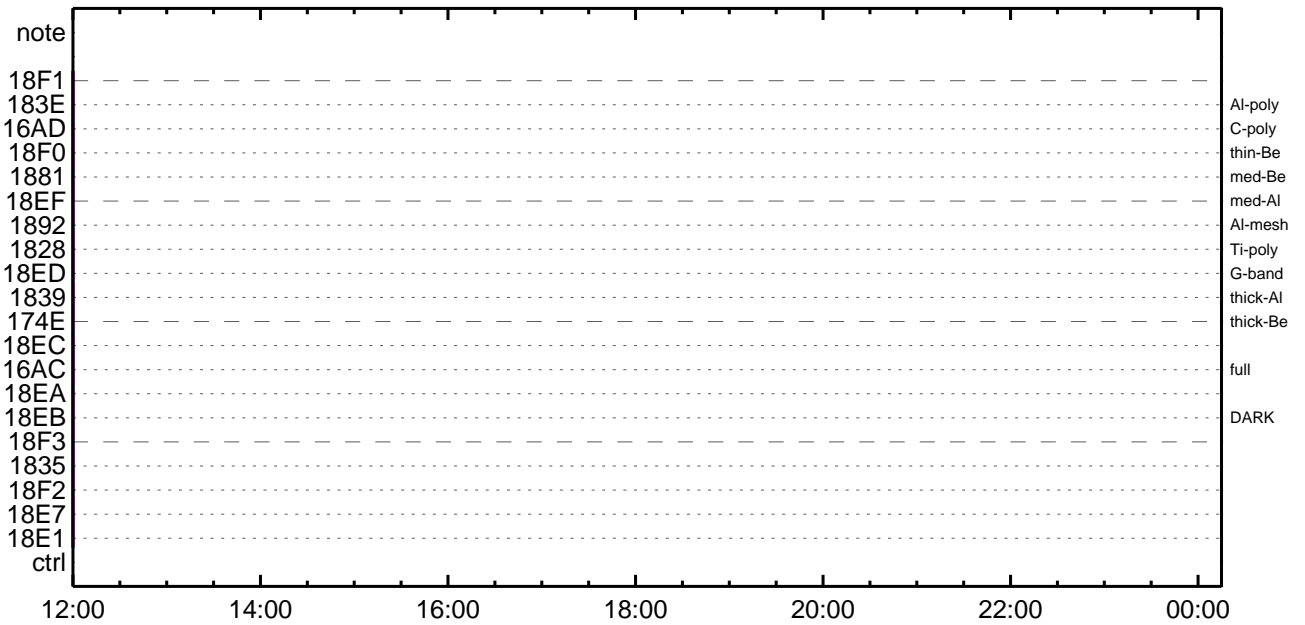
CMDI #0668 2012/06/19



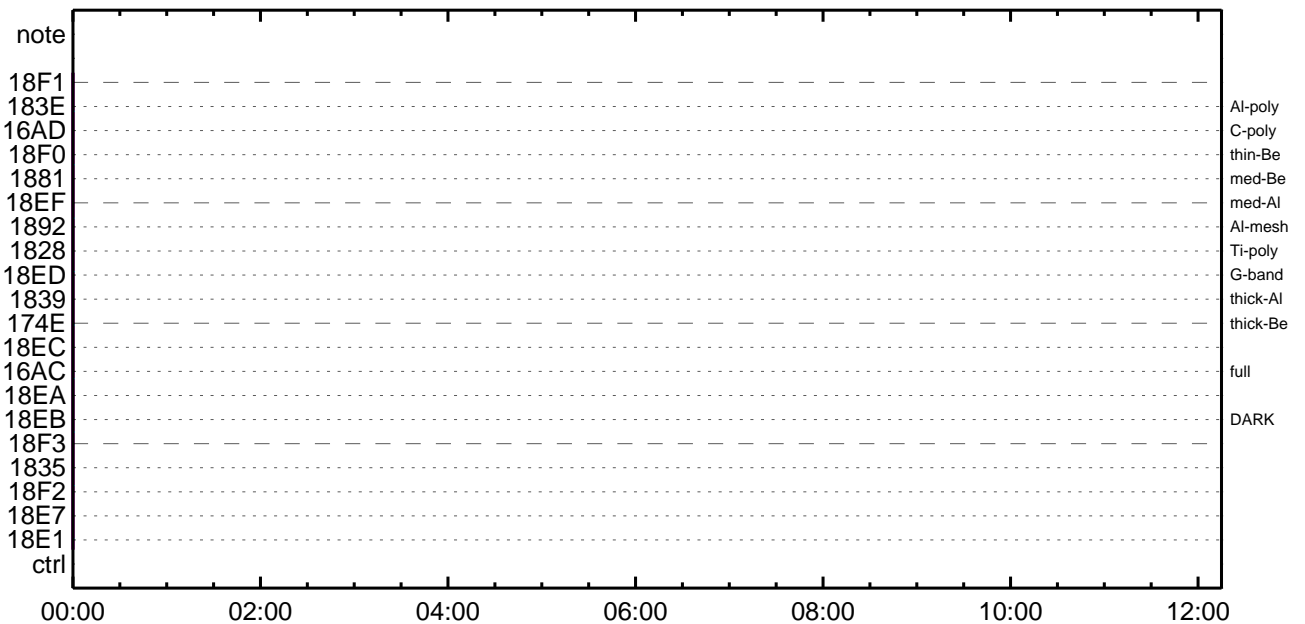
CMDI #0668 2012/06/20



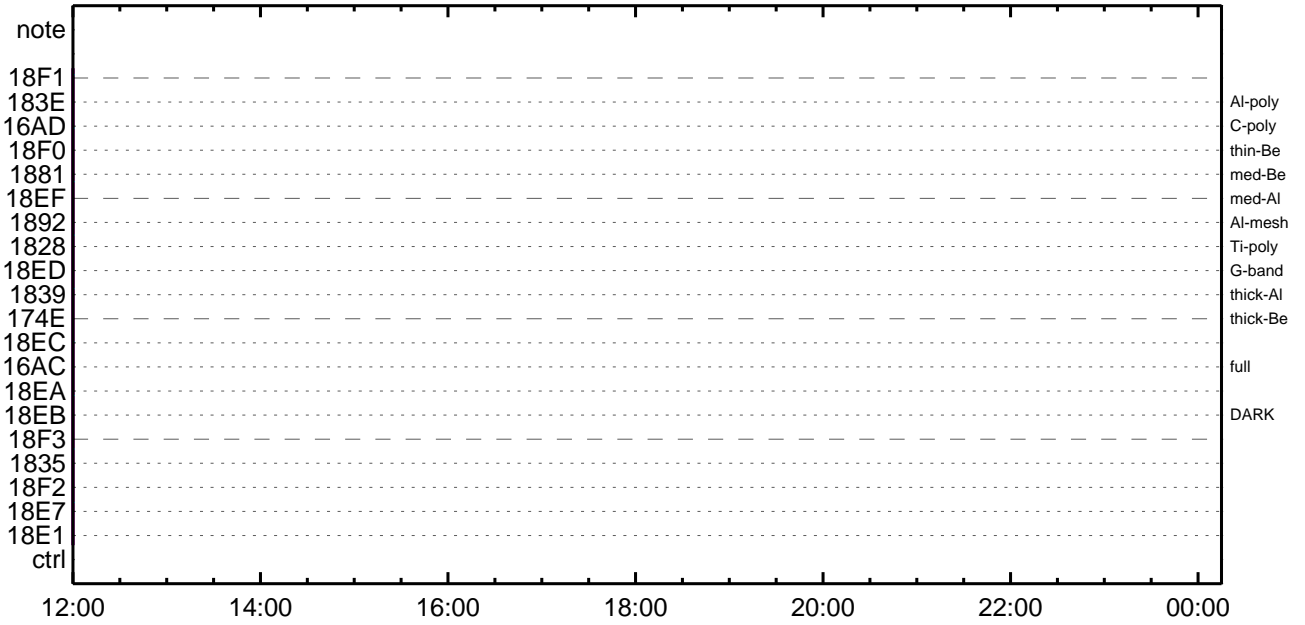
CMDI #0668 2012/06/20



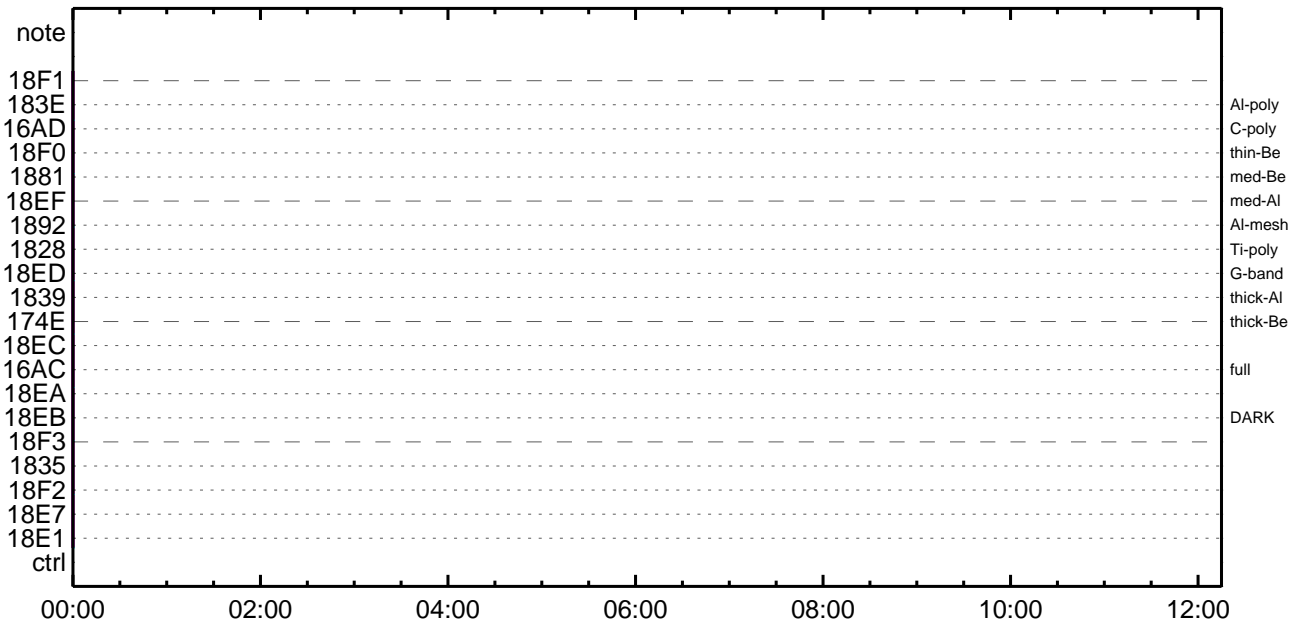
CMDI #0668 2012/06/21



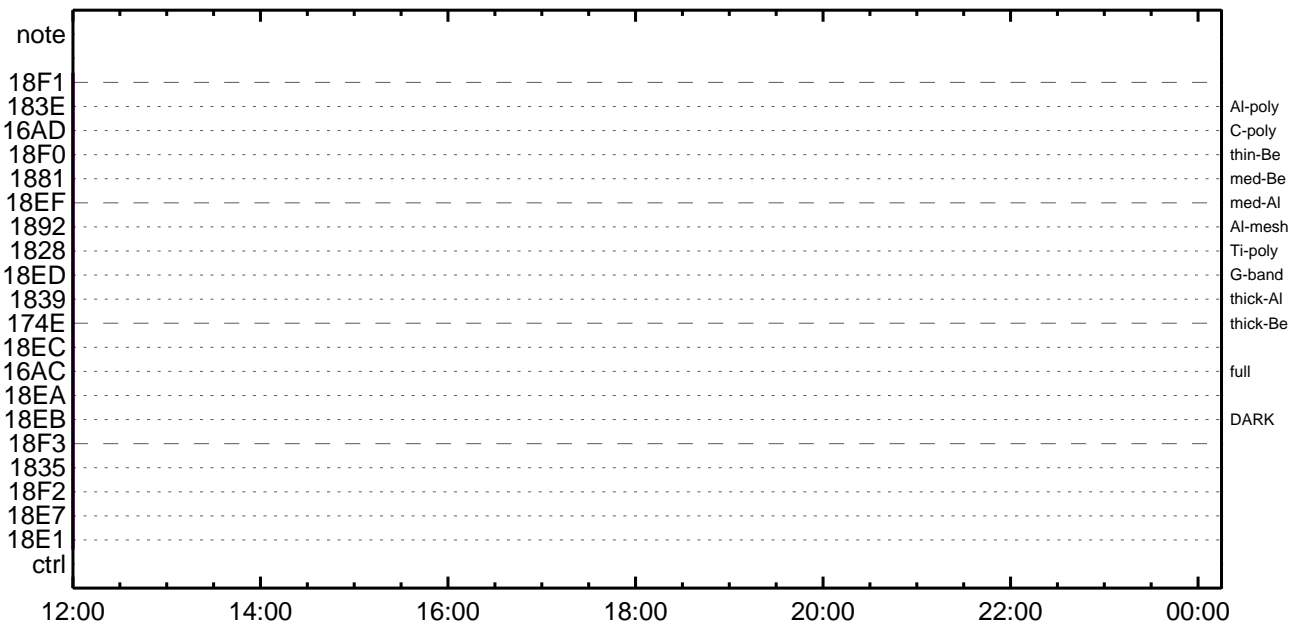
CMDI #0668 2012/06/21



CMDI #0668 2012/06/22

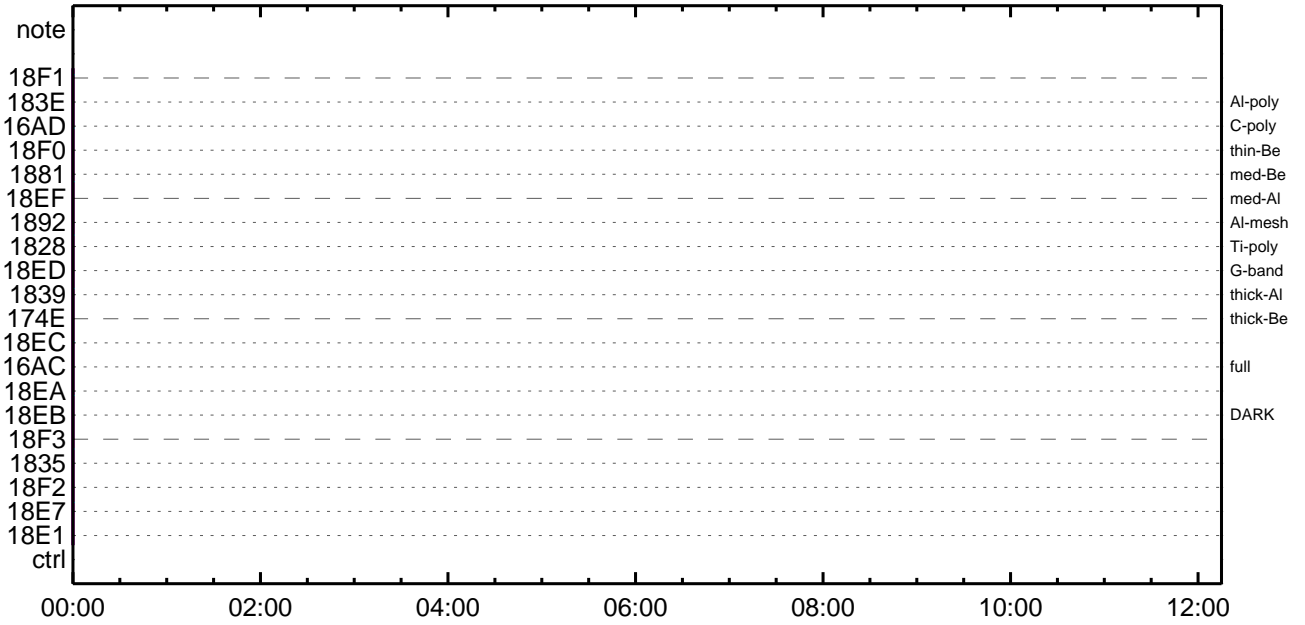


CMDI #0668 2012/06/22

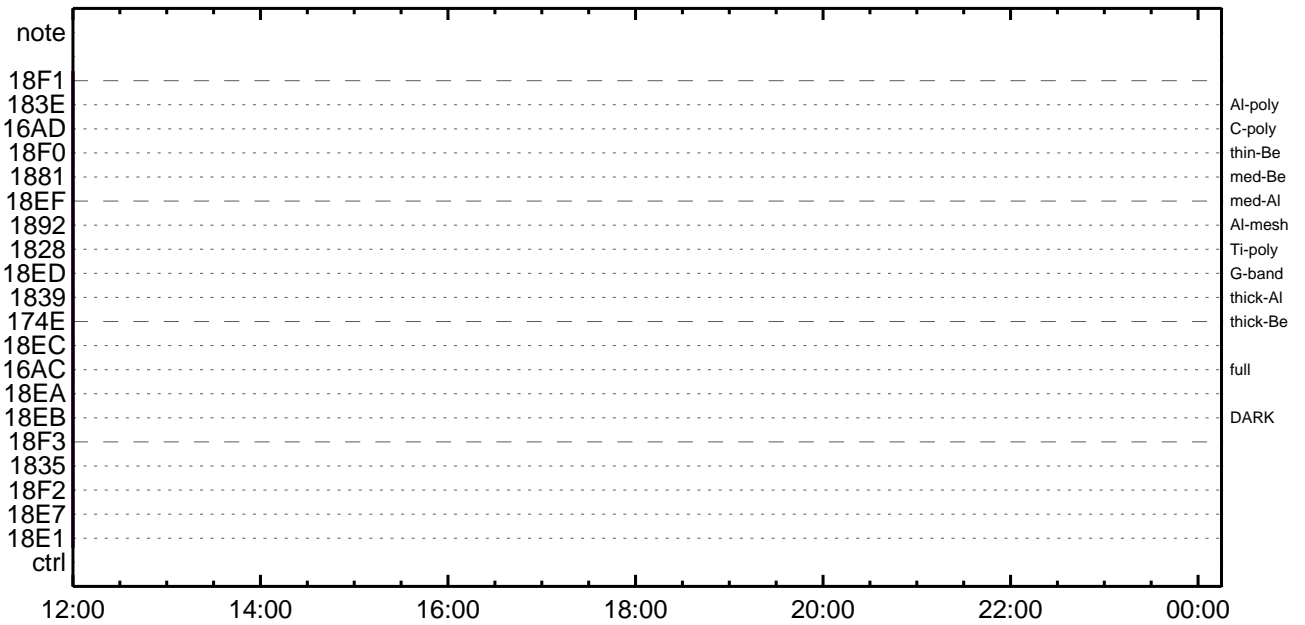




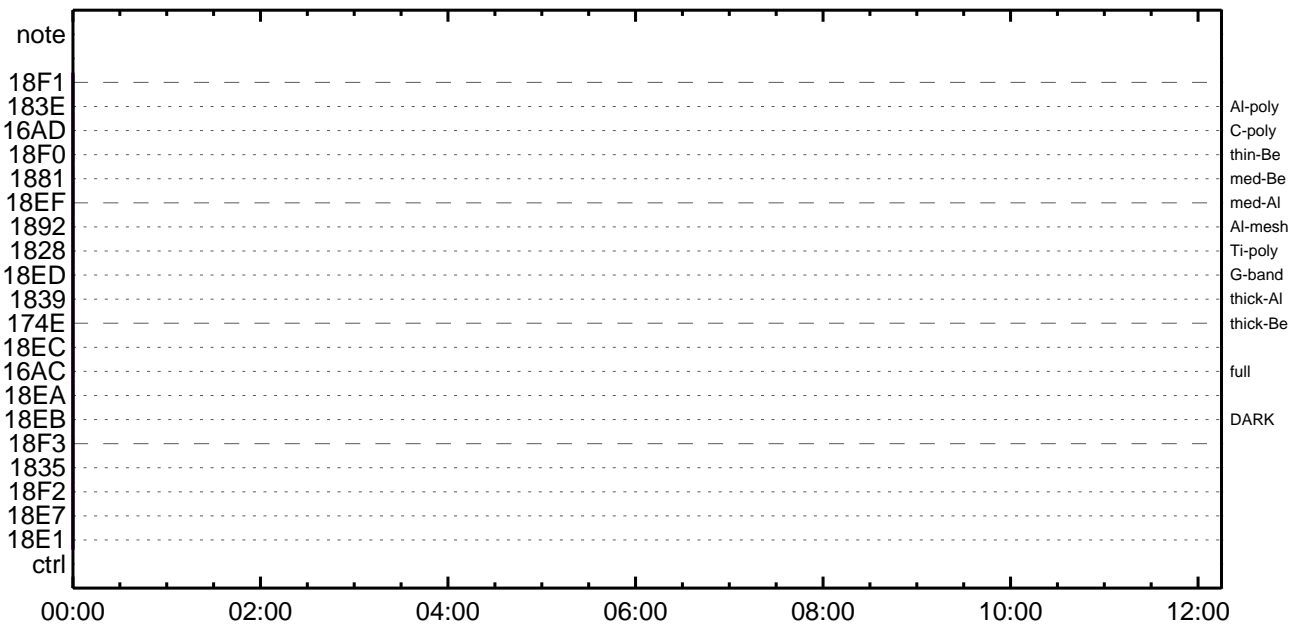
CMDI #0668 2012/06/23



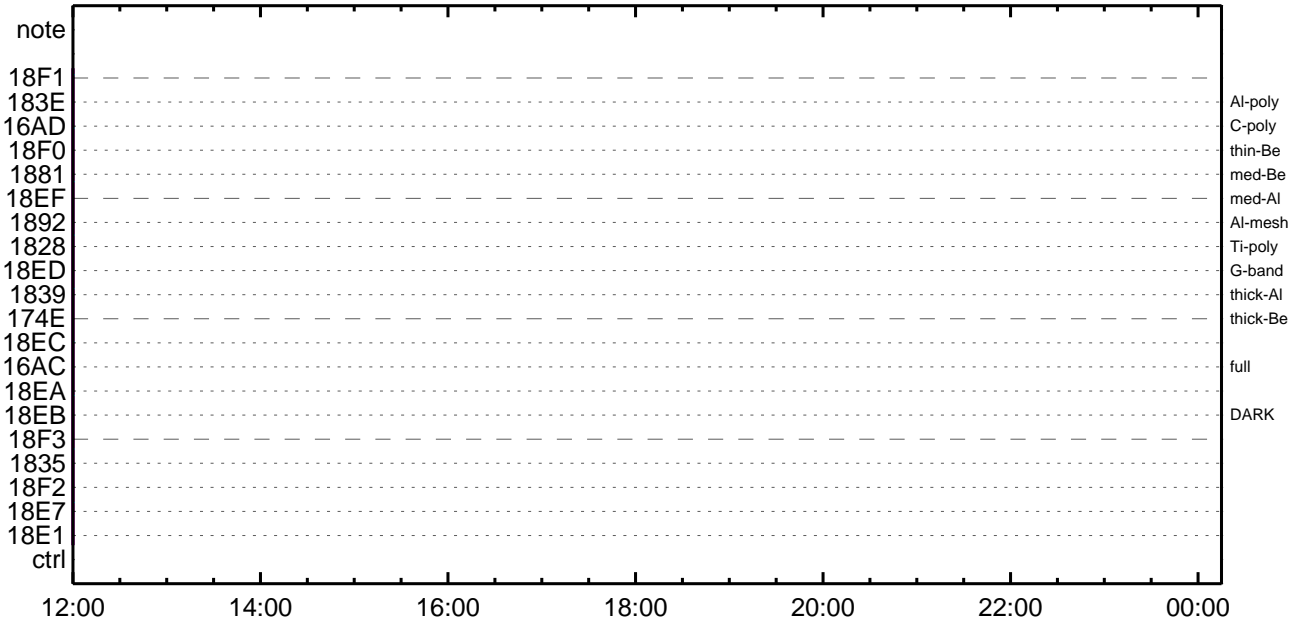
CMDI #0668 2012/06/23



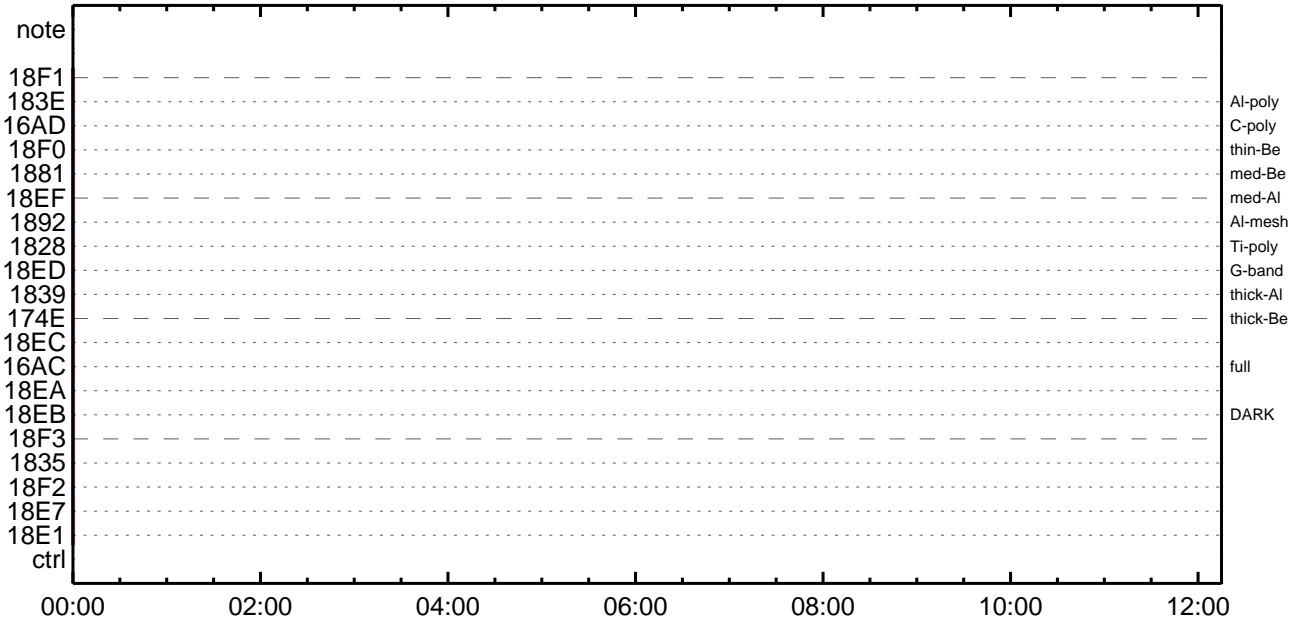
CMDI #0668 2012/06/24



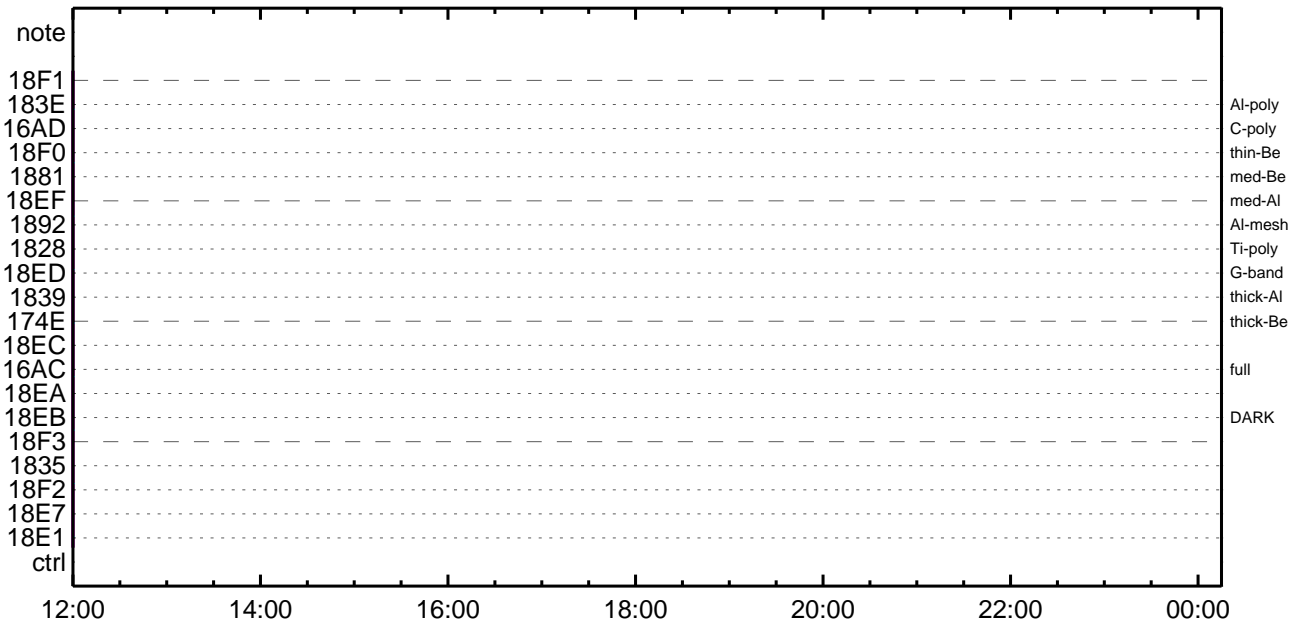
CMDI #0668 2012/06/24



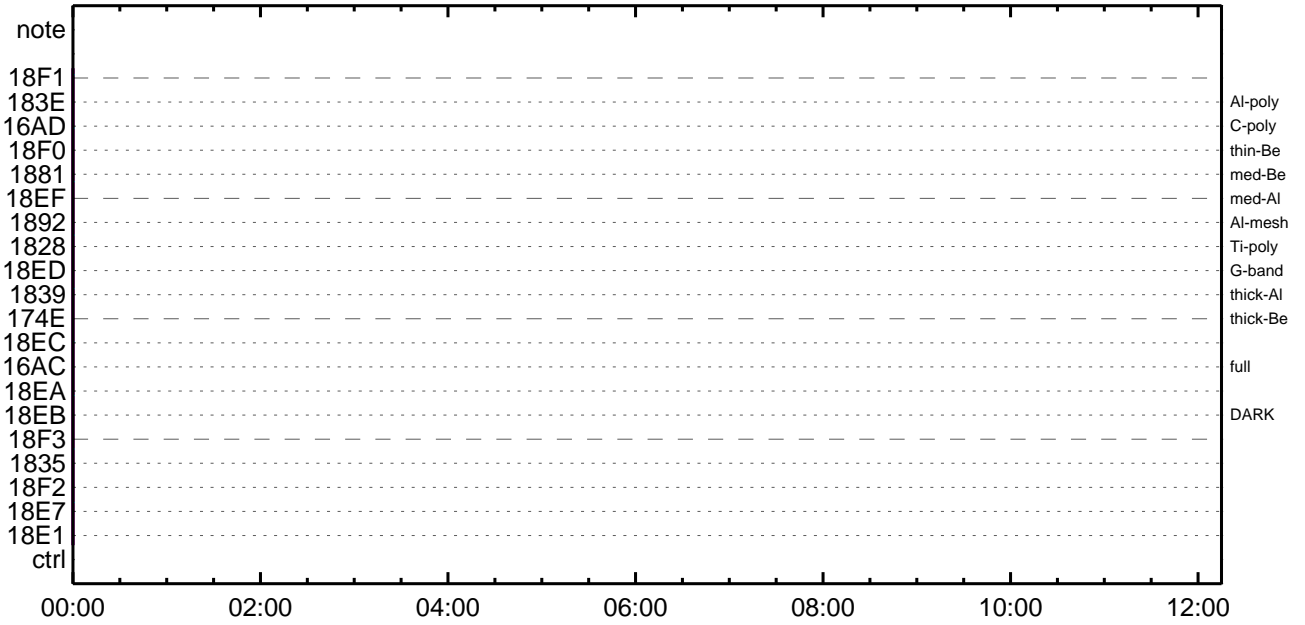
CMDI #0668 2012/06/25



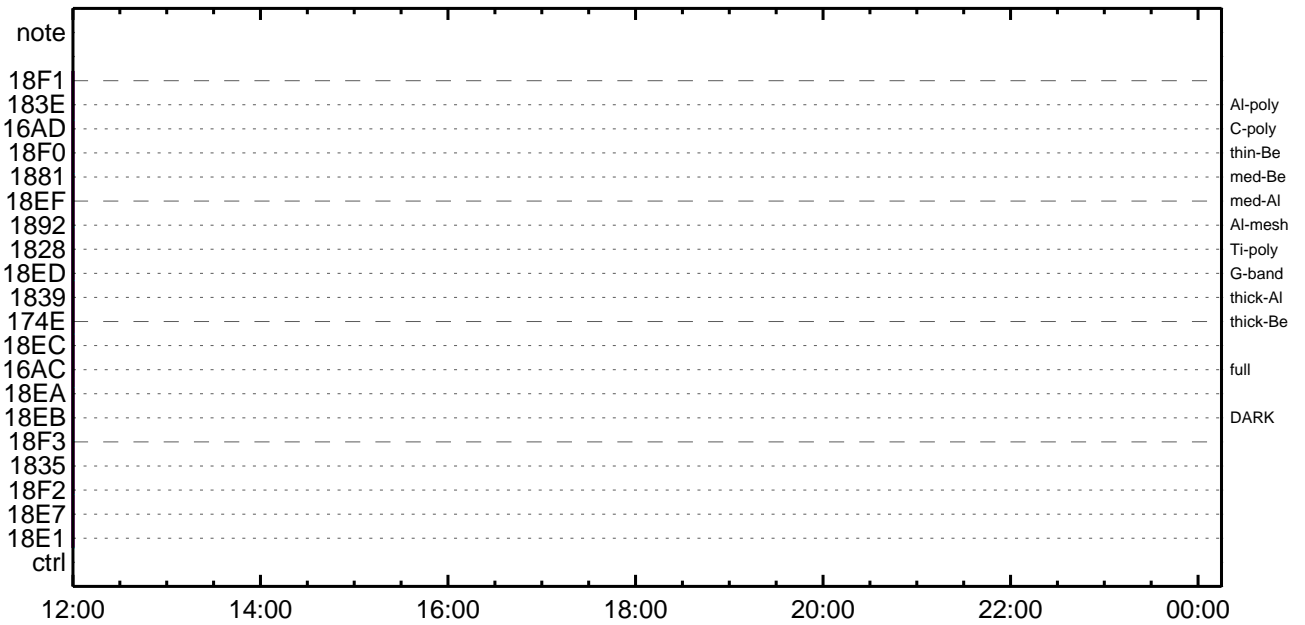
CMDI #0668 2012/06/25



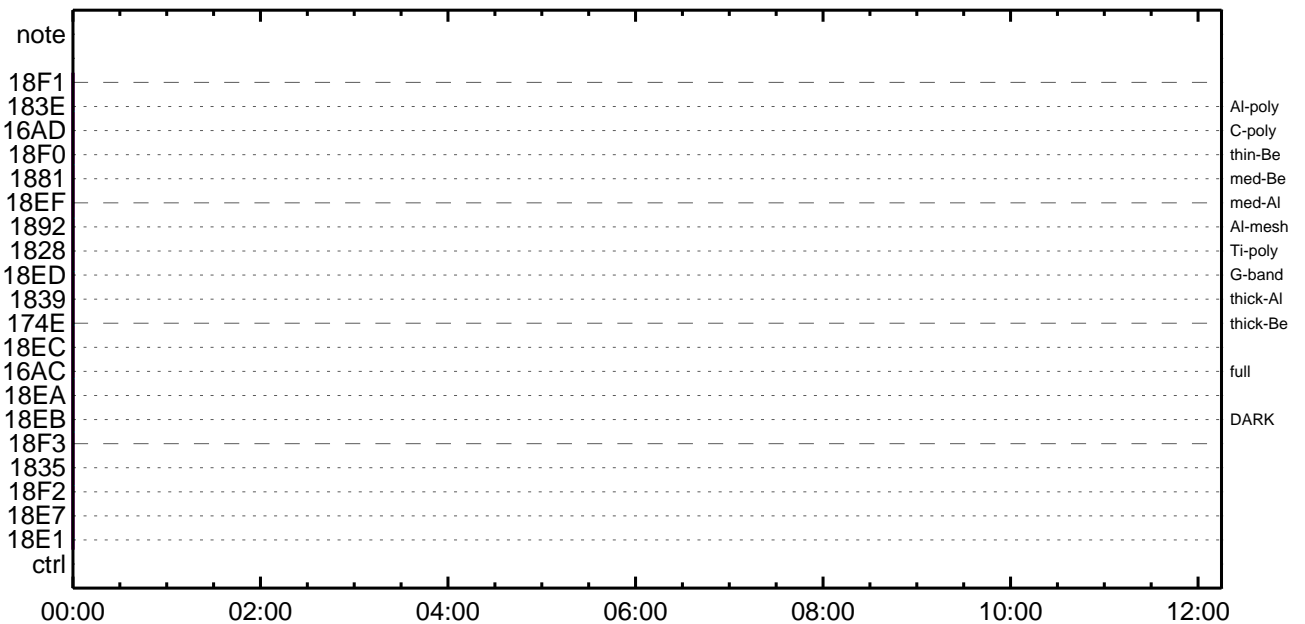
CMDI #0668 2012/06/26



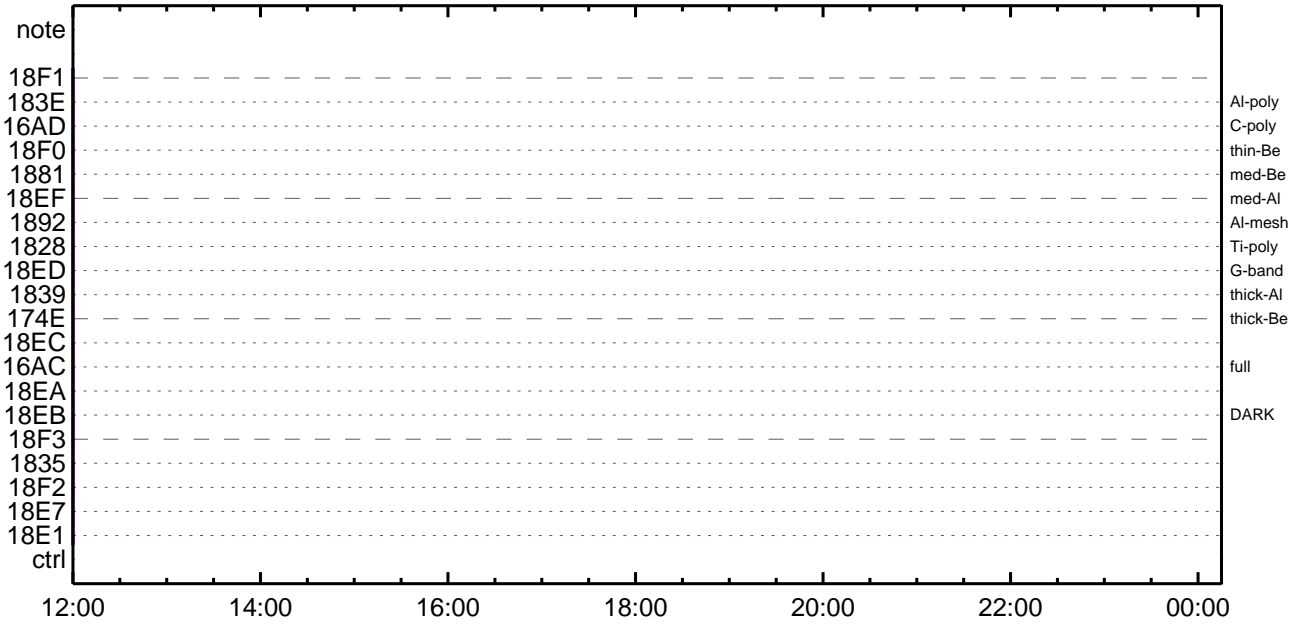
CMDI #0668 2012/06/26



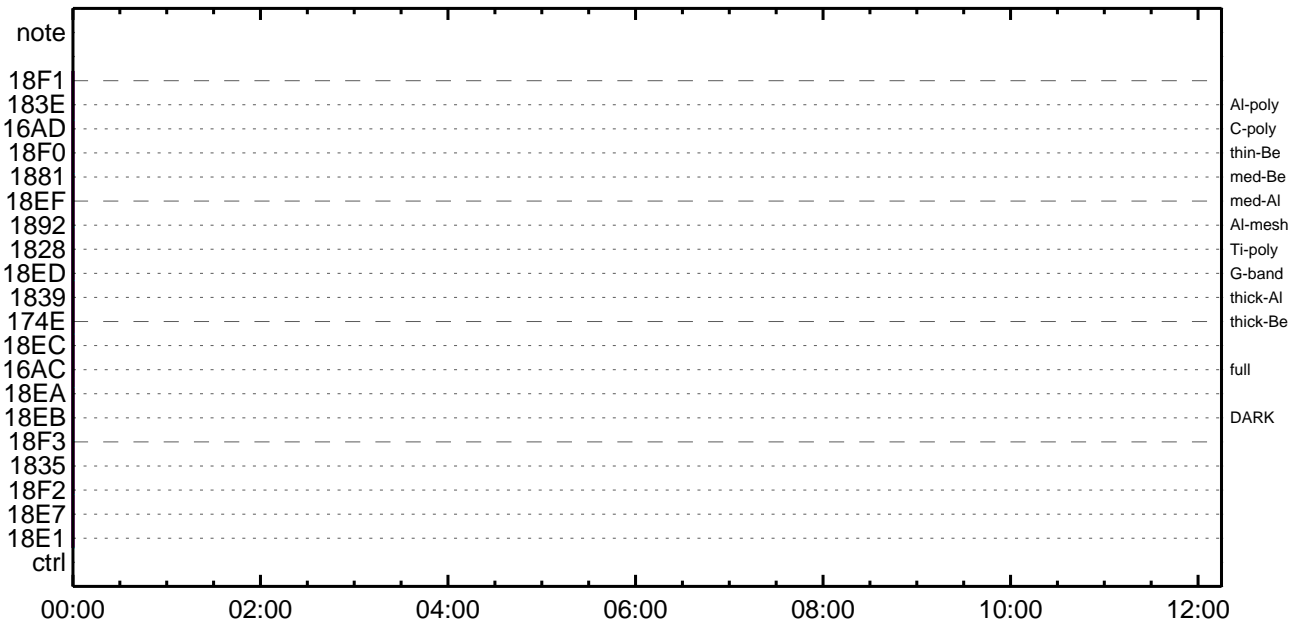
CMDI #0668 2012/06/27



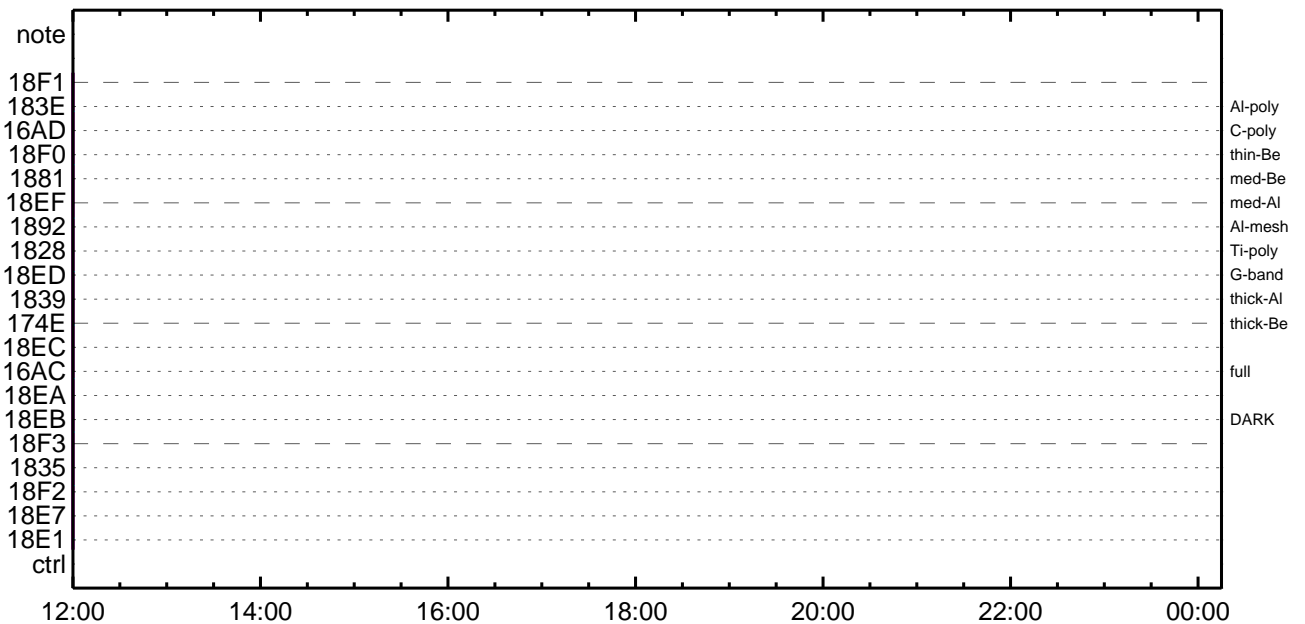
CMDI #0668 2012/06/27



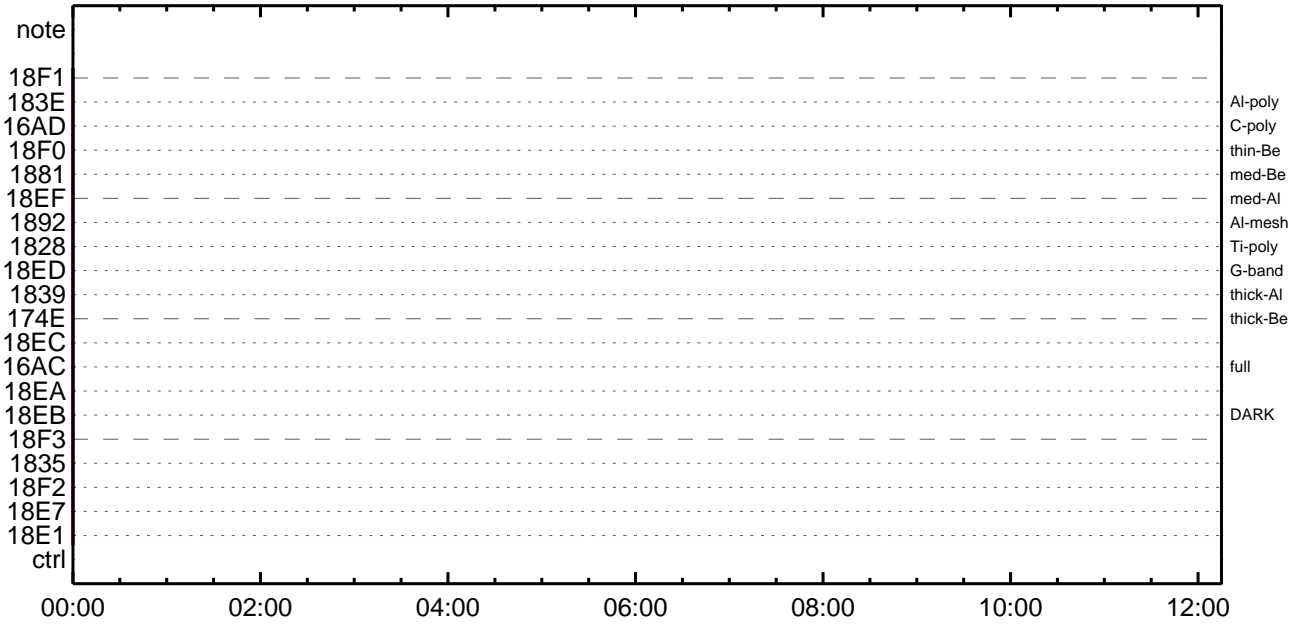
CMDI #0668 2012/06/28



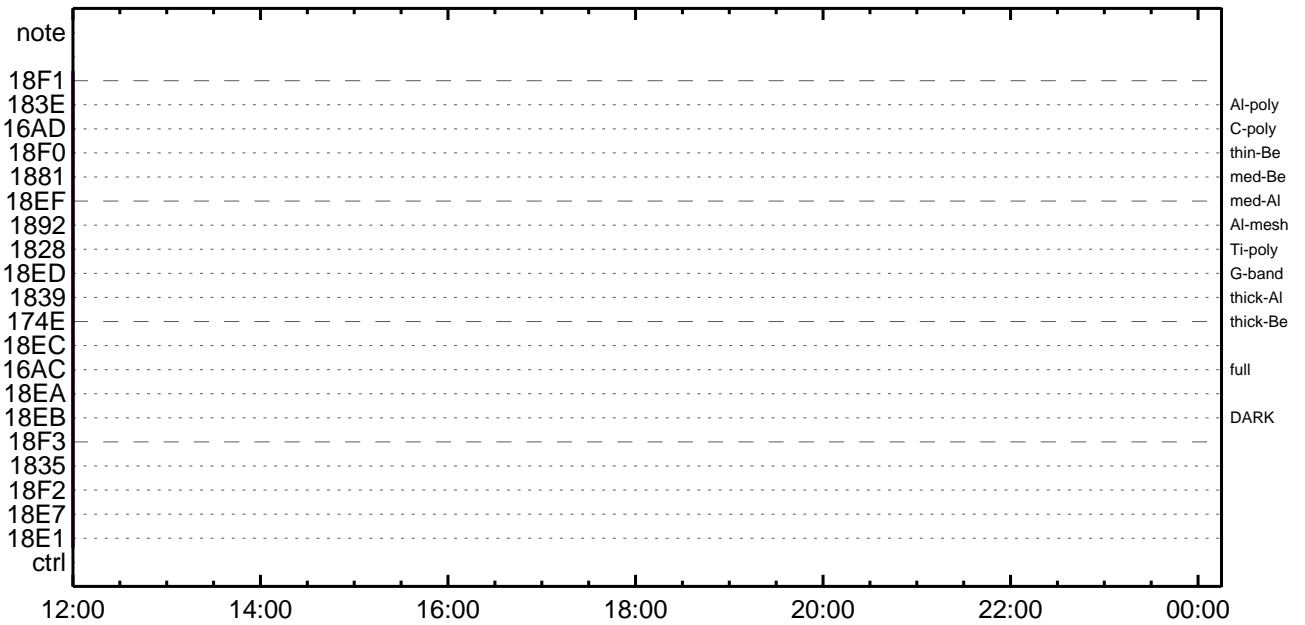
CMDI #0668 2012/06/28



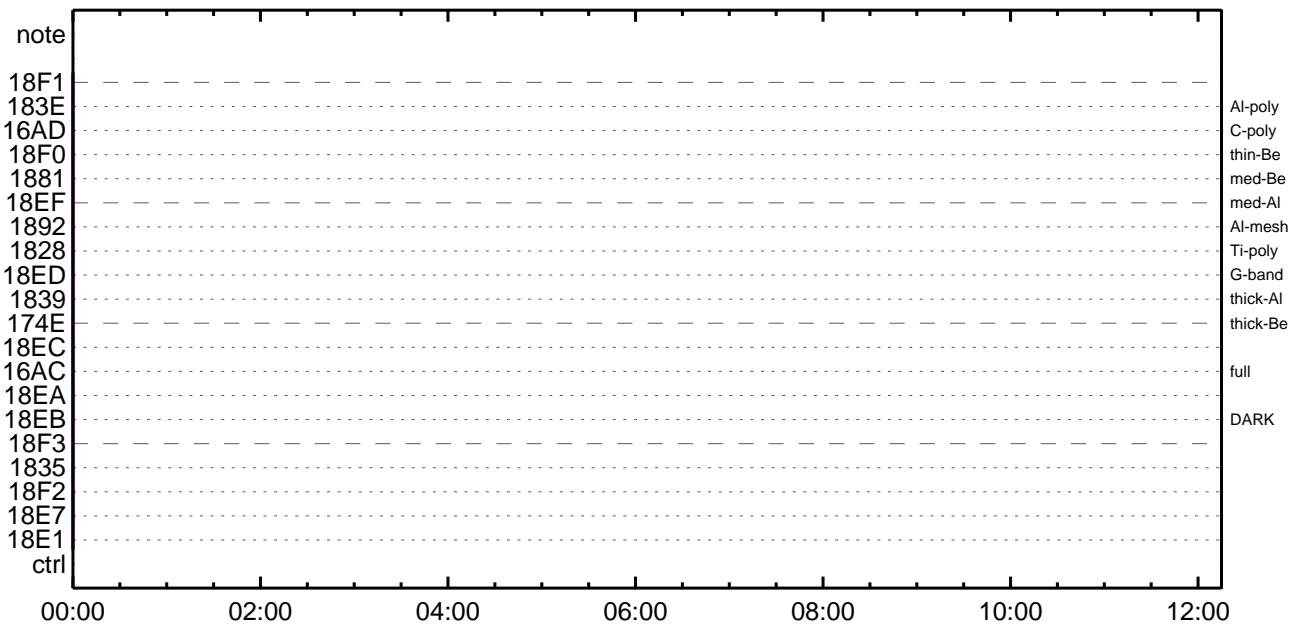
CMDI #0668 2012/06/29



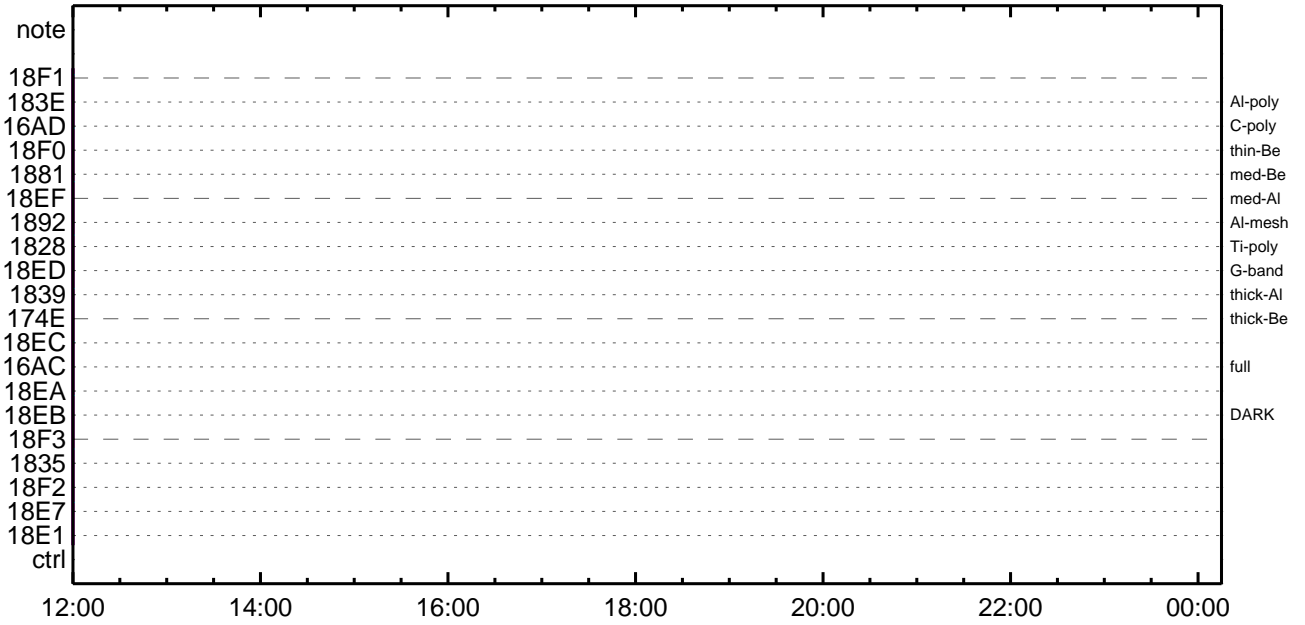
CMDI #0668 2012/06/29



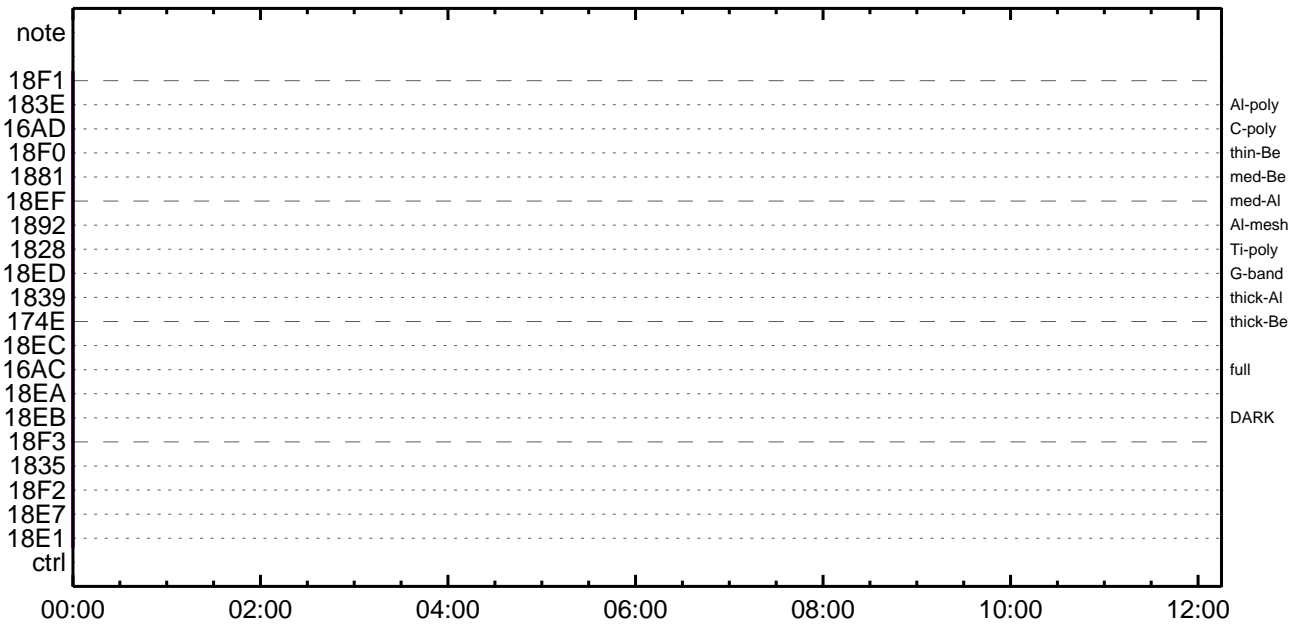
CMDI #0668 2012/06/30



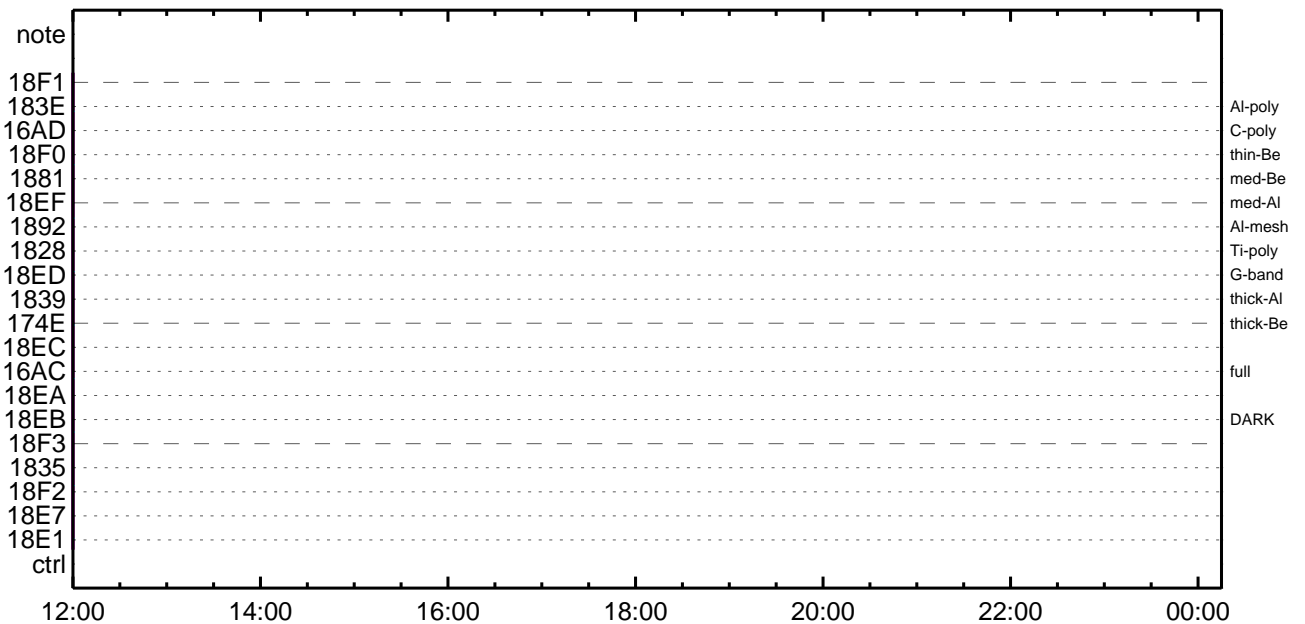
CMDI #0668 2012/06/30



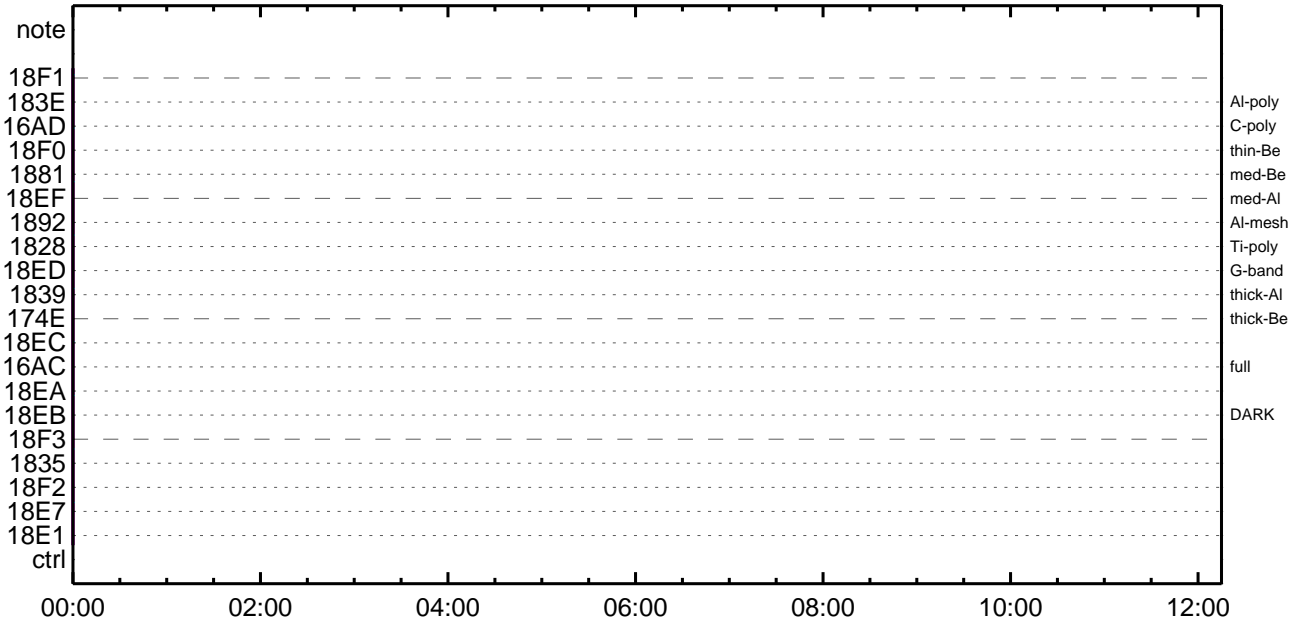
CMDI #0668 2012/07/01



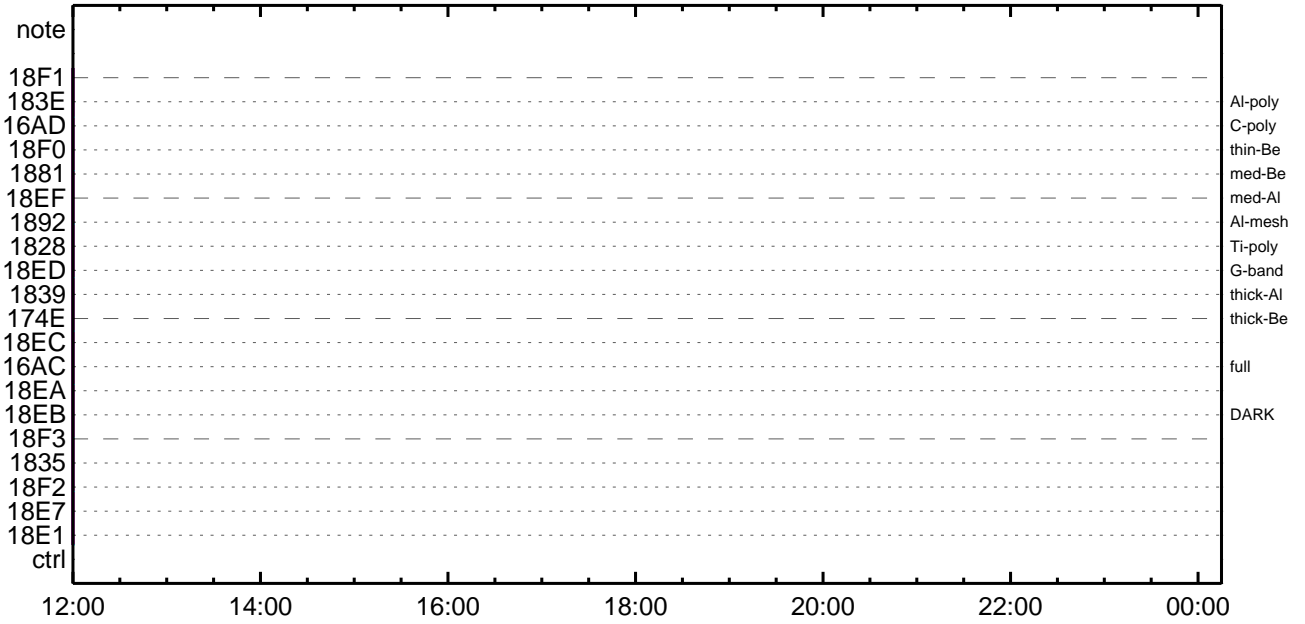
CMDI #0668 2012/07/01



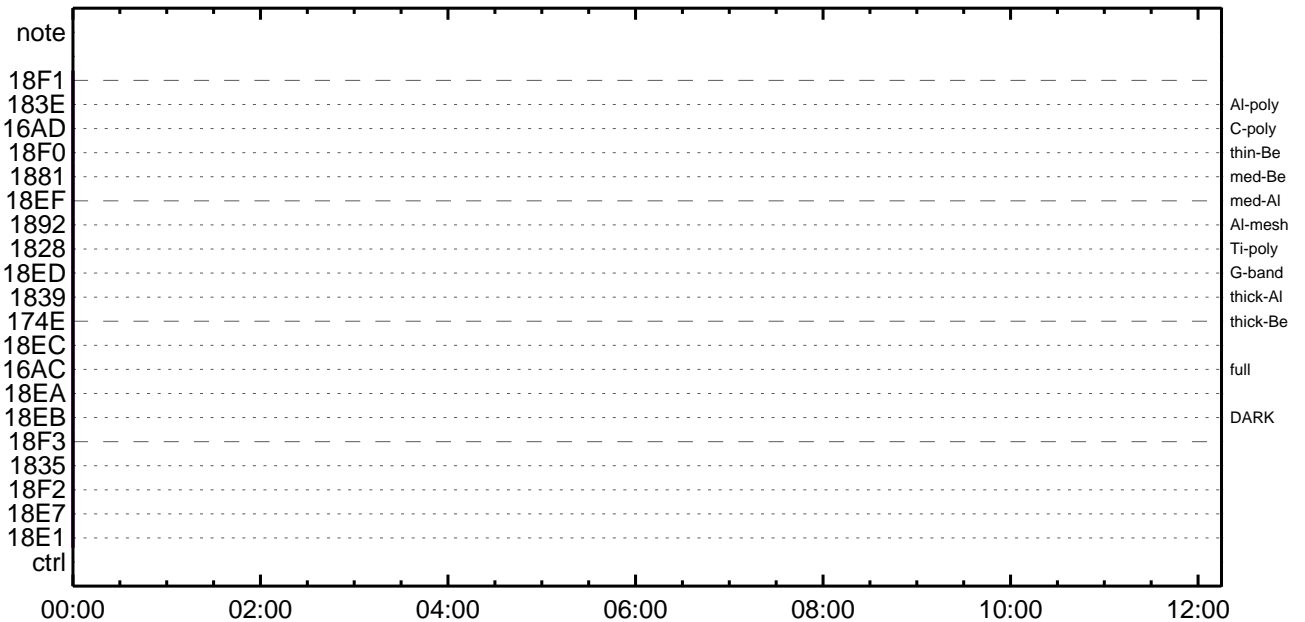
CMDI #0668 2012/07/02



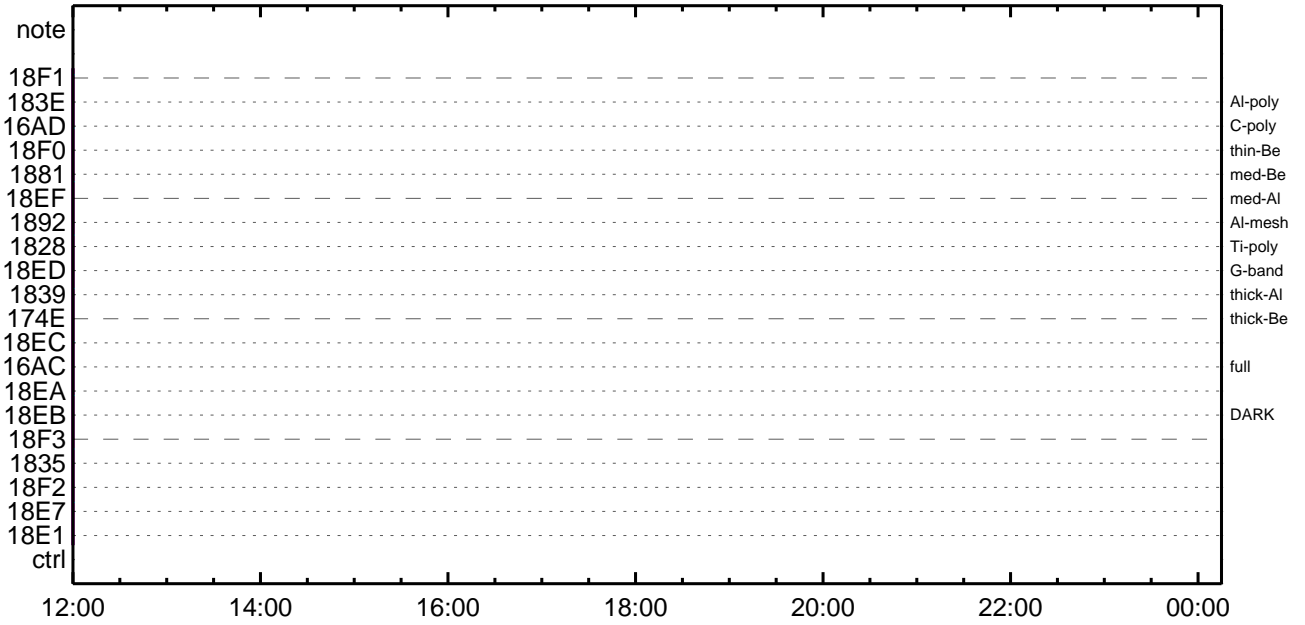
CMDI #0668 2012/07/02



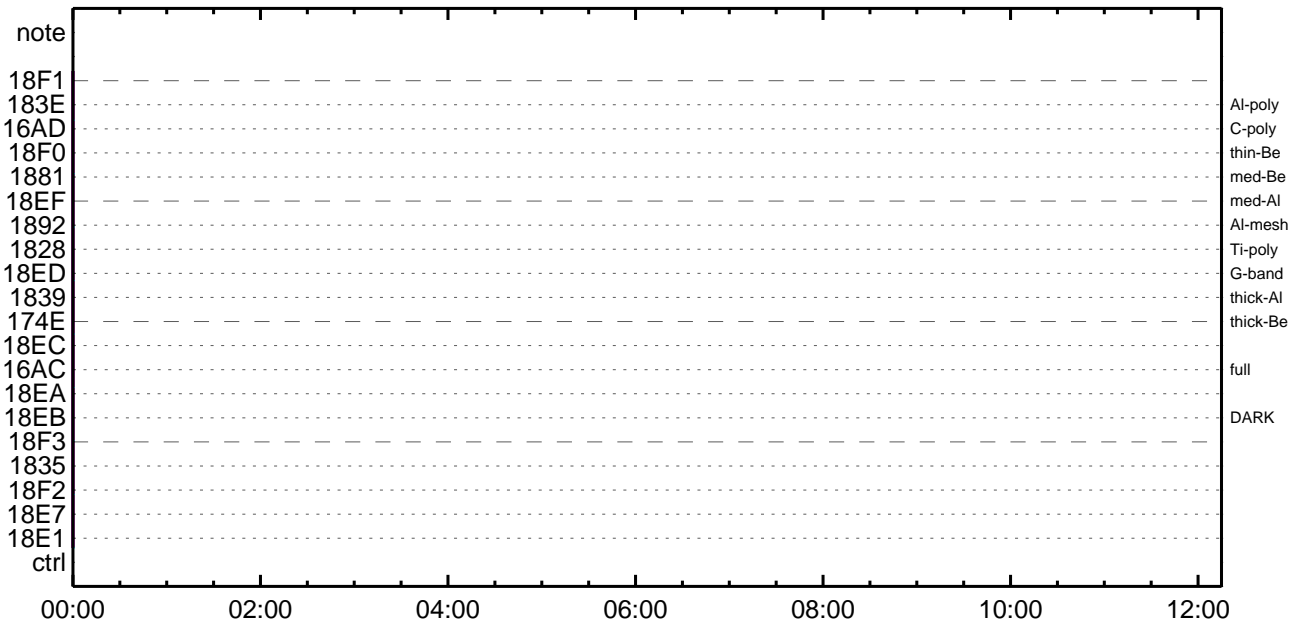
CMDI #0668 2012/07/03



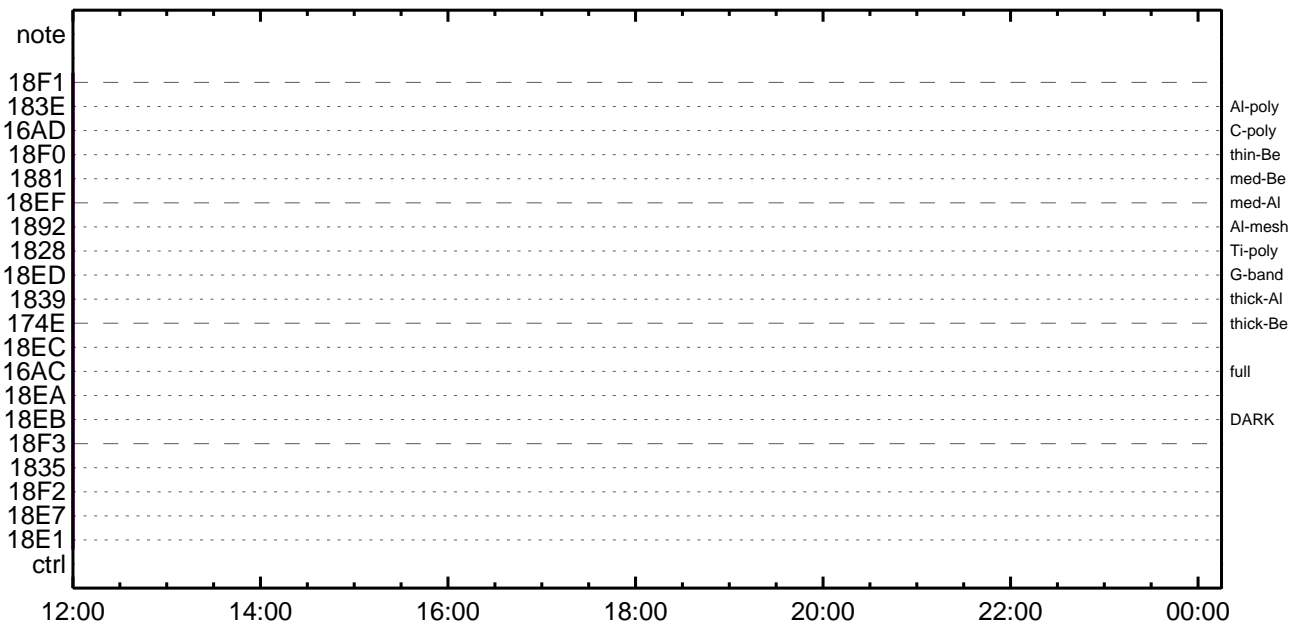
CMDI #0668 2012/07/03



CMDI #0668 2012/07/04

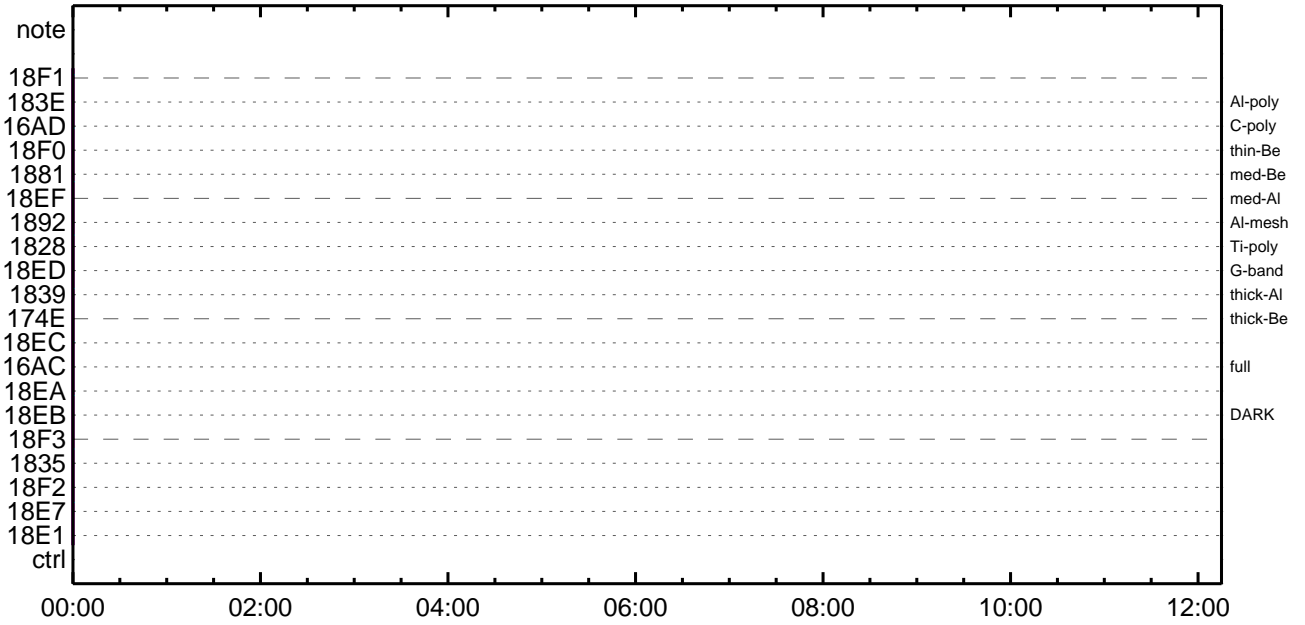


CMDI #0668 2012/07/04

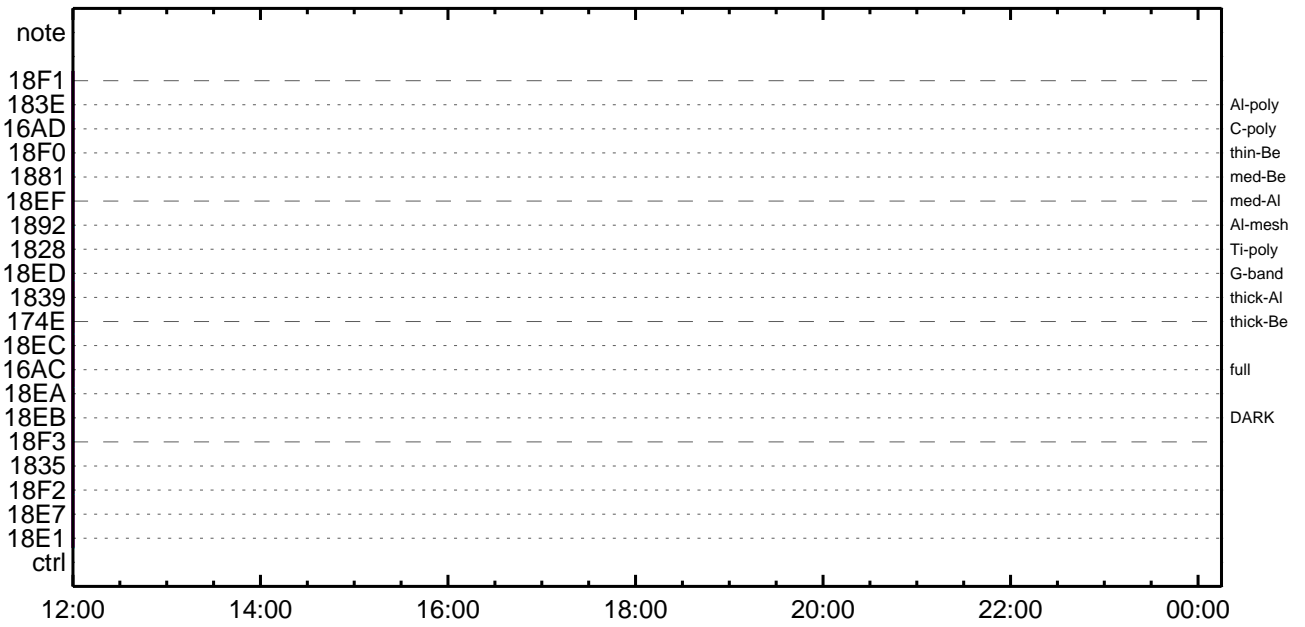




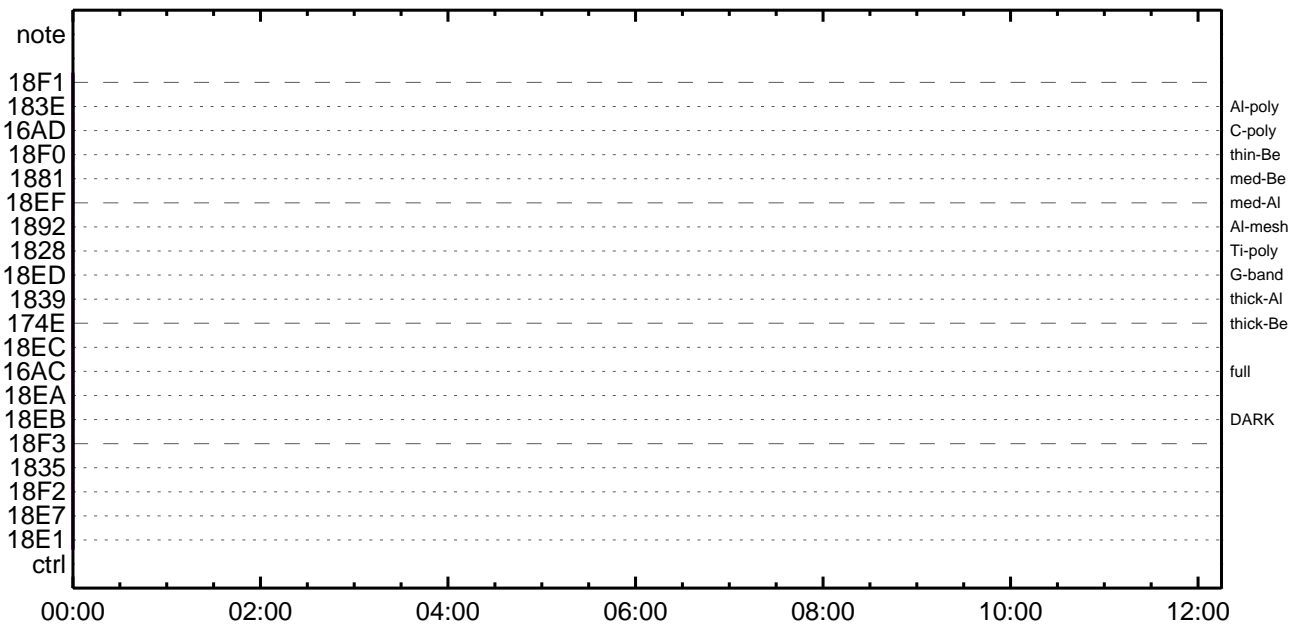
CMDI #0668 2012/07/05



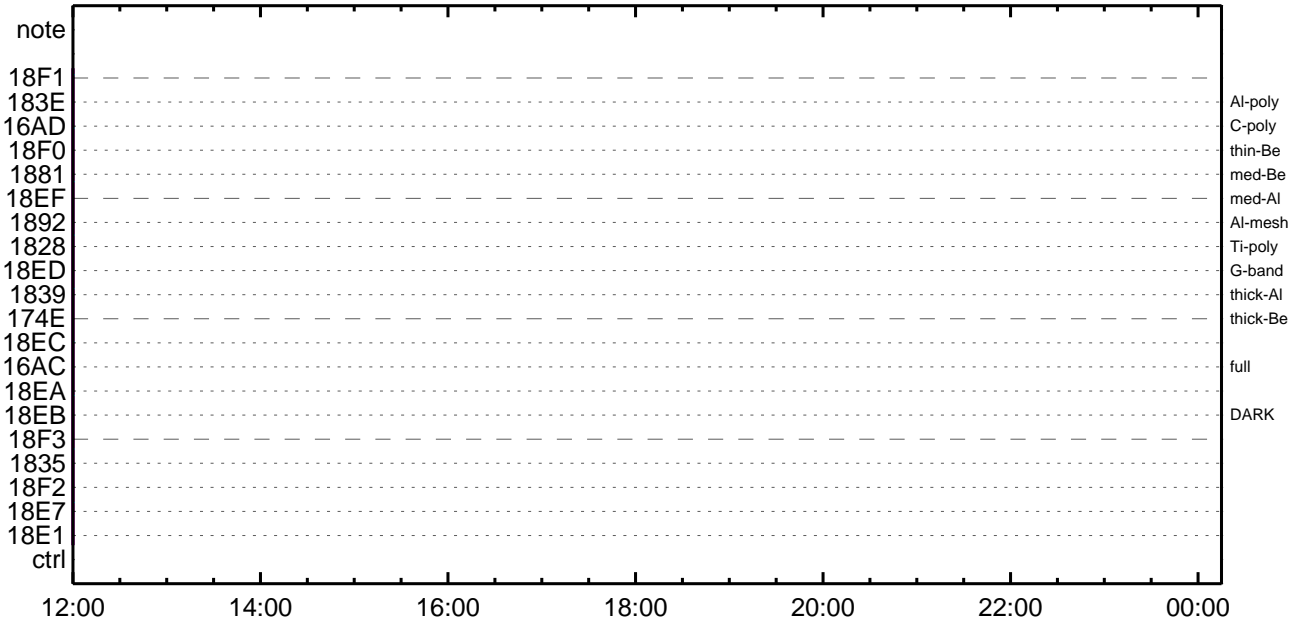
CMDI #0668 2012/07/05



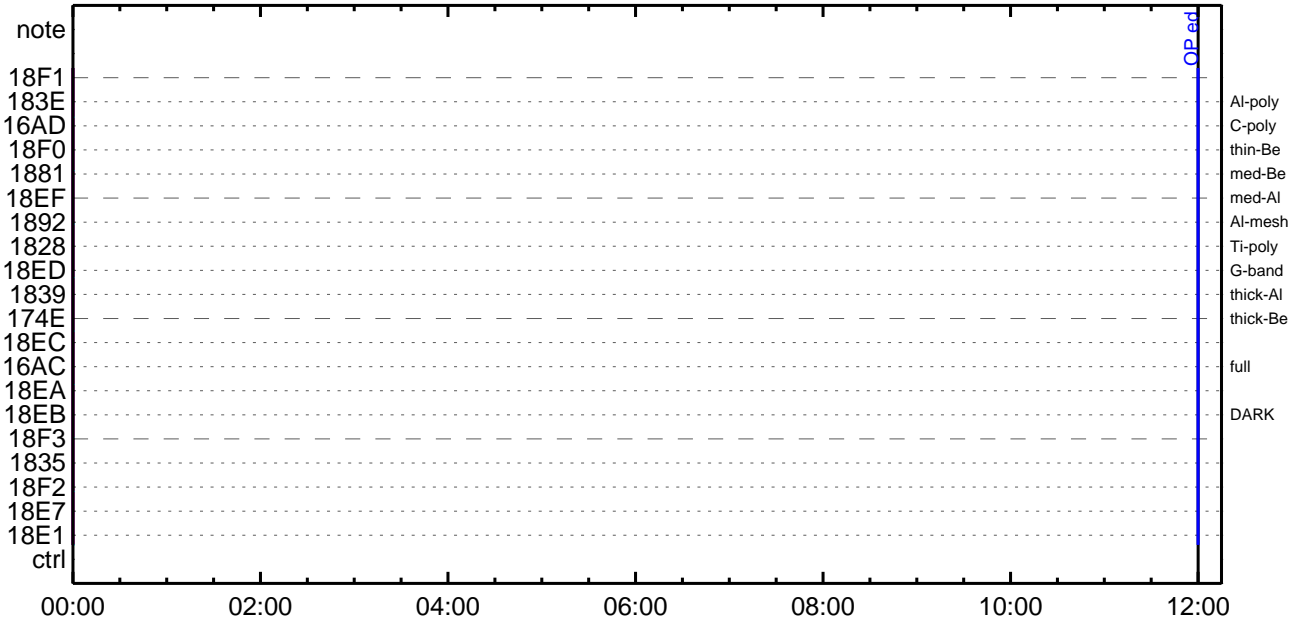
CMDI #0668 2012/07/06



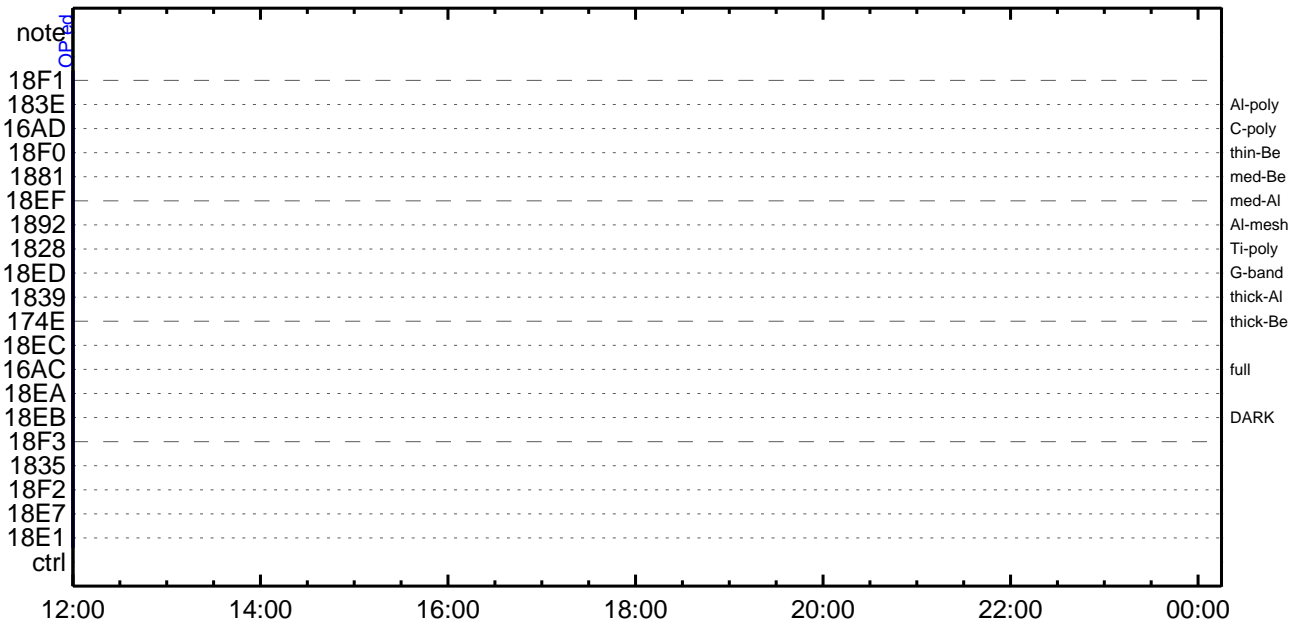
CMDI #0668 2012/07/06



CMDI #0668 2012/07/07



CMDI #0668 2012/07/07





0096 C.  
0097 C.  
0098 C. \*\*\*\*\*  
0099 C. OP/OGY1;4YE;|YAYOX  
0100 C. \*\*\*\*\*  
0101 C.  
0102 . C. ;aOP/OGY1;4YE;a  
0103 . S. OP op-912:OP  
0104 ( )  
0105 . S. OG og-912:OG  
0106 ( )  
0107 C.  
0108 . C. ;aNMOG&OPf^°eYAYOX;a  
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)  
0110 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0111 BC (20 00 7f 01 02)  
0112 C. ;c[HK1\_DMP\_TOP\_ADRS\_1] EQ 40  
0113 C. ;c[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0114 C. ;c[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0115 C. ;c[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0116 C. ;c[HK1\_DMA\_DMP\_PIM] EQ DHU  
0117 +. DC 01-22 DHU\_MODE\_CHNG  
0118 BC (07 0b f8)  
0119 C. ;c[HK1\_PKT\_FORM\_NO] EQ 7  
0120 C. ;c[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0121 C. ;c[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0122 C. ;c[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0123 C. ;c[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0124 . C. YAYOXx1/2^i»od^iCS  
0125 C. ;c[HK1\_DMP\_CHK\_FLG] EQ NON  
0126 . C. RAM ID=NMOGaf^E^1c.e^iOKod^iCS  
0127 C.  
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)  
0129 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0130 BC (20 80 7f 01 02)  
0131 C. ;c[HK1\_DMP\_TOP\_ADRS\_1] EQ 41  
0132 C. ;c[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0133 C. ;c[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0134 C. ;c[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0135 C. ;c[HK1\_DMA\_DMP\_PIM] EQ DHU  
0136 +. DC 01-22 DHU\_MODE\_CHNG  
0137 BC (07 0b f8)  
0138 C. ;c[HK1\_PKT\_FORM\_NO] EQ 7  
0139 C. ;c[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0140 C. ;c[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0141 C. ;c[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0142 C. ;c[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0143 . C. YAYOXx1/2^i»od^iCS  
0144 C. ;c[HK1\_DMP\_CHK\_FLG] EQ NON  
0145 . C. RAM ID=NMOGaf^E^1c.e^iOKod^iCS  
0146 C.  
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)  
0148 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0149 BC (21 00 41 01 02)  
0150 C. ;c[HK1\_DMP\_TOP\_ADRS\_1] EQ 42  
0151 C. ;c[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0152 C. ;c[HK1\_DMP\_BLOCK\_NUM] EQ 65  
0153 C. ;c[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0154 C. ;c[HK1\_DMA\_DMP\_PIM] EQ DHU  
0155 +. DC 01-22 DHU\_MODE\_CHNG  
0156 BC (07 0b f8)  
0157 C. ;c[HK1\_PKT\_FORM\_NO] EQ 7  
0158 C. ;c[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0159 C. ;c[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0160 C. ;c[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0161 C. ;c[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0162 . C. YAYOXx1/2^i»od^iCS  
0163 C. ;c[HK1\_DMP\_CHK\_FLG] EQ NON  
0164 . C. RAM ID=NMOG, RAM ID=OPaf^E^1c.e^iOKod^iCS  
0165 C.  
0166 . C. \*\*\*\*\* oE^2/4oI^A^¶A^oEE^-oA^:z@ (%amu-YAYOXx1/2e^1/2caDAOAcC^1a^o^e%1^1c^ca) \*\*\*\*\*  
0167 C. DHUYa;4YE;E^1/2Y;Y;1;4YE;EodIa^1  
0168 +. DC 01-22 DHU\_MODE\_CHNG  
0169 BC (02 0a f8)  
0170 C. ;c[HK1\_PKT\_FORM\_NO] EQ 2  
0171 C. ;c[HK1\_PKT\_GEN\_TIME] EQ 0.5S  
0172 C. ;c[HK1\_S\_TLM\_BIT\_RATE] EQ 32K  
0173 C. ;c[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0174 C.  
0175 . C. \*\*\*\*\*  
0176 C. TI-CMD SET (OPOG STOP/COPY/START)  
0177 C. \*\*\*\*\*  
0178 C.  
0179 . C. NOTICE ;s OPOG UPLOADa-A^:z@NGuI^1^1c;^oE^2/4oI^TI-CMDA^:z@oI^A^1Oa.oEoaa^3oE;f  
0180 C. oEo;^cSEToEDUMPaI^E^1oY^1oC^1Oa|o^3oE;f  
0181 C.  
0182 . C. TIY^3Y^1/2Y^EodA^I^z(UT)  
0183 +. TI 2012-06-02 10:32:00.0  
0184 DC 01-B3 DHU\_OP\_STOP  
0185 C. ;c[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0186 C.  
0187 +. TI 2012-06-02 10:32:01.0  
0188 DC 01-B4 DHU\_OP\_COPY  
0189 C. ;c[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0190 C.  
0191 +. TI 2012-06-02 10:32:01.0  
0192 DC 01-B5 DHU\_OPOG\_COPY  
0193 C. ;c[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP







```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0f 80 80 06 06)
0136 + DC 07-F0 MDP_XRT_FLD_ENA
0137 BC (d8)
0138 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0139 BC (c8)
0140 + DC 07-F0 MDP_XRT_AEC_RESET
0141 BC (d0)
0142 + DC 07-F0 MDP_XRT_ARS_DIS
0143 BC (d5)
0144 + DC 07-F0 MDP_XRT_FLD_RESET
0145 BC (da)
0146 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0147 BC (c4 13)
0148 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0149 BC (c5 02)
0150 . C. ----- Success Verify ? OK / NG ____
0151 C.
0152 C.
0153 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0154 C.
0155 +. DC 07-F0 MDP_XRT_MODE_OBSV
0156 BC (c2)
0157 +. TI 2012-06-02 10:36:02.0
0158 DC 07-F0 MDP_XRT_MODE_OBSV
0159 BC (c2)
0160 . C. ----- Success Verify ? OK / NG ____
0161 C.
0162 C. ***** XRT END *****
0163 . C. *****
0164 C. SOT table upload
0165 C. *****
0166 . C. < Stop FG table >
0167 +. DC 07-F0 MDP_FG_CTRL_MANU
0168 BC (51)
0169 . C. -----
0170 C. MDP_FG_CTRL_MODE = MANU [ ]
0171 C. -----
0172 C.
0173 . C. <Upload FG Observation Table>
0174 . S. RAM ram-266:MDP_OBS_F
0175 ( )
0176 C.
0177 . C. < Dump RAMID=MDP_OBS_F >
0178 +. DC 07-F0 MDP_DUMP_FGTBL
0179 BC (82 07 00 00 00 38 b8)
0180 C. -----
0181 C. MDP_OBS_F verify = OK/NG [ ]
0182 C. -----
0183 C.
0184 C. *****
0185 C. SOT TI command set
0186 C. *****
0187 C. Execute, after the success of TBL upload.
0188 +. TI 2012-06-02 10:36:18.0
0189 DC 07-F0 MDP_SOT_MODE_OBSV
0190 BC (40)
0191 . C. -----
0192 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0193 C. -----

```





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XRT\_OGLIST\_0668.chk

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\*\*\* OP Sequence for XRT \*\*\*

2012/06/02	10:38:00.5	XRT_CTRL_MANU_429_OG [0x1ad]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/02	10:38:10.5	XRT_FOCUS_RECALIBRATE_428_OG [0x1ac]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2012/06/02	10:42:10.5	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/06/02	10:46:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/02	10:47:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/06/02	10:49:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/06/02	10:49:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/06/02	10:49:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/06/02	10:49:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/06/02	10:49:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/06/02	10:49:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/02	10:49:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/06/02	10:49:58.0	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2012/06/02	10:50:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/02	11:56:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/02	11:56:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/02	11:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/02	11:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/02	12:26:00.0	XRT_Custom_418_OG [0x1a2]			
2012/06/02	12:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/02	13:34:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/02	13:34:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/02	13:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/02	13:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/02	14:05:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/02	14:05:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/02	14:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/02	14:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/02	14:08:30.0	XRT_Custom_418_OG [0x1a2]			
2012/06/02	14:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/02	15:13:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/02	15:13:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/02	15:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/02	15:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/02	16:00:00.0	XRT_Custom_418_OG [0x1a2]			
2012/06/02	16:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/02	16:51:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/02	16:51:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/02	16:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/02	16:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/02	17:36:30.0	XRT_Custom_418_OG [0x1a2]			
2012/06/02	17:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/02	17:53:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/02	17:53:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/06/02	17:54:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/06/02	17:54:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/06/02	17:54:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/06/02	17:54:20.0	XRT_ARS_DIS_438_OG [0x1b6]			

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2012/06/02	17:56:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2012/06/02	17:57:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/02	18:03:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/02	18:04:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00
2012/06/02	18:06:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/06/02	18:06:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/06/02	18:06:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/06/02	18:06:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/06/02	18:06:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/06/02	18:06:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/02	18:06:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/06/02	18:06:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2012/06/02	18:07:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/02	18:30:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/02	18:30:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/02	18:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/02	18:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/02	19:13:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/02	19:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/02	20:08:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/02	20:08:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/02	20:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/02	20:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/02	20:51:00.0	XRT_Custom_418_OG [0x1a2]				
2012/06/02	20:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/02	21:47:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/02	21:47:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/02	21:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/02	21:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/02	22:26:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/02	22:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/02	23:25:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/02	23:25:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/02	23:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/02	23:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/02	23:55:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/02	23:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/03	01:04:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/03	01:04:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/03	01:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/03	01:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/03	01:34:00.0	XRT_Custom_418_OG [0x1a2]				
2012/06/03	01:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/03	02:41:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/03	02:41:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/03	02:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/03	02:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/03	03:12:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/03	03:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2012/06/03	04:10:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	04:10:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/03	04:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/03	04:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/03	04:51:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/03	04:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/03	05:51:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	05:51:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/03	05:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/03	05:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/03	06:29:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/03	06:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/03	06:34:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	06:34:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/06/03	06:35:00.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/06/03	06:35:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/06/03	06:35:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/06/03	06:35:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/03	06:37:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2012/06/03	06:38:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/03	06:44:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	06:45:00.0	AOCS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/06/03	06:47:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/06/03	06:47:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/06/03	06:47:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/06/03	06:47:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/06/03	06:47:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/03	06:47:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/03	06:47:56.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/06/03	06:47:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2012/06/03	06:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/03	07:31:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	07:31:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/03	07:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/03	07:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/03	08:08:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/03	08:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/03	09:11:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	09:11:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/03	09:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/03	09:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/03	09:46:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/03	09:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/03	10:55:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	10:55:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/03	10:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/03	10:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/03	11:25:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/03	11:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2012/06/03	12:33:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	12:33:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/03	12:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/03	12:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/03	13:03:30.5	XRT_Custom_418_OG [0x1a2]							
2012/06/03	13:04:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/03	14:12:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	14:12:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/03	14:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/03	14:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/03	14:49:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/03	14:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/03	15:50:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	15:50:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/03	15:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/03	15:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/03	16:36:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/03	16:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/03	17:29:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	17:29:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/03	17:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/03	17:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/03	18:13:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	18:13:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/06/03	18:13:30.5	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/06/03	18:13:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/06/03	18:13:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/06/03	18:13:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/03	18:16:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2012/06/03	18:16:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/03	18:23:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	18:23:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/06/03	18:25:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/06/03	18:26:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/06/03	18:26:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/06/03	18:26:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/06/03	18:26:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/03	18:26:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/03	18:26:26.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/06/03	18:26:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2012/06/03	18:26:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/03	19:07:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	19:07:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/03	19:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/03	19:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/03	19:50:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/03	19:51:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/03	20:46:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/03	20:46:02.0	XRT_FLD_RESET_412_OG [0x19c]							

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2012/06/03	20:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/03	20:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/03	21:27:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/03	21:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/03	22:24:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/03	22:24:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/03	22:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/03	22:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/03	23:02:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/03	23:03:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/04	00:03:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/04	00:03:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/04	00:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/04	00:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/04	00:33:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/04	00:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/04	01:41:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/04	01:41:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/04	01:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/04	01:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/04	02:11:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/04	02:12:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/04	03:18:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/04	03:18:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/04	03:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/04	03:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/04	03:50:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/04	03:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/04	04:48:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/04	04:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/04	04:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/04	04:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/04	05:28:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/04	05:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/04	05:54:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/04	05:54:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/06/04	05:55:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2012/06/04	05:55:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/06/04	05:55:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/06/04	05:55:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/06/04	05:57:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2012/06/04	05:58:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/04	06:04:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/04	06:07:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/06/04	06:07:46.0	XRT_FLD_ENA_405_OG [0x195]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/06/04	06:07:48.0	XRT_FLRCTRL_ENA_440_OG [0x1b8]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/06/04	06:07:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/06/04	06:07:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/06/04	06:07:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	

2012/06/04	06:07:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e				
2012/06/04	06:07:58.0	XRT_FL_PROG_SET_414_OG [0x19e]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02				
2012/06/04	06:08:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/06/04	06:29:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/06/04	06:29:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/06/04	06:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/06/04	06:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/06/04	07:07:00.0	XRT_Custom_418_OG [0x1a2]								
2012/06/04	07:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/06/04	08:09:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/06/04	08:09:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/06/04	08:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/06/04	08:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/06/04	08:45:30.0	XRT_Custom_418_OG [0x1a2]								
2012/06/04	08:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/06/04	09:49:30.5	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/06/04	09:49:32.5	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/06/04	09:49:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/06/04	09:52:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/06/04	10:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/06/04	10:10:00.0	AOCs_Or-point_Start_1_OG [0x097]								
		AOCU_NM	5	02-76	03	00 00 00 00				
2012/06/04	10:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]								
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00				
2012/06/04	10:12:46.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2012/06/04	10:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2012/06/04	10:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2012/06/04	10:12:52.0	XRT_ARS_DIS_431_OG [0x1af]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/06/04	10:12:54.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/06/04	10:12:56.0	XRT_QT_PROG_SET_415_OG [0x19f]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13				
2012/06/04	10:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02				
2012/06/04	10:24:00.0	XRT_Custom_418_OG [0x1a2]								
2012/06/04	10:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/06/04	11:32:30.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/06/04	11:32:32.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/06/04	11:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/06/04	11:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/06/04	12:02:30.0	XRT_Custom_418_OG [0x1a2]								
2012/06/04	12:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/06/04	13:11:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/06/04	13:11:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/06/04	13:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/06/04	13:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/06/04	13:41:00.0	XRT_Custom_418_OG [0x1a2]								
2012/06/04	13:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/06/04	14:49:30.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/06/04	14:49:32.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/06/04	14:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/06/04	14:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/06/04	15:27:30.0	XRT_Custom_418_OG [0x1a2]								
2012/06/04	15:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/06/04	15:32:00.0	XRT_CTRL_MANU_408_OG [0x198]								

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2012/06/04	15:32:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/04	15:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/04	15:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/04	15:36:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/04	15:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/04	16:28:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/04	16:28:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/04	16:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/04	16:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/04	17:13:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/04	17:14:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/04	17:51:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/04	17:51:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/06/04	17:52:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
			AOCU_NM	5	02-76	00 00 00 00 00		
2012/06/04	17:52:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/06/04	17:52:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/06/04	17:52:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/06/04	17:54:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2012/06/04	17:55:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/04	18:01:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/04	18:02:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 d7 03 46 35		
			AOCU_NM	5	02-76	00 d7 03 46 35		
2012/06/04	18:04:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/06/04	18:04:46.0	XRT_FLD_ENA_405_OG [0x195]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/06/04	18:04:48.0	XRT_FLRCTRL_ENA_440_OG [0x1b8]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/06/04	18:04:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/06/04	18:04:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/06/04	18:04:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/04	18:04:56.0	XRT_QT_PROG_SET_430_OG [0x1ae]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2012/06/04	18:04:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2012/06/04	18:05:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/04	18:06:30.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/04	18:06:32.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/04	18:06:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/04	18:09:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/04	18:50:30.5	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/04	18:51:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/04	19:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/04	19:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/04	19:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/04	19:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/04	20:27:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/04	20:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/04	21:01:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/04	21:02:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00		
			AOCU_NM	5	02-76	03 00 00 00 00		
2012/06/04	21:04:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/06/04	21:04:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/06/04	21:04:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/06/04	21:04:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
			MDP_XRT_AEC_RESET	1	07-F0	d0		



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2012/06/04	21:04:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/04	21:04:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/04	21:04:56.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2012/06/04	21:04:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2012/06/04	21:05:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/04	21:23:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/04	21:23:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/04	21:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/04	21:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/04	22:04:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/04	22:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/04	23:02:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/04	23:02:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/04	23:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/04	23:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/04	23:33:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/04	23:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/05	00:40:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/05	00:40:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/05	00:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/05	00:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/05	01:10:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/05	01:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/05	02:18:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/05	02:18:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/05	02:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/05	02:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/05	02:49:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/05	02:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/05	03:46:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/05	03:46:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/05	03:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/05	03:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/05	04:27:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/05	04:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/05	05:27:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/05	05:27:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/05	05:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/05	05:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/05	06:05:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/05	06:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/05	06:11:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/05	06:11:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/06/05	06:11:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2012/06/05	06:11:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/06/05	06:11:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/06/05	06:11:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/05	06:14:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2012/06/05	06:14:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/05	06:21:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							

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2012/06/05	06:21:30.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			AOCU_NM	5	02-76	03 00 00 00 00
2012/06/05	06:23:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/06/05	06:24:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/06/05	06:24:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/06/05	06:24:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/06/05	06:24:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/06/05	06:24:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/05	06:24:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/06/05	06:24:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2012/06/05	06:24:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/05	07:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/05	07:07:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/05	07:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/05	07:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/05	07:44:00.0	XRT_Custom_418_OG [0x1a2]				
2012/06/05	07:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/05	08:47:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/05	08:47:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/05	08:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/05	08:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/05	09:22:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/05	09:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/05	10:29:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/05	10:29:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/05	10:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/05	10:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/05	10:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/05	11:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00