

# XRT Timeline to be uploaded on 2012/06/07

Period: 2012/06/07 10:27:00 - 2012/07/12 12:00:00

\* \* \* \* \*

## Normal mode

\* \* \* \* \*

| XOB #1565: CCD Monitor During Bakeout - G-band + dark - wide FOV - lower cadence (30min)   |   |     |      |       |      |     |   |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
|--|---|-----|------|-------|------|-----|---|--------------------|-------|------------|----------|--|----------------|----------------|-----|------|-------|------|-----|-----|--------------------|-------|------------|----------|
| Term   | Pointing (x, y)                         |     |      |       |      |     | Comment   |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| 06/07 10:40:00 - 06/07 14:06:54  | Track ( 0.0, -0.1) @ 06/07 10:37:00     |     |      |       |      |     | # OP start + 10min   EIS 3.5hr sensitivity monitor + SOT flat field |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| 06/07 14:10:00 - 06/08 07:59:54  | Track ( 190.2, -330.7) @ 06/07 14:07:00 |     |      |       |      |     | Track AR 11494  |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| 06/08 08:03:00 - 06/08 09:59:54  | Fixed ( 0.0, -988.0)                    |     |      |       |      |     | # HOP 200 south pole  |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| 06/08 10:03:00 - 06/09 06:19:00  | Track ( 350.0, -331.9) @ 06/08 10:00:00 |     |      |       |      |     | # Track AR 11494  |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| <b>PROG= 06 Inf.-time(s)</b>   |   |     |      |       |      |     |   |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| └─ Subr= 1 1-time(s) 1800.0sec   |   |     |      |       |      |     |   |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| └─ Seqn= 64 1-time(s) 4.0sec   |   |     |      |       |      |     |   |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| └─┬─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec   |   |     |      |       |      |     |   |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| └─┬─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec  |   |     |      |       |      |     |   |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| <table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 15%;">Default Filter</td> <td style="width: 15%;">Thicker Filter</td> <td style="width: 15%;">VLS</td> <td style="width: 10%;">mode</td> <td style="width: 10%;">image</td> <td style="width: 10%;">Exp.</td> <td style="width: 10%;">CCD</td> <td style="width: 10%;">Bin</td> <td style="width: 15%;">ROI: size (center)</td> <td style="width: 10%;">Comp.</td> <td style="width: 10%;">AEC Buffer</td> <td style="width: 10%;">Interval</td> </tr> </table> |   |     |      |       |      |     |   |                    |       |            |          |  | Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC Buffer | Interval |
| Default Filter   | Thicker Filter                          | VLS | mode | image | Exp. | CCD | Bin   | ROI: size (center) | Comp. | AEC Buffer | Interval |  |                |                |     |      |       |      |     |     |                    |       |            |          |

| XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV   |   |     |      |       |      |     |                      |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
|--|---|-----|------|-------|------|-----|----------------------|--------------------|-------|------------|----------|--|----------------|----------------|-----|------|-------|------|-----|-----|--------------------|-------|------------|----------|
| Term   | Pointing (x, y)                         |     |      |       |      |     | Comment              |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| 06/09 07:01:06 - 06/09 07:59:30  | Track ( 350.0, -331.9) @ 06/08 10:00:00 |     |      |       |      |     | # Track AR 11494     |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| 06/09 08:36:30 - 06/09 09:40:54  | Fixed ( 0.0, -988.0)                    |     |      |       |      |     | # HOP 200 south pole |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| <b>PROG= 01 Inf.-time(s)</b>   |   |     |      |       |      |     |                      |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| └─ Subr= 1 1-time(s) 600.0sec  |   |     |      |       |      |     |                      |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| └─ Seqn= 64 1-time(s) 4.0sec   |   |     |      |       |      |     |                      |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| └─┬─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec   |   |     |      |       |      |     |                      |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| └─┬─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec  |   |     |      |       |      |     |                      |                    |       |            |          |  |                |                |     |      |       |      |     |     |                    |       |            |          |
| <table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 15%;">Default Filter</td> <td style="width: 15%;">Thicker Filter</td> <td style="width: 15%;">VLS</td> <td style="width: 10%;">mode</td> <td style="width: 10%;">image</td> <td style="width: 10%;">Exp.</td> <td style="width: 10%;">CCD</td> <td style="width: 10%;">Bin</td> <td style="width: 15%;">ROI: size (center)</td> <td style="width: 10%;">Comp.</td> <td style="width: 10%;">AEC Buffer</td> <td style="width: 10%;">Interval</td> </tr> </table> |   |     |      |       |      |     |                      |                    |       |            |          |  | Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC Buffer | Interval |
| Default Filter   | Thicker Filter                          | VLS | mode | image | Exp. | CCD | Bin                  | ROI: size (center) | Comp. | AEC Buffer | Interval |  |                |                |     |      |       |      |     |     |                    |       |            |          |

\* \* \* \* \*

## Flare mode

\* \* \* \* \*

NOT USED

\* \* \* \* \*

## Active Region Search

\* \* \* \* \*

NOT USED

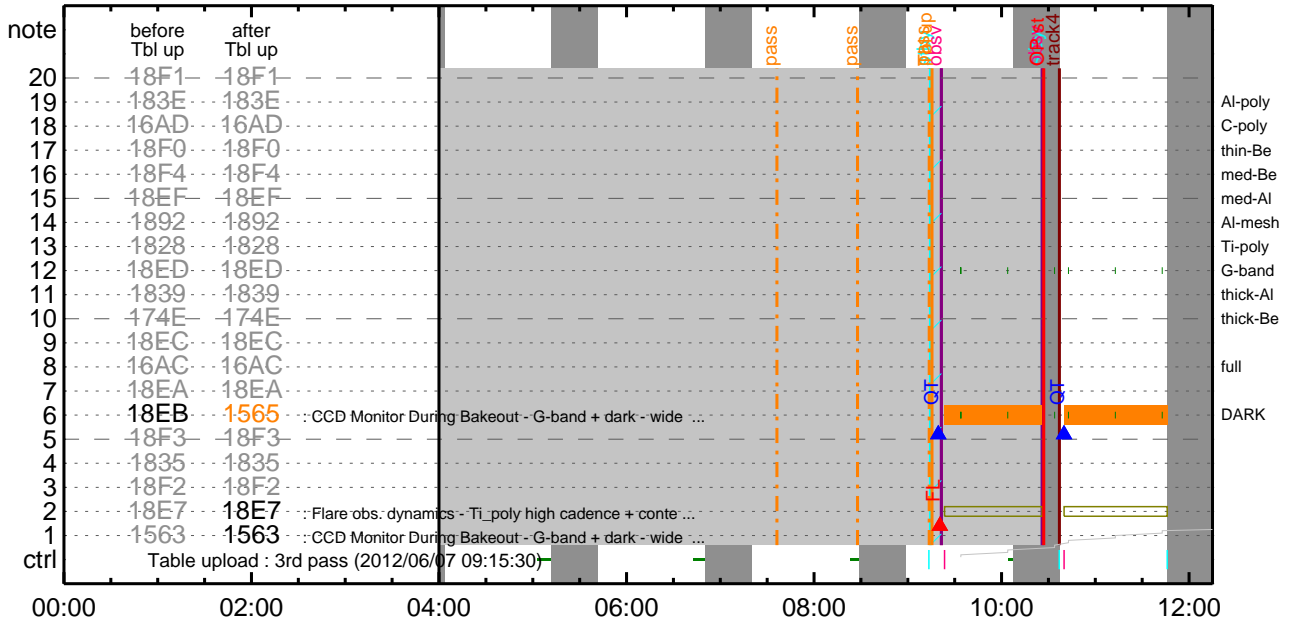
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## Flare Detection

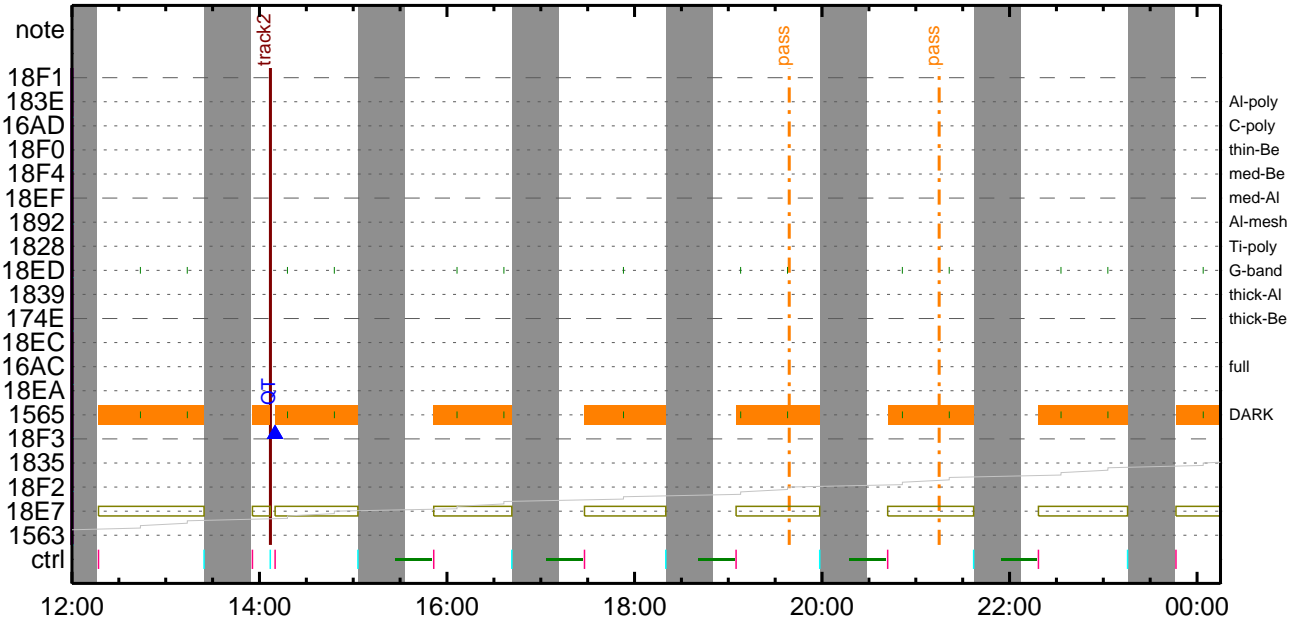
\* \* \* \* \*

NOT USED

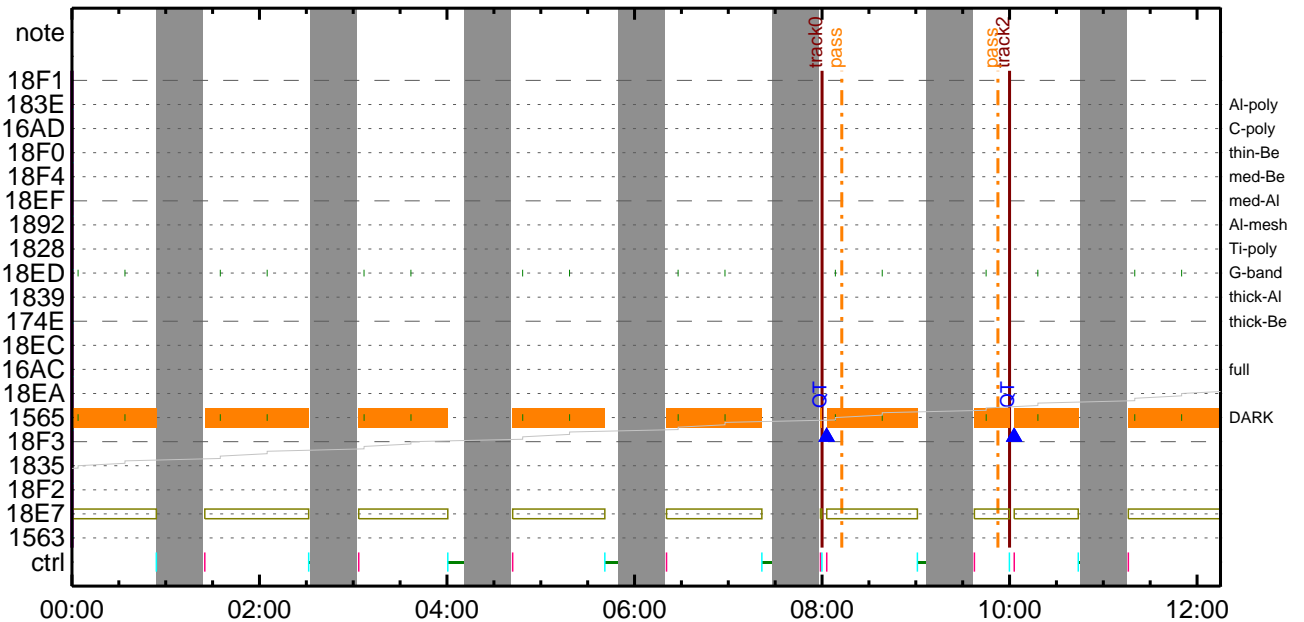
CMDI #0678 2012/06/07



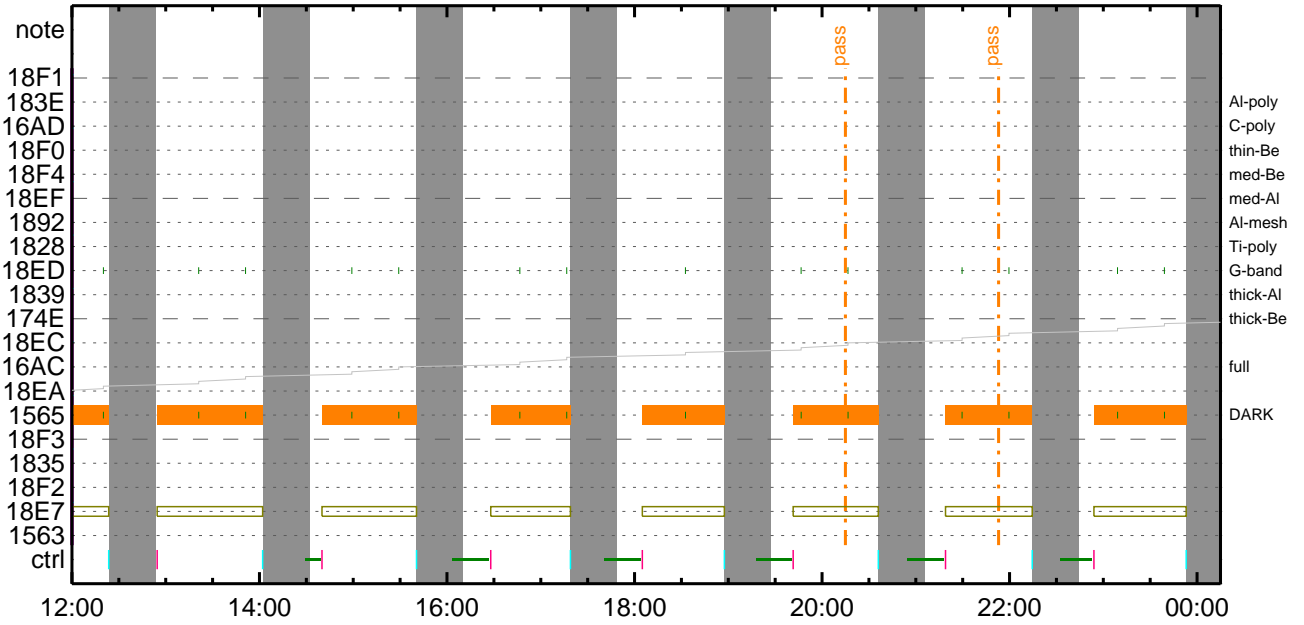
CMDI #0678 2012/06/07



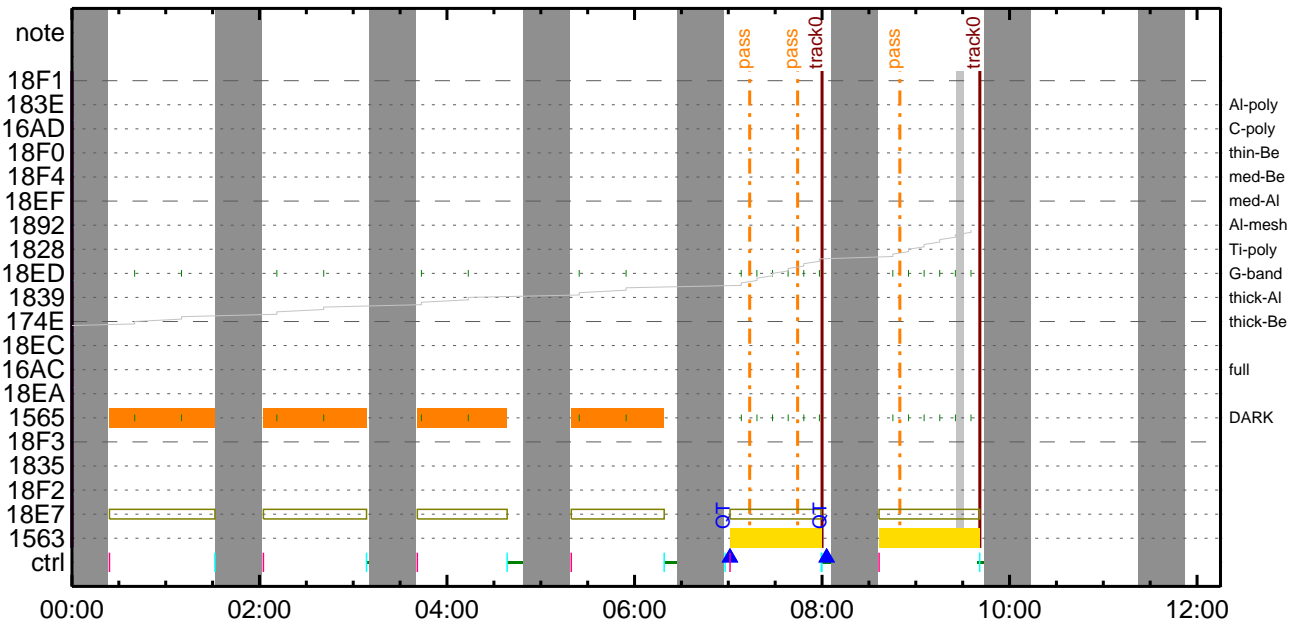
CMDI #0678 2012/06/08



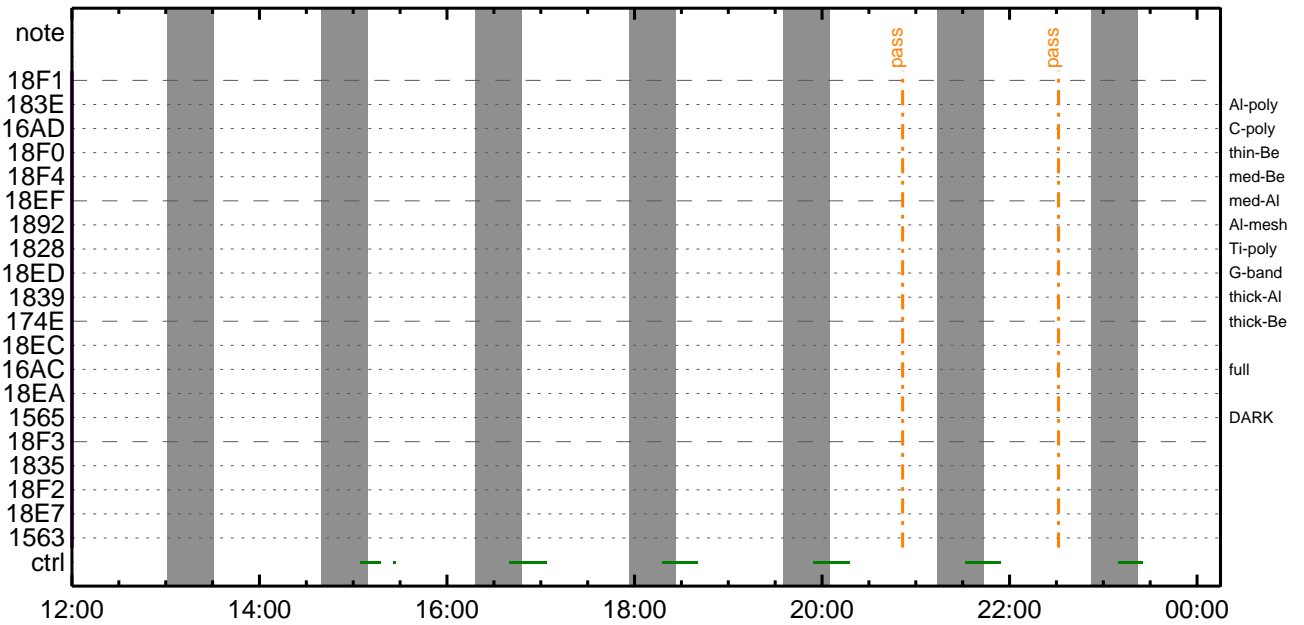
CMDI #0678 2012/06/08



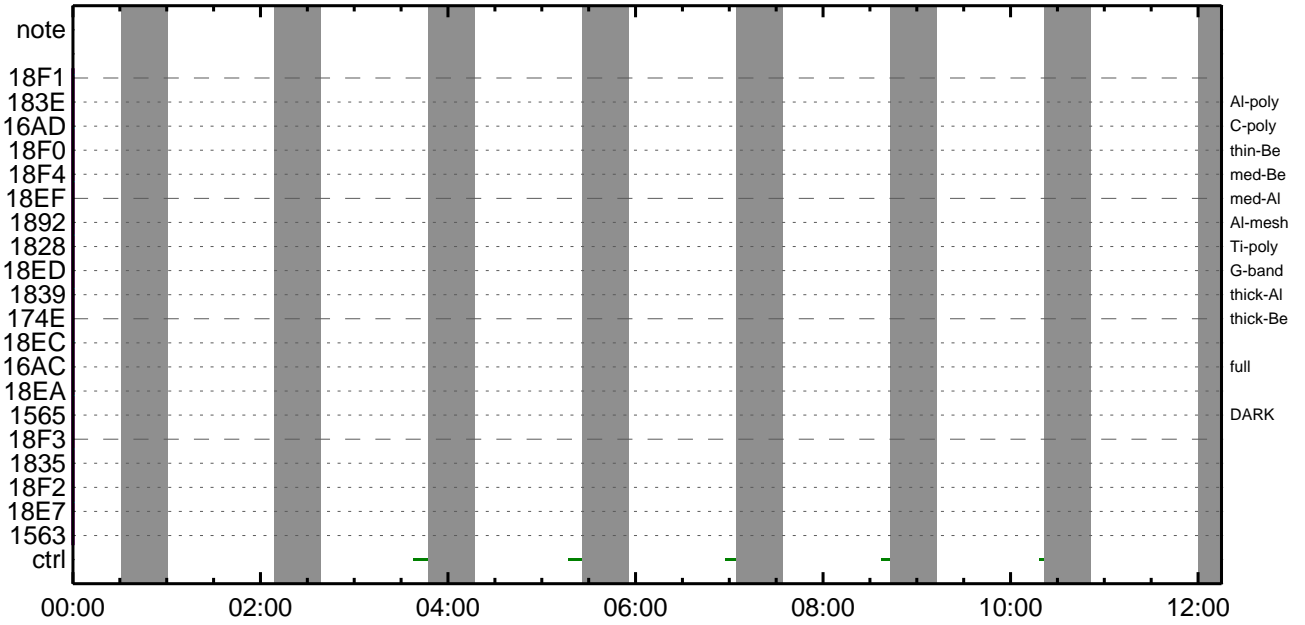
CMDI #0678 2012/06/09



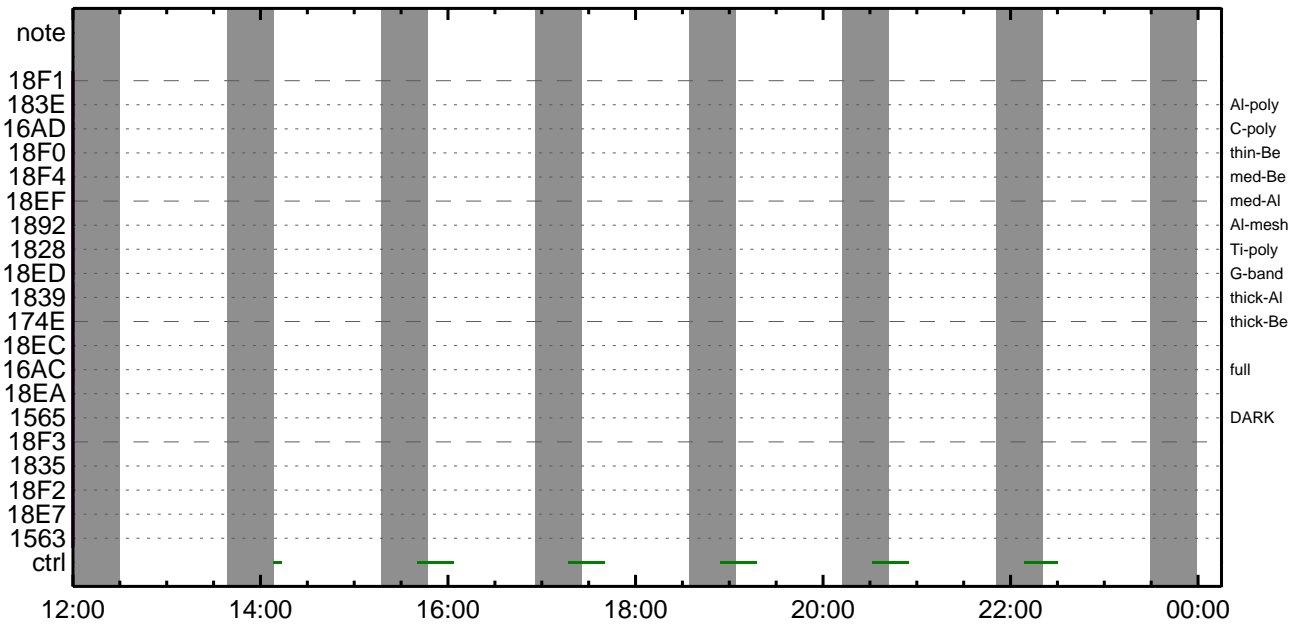
CMDI #0678 2012/06/09



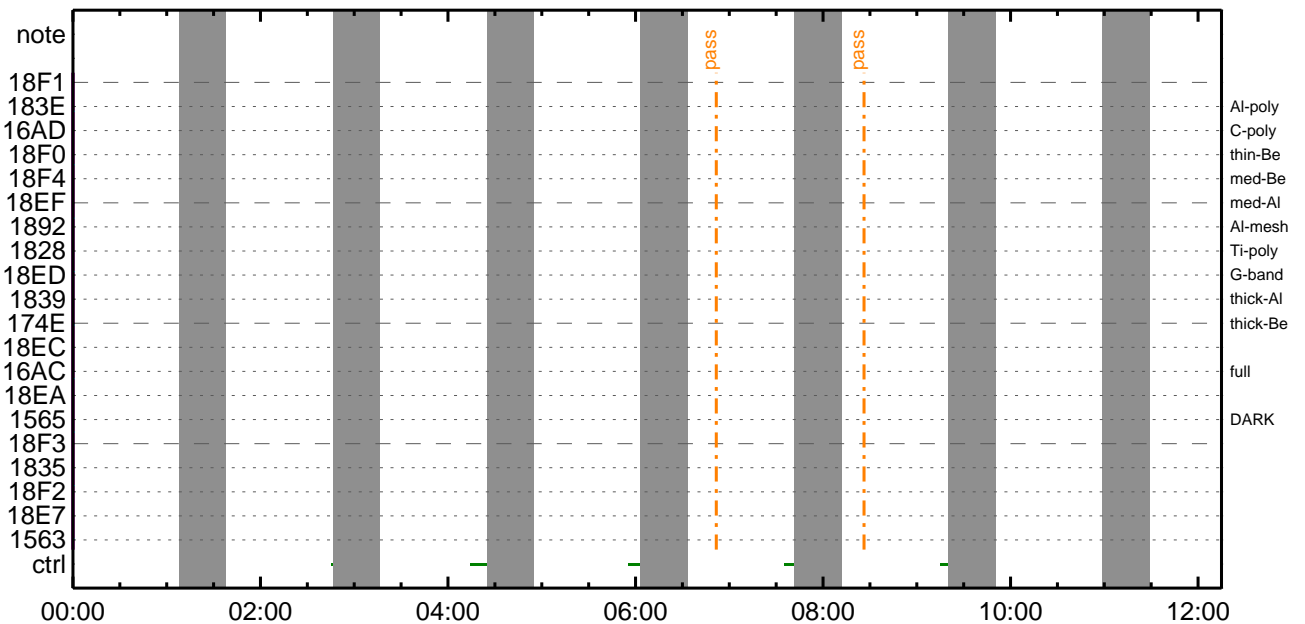
CMDI #0678 2012/06/10



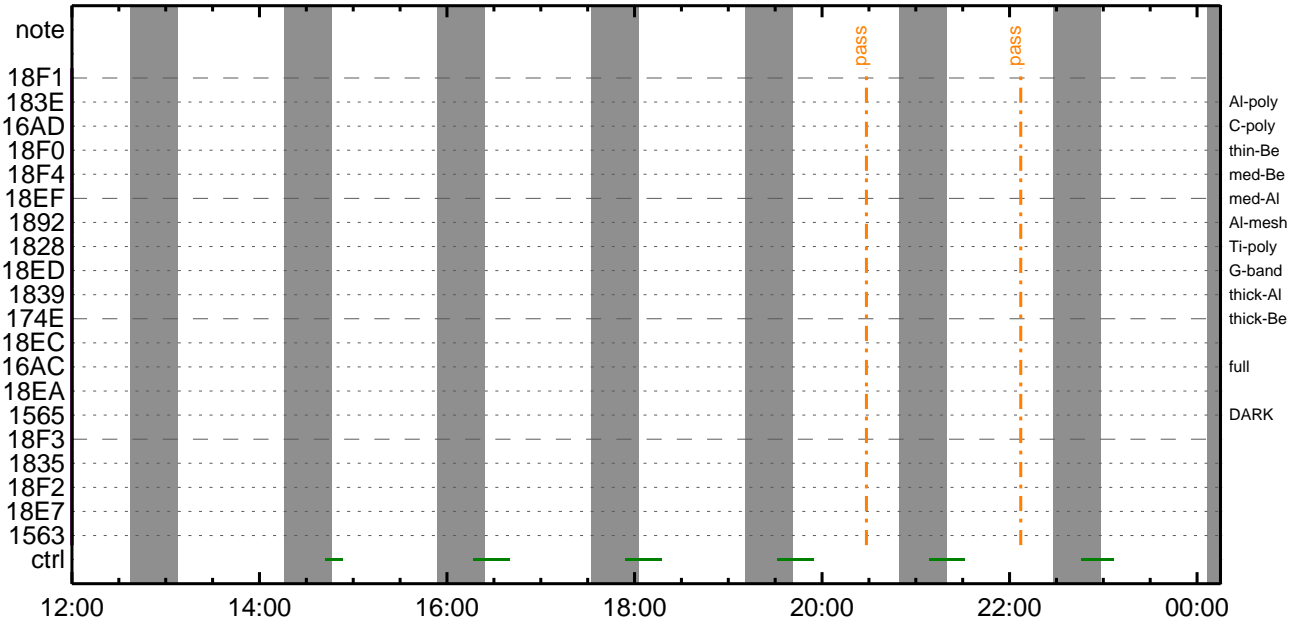
CMDI #0678 2012/06/10



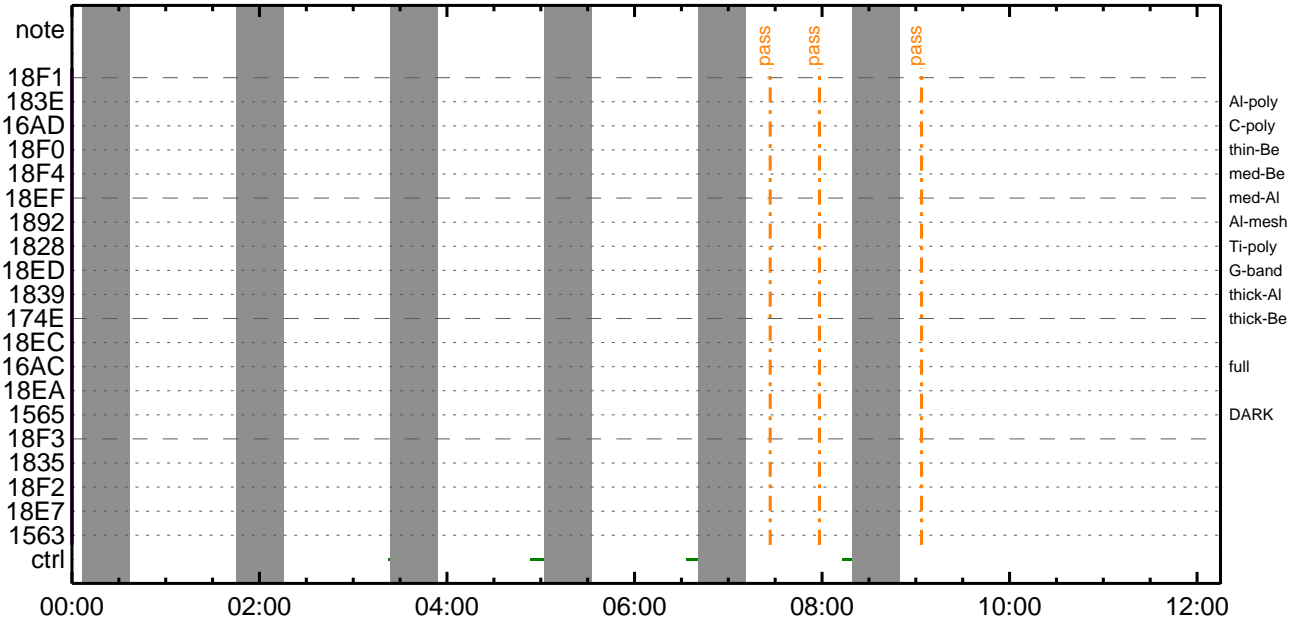
CMDI #0678 2012/06/11



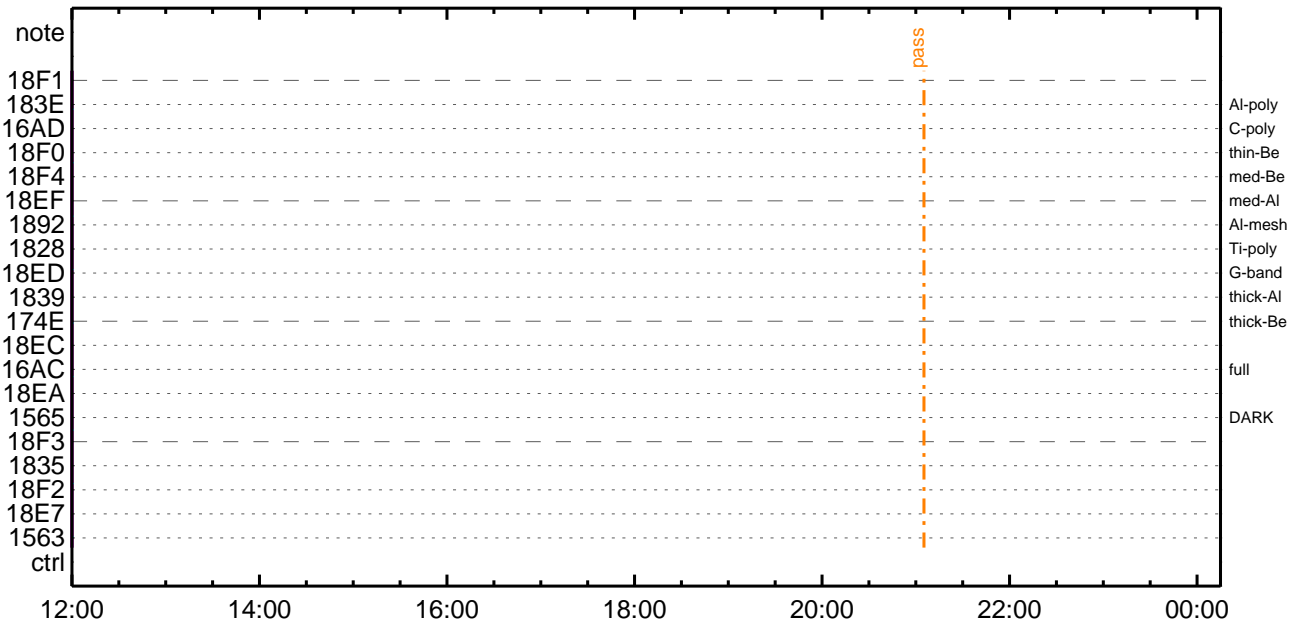
CMDI #0678 2012/06/11



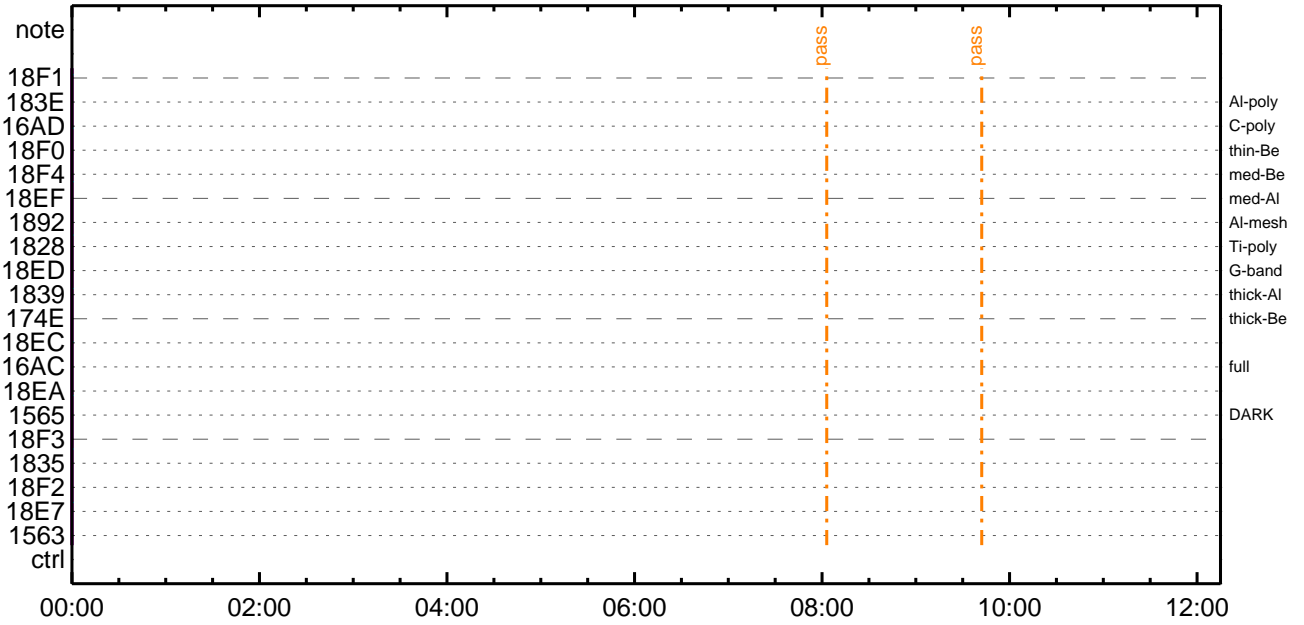
CMDI #0678 2012/06/12



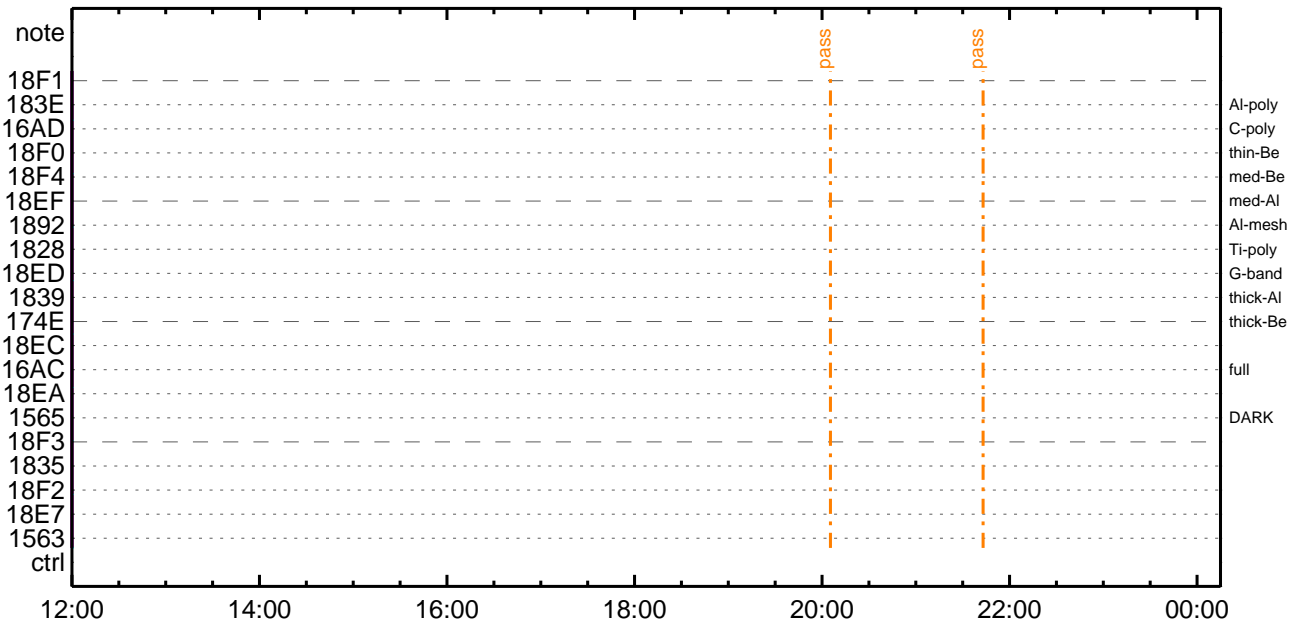
CMDI #0678 2012/06/12



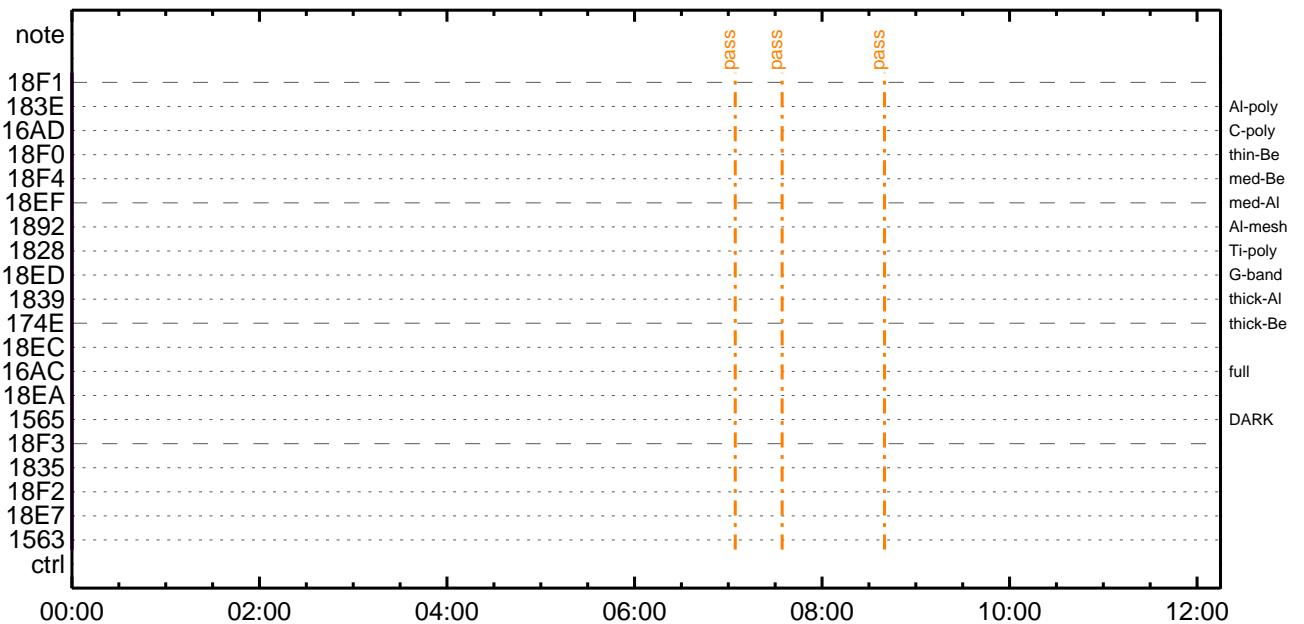
CMDI #0678 2012/06/13



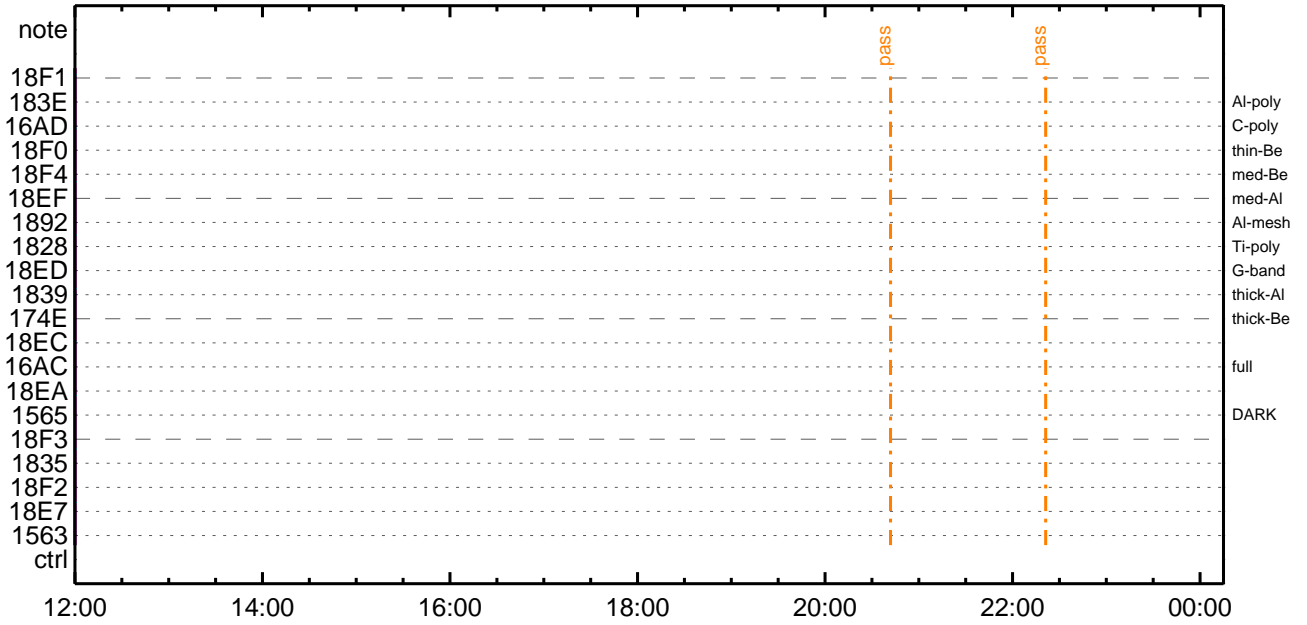
CMDI #0678 2012/06/13



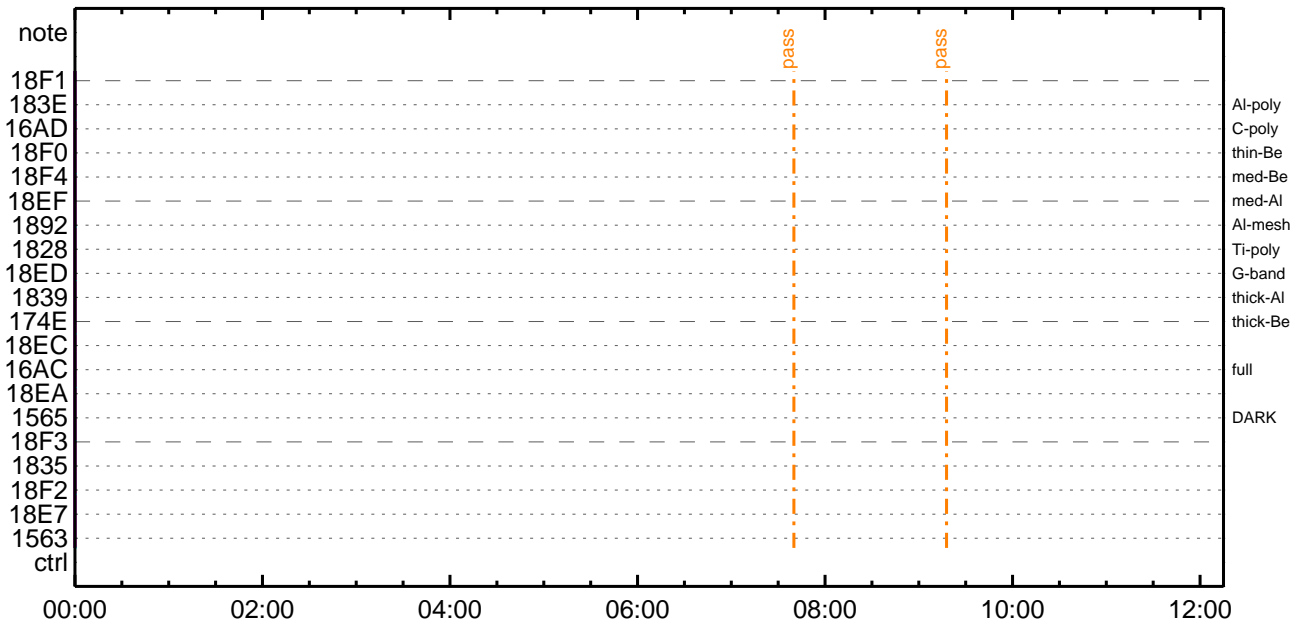
CMDI #0678 2012/06/14



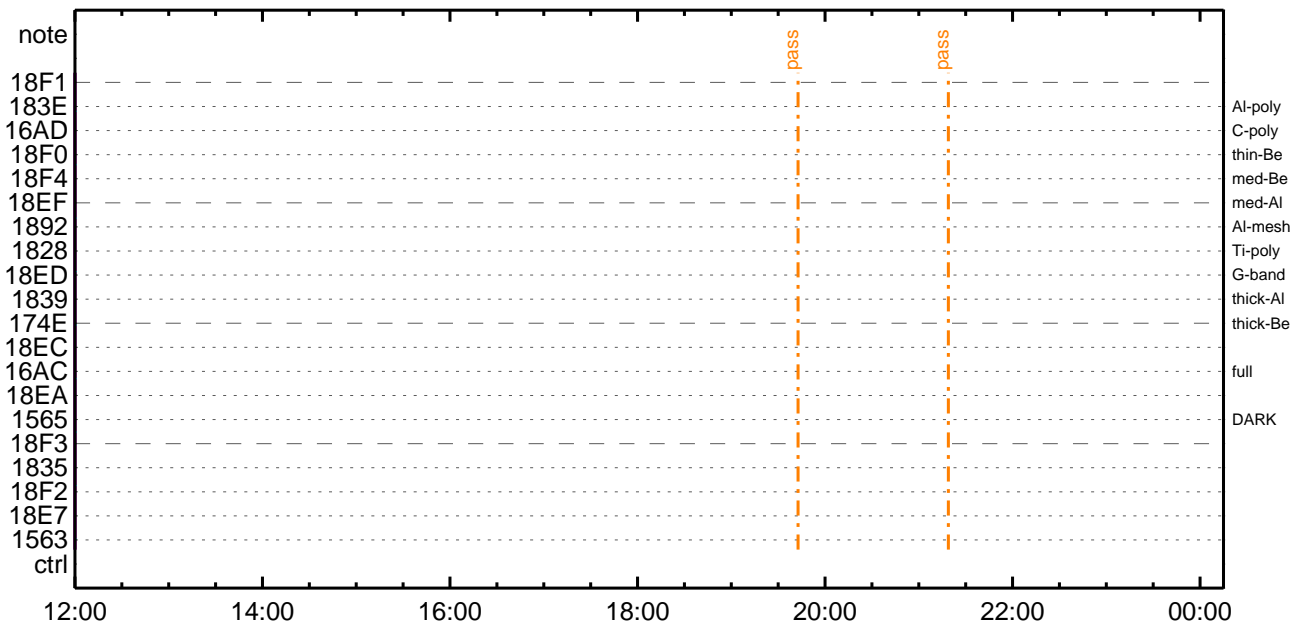
CMDI #0678 2012/06/14



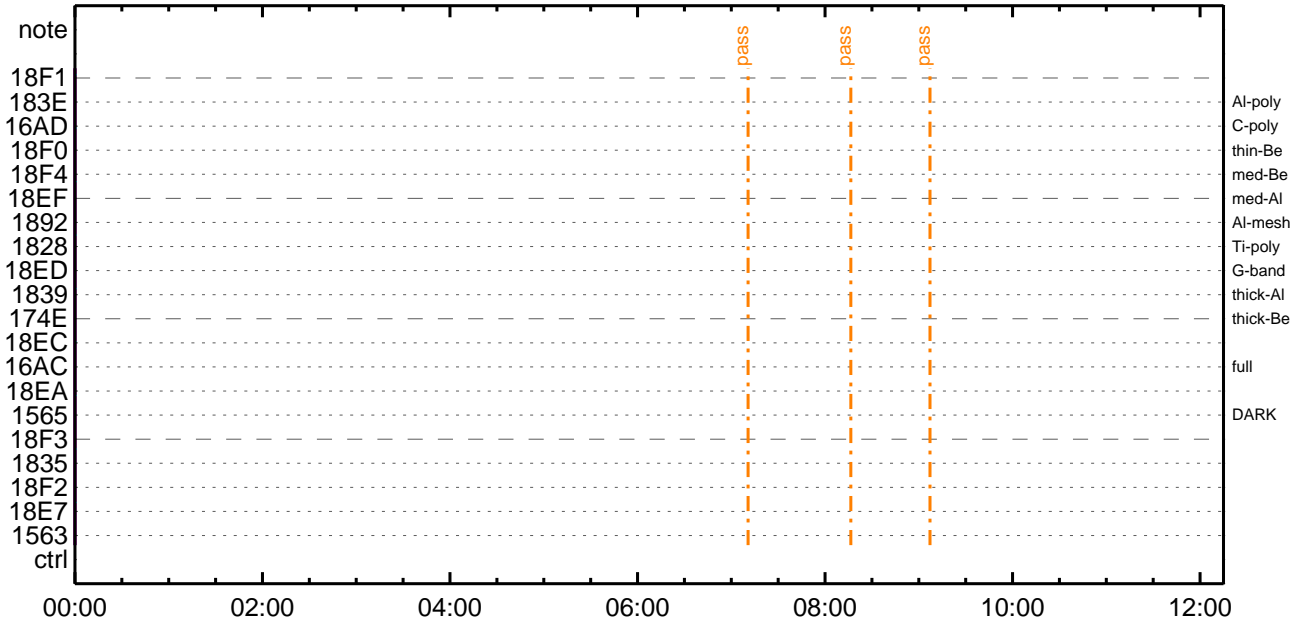
CMDI #0678 2012/06/15



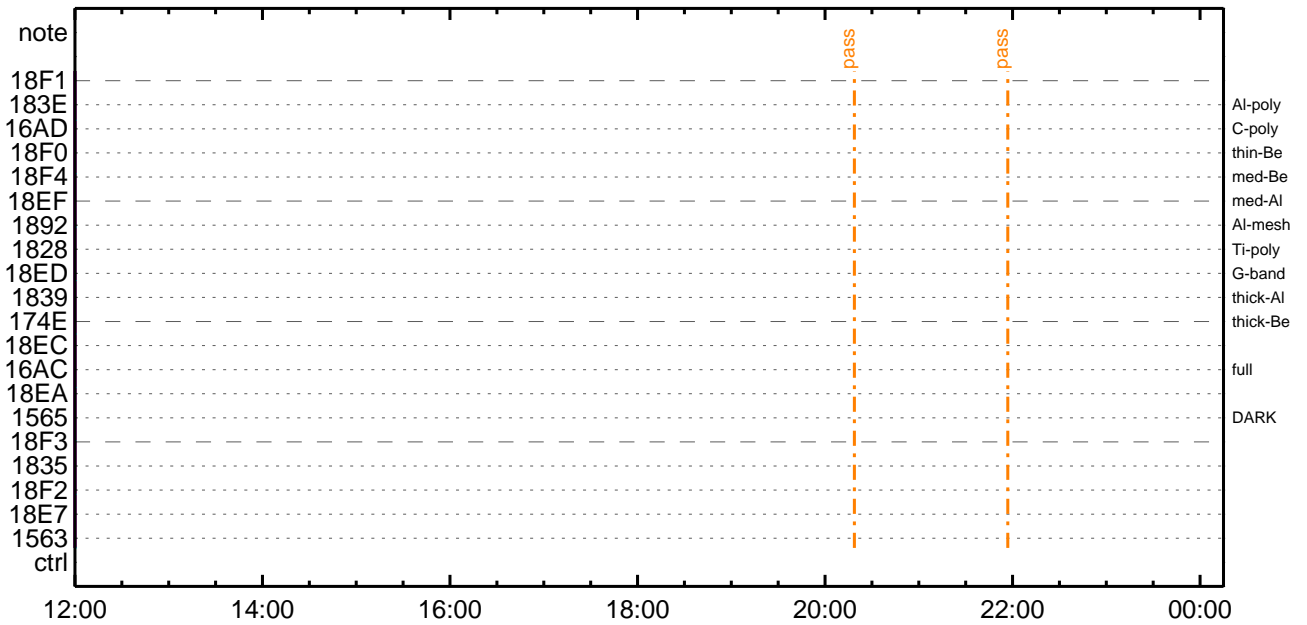
CMDI #0678 2012/06/15



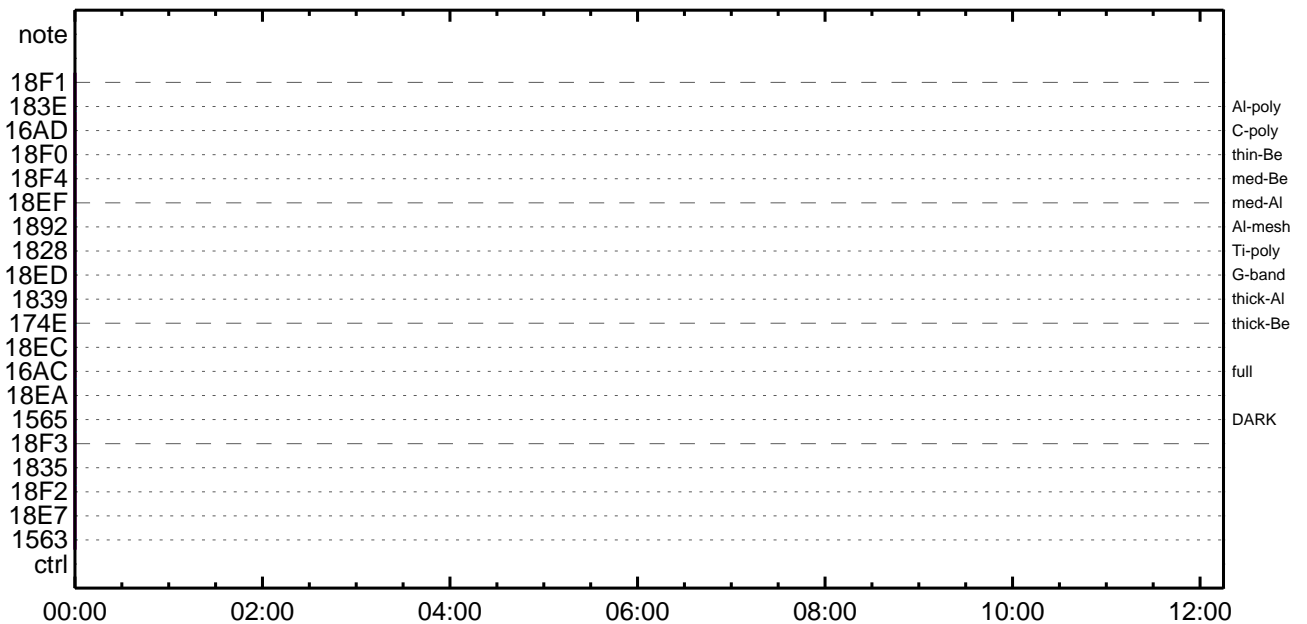
CMDI #0678 2012/06/16



CMDI #0678 2012/06/16

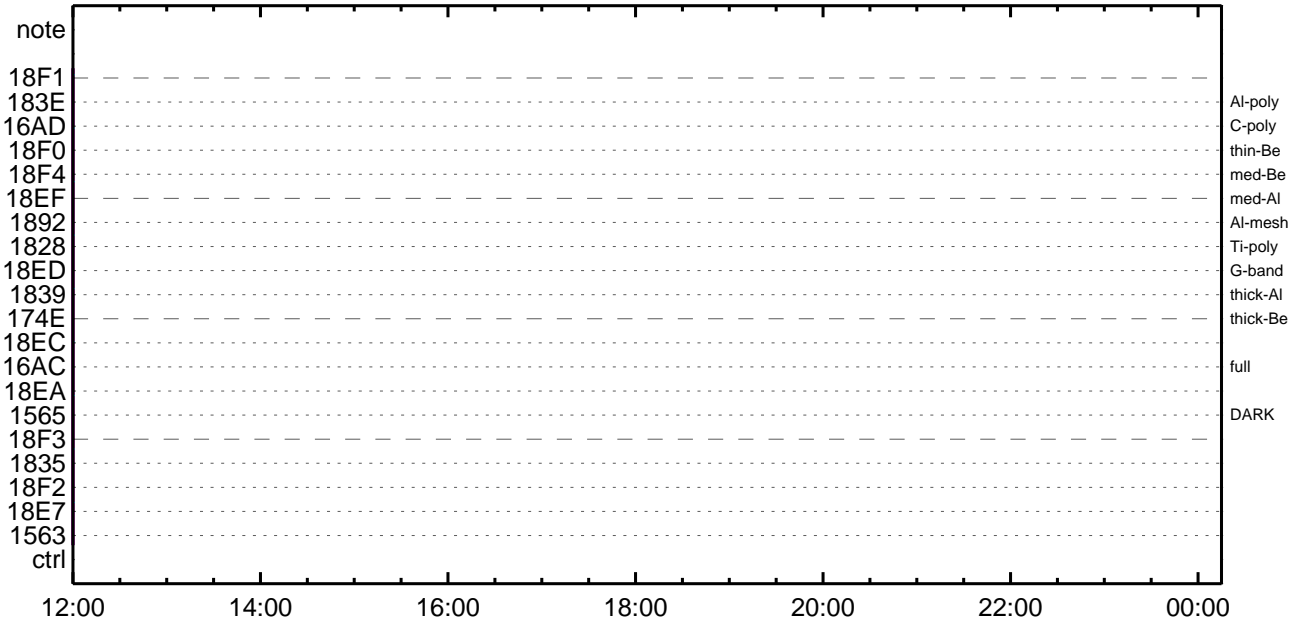


CMDI #0678 2012/06/17

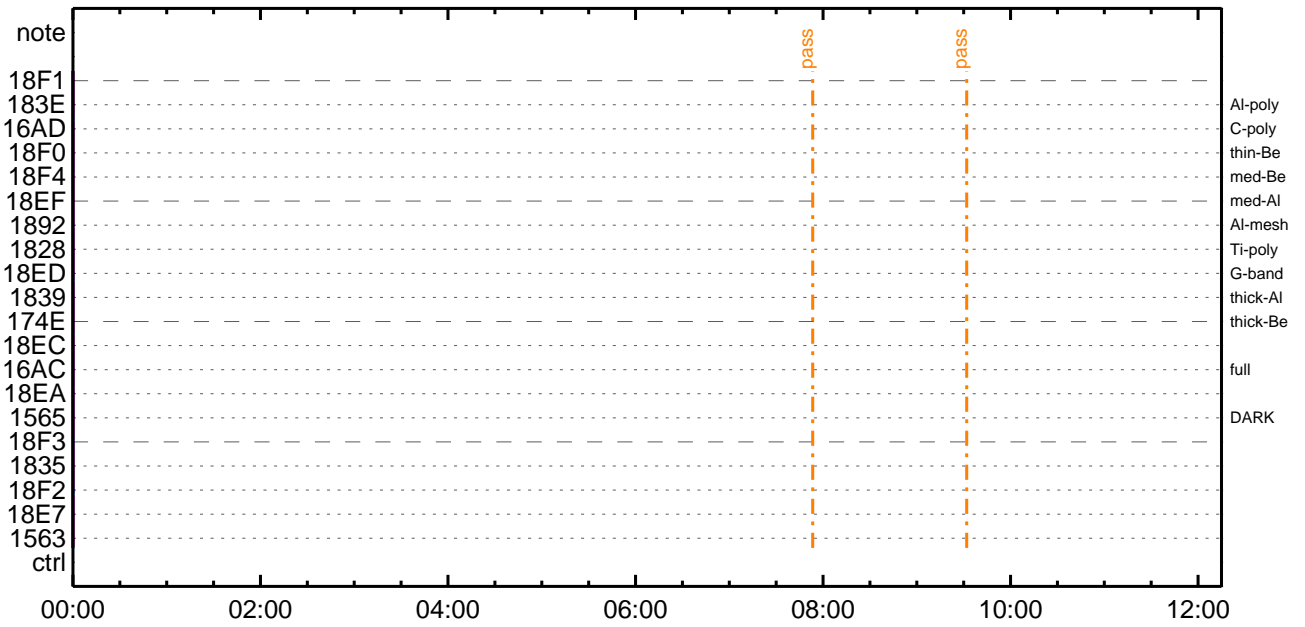




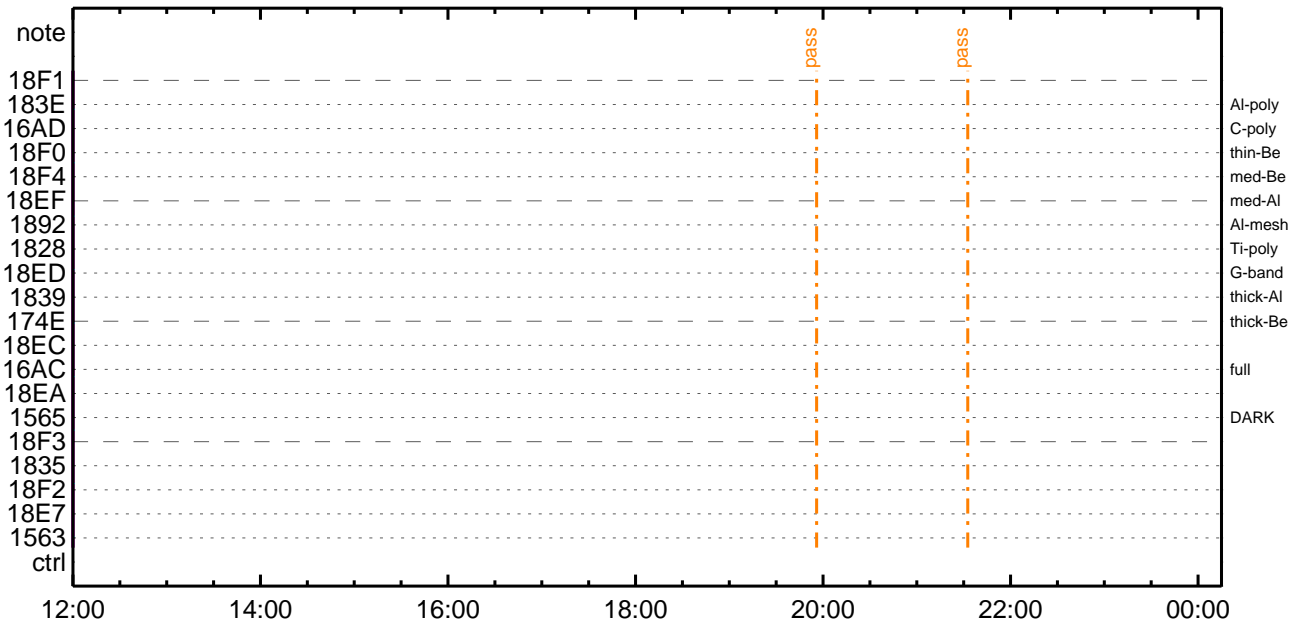
CMDI #0678 2012/06/17



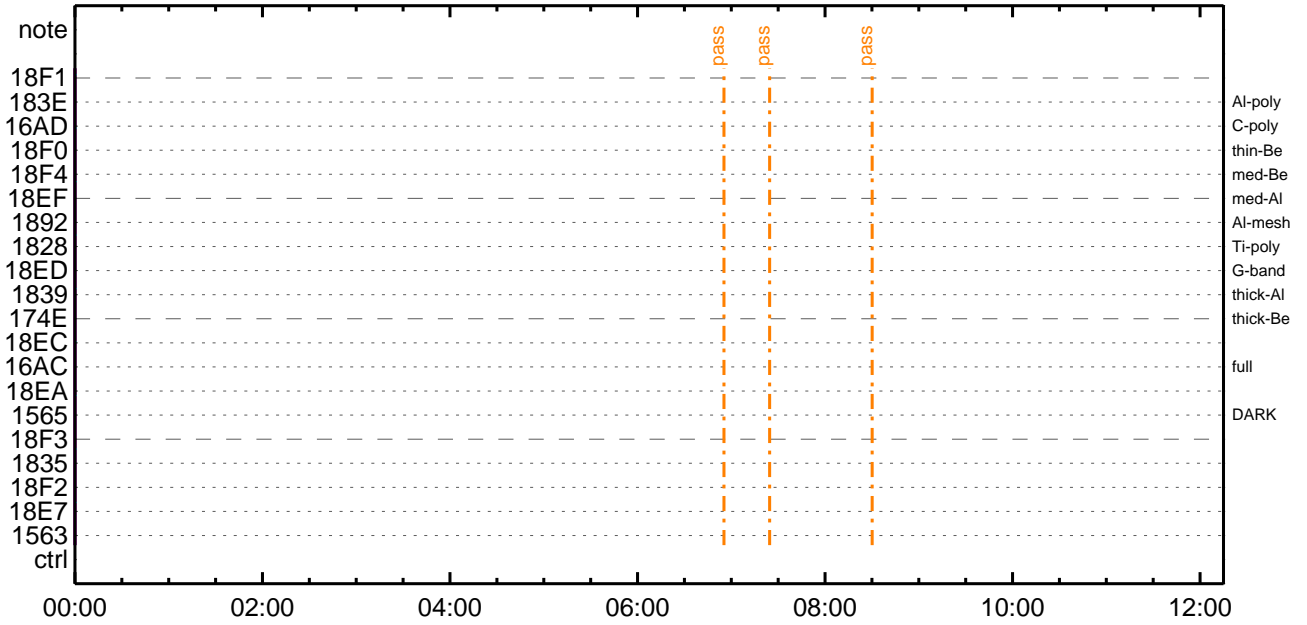
CMDI #0678 2012/06/18



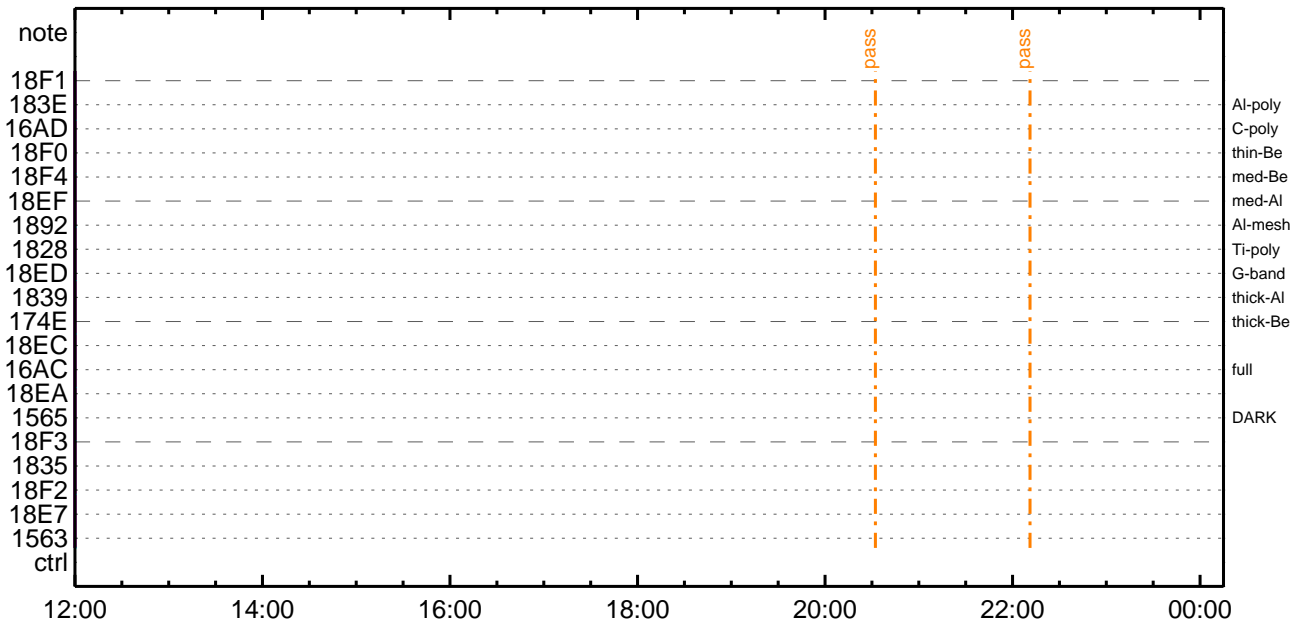
CMDI #0678 2012/06/18



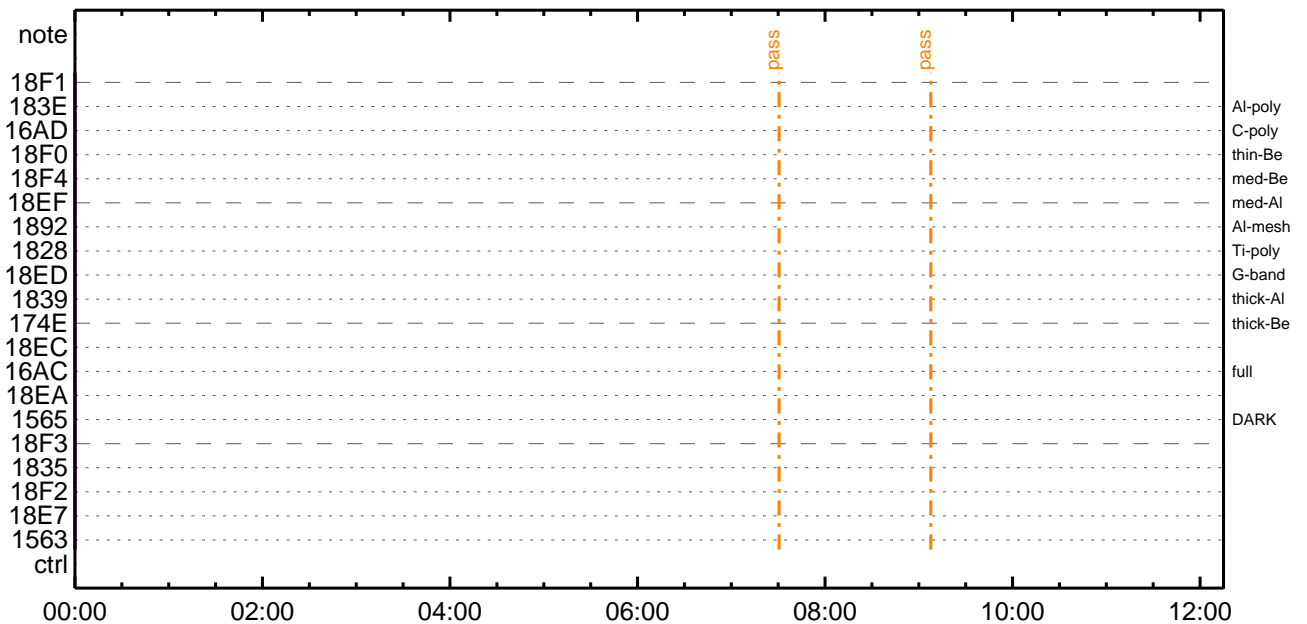
CMDI #0678 2012/06/19



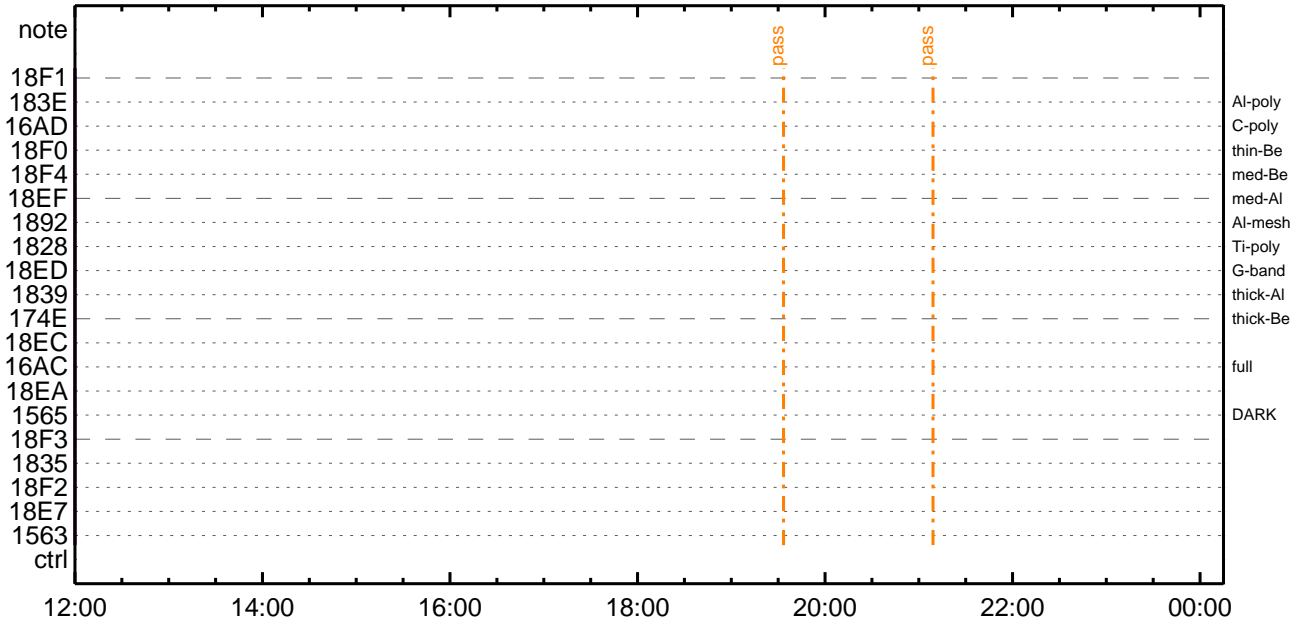
CMDI #0678 2012/06/19



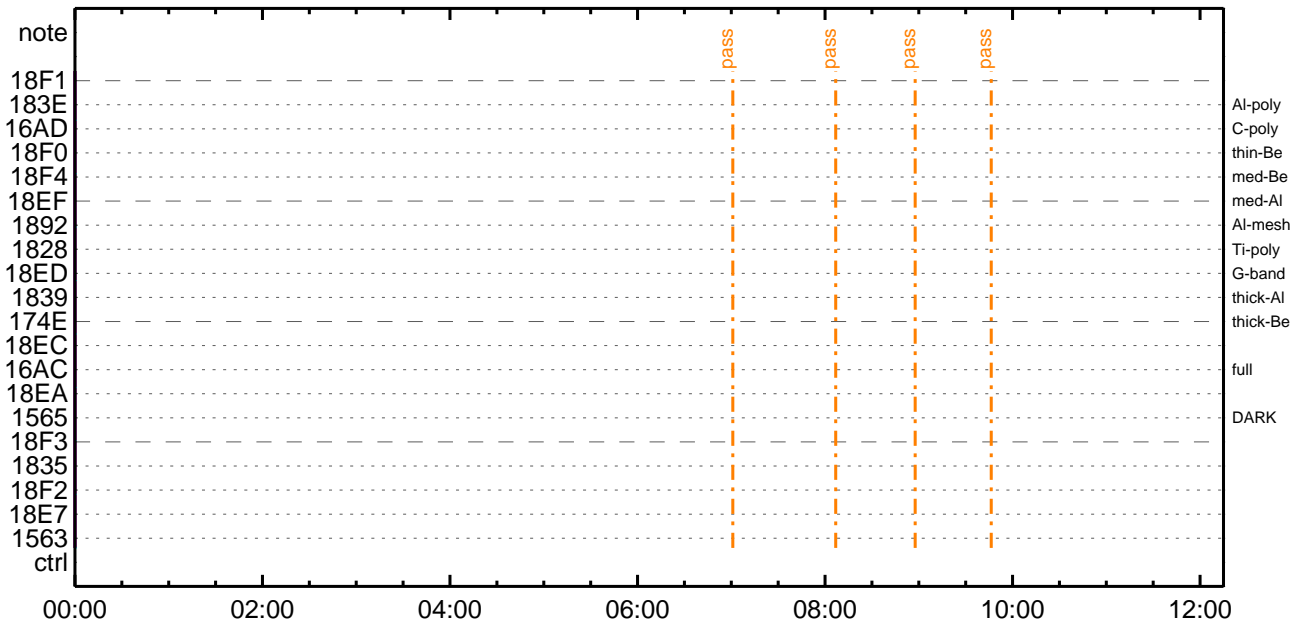
CMDI #0678 2012/06/20



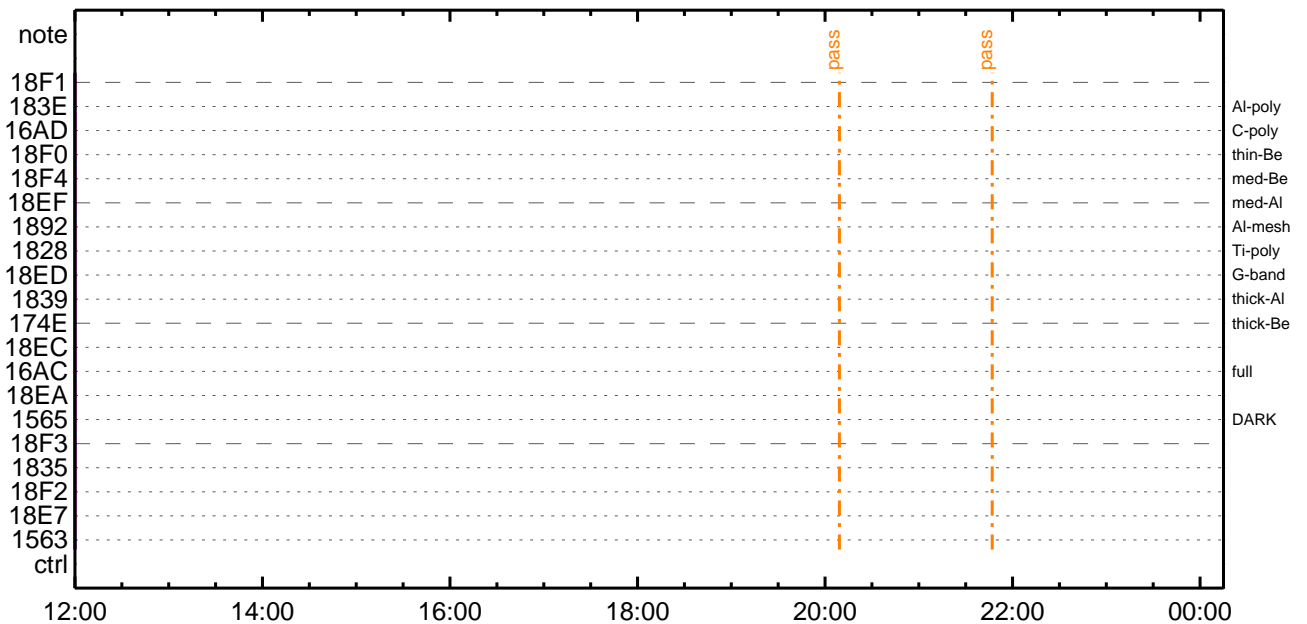
CMDI #0678 2012/06/20



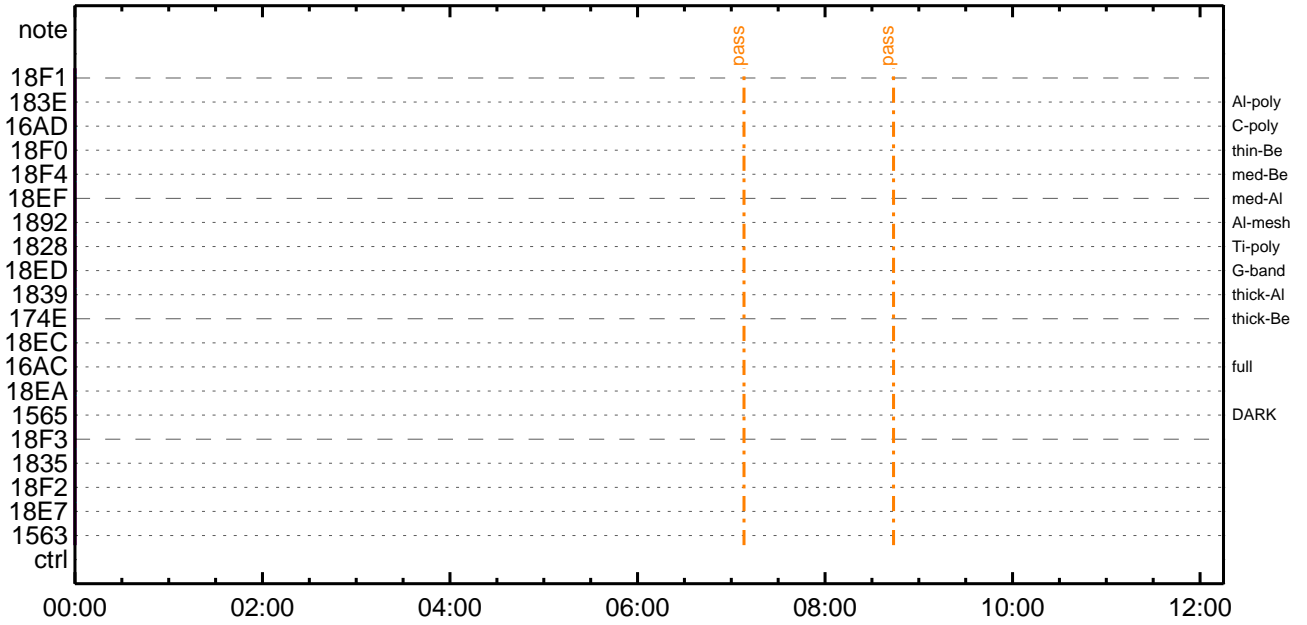
CMDI #0678 2012/06/21



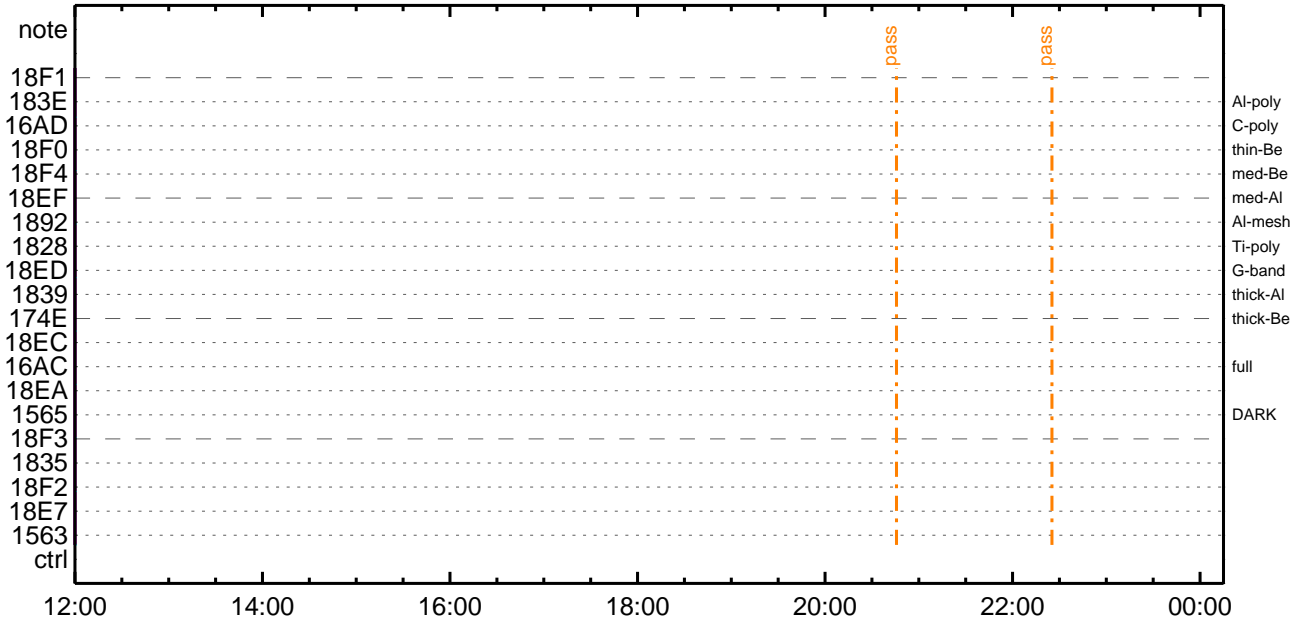
CMDI #0678 2012/06/21



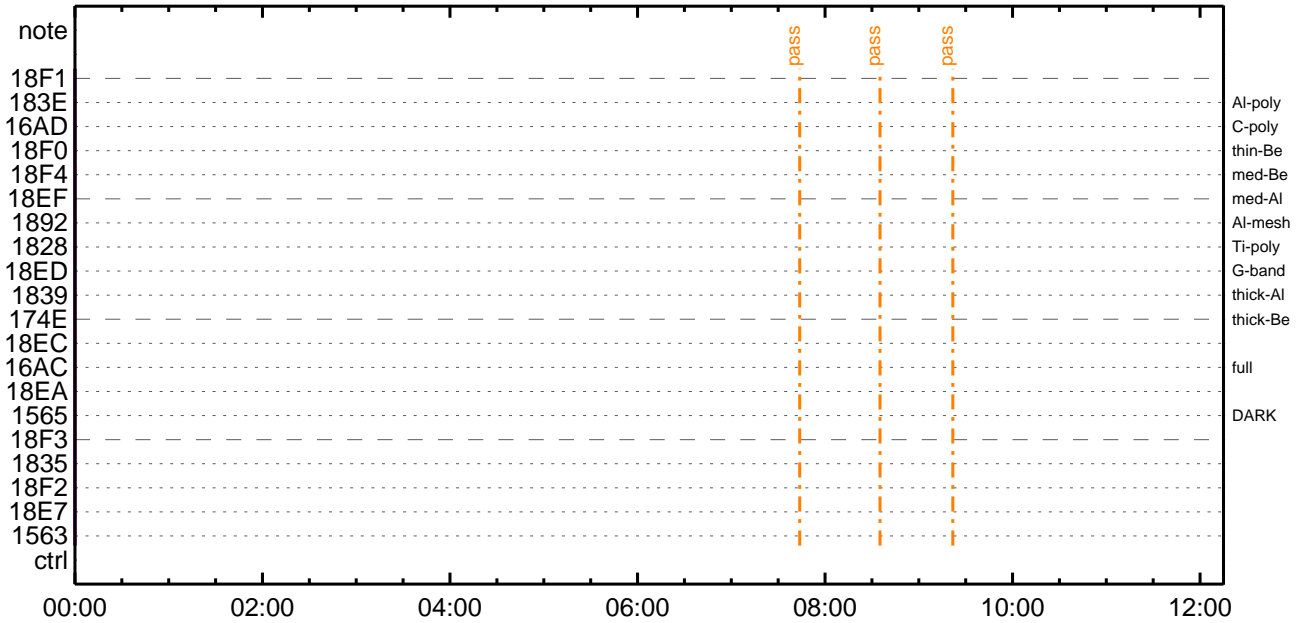
CMDI #0678 2012/06/22



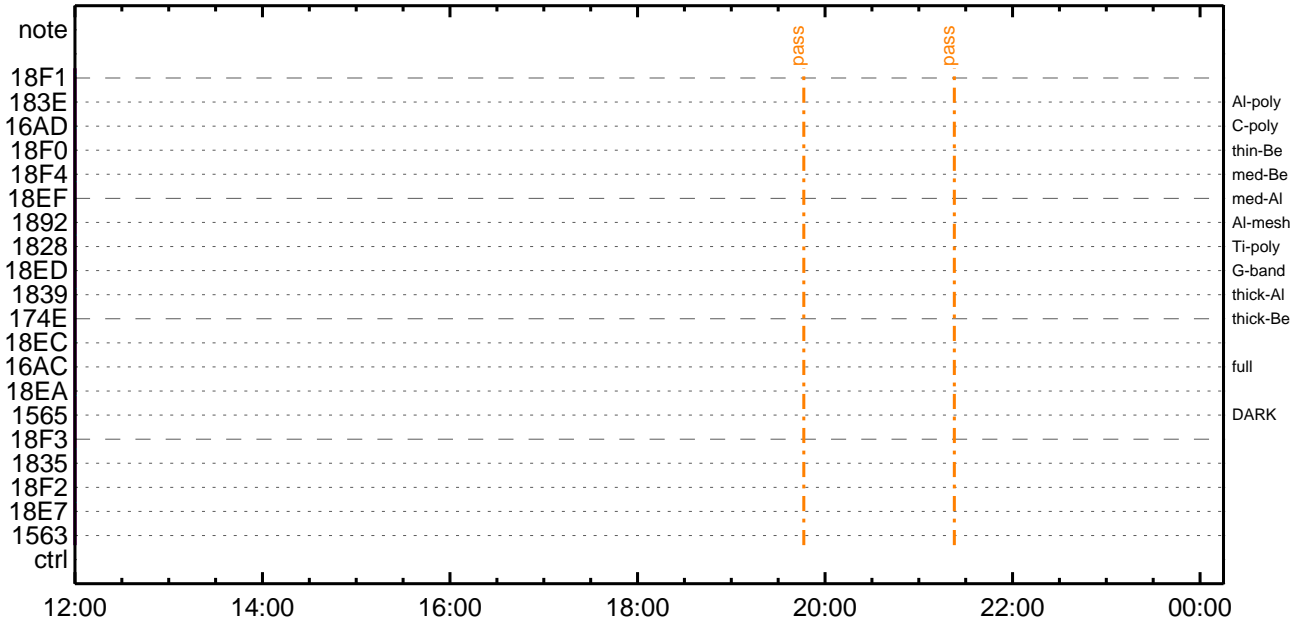
CMDI #0678 2012/06/22



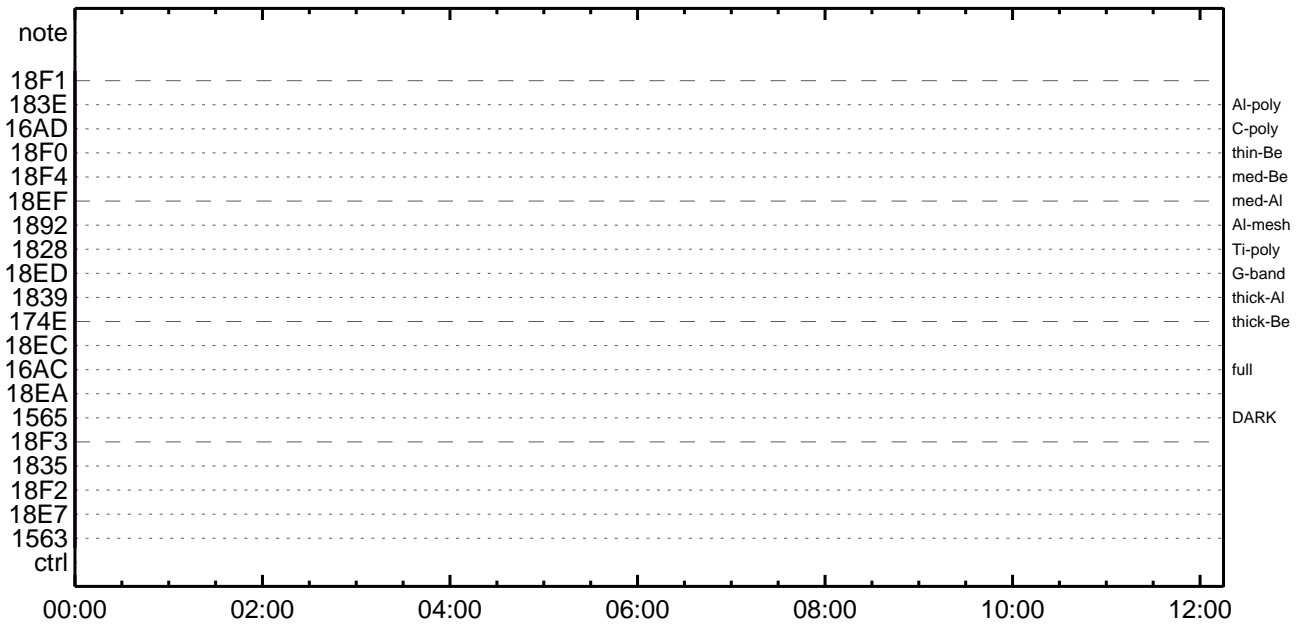
CMDI #0678 2012/06/23



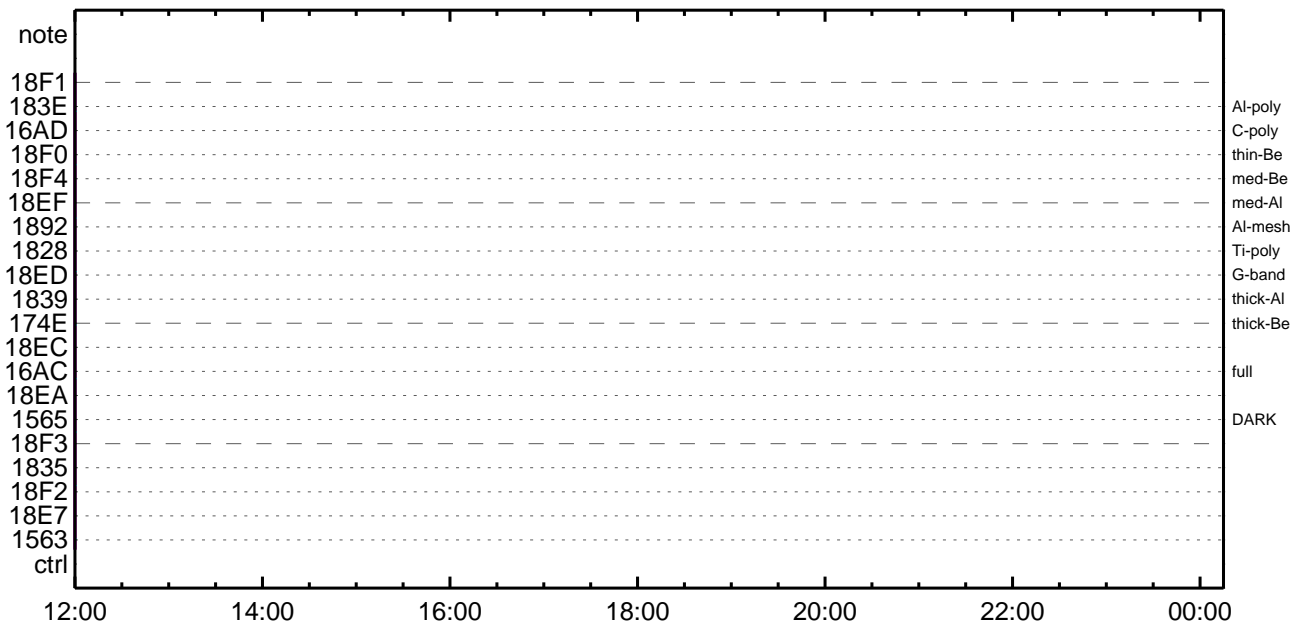
CMDI #0678 2012/06/23



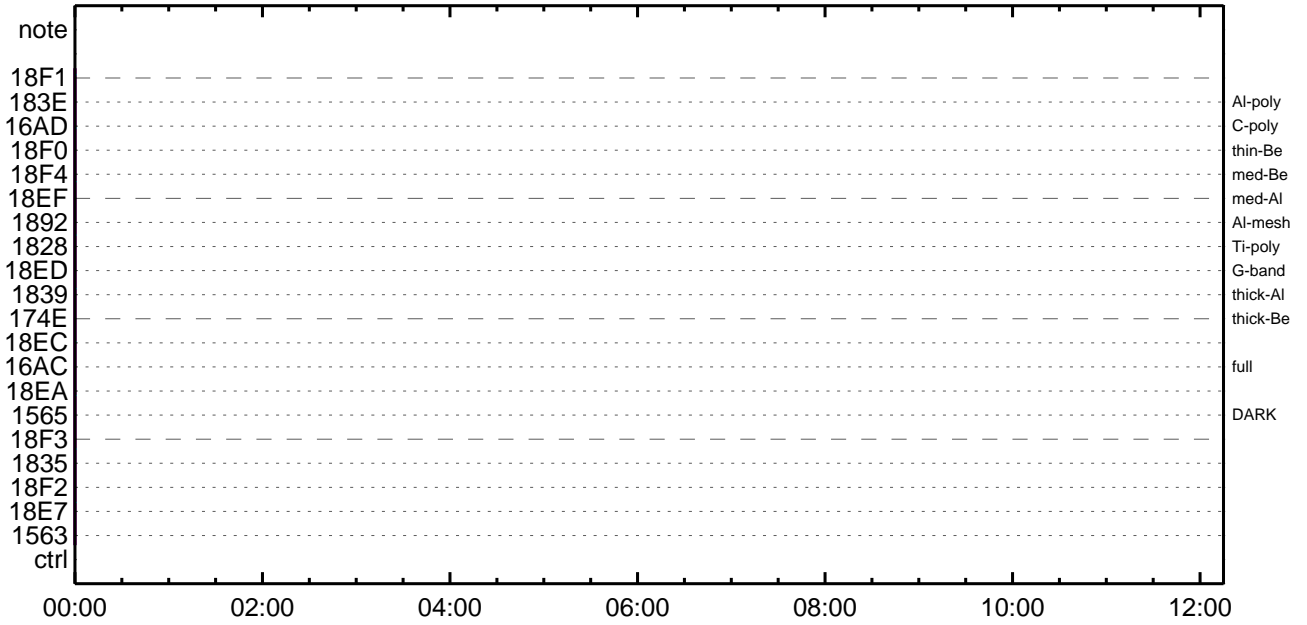
CMDI #0678 2012/06/24



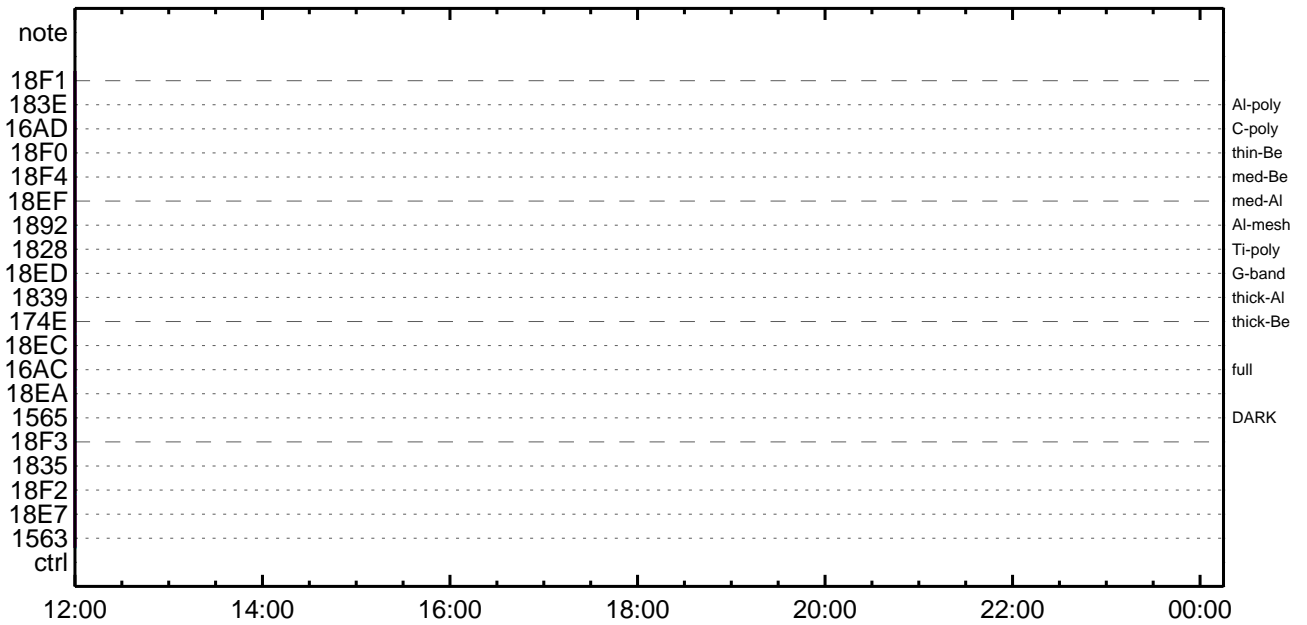
CMDI #0678 2012/06/24



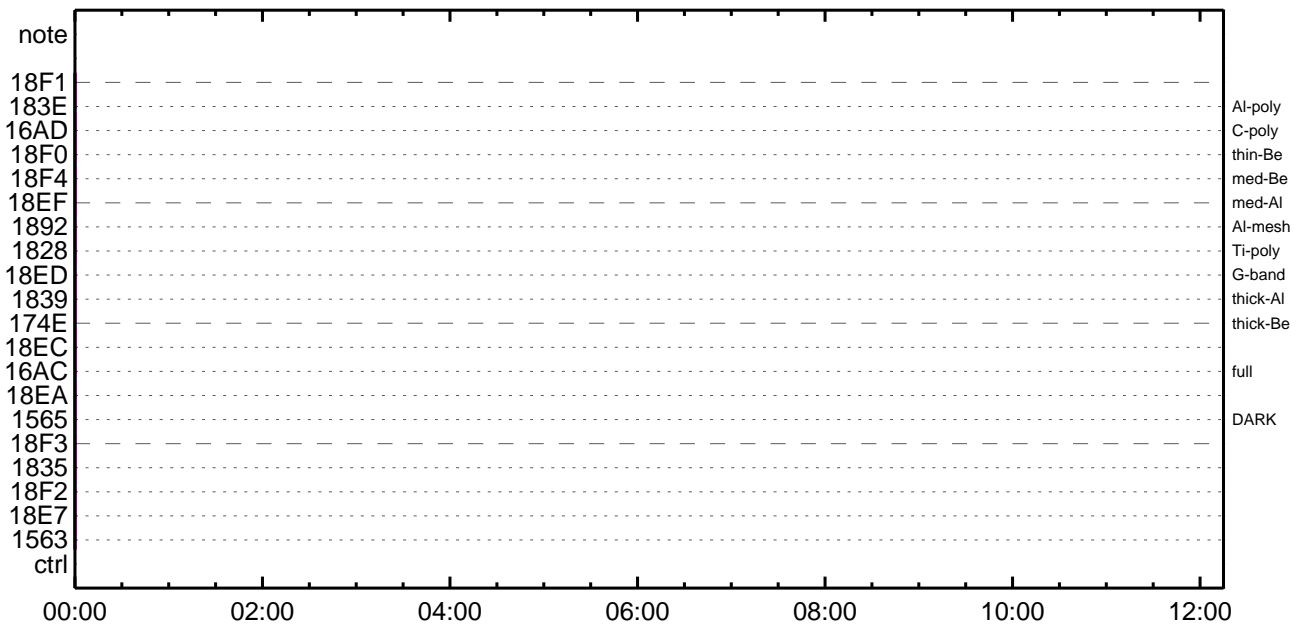
CMDI #0678 2012/06/25



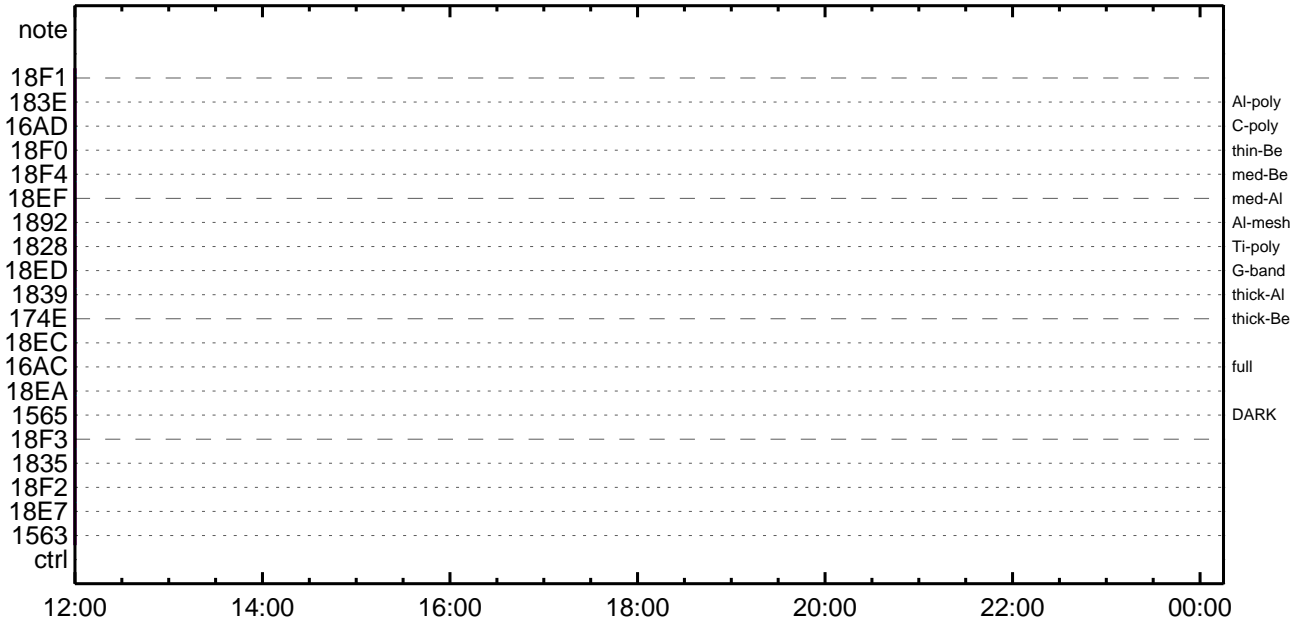
CMDI #0678 2012/06/25



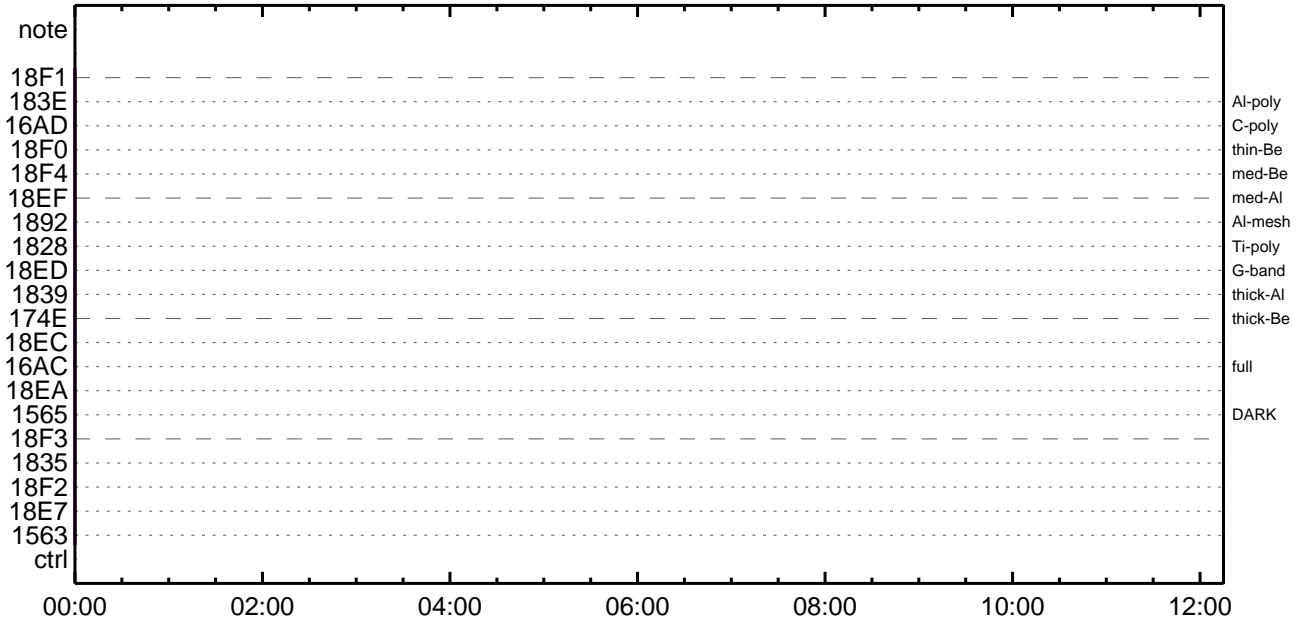
CMDI #0678 2012/06/26



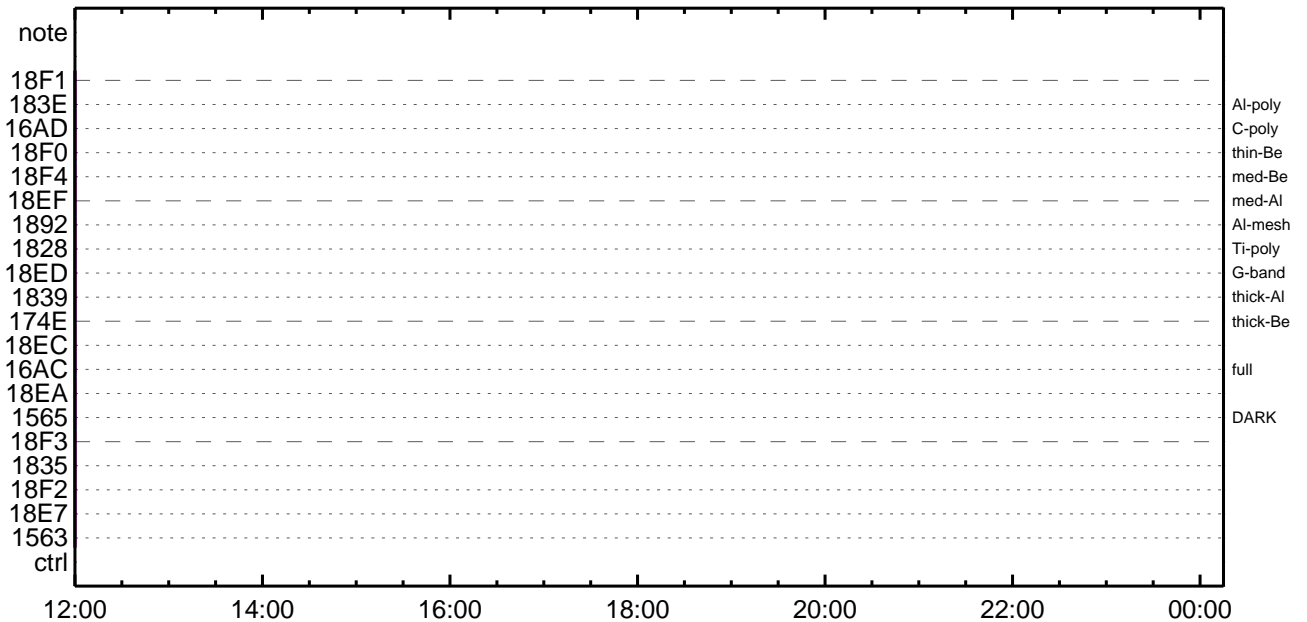
CMDI #0678 2012/06/26



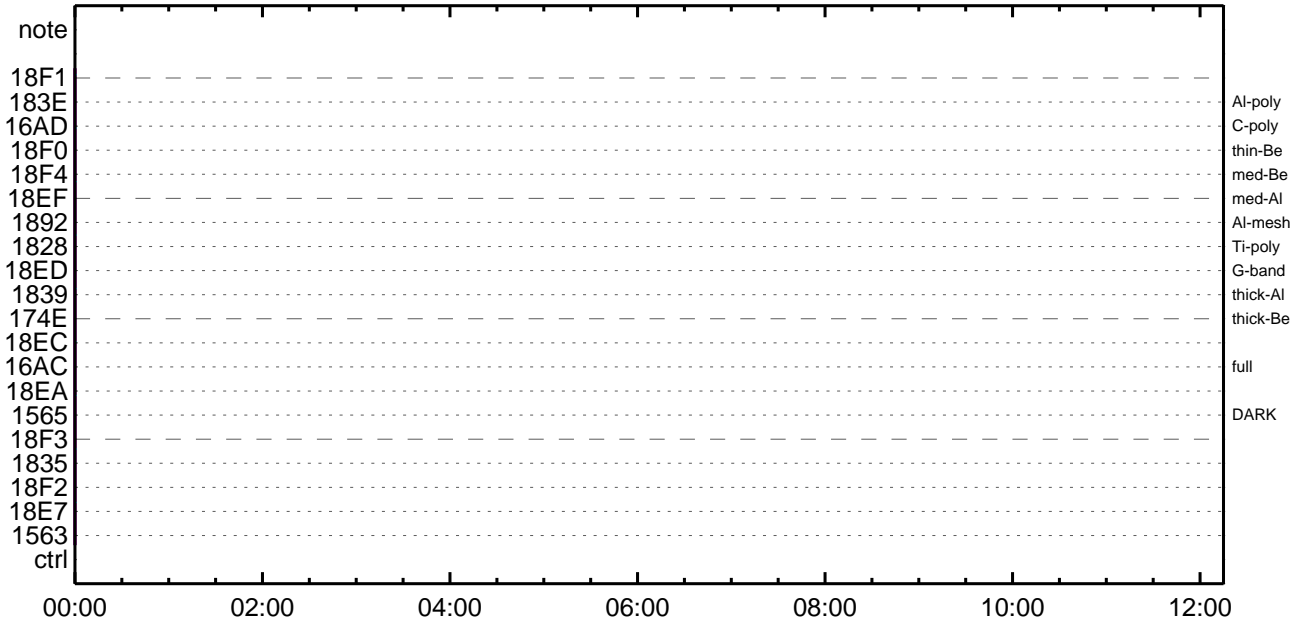
CMDI #0678 2012/06/27



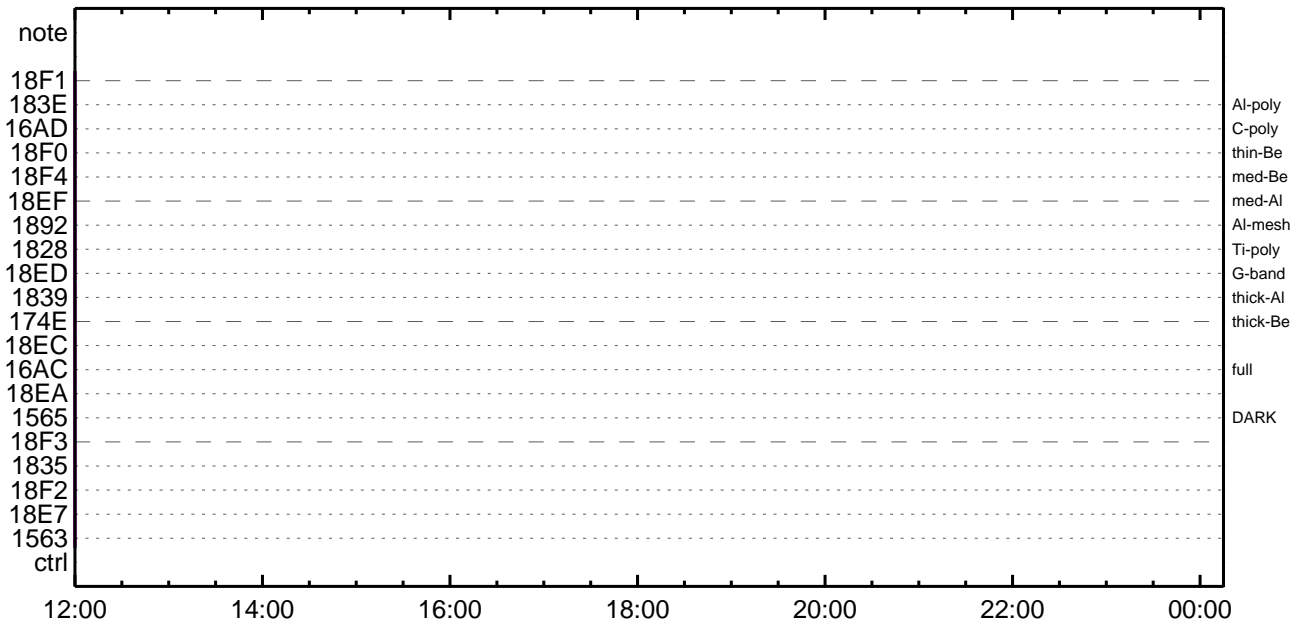
CMDI #0678 2012/06/27



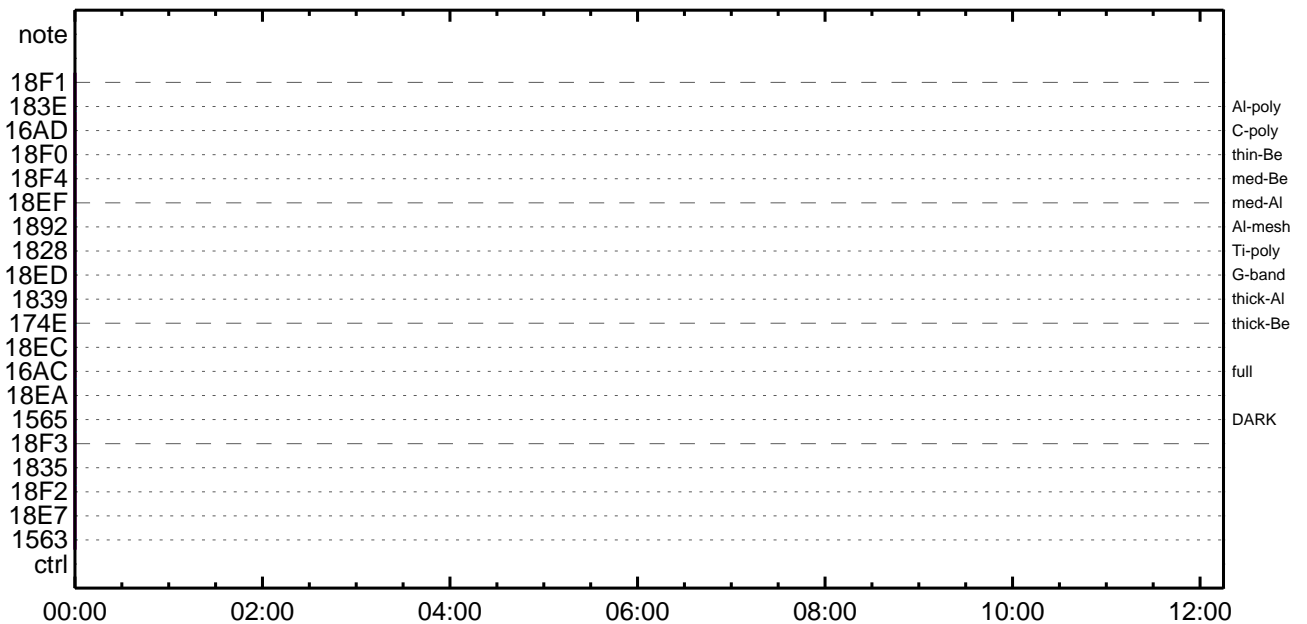
CMDI #0678 2012/06/28



CMDI #0678 2012/06/28

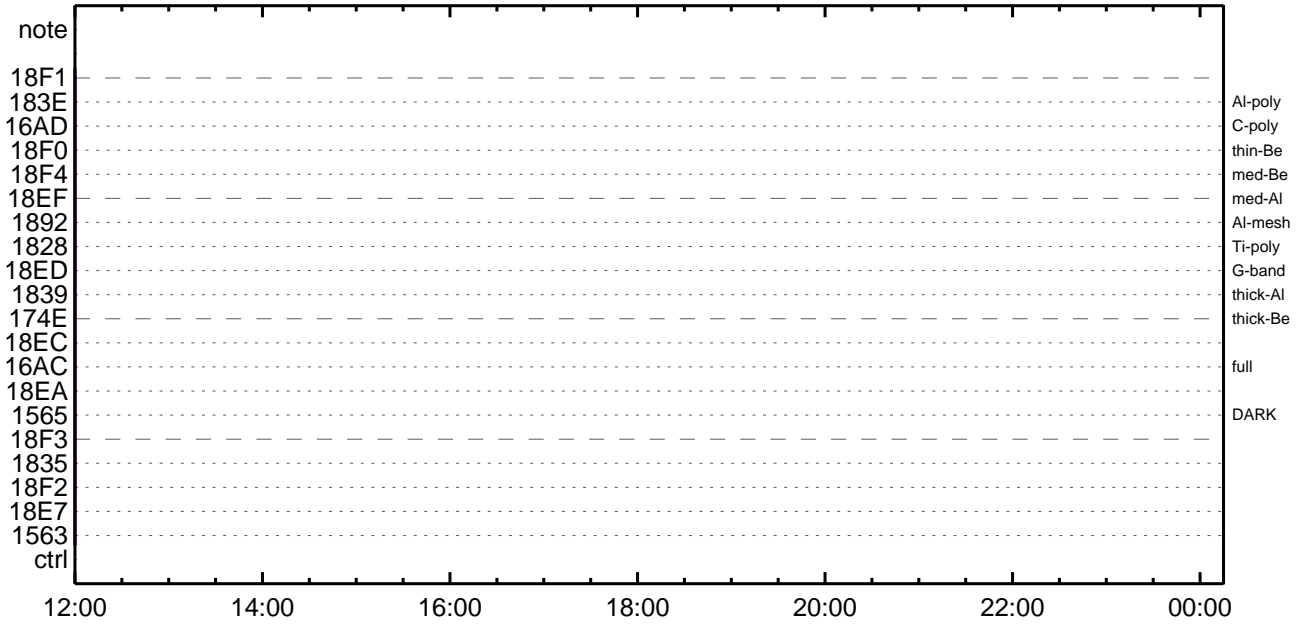


CMDI #0678 2012/06/29

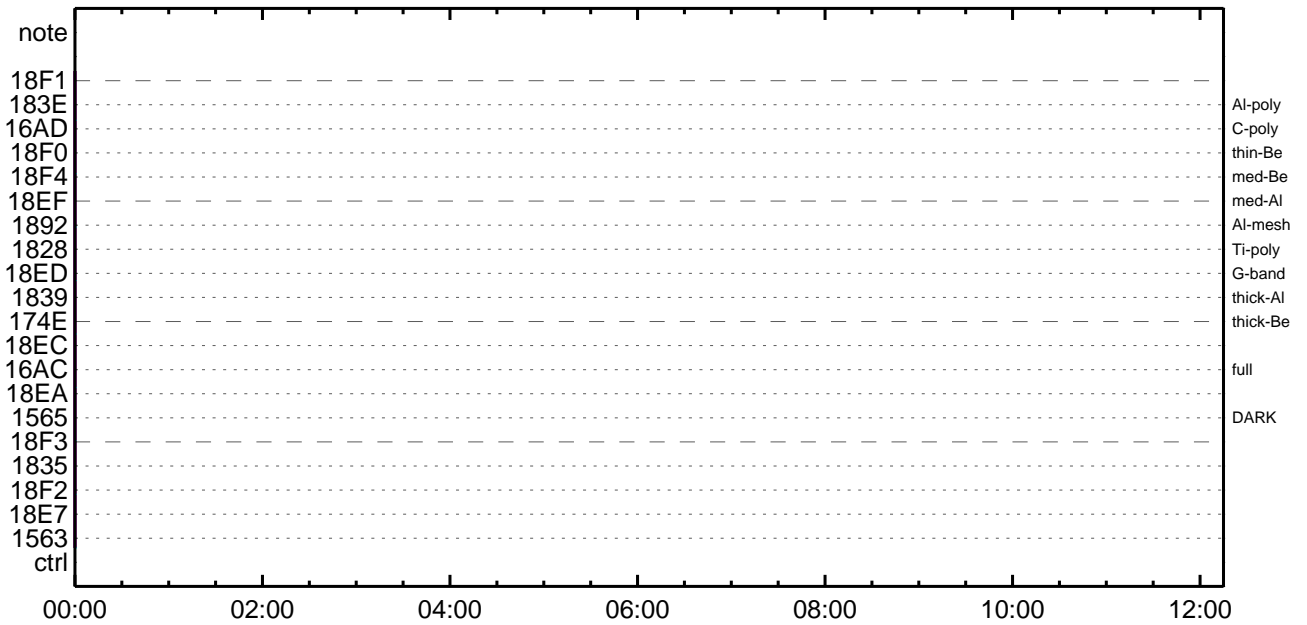




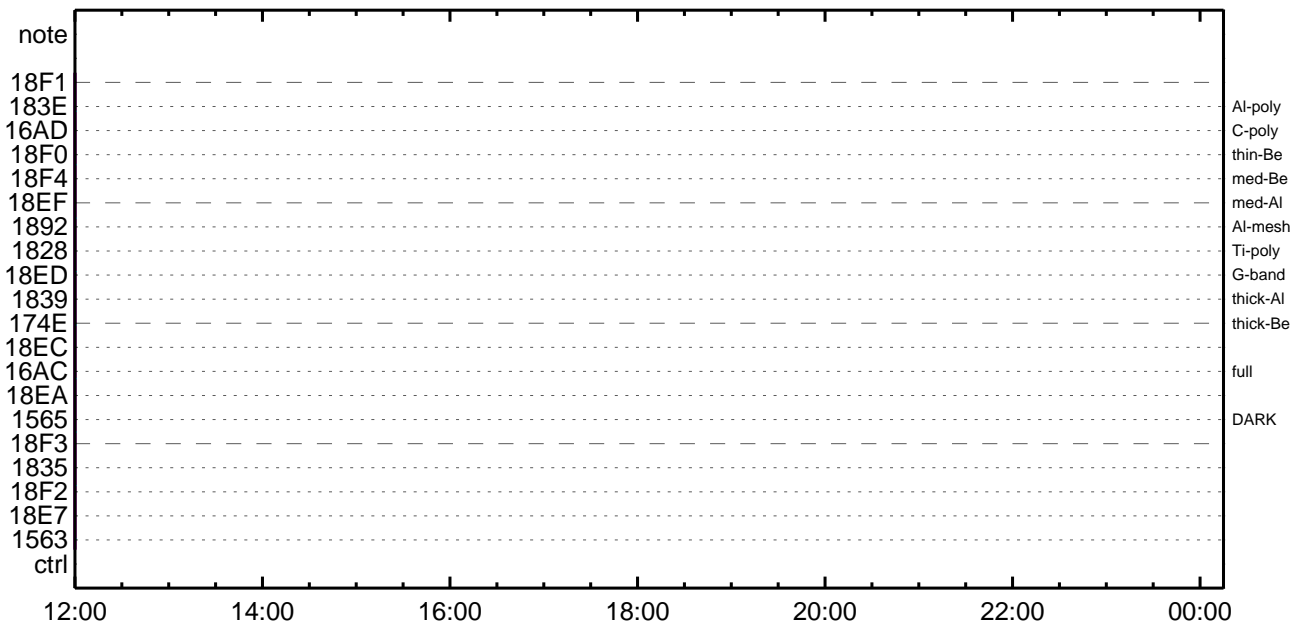
CMDI #0678 2012/06/29



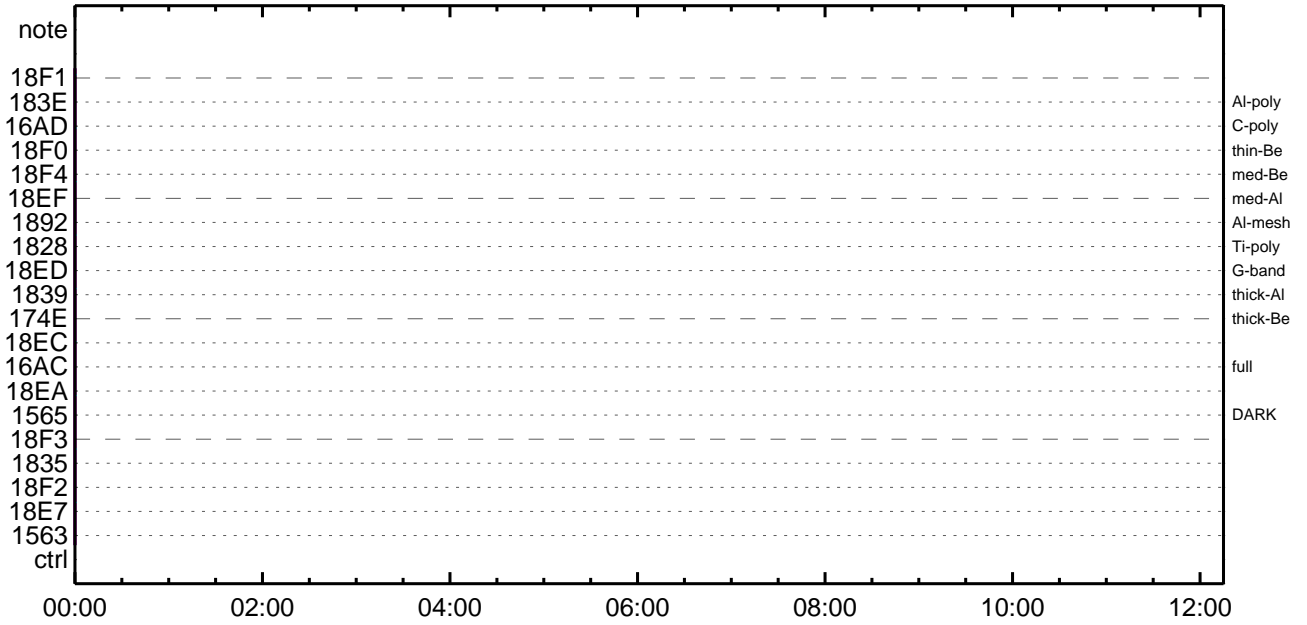
CMDI #0678 2012/06/30



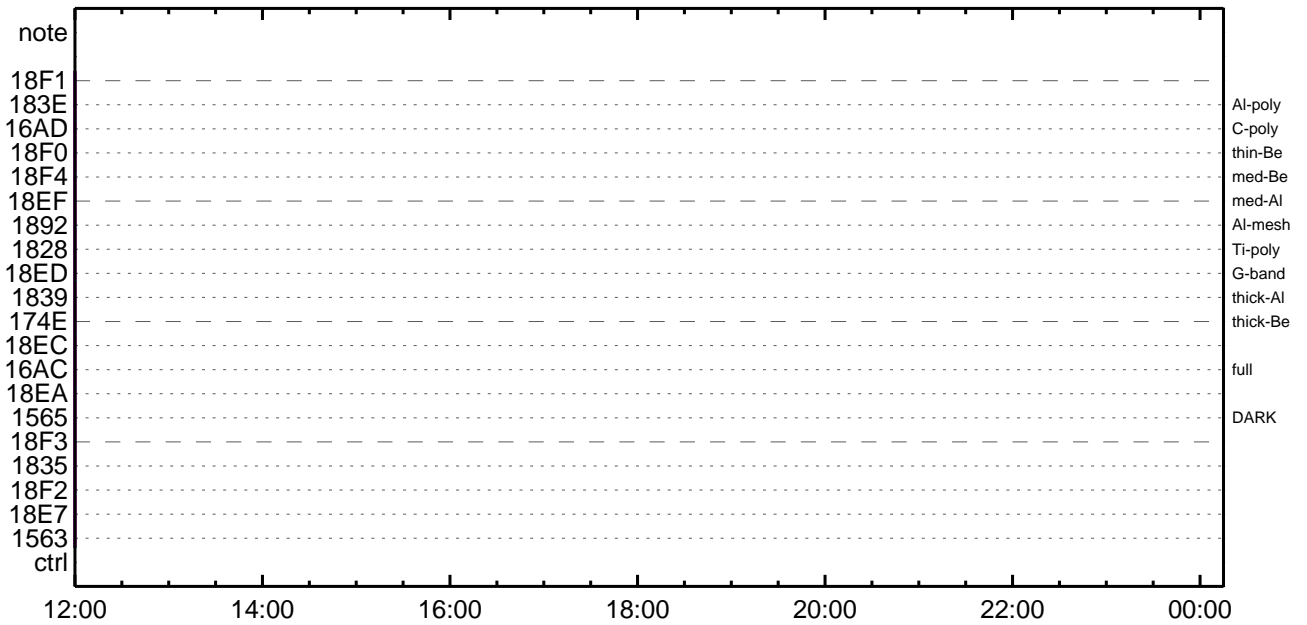
CMDI #0678 2012/06/30



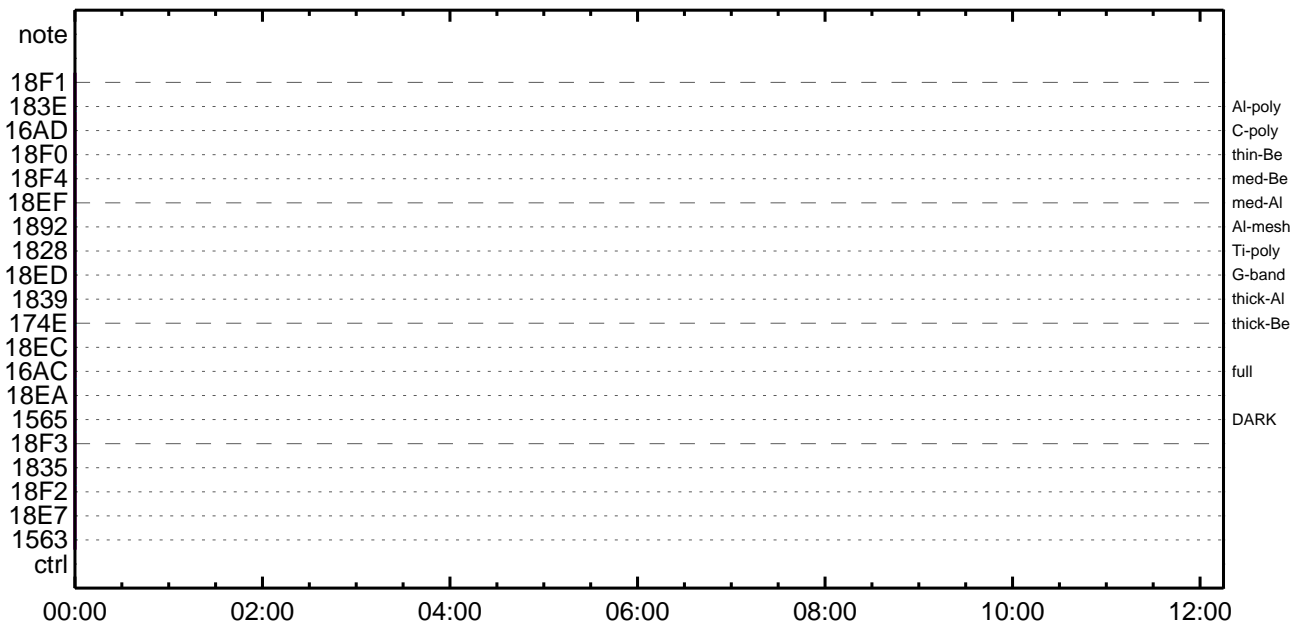
CMDI #0678 2012/07/01



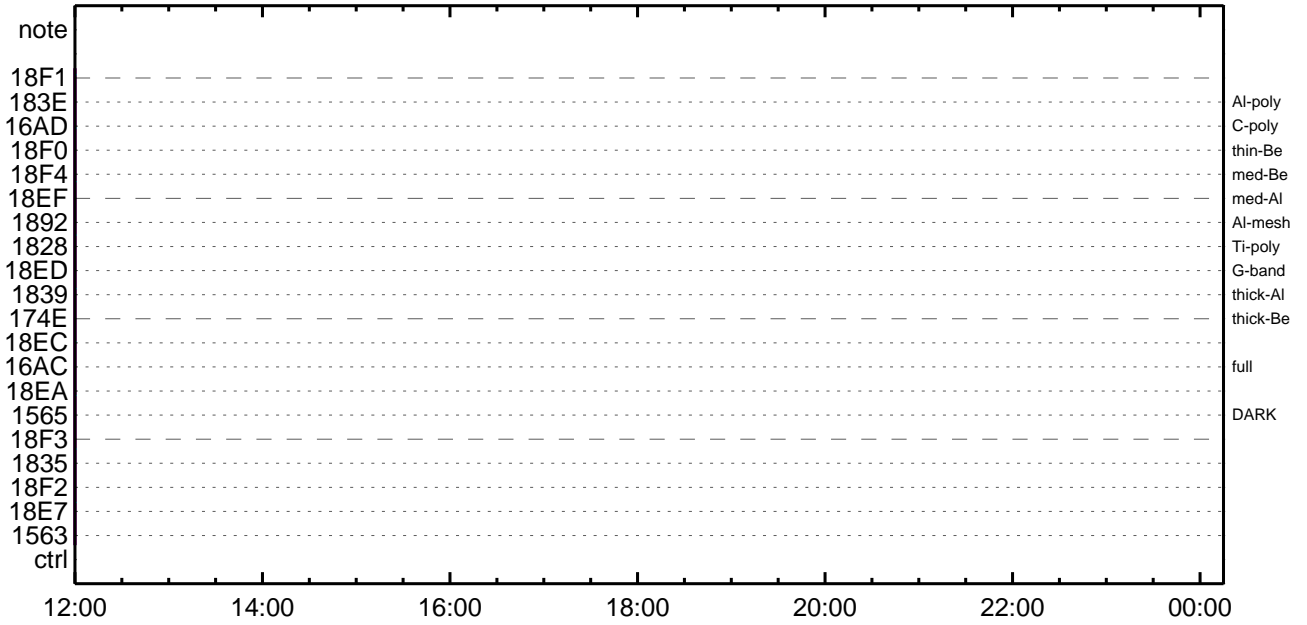
CMDI #0678 2012/07/01



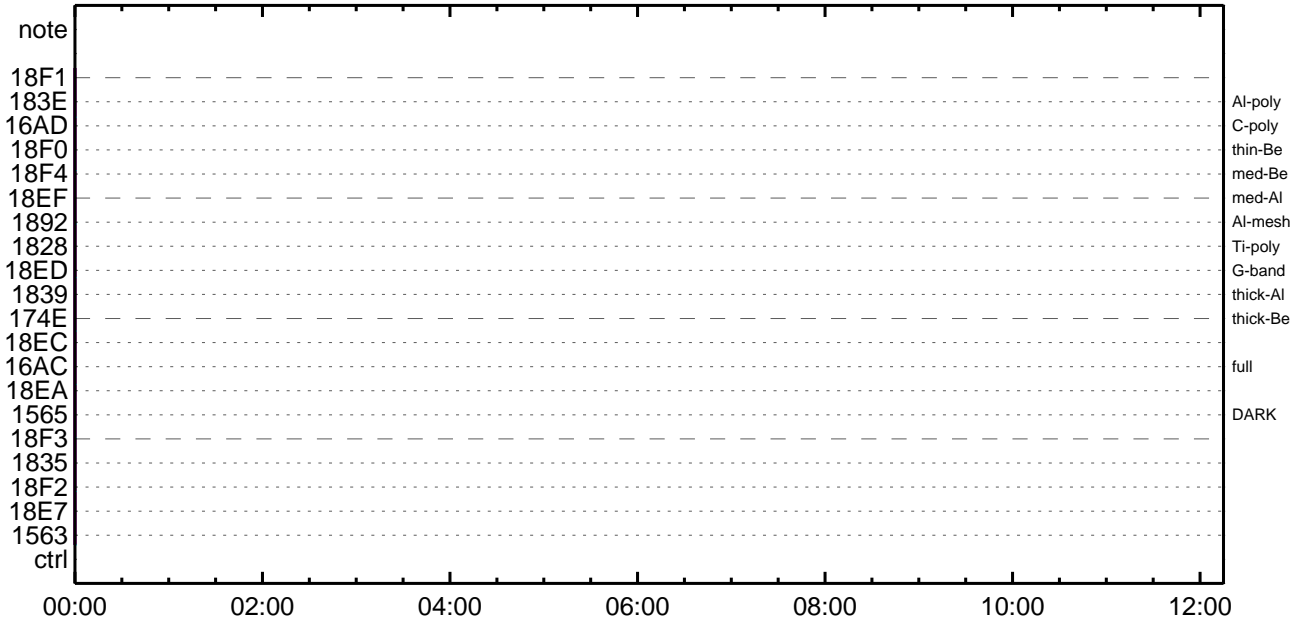
CMDI #0678 2012/07/02



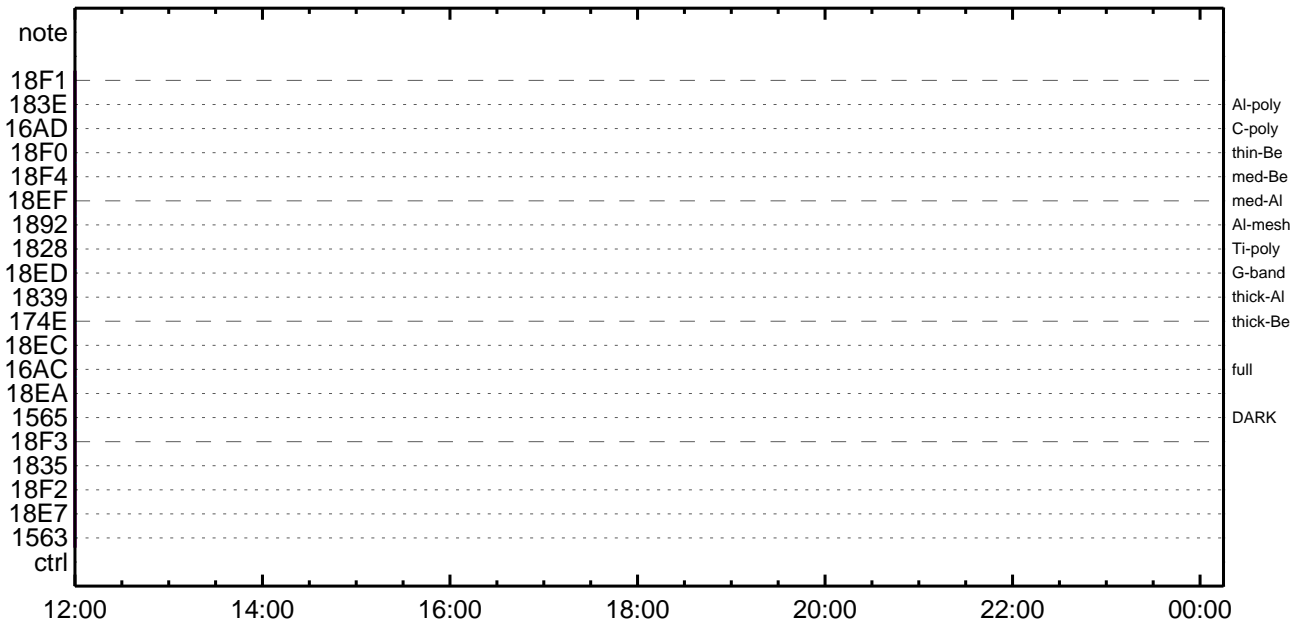
CMDI #0678 2012/07/02



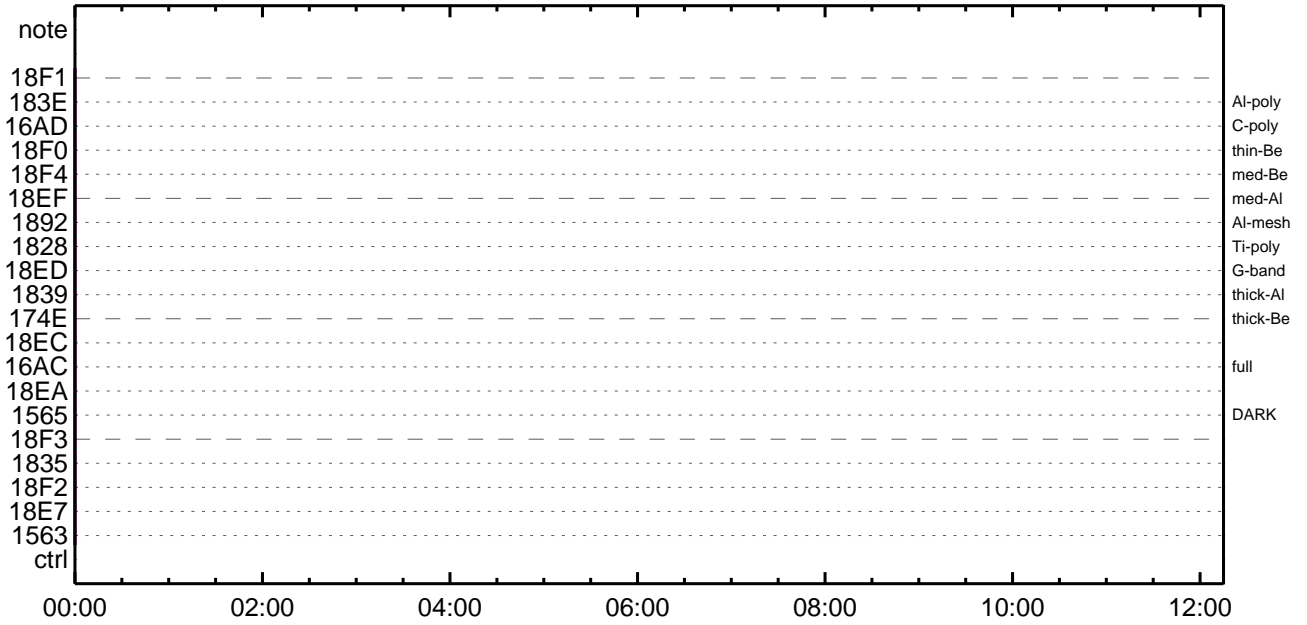
CMDI #0678 2012/07/03



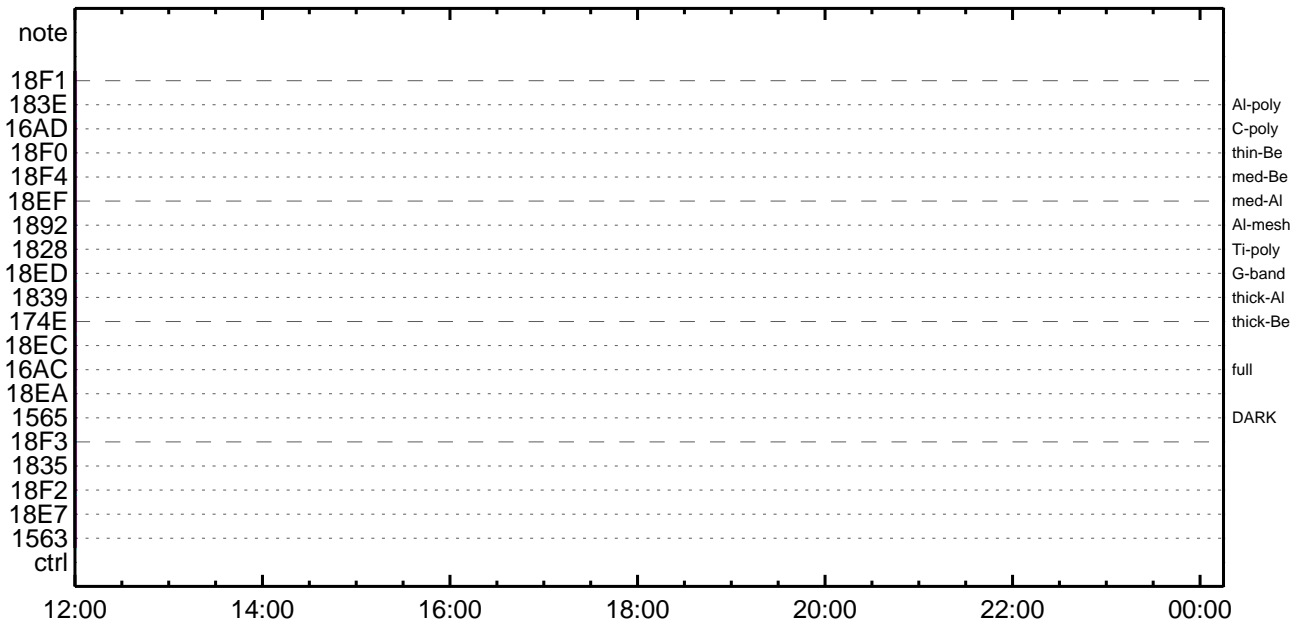
CMDI #0678 2012/07/03



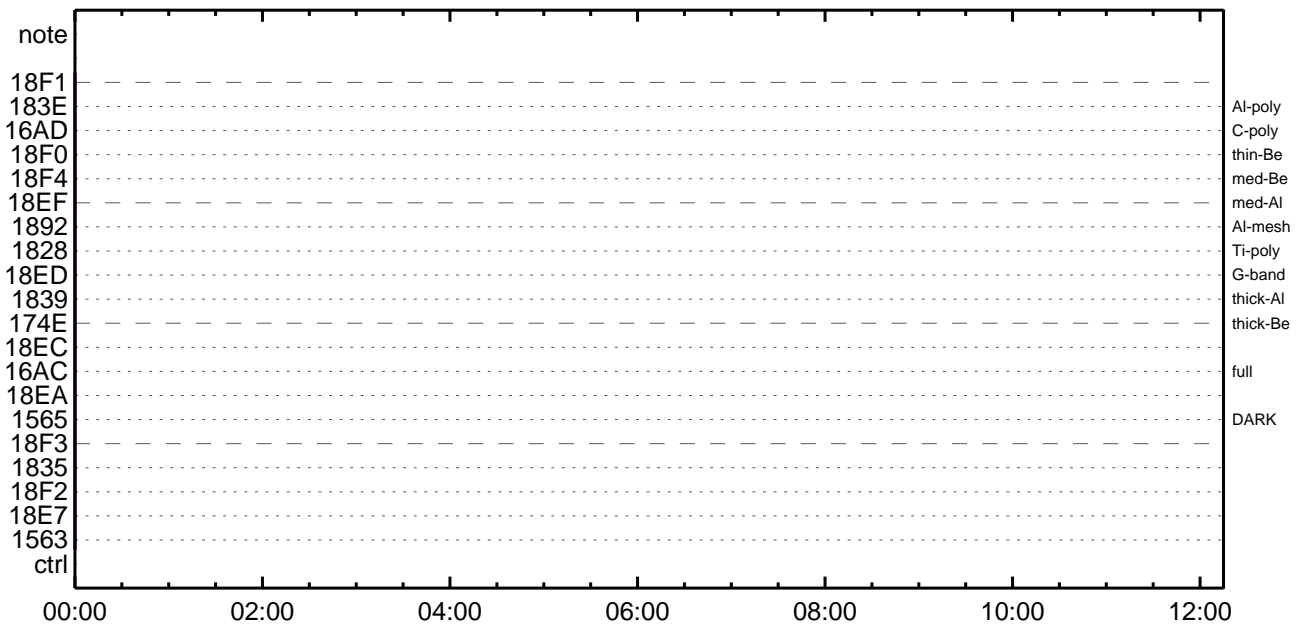
CMDI #0678 2012/07/04



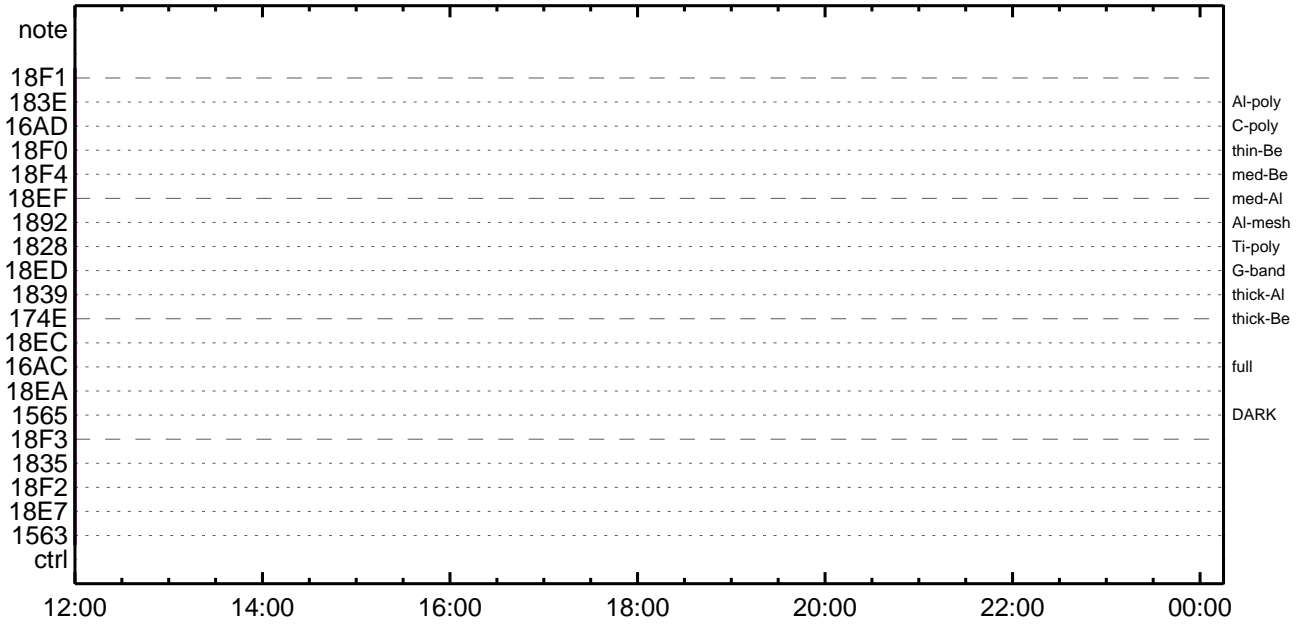
CMDI #0678 2012/07/04



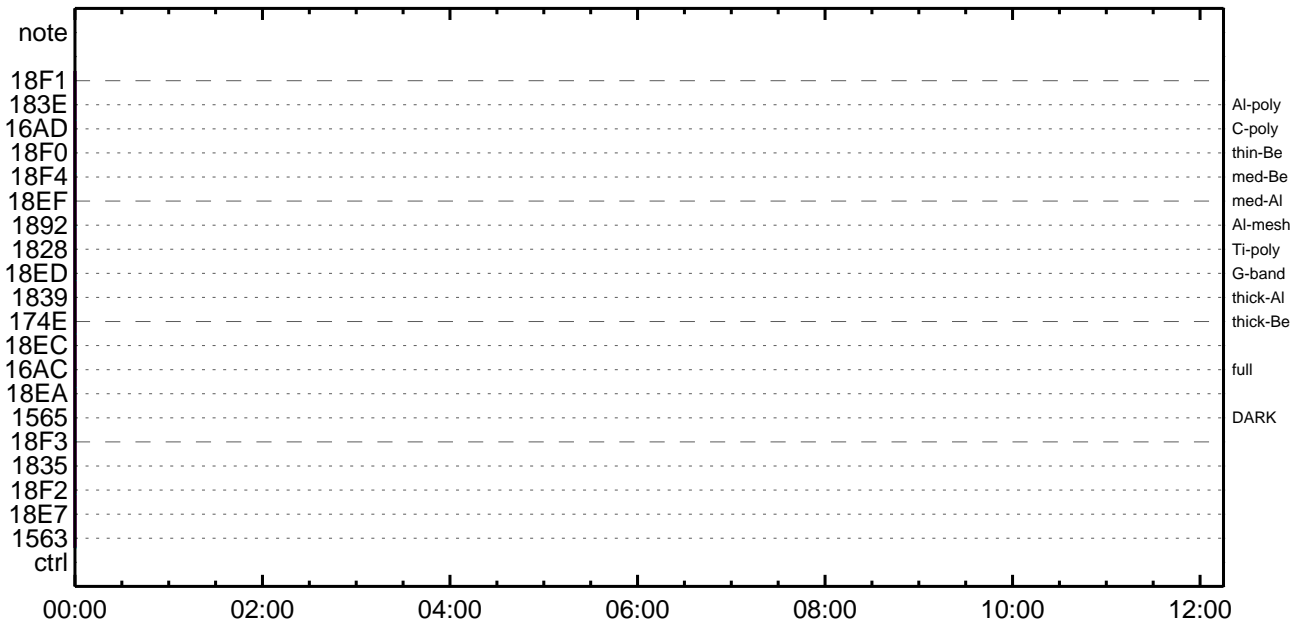
CMDI #0678 2012/07/05



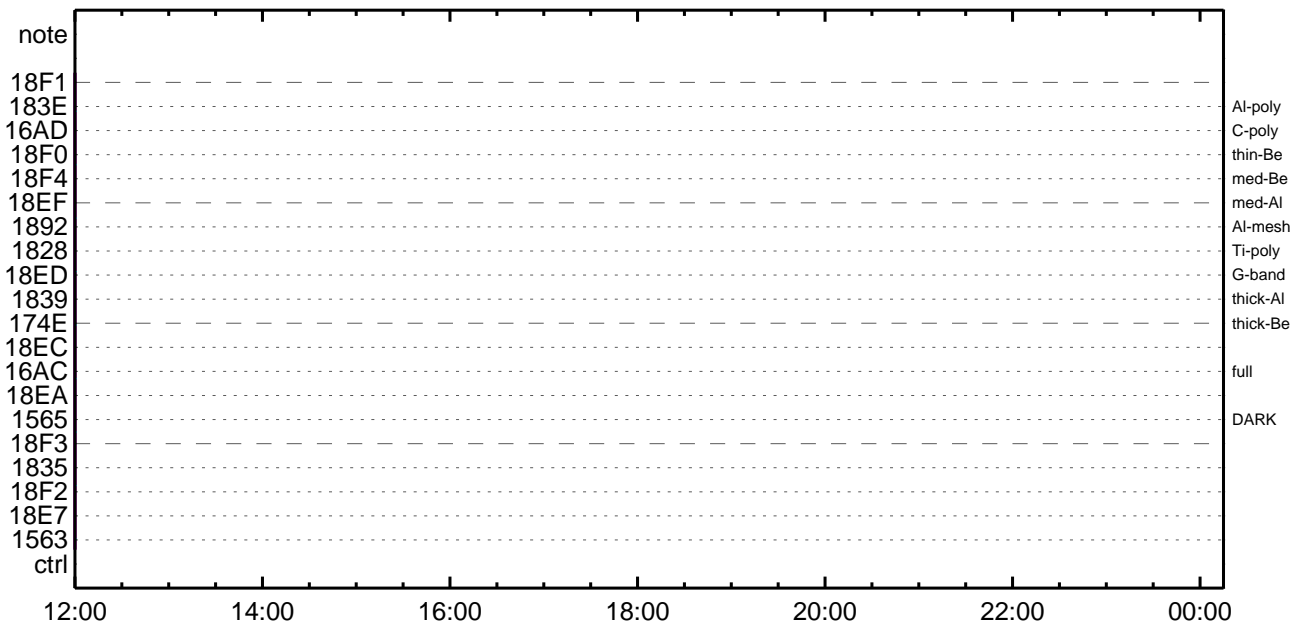
CMDI #0678 2012/07/05



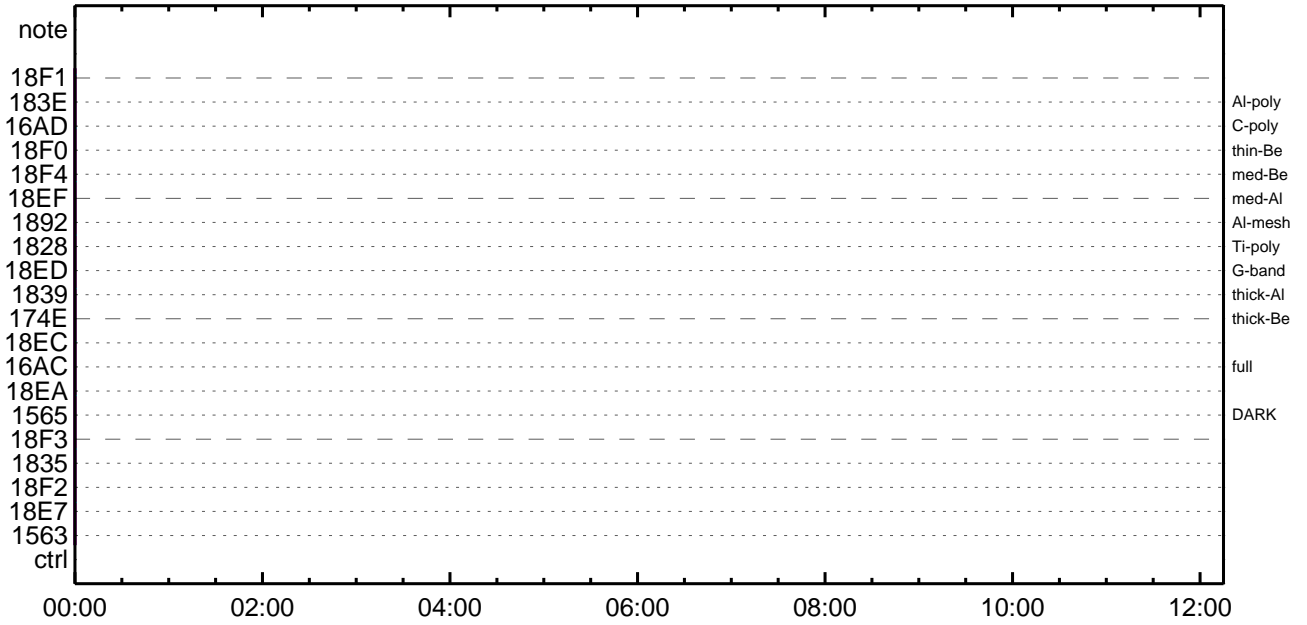
CMDI #0678 2012/07/06



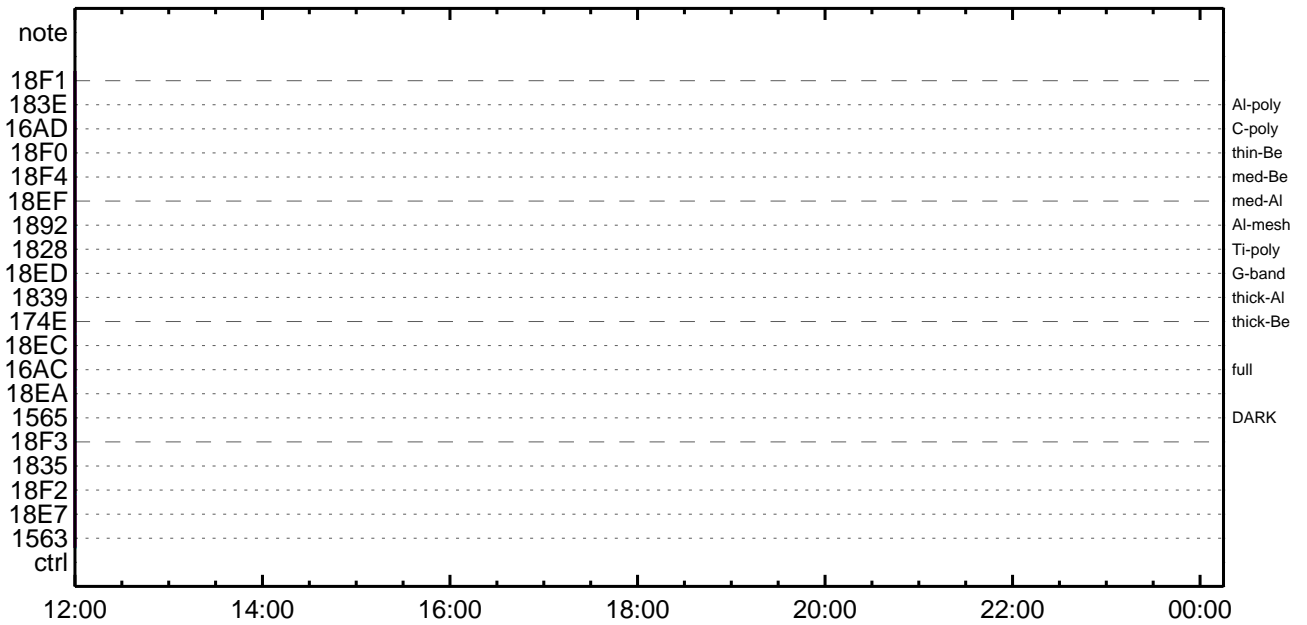
CMDI #0678 2012/07/06



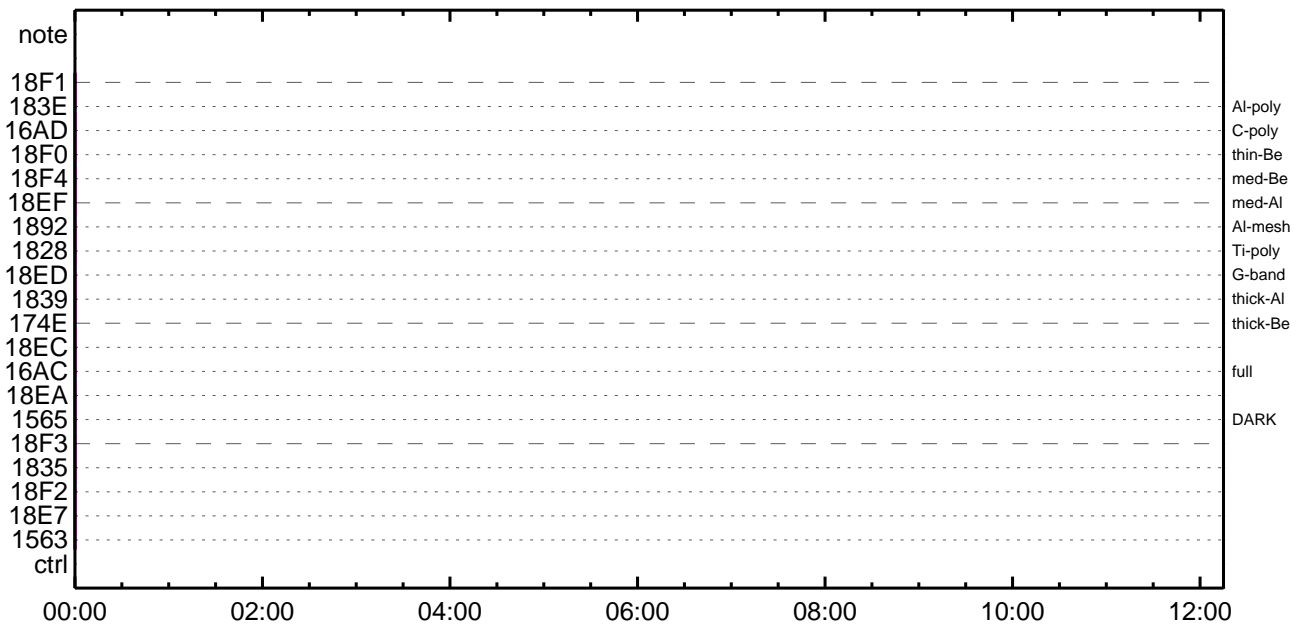
CMDI #0678 2012/07/07



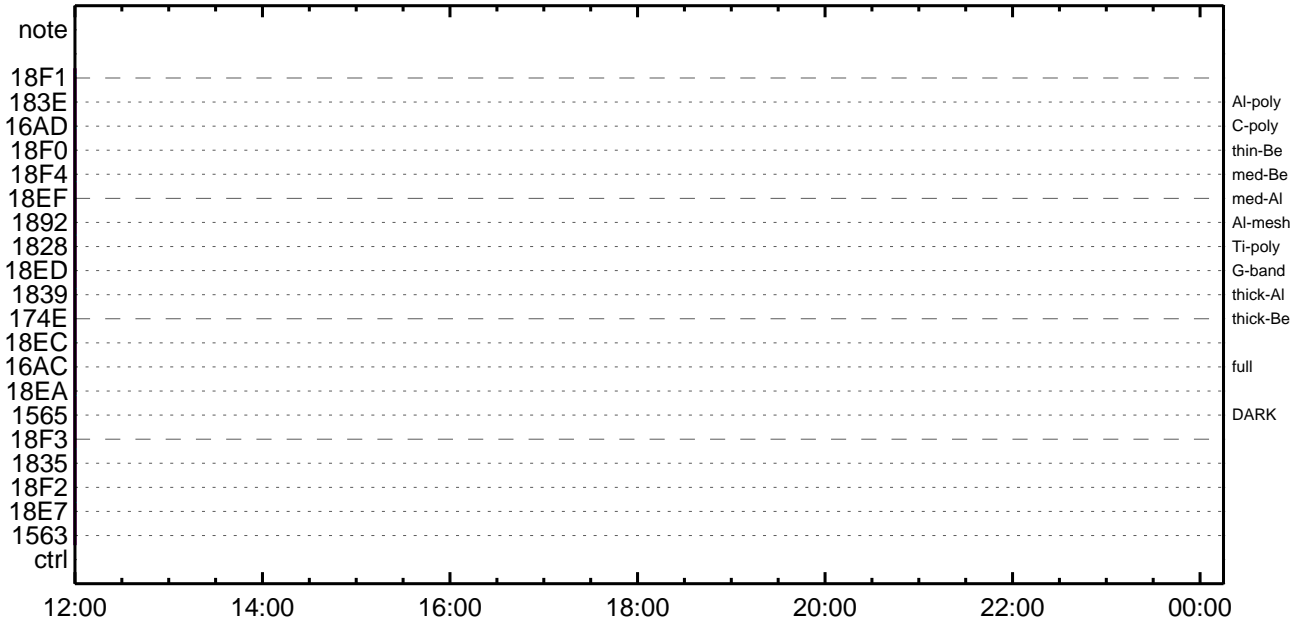
CMDI #0678 2012/07/07



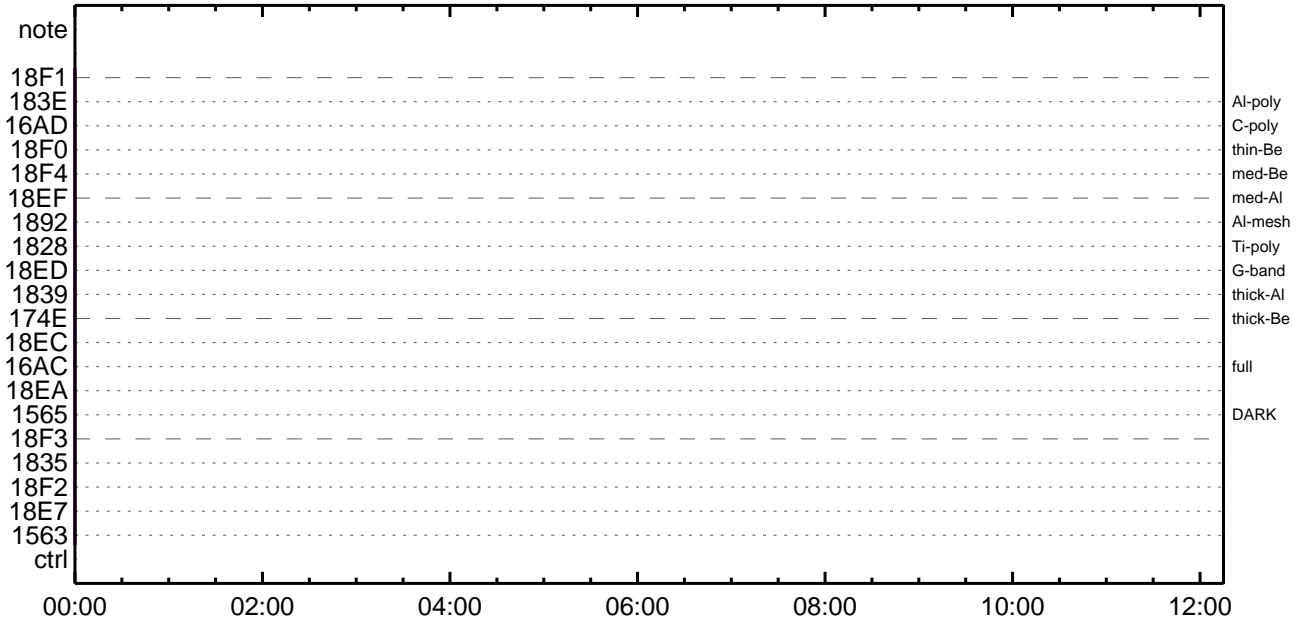
CMDI #0678 2012/07/08



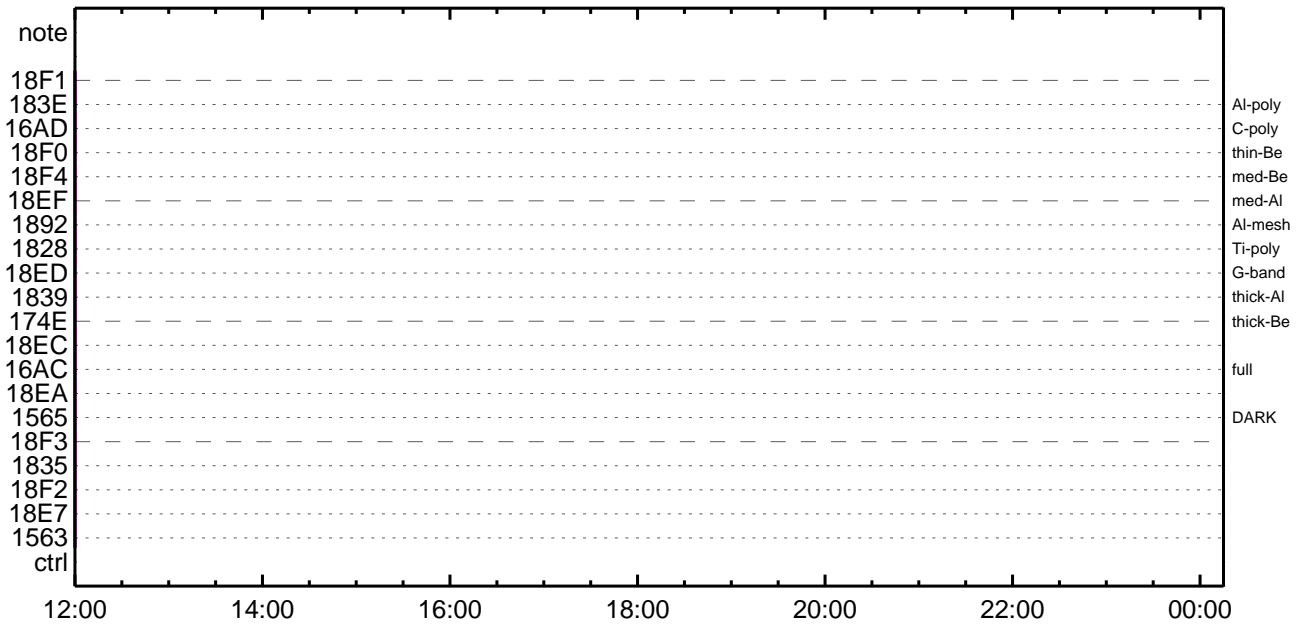
CMDI #0678 2012/07/08



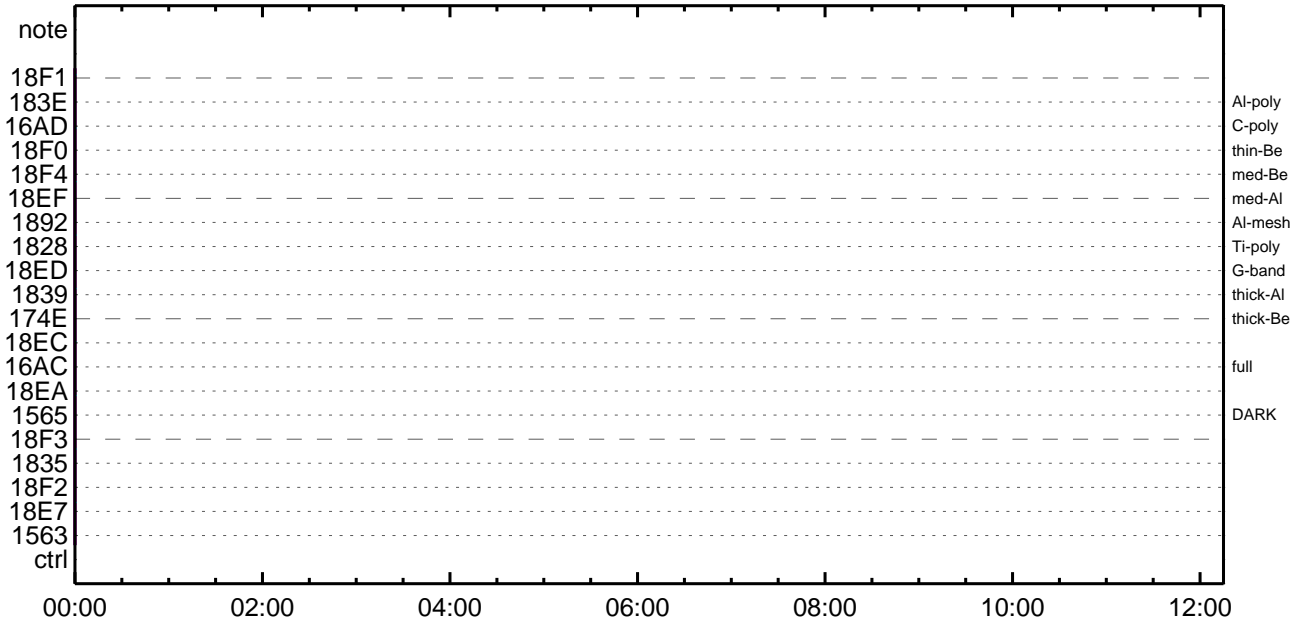
CMDI #0678 2012/07/09



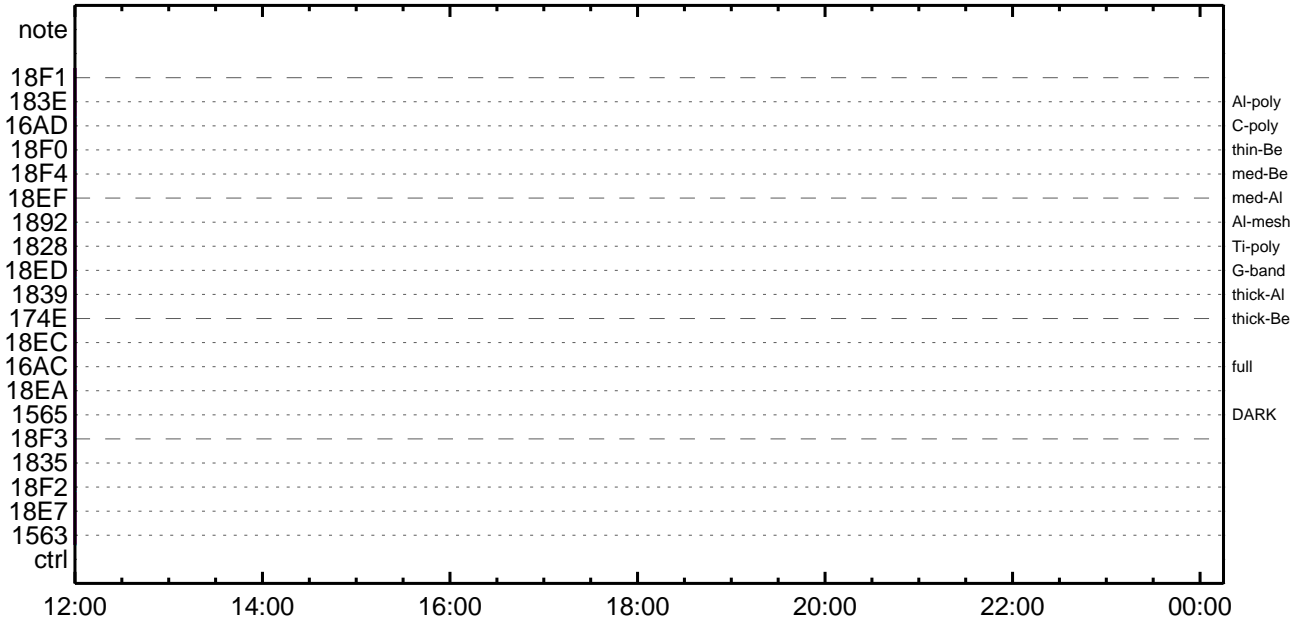
CMDI #0678 2012/07/09



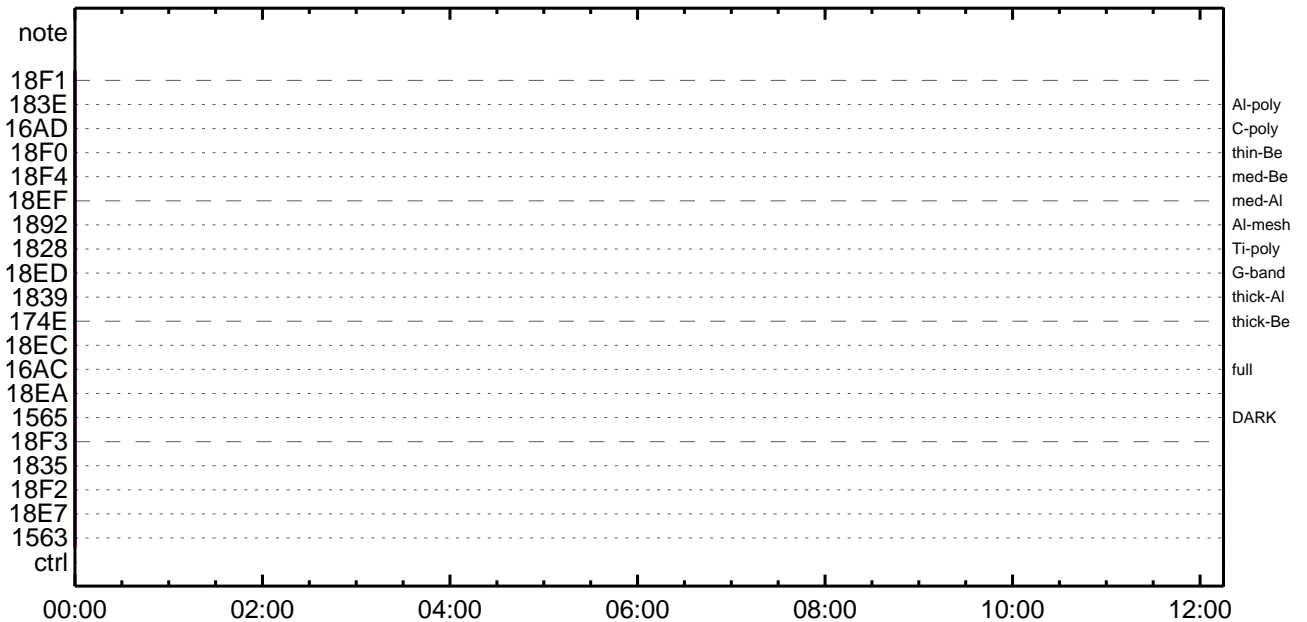
CMDI #0678 2012/07/10



CMDI #0678 2012/07/10

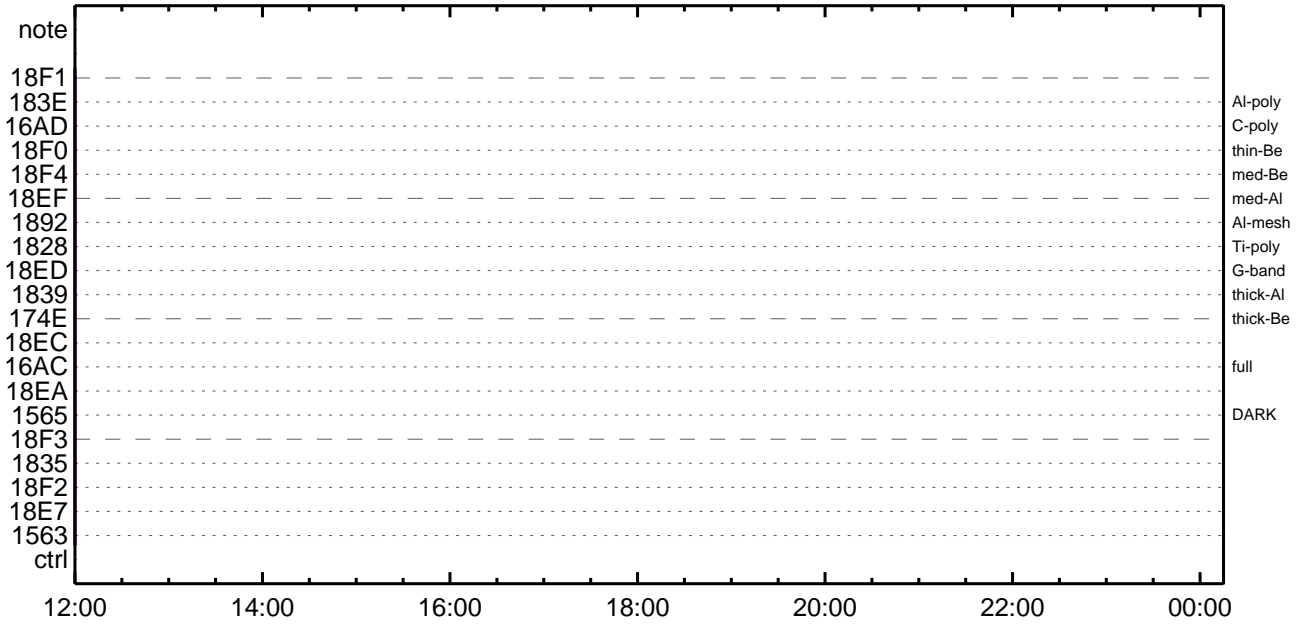


CMDI #0678 2012/07/11

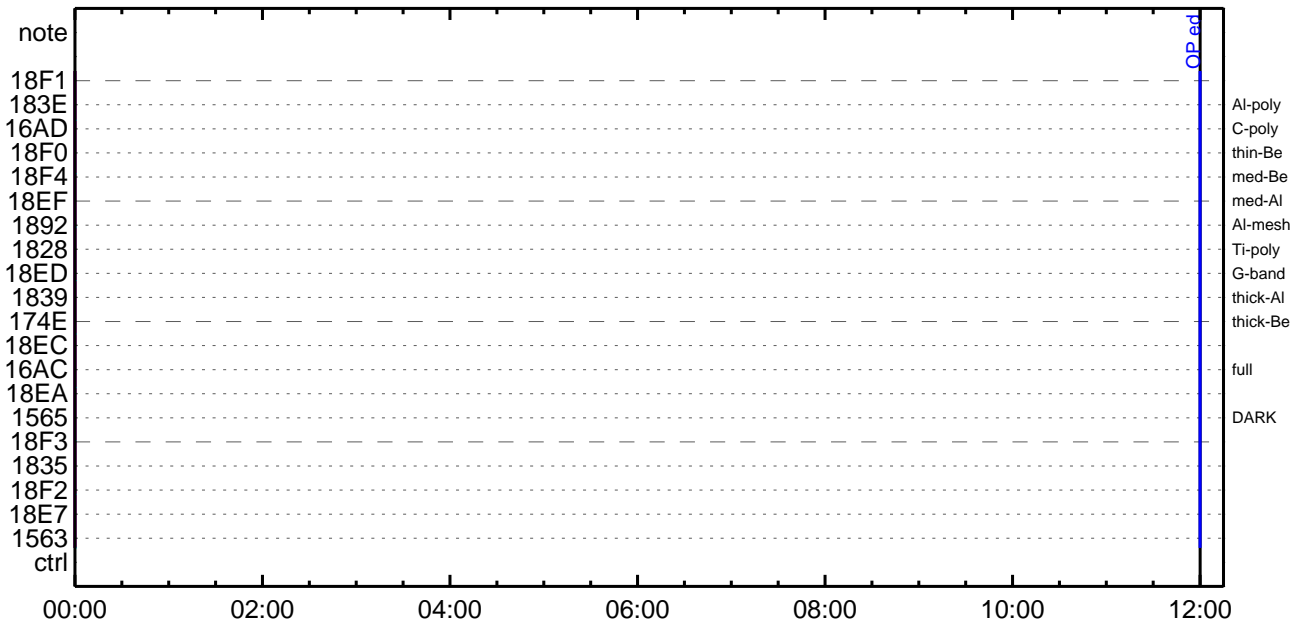




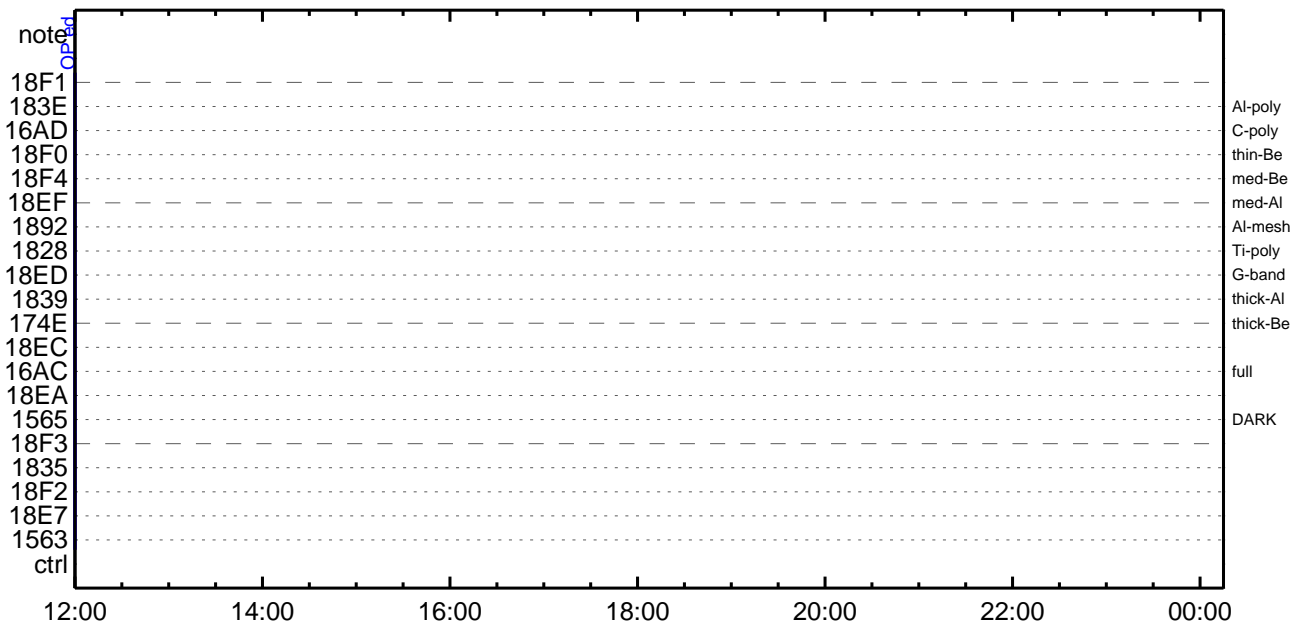
CMDI #0678 2012/07/11



CMDI #0678 2012/07/12



CMDI #0678 2012/07/12













```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 80 80 20 04)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 0f 80 80 06 06)
0128 + DC 07-F0 MDP_XRT_FLD_DIS
0129 BC (d9)
0130 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0131 BC (c9)
0132 + DC 07-F0 MDP_XRT_AEC_RESET
0133 BC (d0)
0134 + DC 07-F0 MDP_XRT_ARS_DIS
0135 BC (d5)
0136 + DC 07-F0 MDP_XRT_FLD_RESET
0137 BC (da)
0138 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0139 BC (c4 06)
0140 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0141 BC (c5 02)
0142 . C. ----- Success Verify ? OK / NG ____
0143 C.
0144 C.
0145 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0146 C.
0147 +. DC 07-F0 MDP_XRT_MODE_OBSV
0148 BC (c2)
0149 +. TI 2012-06-07 10:26:02.0
0150 DC 07-F0 MDP_XRT_MODE_OBSV
0151 BC (c2)
0152 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0153 BC (c0)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C. ***** XRT END *****
0157 . C. *****
0158 C. SOT table upload
0159 C. *****
0160 . C. < Stop FG table >
0161 +. DC 07-F0 MDP_FG_CTRL_MANU
0162 BC (51)
0163 . C. -----
0164 C. MDP_FG_CTRL_MODE = MANU [ ]
0165 C. -----
0166 C.
0167 . C. <Upload FG Observation Table>
0168 . S. RAM ram-268:MDP_OBS_F
0169 ( )
0170 C.
0171 . C. < Dump RAMID=MDP_OBS_F >
0172 +. DC 07-F0 MDP_DUMP_FGTBL
0173 BC (82 07 00 00 00 38 b8)
0174 C. -----
0175 C. MDP_OBS_F verify = OK/NG [ ]
0176 C. -----
0177 C.
0178 C. *****
0179 C. SOT TI command set
0180 C. *****
0181 C. Execute, after the success of TBL upload.
0182 +. TI 2012-06-07 10:26:18.0
0183 DC 07-F0 MDP_SOT_MODE_OBSV
0184 BC (40)
0185 . C. -----
0186 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0187 C. -----
0188 C.
0189 C.
0190 . C. ***** MDP `uAifî»ô%YqÊAÐa¹aêDCBC•x²è *****
0191 C. (%â°i¥ôYÄYÈYÏYËYáYçYèaE¼%aa¼A»Û¹aè)
0192 . S. DC-BC dcbc-402:DCBC
0193 (MDP_known_event)

```

0194 C.  
0195 C.  
0196 . C. \*\*\*\*\* ¥Ð¥!•ï Daily±¿ÍÑ¤È´Ø¤¹¤èDCBC•x²è \*\*\*\*\*  
0197 . S. DC-BC dcbc-153:DCBC  
0198 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0199 C.  
0200 C.  
0201 . C. ;ãLOS¥Á¥\$¥Ã¥-¼Á»Û;ã  
0202 C.  
0203 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0204 C.



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\*\*\* OP Sequence for XRT \*\*\*

|            |            |                                   |   |       |                |  |
|------------|------------|-----------------------------------|---|-------|----------------|--|
| 2012/06/07 | 10:36:54.0 | XRT_CTRL_MANU_400_OG [0x190]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0 | c1             |  |
| 2012/06/07 | 10:36:56.0 | XRT_ARS_DIS_429_OG [0x1ad]        |   |       |                |  |
|            |            | MDP_XRT_ARS_DIS                   | 1 | 07-F0 | d5             |  |
| 2012/06/07 | 10:36:58.0 | XRT_FLD_DIS_425_OG [0x1a9]        |   |       |                |  |
|            |            | MDP_XRT_FLD_DIS                   | 1 | 07-F0 | d9             |  |
| 2012/06/07 | 10:37:00.0 | AOCS_Ore-point_Start_1_OG [0x097] |   |       |                |  |
|            |            | AOCU_NM                           | 5 | 02-76 | 04 00 00 00 00 |  |
| 2012/06/07 | 10:37:00.5 | XRT_FLRCTRL_DIS_430_OG [0x1ae]    |   |       |                |  |
|            |            | MDP_XRT_FLRCTRL_DIS               | 1 | 07-F0 | c9             |  |
| 2012/06/07 | 10:39:58.5 | XRT_QT_PROG_SET_437_OG [0x1b5]    |   |       |                |  |
|            |            | MDP_XRT_QT_PROG_SET               | 2 | 07-F0 | c4 06          |  |
| 2012/06/07 | 10:40:00.5 | XRT_CTRL_AUTO_406_OG [0x196]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0 | c0             |  |
| 2012/06/07 | 11:46:00.0 | XRT_CTRL_MANU_408_OG [0x198]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0 | c1             |  |
| 2012/06/07 | 11:46:02.0 | XRT_FLD_RESET_412_OG [0x19c]      |   |       |                |  |
|            |            | MDP_XRT_FLD_RESET                 | 1 | 07-F0 | da             |  |
| 2012/06/07 | 11:46:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    |   |       |                |  |
|            |            | MDP_XRT_PREFLR_STRT               | 1 | 07-F0 | e8             |  |
| 2012/06/07 | 11:49:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |       |                |  |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0 | e9             |  |
| 2012/06/07 | 12:16:00.0 | XRT_Custom_418_OG [0x1a2]         |   |       |                |  |
| 2012/06/07 | 12:17:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0 | c0             |  |
| 2012/06/07 | 13:24:30.0 | XRT_CTRL_MANU_408_OG [0x198]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0 | c1             |  |
| 2012/06/07 | 13:24:32.0 | XRT_FLD_RESET_412_OG [0x19c]      |   |       |                |  |
|            |            | MDP_XRT_FLD_RESET                 | 1 | 07-F0 | da             |  |
| 2012/06/07 | 13:24:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    |   |       |                |  |
|            |            | MDP_XRT_PREFLR_STRT               | 1 | 07-F0 | e8             |  |
| 2012/06/07 | 13:27:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |       |                |  |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0 | e9             |  |
| 2012/06/07 | 13:54:30.0 | XRT_Custom_418_OG [0x1a2]         |   |       |                |  |
| 2012/06/07 | 13:55:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0 | c0             |  |
| 2012/06/07 | 14:06:54.0 | XRT_CTRL_MANU_400_OG [0x190]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0 | c1             |  |
| 2012/06/07 | 14:06:56.0 | XRT_ARS_DIS_429_OG [0x1ad]        |   |       |                |  |
|            |            | MDP_XRT_ARS_DIS                   | 1 | 07-F0 | d5             |  |
| 2012/06/07 | 14:06:58.0 | XRT_FLD_DIS_425_OG [0x1a9]        |   |       |                |  |
|            |            | MDP_XRT_FLD_DIS                   | 1 | 07-F0 | d9             |  |
| 2012/06/07 | 14:07:00.0 | AOCS_Ore-point_Start_2_OG [0x098] |   |       |                |  |
|            |            | AOCU_NM                           | 5 | 02-76 | 02 00 00 00 00 |  |
| 2012/06/07 | 14:07:00.5 | XRT_FLRCTRL_DIS_430_OG [0x1ae]    |   |       |                |  |
|            |            | MDP_XRT_FLRCTRL_DIS               | 1 | 07-F0 | c9             |  |
| 2012/06/07 | 14:09:58.5 | XRT_QT_PROG_SET_437_OG [0x1b5]    |   |       |                |  |
|            |            | MDP_XRT_QT_PROG_SET               | 2 | 07-F0 | c4 06          |  |
| 2012/06/07 | 14:10:00.5 | XRT_CTRL_AUTO_406_OG [0x196]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0 | c0             |  |
| 2012/06/07 | 15:03:00.0 | XRT_CTRL_MANU_408_OG [0x198]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0 | c1             |  |
| 2012/06/07 | 15:03:02.0 | XRT_FLD_RESET_412_OG [0x19c]      |   |       |                |  |
|            |            | MDP_XRT_FLD_RESET                 | 1 | 07-F0 | da             |  |
| 2012/06/07 | 15:03:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    |   |       |                |  |
|            |            | MDP_XRT_PREFLR_STRT               | 1 | 07-F0 | e8             |  |
| 2012/06/07 | 15:06:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |       |                |  |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0 | e9             |  |
| 2012/06/07 | 15:50:30.0 | XRT_Custom_418_OG [0x1a2]         |   |       |                |  |
| 2012/06/07 | 15:51:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0 | c0             |  |
| 2012/06/07 | 16:41:30.0 | XRT_CTRL_MANU_408_OG [0x198]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0 | c1             |  |
| 2012/06/07 | 16:41:32.0 | XRT_FLD_RESET_412_OG [0x19c]      |   |       |                |  |
|            |            | MDP_XRT_FLD_RESET                 | 1 | 07-F0 | da             |  |
| 2012/06/07 | 16:41:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    |   |       |                |  |
|            |            | MDP_XRT_PREFLR_STRT               | 1 | 07-F0 | e8             |  |
| 2012/06/07 | 16:44:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |       |                |  |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0 | e9             |  |
| 2012/06/07 | 17:27:00.0 | XRT_Custom_418_OG [0x1a2]         |   |       |                |  |
| 2012/06/07 | 17:28:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0 | c0             |  |
| 2012/06/07 | 18:20:00.0 | XRT_CTRL_MANU_408_OG [0x198]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0 | c1             |  |
| 2012/06/07 | 18:20:02.0 | XRT_FLD_RESET_412_OG [0x19c]      |   |       |                |  |
|            |            | MDP_XRT_FLD_RESET                 | 1 | 07-F0 | da             |  |
| 2012/06/07 | 18:20:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    |   |       |                |  |
|            |            | MDP_XRT_PREFLR_STRT               | 1 | 07-F0 | e8             |  |
| 2012/06/07 | 18:23:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |       |                |  |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0 | e9             |  |
| 2012/06/07 | 19:04:00.0 | XRT_Custom_418_OG [0x1a2]         |   |       |                |  |
| 2012/06/07 | 19:05:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0 | c0             |  |
| 2012/06/07 | 19:58:30.0 | XRT_CTRL_MANU_408_OG [0x198]      |   |       |                |  |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0 | c1             |  |
| 2012/06/07 | 19:58:32.0 | XRT_FLD_RESET_412_OG [0x19c]      |   |       |                |  |
|            |            | MDP_XRT_FLD_RESET                 | 1 | 07-F0 | da             |  |
| 2012/06/07 | 19:58:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    |   |       |                |  |
|            |            | MDP_XRT_PREFLR_STRT               | 1 | 07-F0 | e8             |  |
| 2012/06/07 | 20:01:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |       |                |  |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0 | e9             |  |

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|            |            |                                   |   |                      |       |
|------------|------------|-----------------------------------|---|----------------------|-------|
| 2012/06/07 | 20:41:00.0 | XRT_Custom_418_OG [0x1a2]         |   |                      |       |
| 2012/06/07 | 20:42:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |                      |       |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0                | c0    |
| 2012/06/07 | 21:37:00.0 | XRT_CTRL_MANU_408_OG [0x198]      |   |                      |       |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0                | c1    |
| 2012/06/07 | 21:37:02.0 | XRT_FLD_RESET_412_OG [0x19c]      |   |                      |       |
|            |            | MDP_XRT_FLD_RESET                 | 1 | 07-F0                | da    |
| 2012/06/07 | 21:37:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STRT               | 1 | 07-F0                | e8    |
| 2012/06/07 | 21:40:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0                | e9    |
| 2012/06/07 | 22:17:30.0 | XRT_Custom_418_OG [0x1a2]         |   |                      |       |
| 2012/06/07 | 22:18:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |                      |       |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0                | c0    |
| 2012/06/07 | 23:15:30.0 | XRT_CTRL_MANU_408_OG [0x198]      |   |                      |       |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0                | c1    |
| 2012/06/07 | 23:15:32.0 | XRT_FLD_RESET_412_OG [0x19c]      |   |                      |       |
|            |            | MDP_XRT_FLD_RESET                 | 1 | 07-F0                | da    |
| 2012/06/07 | 23:15:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STRT               | 1 | 07-F0                | e8    |
| 2012/06/07 | 23:18:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0                | e9    |
| 2012/06/07 | 23:45:30.0 | XRT_Custom_418_OG [0x1a2]         |   |                      |       |
| 2012/06/07 | 23:46:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |                      |       |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0                | c0    |
| 2012/06/08 | 00:54:00.0 | XRT_CTRL_MANU_408_OG [0x198]      |   |                      |       |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0                | c1    |
| 2012/06/08 | 00:54:02.0 | XRT_FLD_RESET_412_OG [0x19c]      |   |                      |       |
|            |            | MDP_XRT_FLD_RESET                 | 1 | 07-F0                | da    |
| 2012/06/08 | 00:54:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STRT               | 1 | 07-F0                | e8    |
| 2012/06/08 | 00:57:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0                | e9    |
| 2012/06/08 | 01:24:00.0 | XRT_Custom_418_OG [0x1a2]         |   |                      |       |
| 2012/06/08 | 01:25:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |                      |       |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0                | c0    |
| 2012/06/08 | 02:31:30.0 | XRT_CTRL_MANU_408_OG [0x198]      |   |                      |       |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0                | c1    |
| 2012/06/08 | 02:31:32.0 | XRT_FLD_RESET_412_OG [0x19c]      |   |                      |       |
|            |            | MDP_XRT_FLD_RESET                 | 1 | 07-F0                | da    |
| 2012/06/08 | 02:31:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STRT               | 1 | 07-F0                | e8    |
| 2012/06/08 | 02:34:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0                | e9    |
| 2012/06/08 | 03:02:30.0 | XRT_Custom_418_OG [0x1a2]         |   |                      |       |
| 2012/06/08 | 03:03:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |                      |       |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0                | c0    |
| 2012/06/08 | 04:00:30.0 | XRT_CTRL_MANU_408_OG [0x198]      |   |                      |       |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0                | c1    |
| 2012/06/08 | 04:00:32.0 | XRT_FLD_RESET_412_OG [0x19c]      |   |                      |       |
|            |            | MDP_XRT_FLD_RESET                 | 1 | 07-F0                | da    |
| 2012/06/08 | 04:00:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STRT               | 1 | 07-F0                | e8    |
| 2012/06/08 | 04:03:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0                | e9    |
| 2012/06/08 | 04:41:00.0 | XRT_Custom_418_OG [0x1a2]         |   |                      |       |
| 2012/06/08 | 04:42:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |                      |       |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0                | c0    |
| 2012/06/08 | 05:41:00.5 | XRT_CTRL_MANU_408_OG [0x198]      |   |                      |       |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0                | c1    |
| 2012/06/08 | 05:41:02.5 | XRT_FLD_RESET_412_OG [0x19c]      |   |                      |       |
|            |            | MDP_XRT_FLD_RESET                 | 1 | 07-F0                | da    |
| 2012/06/08 | 05:41:04.5 | XRT_PREFLR_STRT_422_OG [0x1a6]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STRT               | 1 | 07-F0                | e8    |
| 2012/06/08 | 05:44:14.5 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0                | e9    |
| 2012/06/08 | 06:19:30.0 | XRT_Custom_418_OG [0x1a2]         |   |                      |       |
| 2012/06/08 | 06:20:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |                      |       |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0                | c0    |
| 2012/06/08 | 07:21:30.0 | XRT_CTRL_MANU_408_OG [0x198]      |   |                      |       |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0                | c1    |
| 2012/06/08 | 07:21:32.0 | XRT_FLD_RESET_412_OG [0x19c]      |   |                      |       |
|            |            | MDP_XRT_FLD_RESET                 | 1 | 07-F0                | da    |
| 2012/06/08 | 07:21:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STRT               | 1 | 07-F0                | e8    |
| 2012/06/08 | 07:24:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |                      |       |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0                | e9    |
| 2012/06/08 | 07:58:00.5 | XRT_Custom_418_OG [0x1a2]         |   |                      |       |
| 2012/06/08 | 07:59:00.5 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |                      |       |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0                | c0    |
| 2012/06/08 | 07:59:54.0 | XRT_CTRL_MANU_400_OG [0x190]      |   |                      |       |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0                | c1    |
| 2012/06/08 | 07:59:56.0 | XRT_ARS_DIS_429_OG [0x1ad]        |   |                      |       |
|            |            | MDP_XRT_ARS_DIS                   | 1 | 07-F0                | d5    |
| 2012/06/08 | 07:59:58.0 | XRT_FLD_DIS_425_OG [0x1a9]        |   |                      |       |
|            |            | MDP_XRT_FLD_DIS                   | 1 | 07-F0                | d9    |
| 2012/06/08 | 08:00:00.0 | AOCS_Ore-point_Start_3_OG [0x099] |   |                      |       |
|            |            | AOCU_NM                           | 5 | 02-76 00 57 ce 00 00 |       |
| 2012/06/08 | 08:00:00.5 | XRT_FLRCTRL_DIS_430_OG [0x1ae]    |   |                      |       |
|            |            | MDP_XRT_FLRCTRL_DIS               | 1 | 07-F0                | c9    |
| 2012/06/08 | 08:02:58.5 | XRT_QT_PROG_SET_437_OG [0x1b5]    |   |                      |       |
|            |            | MDP_XRT_QT_PROG_SET               | 2 | 07-F0                | c4 06 |
| 2012/06/08 | 08:03:00.5 | XRT_CTRL_AUTO_406_OG [0x196]      |   |                      |       |

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|            |            |                                   |                              |   |       |                |  |
|------------|------------|-----------------------------------|------------------------------|---|-------|----------------|--|
| 2012/06/08 | 09:01:00.0 | XRT_CTRL_MANU_408_OG [0x198]      | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
|            |            |                                   | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
| 2012/06/08 | 09:01:02.0 | XRT_FLD_RESET_412_OG [0x19c]      | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | da             |  |
|            |            |                                   | MDP_XRT_FLD_RESET            | 1 | 07-F0 | da             |  |
| 2012/06/08 | 09:01:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
|            |            |                                   | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
| 2012/06/08 | 09:04:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
|            |            |                                   | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
| 2012/06/08 | 09:36:30.0 | XRT_Custom_418_OG [0x1a2]         | XRT_CTRL_AUTO_419_OG [0x1a3] |   |       |                |  |
| 2012/06/08 | 09:37:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
|            |            |                                   | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
| 2012/06/08 | 09:59:54.0 | XRT_CTRL_MANU_400_OG [0x190]      | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
|            |            |                                   | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
| 2012/06/08 | 09:59:56.0 | XRT_ARS_DIS_429_OG [0x1ad]        | MDP_XRT_ARS_DIS              | 1 | 07-F0 | d5             |  |
|            |            |                                   | MDP_XRT_ARS_DIS              | 1 | 07-F0 | d5             |  |
| 2012/06/08 | 09:59:58.0 | XRT_FLD_DIS_425_OG [0x1a9]        | MDP_XRT_FLD_DIS              | 1 | 07-F0 | d9             |  |
|            |            |                                   | MDP_XRT_FLD_DIS              | 1 | 07-F0 | d9             |  |
| 2012/06/08 | 10:00:00.0 | AOCS_Ore-point_Start_2_OG [0x098] | AOCU_NM                      | 5 | 02-76 | 02 00 00 00 00 |  |
|            |            |                                   | AOCU_NM                      | 5 | 02-76 | 02 00 00 00 00 |  |
| 2012/06/08 | 10:00:00.5 | XRT_FLRCTRL_DIS_430_OG [0x1ae]    | MDP_XRT_FLRCTRL_DIS          | 1 | 07-F0 | c9             |  |
|            |            |                                   | MDP_XRT_FLRCTRL_DIS          | 1 | 07-F0 | c9             |  |
| 2012/06/08 | 10:02:58.5 | XRT_QT_PROG_SET_437_OG [0x1b5]    | MDP_XRT_QT_PROG_SET          | 2 | 07-F0 | c4 06          |  |
|            |            |                                   | MDP_XRT_QT_PROG_SET          | 2 | 07-F0 | c4 06          |  |
| 2012/06/08 | 10:03:00.5 | XRT_CTRL_AUTO_406_OG [0x196]      | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
|            |            |                                   | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
| 2012/06/08 | 10:44:00.0 | XRT_CTRL_MANU_408_OG [0x198]      | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
|            |            |                                   | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
| 2012/06/08 | 10:44:02.0 | XRT_FLD_RESET_412_OG [0x19c]      | MDP_XRT_FLD_RESET            | 1 | 07-F0 | da             |  |
|            |            |                                   | MDP_XRT_FLD_RESET            | 1 | 07-F0 | da             |  |
| 2012/06/08 | 10:44:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
|            |            |                                   | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
| 2012/06/08 | 10:47:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
|            |            |                                   | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
| 2012/06/08 | 11:15:00.0 | XRT_Custom_418_OG [0x1a2]         | XRT_CTRL_AUTO_419_OG [0x1a3] |   |       |                |  |
| 2012/06/08 | 11:16:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
|            |            |                                   | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
| 2012/06/08 | 12:23:30.0 | XRT_CTRL_MANU_408_OG [0x198]      | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
|            |            |                                   | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
| 2012/06/08 | 12:23:32.0 | XRT_FLD_RESET_412_OG [0x19c]      | MDP_XRT_FLD_RESET            | 1 | 07-F0 | da             |  |
|            |            |                                   | MDP_XRT_FLD_RESET            | 1 | 07-F0 | da             |  |
| 2012/06/08 | 12:23:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
|            |            |                                   | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
| 2012/06/08 | 12:26:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
|            |            |                                   | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
| 2012/06/08 | 12:53:30.0 | XRT_Custom_418_OG [0x1a2]         | XRT_CTRL_AUTO_419_OG [0x1a3] |   |       |                |  |
| 2012/06/08 | 12:54:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
|            |            |                                   | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
| 2012/06/08 | 14:02:00.0 | XRT_CTRL_MANU_408_OG [0x198]      | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
|            |            |                                   | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
| 2012/06/08 | 14:02:02.0 | XRT_FLD_RESET_412_OG [0x19c]      | MDP_XRT_FLD_RESET            | 1 | 07-F0 | da             |  |
|            |            |                                   | MDP_XRT_FLD_RESET            | 1 | 07-F0 | da             |  |
| 2012/06/08 | 14:02:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
|            |            |                                   | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
| 2012/06/08 | 14:05:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
|            |            |                                   | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
| 2012/06/08 | 14:39:00.0 | XRT_Custom_418_OG [0x1a2]         | XRT_CTRL_AUTO_419_OG [0x1a3] |   |       |                |  |
| 2012/06/08 | 14:40:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
|            |            |                                   | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
| 2012/06/08 | 15:40:30.0 | XRT_CTRL_MANU_408_OG [0x198]      | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
|            |            |                                   | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
| 2012/06/08 | 15:40:32.0 | XRT_FLD_RESET_412_OG [0x19c]      | MDP_XRT_FLD_RESET            | 1 | 07-F0 | da             |  |
|            |            |                                   | MDP_XRT_FLD_RESET            | 1 | 07-F0 | da             |  |
| 2012/06/08 | 15:40:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
|            |            |                                   | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
| 2012/06/08 | 15:43:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
|            |            |                                   | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
| 2012/06/08 | 16:27:00.0 | XRT_Custom_418_OG [0x1a2]         | XRT_CTRL_AUTO_419_OG [0x1a3] |   |       |                |  |
| 2012/06/08 | 16:28:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
|            |            |                                   | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
| 2012/06/08 | 17:19:00.0 | XRT_CTRL_MANU_408_OG [0x198]      | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
|            |            |                                   | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
| 2012/06/08 | 17:19:02.0 | XRT_FLD_RESET_412_OG [0x19c]      | MDP_XRT_FLD_RESET            | 1 | 07-F0 | da             |  |
|            |            |                                   | MDP_XRT_FLD_RESET            | 1 | 07-F0 | da             |  |
| 2012/06/08 | 17:19:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
|            |            |                                   | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
| 2012/06/08 | 17:22:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
|            |            |                                   | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
| 2012/06/08 | 18:04:00.0 | XRT_Custom_418_OG [0x1a2]         | XRT_CTRL_AUTO_419_OG [0x1a3] |   |       |                |  |
| 2012/06/08 | 18:05:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
|            |            |                                   | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
| 2012/06/08 | 18:57:30.0 | XRT_CTRL_MANU_408_OG [0x198]      | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
|            |            |                                   | MDP_XRT_CTRL_MANU            | 1 | 07-F0 | c1             |  |
| 2012/06/08 | 18:57:32.0 | XRT_FLD_RESET_412_OG [0x19c]      | MDP_XRT_FLD_RESET            | 1 | 07-F0 | da             |  |
|            |            |                                   | MDP_XRT_FLD_RESET            | 1 | 07-F0 | da             |  |
| 2012/06/08 | 18:57:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]    | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
|            |            |                                   | MDP_XRT_PREFLR_STRT          | 1 | 07-F0 | e8             |  |
| 2012/06/08 | 19:00:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
|            |            |                                   | MDP_XRT_PREFLR_STOP          | 1 | 07-F0 | e9             |  |
| 2012/06/08 | 19:40:30.0 | XRT_Custom_418_OG [0x1a2]         | XRT_CTRL_AUTO_419_OG [0x1a3] |   |       |                |  |
| 2012/06/08 | 19:41:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
|            |            |                                   | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |
| 2012/06/08 | 20:36:00.0 | XRT_CTRL_MANU_408_OG [0x198]      | MDP_XRT_CTRL_AUTO            | 1 | 07-F0 | c0             |  |

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|            |            |   |                      |   |       |       |
|------------|------------|---|----------------------|---|-------|-------|
| 2012/06/08 | 20:36:02.0 | XRT_FLD_RESET_412_OG [0x19c]            | MDP_XRT_CTRL_MANU    | 1 | 07-F0 | c1    |
|            |            |   | MDP_XRT_FLD_RESET    | 1 | 07-F0 | da    |
| 2012/06/08 | 20:36:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6]          | MDP_XRT_PREFLR_STRT  | 1 | 07-F0 | e8    |
| 2012/06/08 | 20:39:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8]          | MDP_XRT_PREFLR_STOP  | 1 | 07-F0 | e9    |
| 2012/06/08 | 21:18:00.0 | XRT_Custom_418_OG [0x1a2]               |                      |   |       |       |
| 2012/06/08 | 21:19:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]            | MDP_XRT_CTRL_AUTO    | 1 | 07-F0 | c0    |
| 2012/06/08 | 22:14:30.0 | XRT_CTRL_MANU_408_OG [0x198]            | MDP_XRT_CTRL_MANU    | 1 | 07-F0 | c1    |
| 2012/06/08 | 22:14:32.0 | XRT_FLD_RESET_412_OG [0x19c]            | MDP_XRT_FLD_RESET    | 1 | 07-F0 | da    |
| 2012/06/08 | 22:14:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]          | MDP_XRT_PREFLR_STRT  | 1 | 07-F0 | e8    |
| 2012/06/08 | 22:17:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]          | MDP_XRT_PREFLR_STOP  | 1 | 07-F0 | e9    |
| 2012/06/08 | 22:53:00.0 | XRT_Custom_418_OG [0x1a2]               |                      |   |       |       |
| 2012/06/08 | 22:54:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]            | MDP_XRT_CTRL_AUTO    | 1 | 07-F0 | c0    |
| 2012/06/08 | 23:53:00.0 | XRT_CTRL_MANU_408_OG [0x198]            | MDP_XRT_CTRL_MANU    | 1 | 07-F0 | c1    |
| 2012/06/08 | 23:53:02.0 | XRT_FLD_RESET_412_OG [0x19c]            | MDP_XRT_FLD_RESET    | 1 | 07-F0 | da    |
| 2012/06/08 | 23:53:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6]          | MDP_XRT_PREFLR_STRT  | 1 | 07-F0 | e8    |
| 2012/06/08 | 23:56:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8]          | MDP_XRT_PREFLR_STOP  | 1 | 07-F0 | e9    |
| 2012/06/09 | 00:23:00.0 | XRT_Custom_418_OG [0x1a2]               |                      |   |       |       |
| 2012/06/09 | 00:24:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]            | MDP_XRT_CTRL_AUTO    | 1 | 07-F0 | c0    |
| 2012/06/09 | 01:31:30.0 | XRT_CTRL_MANU_408_OG [0x198]            | MDP_XRT_CTRL_MANU    | 1 | 07-F0 | c1    |
| 2012/06/09 | 01:31:32.0 | XRT_FLD_RESET_412_OG [0x19c]            | MDP_XRT_FLD_RESET    | 1 | 07-F0 | da    |
| 2012/06/09 | 01:31:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]          | MDP_XRT_PREFLR_STRT  | 1 | 07-F0 | e8    |
| 2012/06/09 | 01:34:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]          | MDP_XRT_PREFLR_STOP  | 1 | 07-F0 | e9    |
| 2012/06/09 | 02:01:30.0 | XRT_Custom_418_OG [0x1a2]               |                      |   |       |       |
| 2012/06/09 | 02:02:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3]            | MDP_XRT_CTRL_AUTO    | 1 | 07-F0 | c0    |
| 2012/06/09 | 03:08:30.0 | XRT_CTRL_MANU_408_OG [0x198]            | MDP_XRT_CTRL_MANU    | 1 | 07-F0 | c1    |
| 2012/06/09 | 03:08:32.0 | XRT_FLD_RESET_412_OG [0x19c]            | MDP_XRT_FLD_RESET    | 1 | 07-F0 | da    |
| 2012/06/09 | 03:08:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]          | MDP_XRT_PREFLR_STRT  | 1 | 07-F0 | e8    |
| 2012/06/09 | 03:11:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]          | MDP_XRT_PREFLR_STOP  | 1 | 07-F0 | e9    |
| 2012/06/09 | 03:40:00.0 | XRT_Custom_418_OG [0x1a2]               |                      |   |       |       |
| 2012/06/09 | 03:41:00.0 | XRT_CTRL_AUTO_419_OG [0x1a3]            | MDP_XRT_CTRL_AUTO    | 1 | 07-F0 | c0    |
| 2012/06/09 | 04:38:30.0 | XRT_CTRL_MANU_408_OG [0x198]            | MDP_XRT_CTRL_MANU    | 1 | 07-F0 | c1    |
| 2012/06/09 | 04:38:32.0 | XRT_FLD_RESET_412_OG [0x19c]            | MDP_XRT_FLD_RESET    | 1 | 07-F0 | da    |
| 2012/06/09 | 04:38:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]          | MDP_XRT_PREFLR_STRT  | 1 | 07-F0 | e8    |
| 2012/06/09 | 04:41:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]          | MDP_XRT_PREFLR_STOP  | 1 | 07-F0 | e9    |
| 2012/06/09 | 05:18:30.0 | XRT_Custom_418_OG [0x1a2]               |                      |   |       |       |
| 2012/06/09 | 05:19:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3]            | MDP_XRT_CTRL_AUTO    | 1 | 07-F0 | c0    |
| 2012/06/09 | 06:19:00.0 | XRT_CTRL_MANU_408_OG [0x198]            | MDP_XRT_CTRL_MANU    | 1 | 07-F0 | c1    |
| 2012/06/09 | 06:19:02.0 | XRT_FLD_RESET_412_OG [0x19c]            | MDP_XRT_FLD_RESET    | 1 | 07-F0 | da    |
| 2012/06/09 | 06:19:04.0 | XRT_PREFLR_STRT_422_OG [0x1a6]          | MDP_XRT_PREFLR_STRT  | 1 | 07-F0 | e8    |
| 2012/06/09 | 06:22:14.0 | XRT_PREFLR_STOP_424_OG [0x1a8]          | MDP_XRT_PREFLR_STOP  | 1 | 07-F0 | e9    |
| 2012/06/09 | 06:58:00.0 | XRT_CTRL_MANU_400_OG [0x190]            | MDP_XRT_CTRL_MANU    | 1 | 07-F0 | c1    |
| 2012/06/09 | 06:58:02.0 | XRT_ARS_DIS_429_OG [0x1ad]              | MDP_XRT_ARS_DIS      | 1 | 07-F0 | d5    |
| 2012/06/09 | 06:58:04.0 | XRT_FLD_DIS_425_OG [0x1a9]              | MDP_XRT_FLD_DIS      | 1 | 07-F0 | d9    |
| 2012/06/09 | 06:58:06.0 | XRT_FLRCTRL_DIS_430_OG [0x1ae]          | MDP_XRT_FLRCTRL_DIS  | 1 | 07-F0 | c9    |
| 2012/06/09 | 07:01:04.0 | XRT_QT_PROG_SET_444_OG [0x1bc]          | MDP_XRT_QT_PROG_SET  | 2 | 07-F0 | c4 01 |
| 2012/06/09 | 07:01:06.0 | XRT_CTRL_AUTO_406_OG [0x196]            | MDP_XRT_CTRL_AUTO    | 1 | 07-F0 | c0    |
| 2012/06/09 | 07:02:00.5 | XRT_TCIB_XRT_S_HTR_A_DIS_436_OG [0x1b4] | TCIB_XRT_S_HTR_A_DIS | 0 | 04-C0 |       |
| 2012/06/09 | 07:59:30.0 | XRT_CTRL_MANU_408_OG [0x198]            | MDP_XRT_CTRL_MANU    | 1 | 07-F0 | c1    |
| 2012/06/09 | 07:59:32.0 | XRT_FLD_RESET_412_OG [0x19c]            | MDP_XRT_FLD_RESET    | 1 | 07-F0 | da    |
| 2012/06/09 | 07:59:34.0 | XRT_PREFLR_STRT_422_OG [0x1a6]          | MDP_XRT_PREFLR_STRT  | 1 | 07-F0 | e8    |

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|            |            |                                   |   |       |                   |  |  |  |  |
|------------|------------|-----------------------------------|---|-------|-------------------|--|--|--|--|
| 2012/06/09 | 07:59:54.0 | XRT_CTRL_MANU_400_OG [0x190]      |   |       |                   |  |  |  |  |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0 | c1                |  |  |  |  |
| 2012/06/09 | 07:59:56.0 | XRT_ARS_DIS_429_OG [0x1ad]        |   |       |                   |  |  |  |  |
|            |            | MDP_XRT_ARS_DIS                   | 1 | 07-F0 | d5                |  |  |  |  |
| 2012/06/09 | 07:59:58.0 | XRT_FLD_DIS_425_OG [0x1a9]        |   |       |                   |  |  |  |  |
|            |            | MDP_XRT_FLD_DIS                   | 1 | 07-F0 | d9                |  |  |  |  |
| 2012/06/09 | 08:00:00.0 | AOCS_OrE-point_Start_3_OG [0x099] |   |       |                   |  |  |  |  |
|            |            | AOCU_NM                           | 5 | 02-76 | 00 57 ce 00 00    |  |  |  |  |
| 2012/06/09 | 08:00:00.5 | XRT_FLRCTRL_DIS_430_OG [0x1ae]    |   |       |                   |  |  |  |  |
|            |            | MDP_XRT_FLRCTRL_DIS               | 1 | 07-F0 | c9                |  |  |  |  |
| 2012/06/09 | 08:02:44.0 | XRT_PREFLR_STOP_424_OG [0x1a8]    |   |       |                   |  |  |  |  |
|            |            | MDP_XRT_PREFLR_STOP               | 1 | 07-F0 | e9                |  |  |  |  |
| 2012/06/09 | 08:02:58.5 | XRT_QT_PROG_SET_444_OG [0x1bc]    |   |       |                   |  |  |  |  |
|            |            | MDP_XRT_QT_PROG_SET               | 2 | 07-F0 | c4 01             |  |  |  |  |
| 2012/06/09 | 08:35:30.0 | XRT_Custom_418_OG [0x1a2]         |   |       |                   |  |  |  |  |
| 2012/06/09 | 08:36:30.0 | XRT_CTRL_AUTO_419_OG [0x1a3]      |   |       |                   |  |  |  |  |
|            |            | MDP_XRT_CTRL_AUTO                 | 1 | 07-F0 | c0                |  |  |  |  |
| 2012/06/09 | 09:40:54.0 | XRT_CTRL_MANU_400_OG [0x190]      |   |       |                   |  |  |  |  |
|            |            | MDP_XRT_CTRL_MANU                 | 1 | 07-F0 | c1                |  |  |  |  |
| 2012/06/09 | 09:41:00.0 | AOCS_OrE-point_Start_4_OG [0x09a] |   |       |                   |  |  |  |  |
|            |            | AOCU_NM                           | 5 | 02-76 | 00 00 00 00 00 00 |  |  |  |  |