

# XRT Timeline to be uploaded on 2012/06/09

Period: 2012/06/09 09:31:00 - 2012/07/14 12:00:00

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Normal mode

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XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV													
Term		Pointing (x, y)					Comment						
06/09 10:15:00 - 06/09 10:59:54		Fixed ( 0.0, -988.0)					# OP start + 10min   HOP 200 South Pole						
06/09 11:03:00 - 06/09 13:01:00		Fixed ( 0.0, 0.0)					SOT Flat Field at disk center						
<b>PROG= 01 1-time(s)</b>													
└ Subr= 1 1-time(s) 600.0sec													
└└ Seqn= 64 1-time(s) 4.0sec													
└└└ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
└└└ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long													
Term		Pointing (x, y)					Comment						
06/09 15:33:00 - 06/09 15:39:54		Fixed ( -528.4, -528.4)					XRT Post-Bakeout Quadrant-1/4						
<b>PROG= 09 1-time(s)</b>													
└ Subr= 1 1-time(s) 12.0sec													
└└ Seqn= 38 1-time(s) 12.0sec													
└└└ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
└└└ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
└└└ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└└└ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└ Subr= 2 1-time(s) 2.0sec													
└└ Seqn= 93 2-time(s) 2.0sec													
└└└ Open/Al-mesh		Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└└└ Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly -long													
Term		Pointing (x, y)					Comment						
06/09 15:43:00 - 06/09 15:49:54		Fixed ( 528.4, -528.4)					XRT Post-Bakeout Quadrant-2/4						
<b>PROG= 12 1-time(s)</b>													
└ Subr= 1 1-time(s) 12.0sec													
└└ Seqn= 36 1-time(s) 12.0sec													
└└└ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
└└└ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
└└└ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
└└└ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
└ Subr= 2 1-time(s) 2.0sec													
└└ Seqn= 93 2-time(s) 2.0sec													
└└└ Open/Al-mesh		Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└└└ Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long													
Term		Pointing (x, y)					Comment						
06/09 15:53:00 - 06/09 15:59:54		Fixed ( 528.4, 528.4)					XRT Post-Bakeout Quadrant-3/4						
<b>PROG= 17 1-time(s)</b>													
└ Subr= 1 1-time(s) 12.0sec													
└└ Seqn= 39 1-time(s) 12.0sec													
└└└ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
└└└ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
└└└ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
└└└ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
└ Subr= 2 1-time(s) 2.0sec													
└└ Seqn= 93 2-time(s) 2.0sec													
└└└ Open/Al-mesh		Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└└└ Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long													
Term		Pointing (x, y)					Comment						
06/09 16:03:00 - 06/09 16:09:54		Fixed ( -528.4, 528.4)					XRT Post-Bakeout Quadrant-4/4						
<b>PROG= 15 1-time(s)</b>													
└ Subr= 1 1-time(s) 12.0sec													
└└ Seqn= 40 1-time(s) 12.0sec													
└└└ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
└└└ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
└└└ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
└└└ Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
└ Subr= 2 1-time(s) 2.0sec													

<b>Seqn= 93 2-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1835: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 40sec-cad**

Term	Pointing (x, y)	Comment
06/09 16:13:00 - 06/09 17:41:24	Track ( 485.2, 209.7) @ 06/09 16:10:00	Track AR 11493/11496
06/09 17:54:30 - 06/10 06:01:24	Track ( 497.9, 209.7) @ 06/09 17:51:30	# Track AR 11493/11496 (cont.)
06/10 06:14:30 - 06/10 07:59:54	Track ( 586.2, 209.3) @ 06/10 06:11:30	# Track AR 11493/11946 (cont.)
06/10 11:03:00 - 06/10 17:57:54	Track ( 618.5, 209.2) @ 06/10 11:00:00	Track AR 11493/11496 (cont.)

**PROG= 04 Inf.-time(s)**

<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 19 1-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 41 4-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 1 1-time(s) 2.0sec</b>												
<b>Seqn= 52 45-time(s) 24.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #18EA: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-band(16)**

Term	Pointing (x, y)	Comment
06/09 17:44:30 - 06/09 17:51:24	Fixed ( 0.0, 0.0)	synoptic, shifted -18.5 min
06/10 06:04:30 - 06/10 06:11:24	Fixed ( 0.0, 0.0)	synoptic, shifted 1.5 min
06/10 18:01:00 - 06/10 18:07:54	Fixed ( 0.0, 0.0)	synoptic, shifted -2.0 min
06/11 05:30:30 - 06/11 05:37:24	Fixed ( 0.0, 0.0)	synoptic, shifted -32.5 min
06/11 18:20:30 - 06/11 18:27:24	Fixed ( 0.0, 0.0)	synoptic, shifted 17.5 min
06/12 06:06:30 - 06/12 06:13:24	Fixed ( 0.0, 0.0)	synoptic, shifted 3.5 min

**PROG= 07 1-time(s)**

<b>Subr= 1 1-time(s) 12.0sec</b>												
<b>Seqn= 46 1-time(s) 4.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 5 1-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 69 1-time(s) 4.0sec</b>												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 4 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #183E: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad**

Term	Pointing (x, y)	Comment
06/10 18:11:00 - 06/11 05:27:24	Track ( 663.8, 209.1) @ 06/10 18:08:00	# Track AR 11493/11496 (cont.)
06/11 05:40:30 - 06/11 07:35:30	Track ( 730.1, 209.2) @ 06/11 05:37:30	# Track AR 11493/11496 (cont.)
06/11 11:30:00 - 06/11 17:32:30	Track ( 758.1, 209.3) @ 06/11 11:00:00	Track AR 11493/11496 (cont.)
06/11 18:30:30 - 06/12 06:03:24	Track ( 793.5, 209.6) @ 06/11 18:27:30	# Track AR 11493/11496 (cont.)
06/12 06:16:30 - 06/12 07:59:54	Track ( 841.0, 210.1) @ 06/12 06:13:30	# Track AR 11493/11496 (cont.)

**PROG= 19 Inf.-time(s)**

<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 19 1-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 41 4-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 1 1-time(s) 2.0sec</b>												
<b>Seqn= 16 30-time(s) 80.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	20.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	20.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	20.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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#### XOB #18E7: Flare obs. dynamics - Ti\_poly high cadence + context (thick-AI-384x384)-15 loops

Term	Pointing (x, y)		Comment									
06/09 16:13:00 - 06/09 17:41:24	Track ( 485.2, 209.7)	<small>@ 06/09 16:10:00</small>	Track AR 11493/11496									
06/09 17:54:30 - 06/10 06:01:24	Track ( 497.9, 209.7)	<small>@ 06/09 17:51:30</small>	# Track AR 11493/11496 (cont.)									
06/10 06:14:30 - 06/10 07:59:54	Track ( 586.2, 209.3)	<small>@ 06/10 06:11:30</small>	# Track AR 11493/11946 (cont.)									
06/10 11:03:00 - 06/10 17:57:54	Track ( 618.5, 209.2)	<small>@ 06/10 11:00:00</small>	Track AR 11493/11496 (cont.)									
06/10 18:11:00 - 06/11 05:27:24	Track ( 663.8, 209.1)	<small>@ 06/10 18:08:00</small>	# Track AR 11493/11496 (cont.)									
06/11 05:40:30 - 06/11 07:35:30	Track ( 730.1, 209.2)	<small>@ 06/11 05:37:30</small>	# Track AR 11493/11496 (cont.)									
06/11 11:30:00 - 06/11 17:32:30	Track ( 758.1, 209.3)	<small>@ 06/11 11:00:00</small>	Track AR 11493/11496 (cont.)									
06/11 18:30:30 - 06/12 06:03:24	Track ( 793.5, 209.6)	<small>@ 06/11 18:27:30</small>	# Track AR 11493/11496 (cont.)									
06/12 06:16:30 - 06/12 07:59:54	Track ( 841.0, 210.1)	<small>@ 06/12 06:13:30</small>	# Track AR 11493/11496 (cont.)									
<b>PROG= 02 15-time(s)</b>												
└─ Subr= 1 45-time(s) 10.0sec												
└─┬─ Seqn= 92 1-time(s) 2.0sec												
└─┬─ Open/Ti-poly Open/thick-AI close Safe Norm 4ms Obs 1x1 384x384 (1024, 1024) DPCM 2 0 2.0sec												
└─┬─ Open/Ti-poly Open/thick-AI close Safe Norm 4ms Obs 1x1 384x384 (1024, 1024) DPCM 3 0 2.0sec												
└─┬─ Subr= 2 1-time(s) 10.0sec												
└─┬─ Seqn= 54 1-time(s) 2.0sec												
└─┬─ Open/thick-AI Open/thick-AI close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) DPCM 2 0 2.0sec												
└─┬─ Open/thick-AI Open/thick-AI close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) DPCM 3 0 2.0sec												
└─┬─ Seqn= 79 1-time(s) 2.0sec												
└─┬─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─ Open/thick-AI Open/thick-AI close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

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### Active Region Search

\* \* \* \* \*

NOT USED

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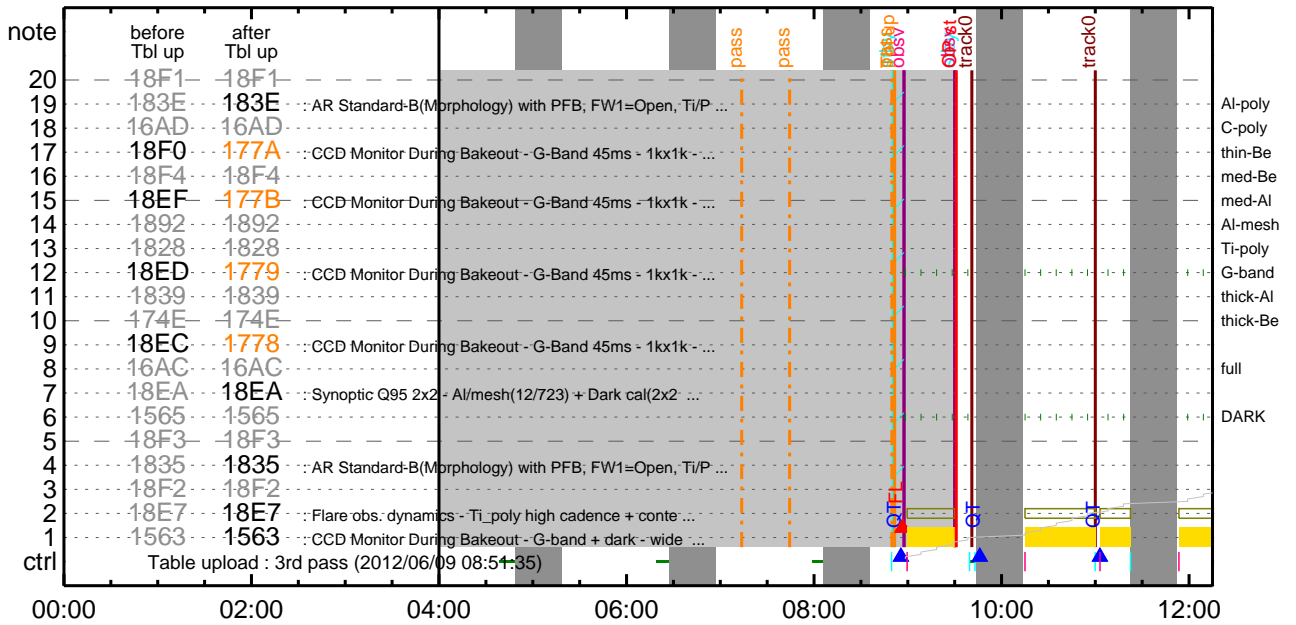
### Flare Detection

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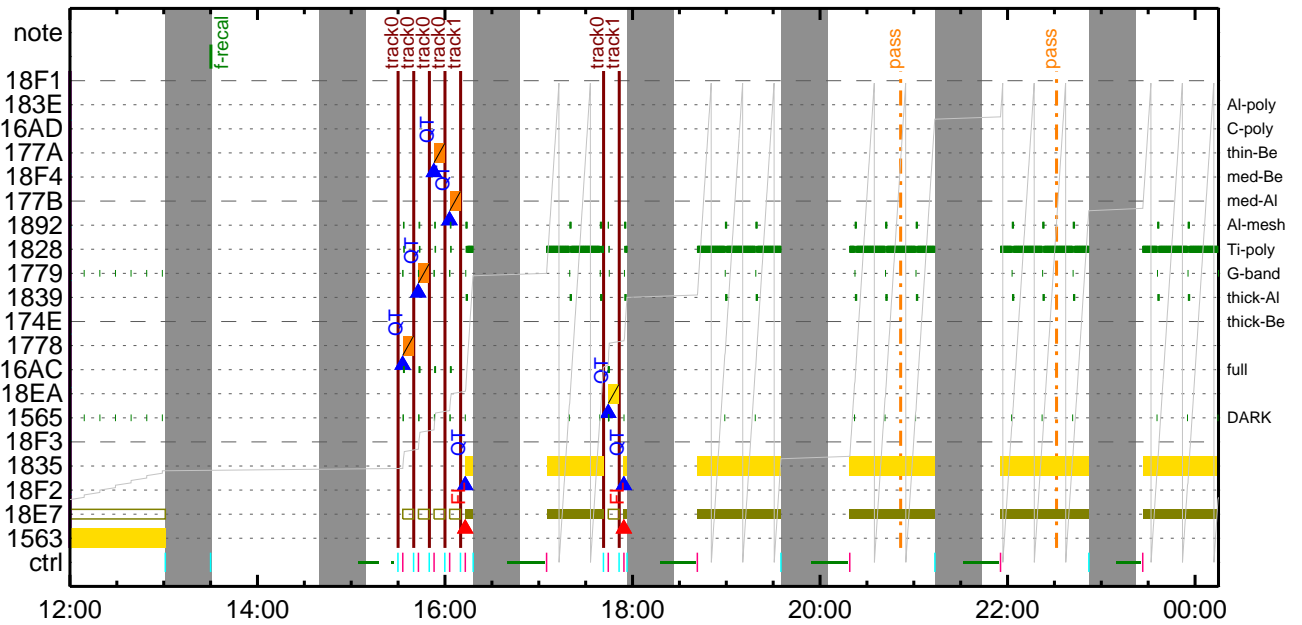
#### FLD Patrol

Term	Pointing (x, y)		Comment									
06/09 16:12:46 - 06/09 17:41:46	Track ( 485.2, 209.7)	<small>@ 06/09 16:10:00</small>	Track AR 11493/11496									
06/09 17:54:16 - 06/10 06:01:46	Track ( 497.9, 209.7)	<small>@ 06/09 17:51:30</small>	# Track AR 11493/11496 (cont.)									
06/10 06:14:16 - 06/10 17:58:16	Track ( 586.2, 209.3)	<small>@ 06/10 06:11:30</small>	# Track AR 11493/11946 (cont.)									
06/10 18:10:46 - 06/11 05:27:46	Track ( 663.8, 209.1)	<small>@ 06/10 18:08:00</small>	# Track AR 11493/11496 (cont.)									
06/11 05:40:16 - 06/11 18:17:46	Track ( 730.1, 209.2)	<small>@ 06/11 05:37:30</small>	# Track AR 11493/11496 (cont.)									
06/11 18:30:16 - 06/12 06:03:46	Track ( 793.5, 209.6)	<small>@ 06/11 18:27:30</small>	# Track AR 11493/11496 (cont.)									
06/12 06:16:16 - 07/14 12:00:00	Track ( 841.0, 210.1)	<small>@ 06/12 06:13:30</small>	# Track AR 11493/11496 (cont.)									
Open/Ti-poly Open/thick-AI close Safe Norm 8ms Obs 8x8 Q=50 30sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

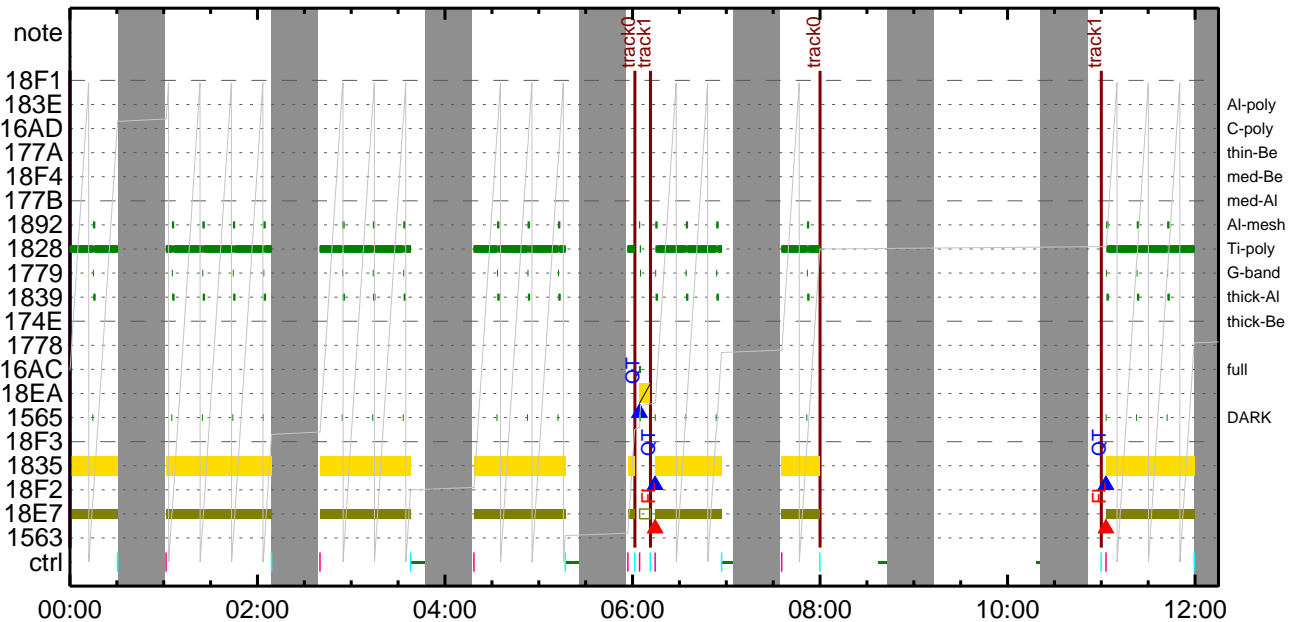
### CMDI #0684 2012/06/09



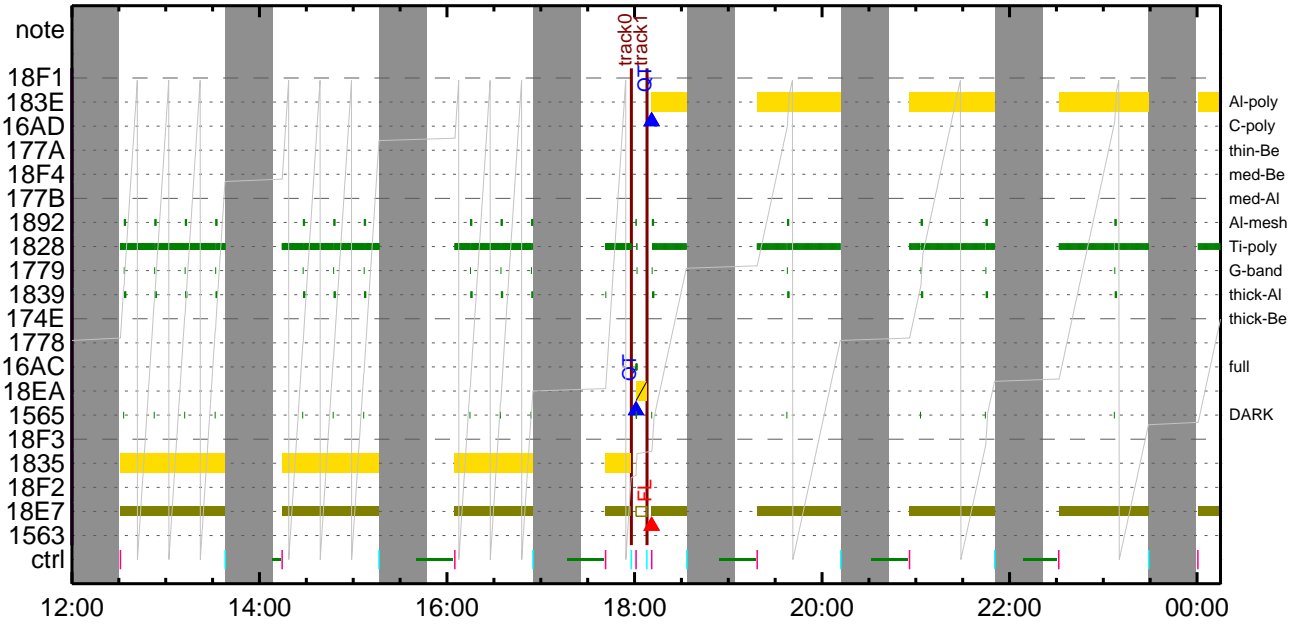
### CMDI #0684 2012/06/09



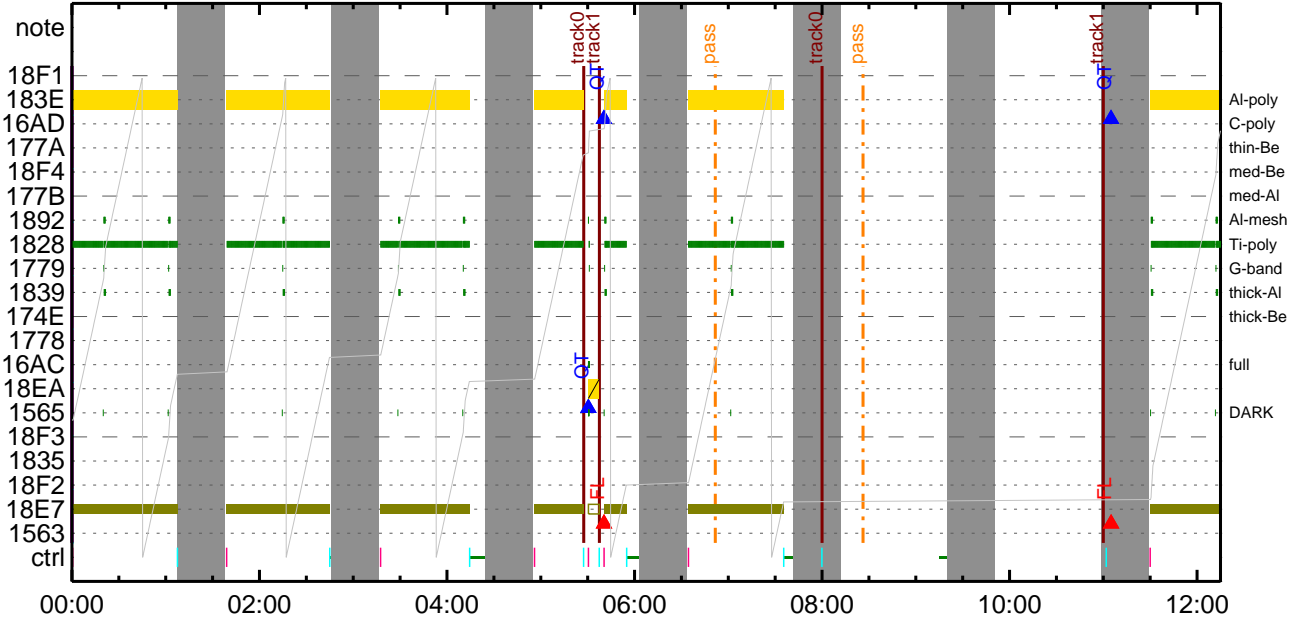
### CMDI #0684 2012/06/10



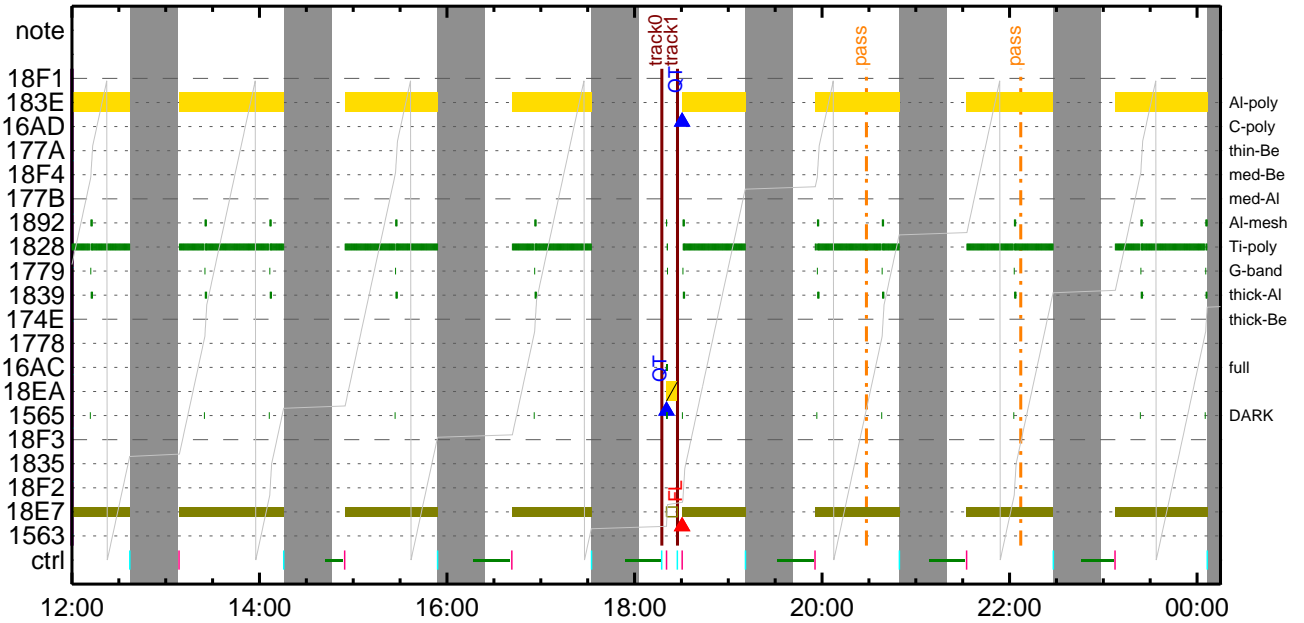
CMDI #0684 2012/06/10



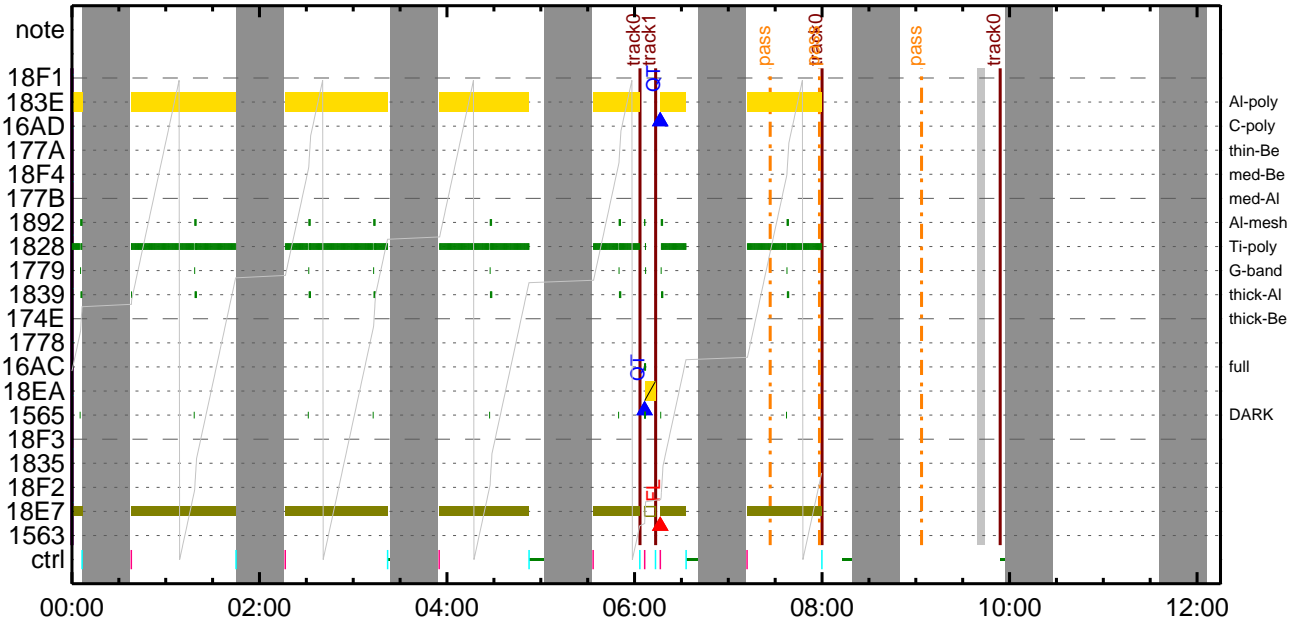
CMDI #0684 2012/06/11



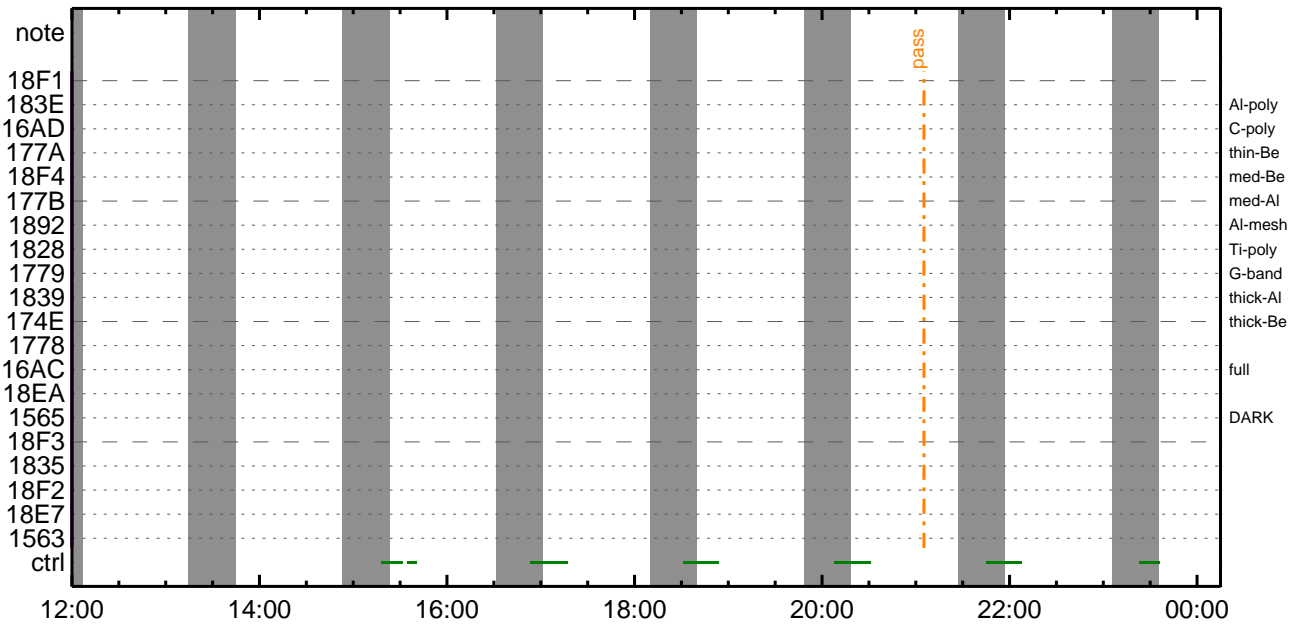
CMDI #0684 2012/06/11



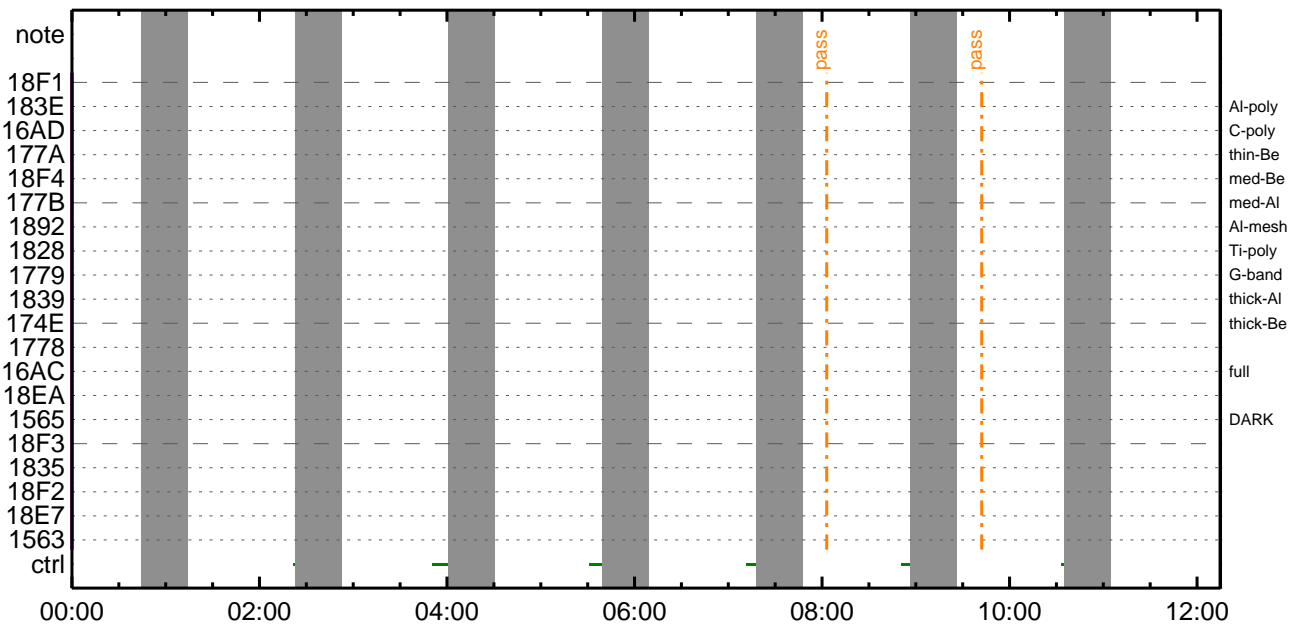
CMDI #0684 2012/06/12



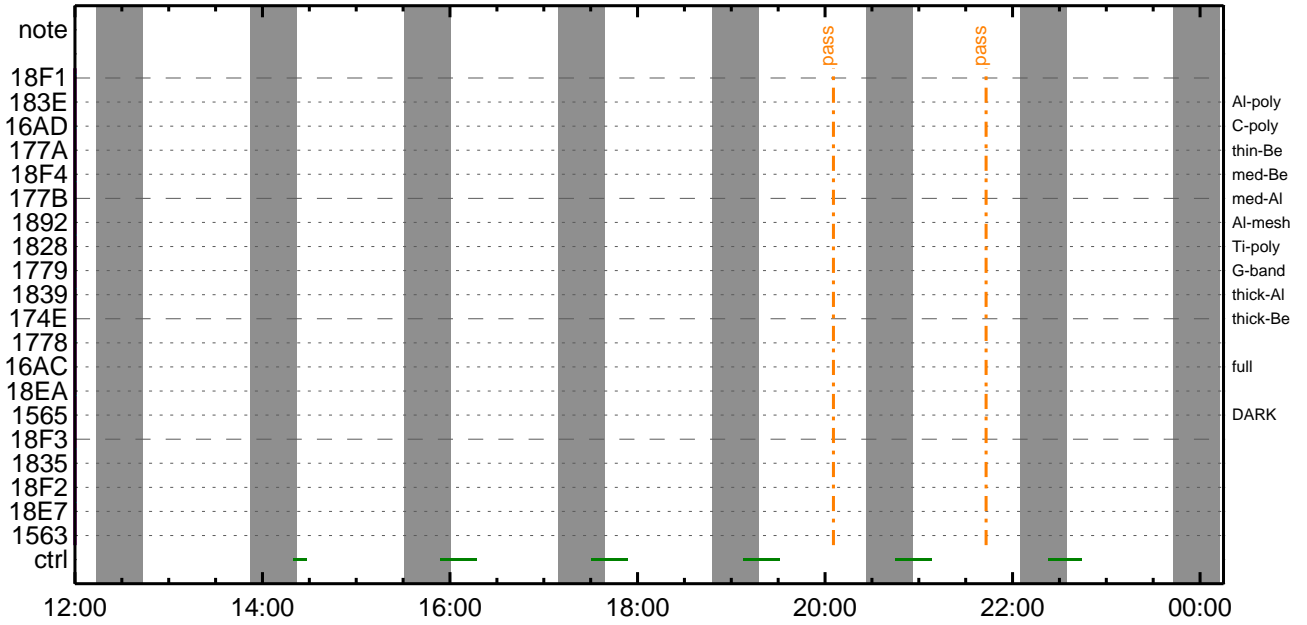
CMDI #0684 2012/06/12



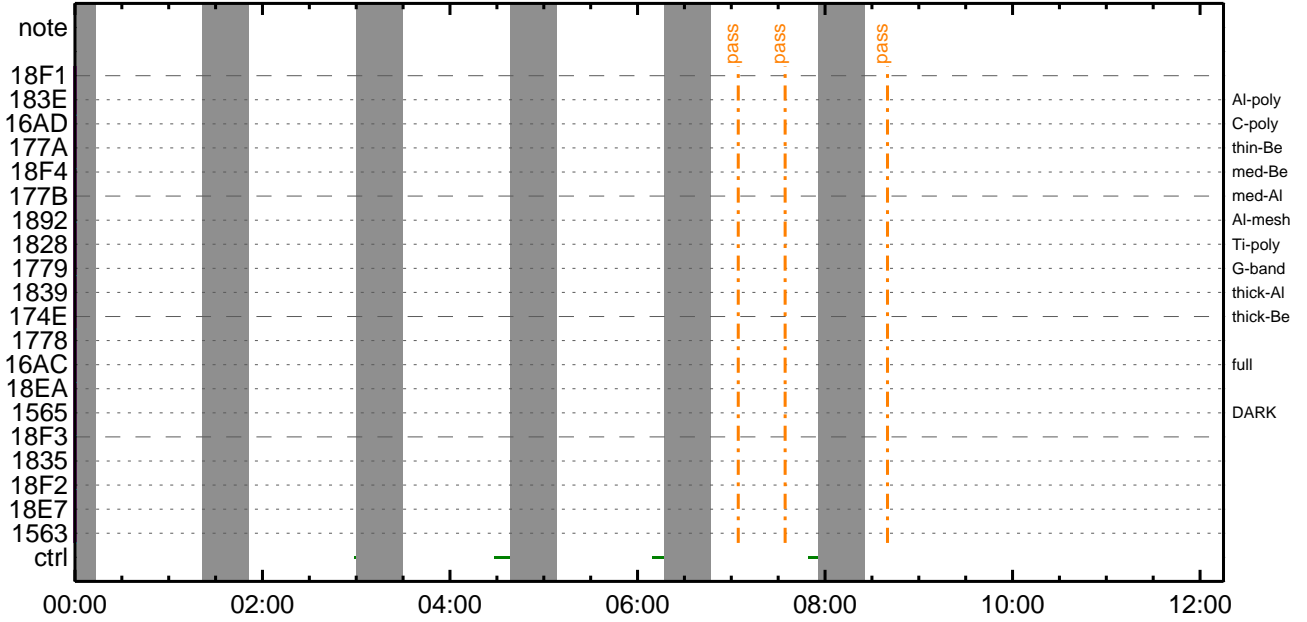
CMDI #0684 2012/06/13



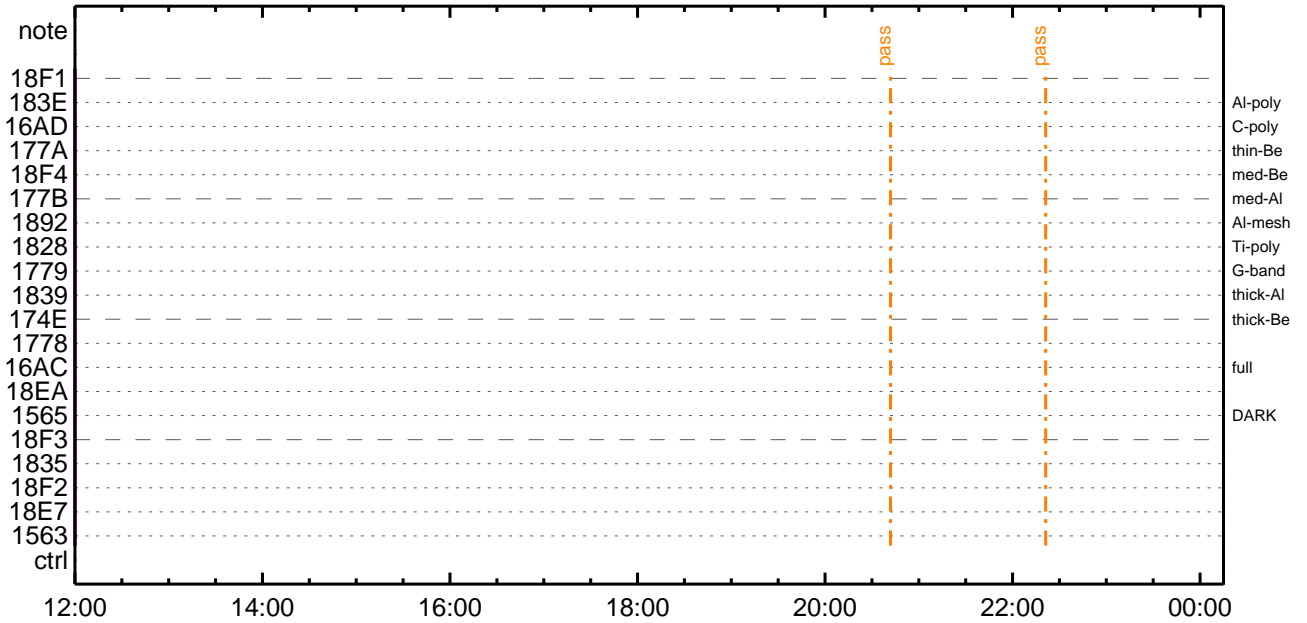
CMDI #0684 2012/06/13



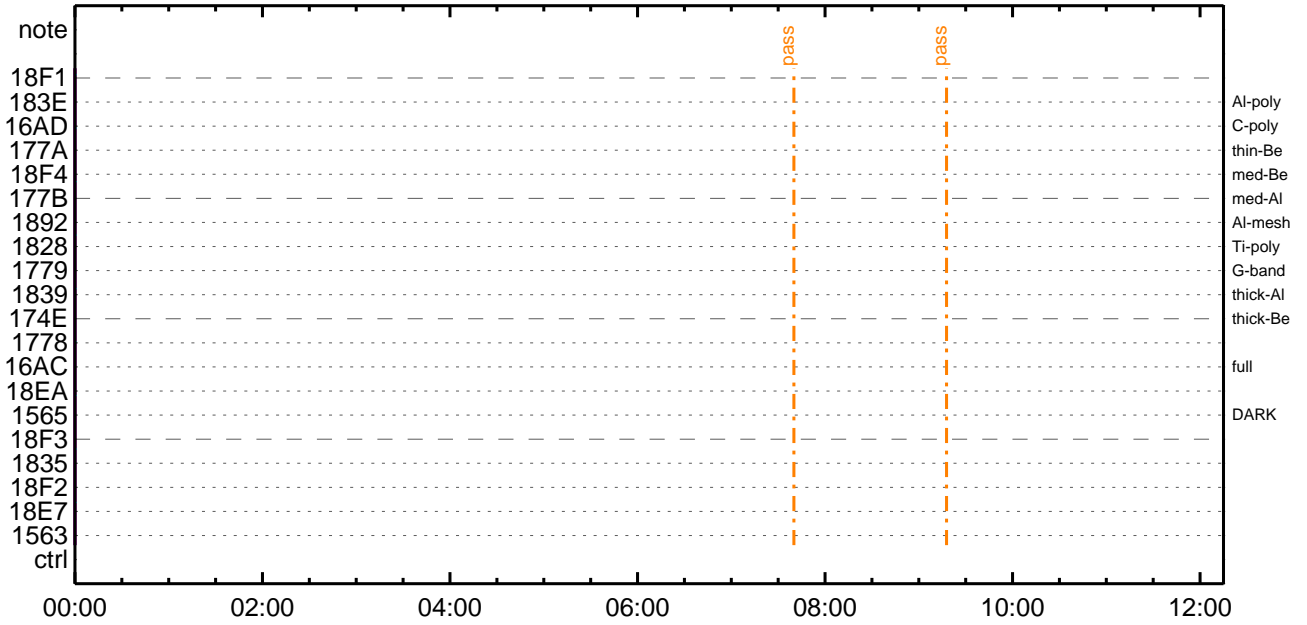
CMDI #0684 2012/06/14



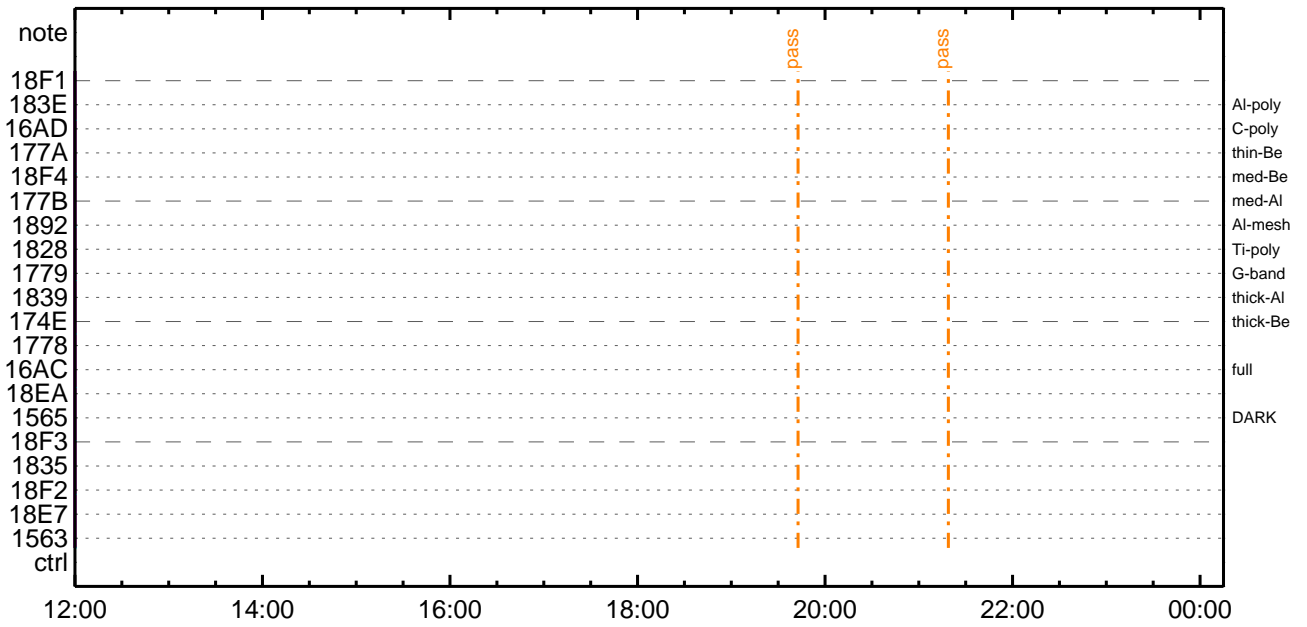
CMDI #0684 2012/06/14



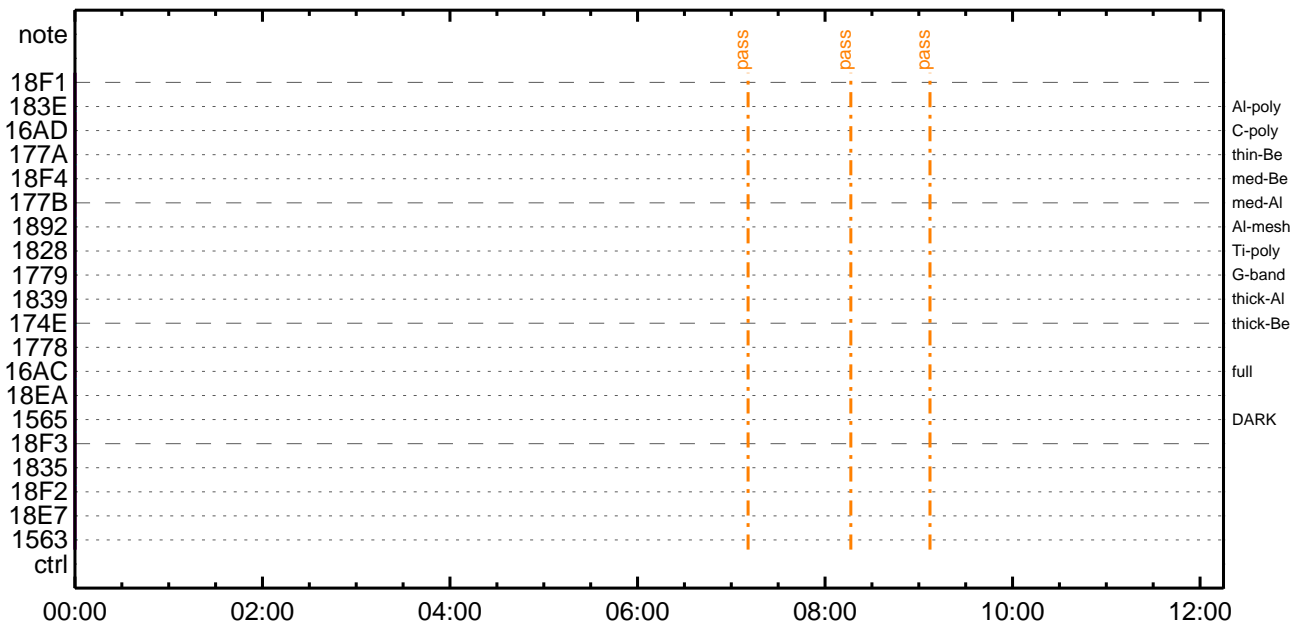
CMDI #0684 2012/06/15



CMDI #0684 2012/06/15

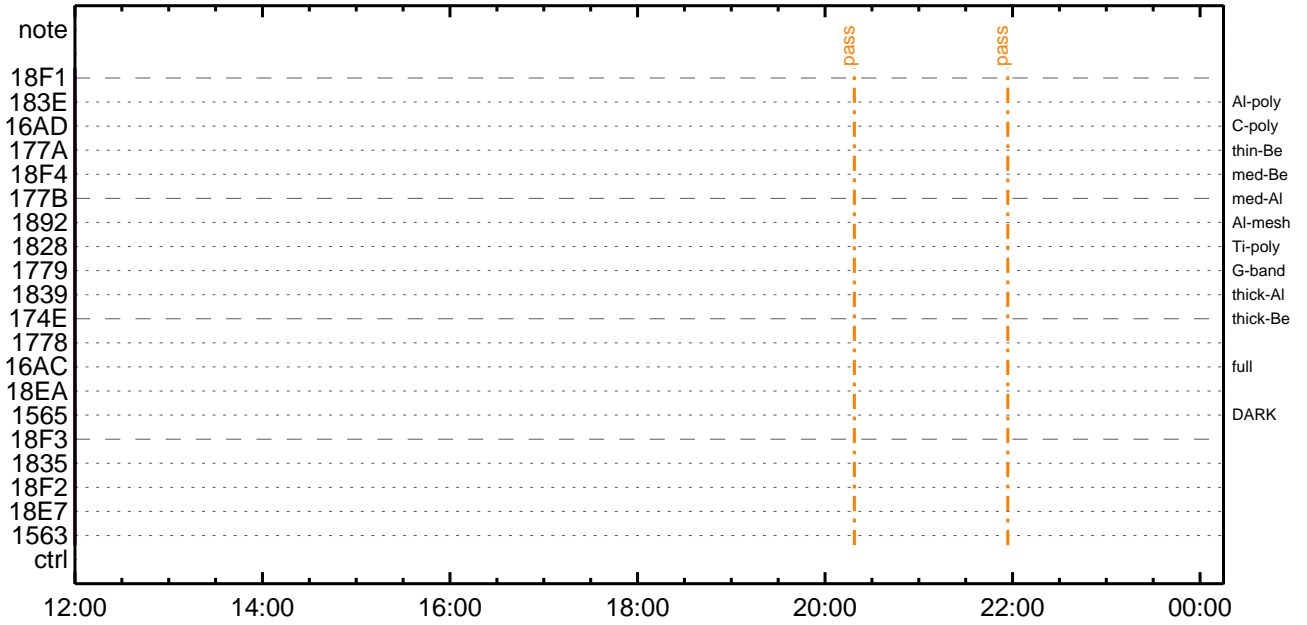


CMDI #0684 2012/06/16

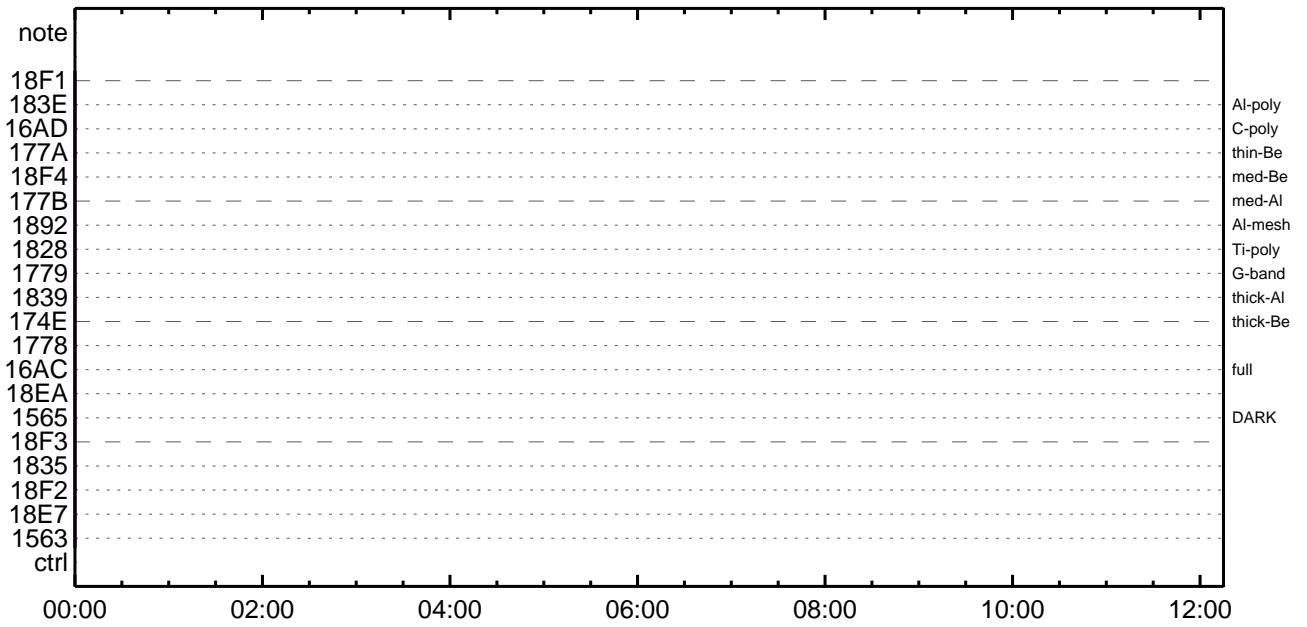




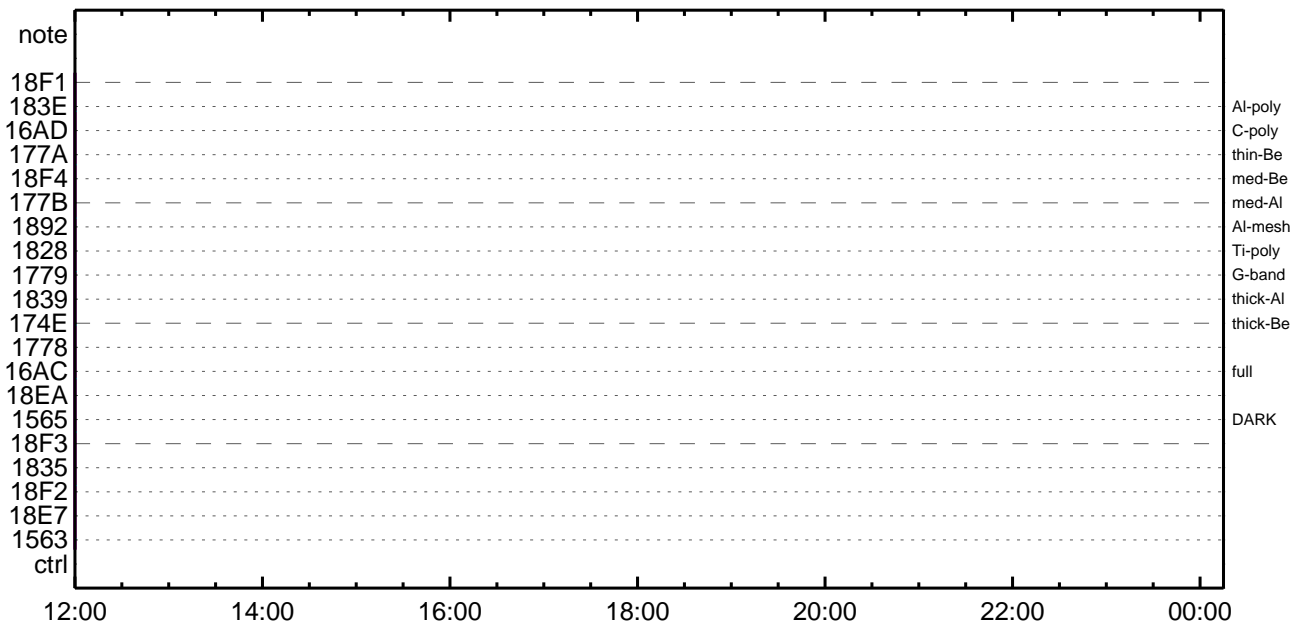
CMDI #0684 2012/06/16



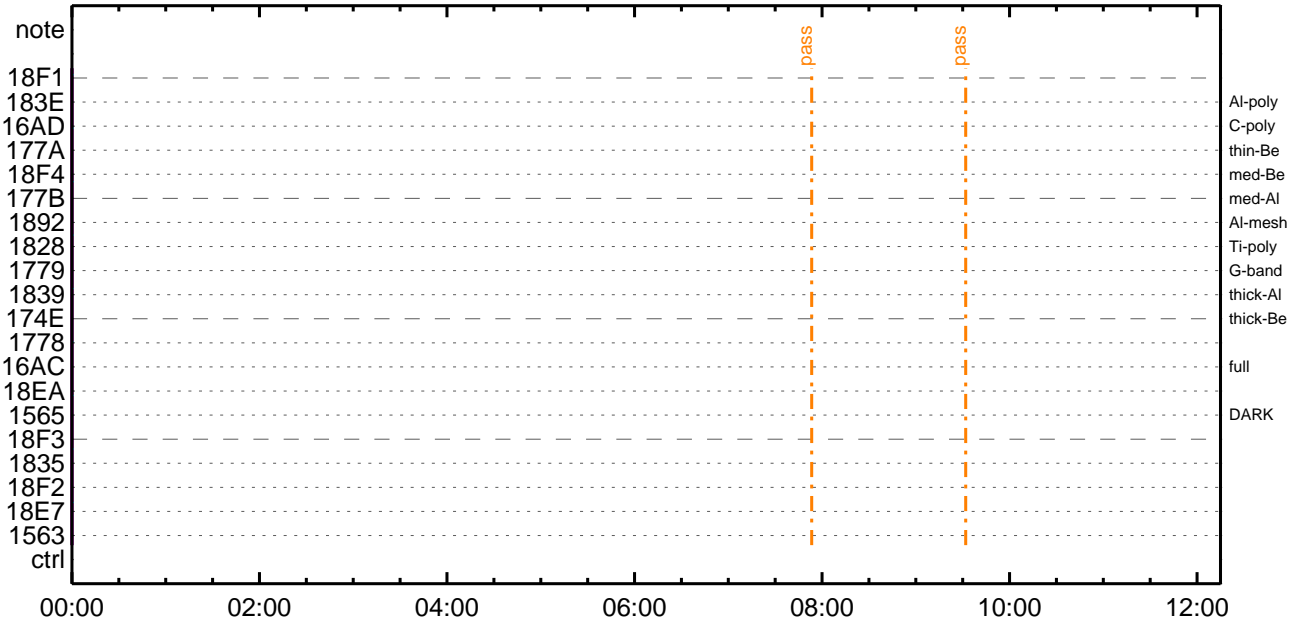
CMDI #0684 2012/06/17



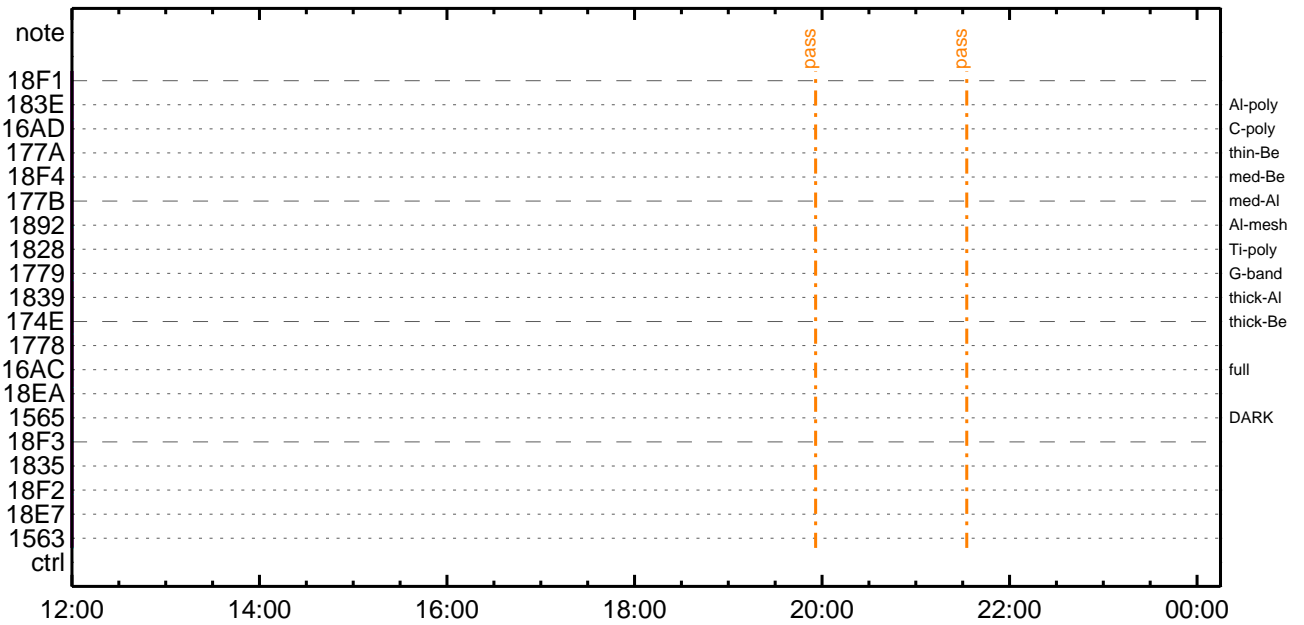
CMDI #0684 2012/06/17



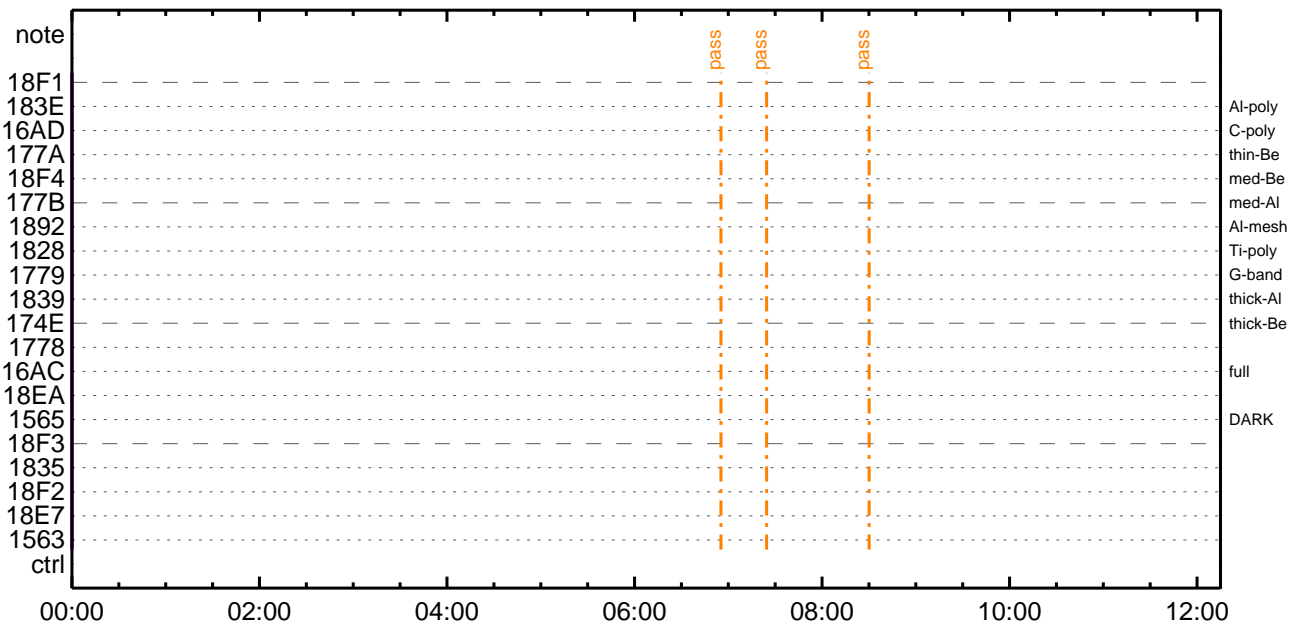
CMDI #0684 2012/06/18



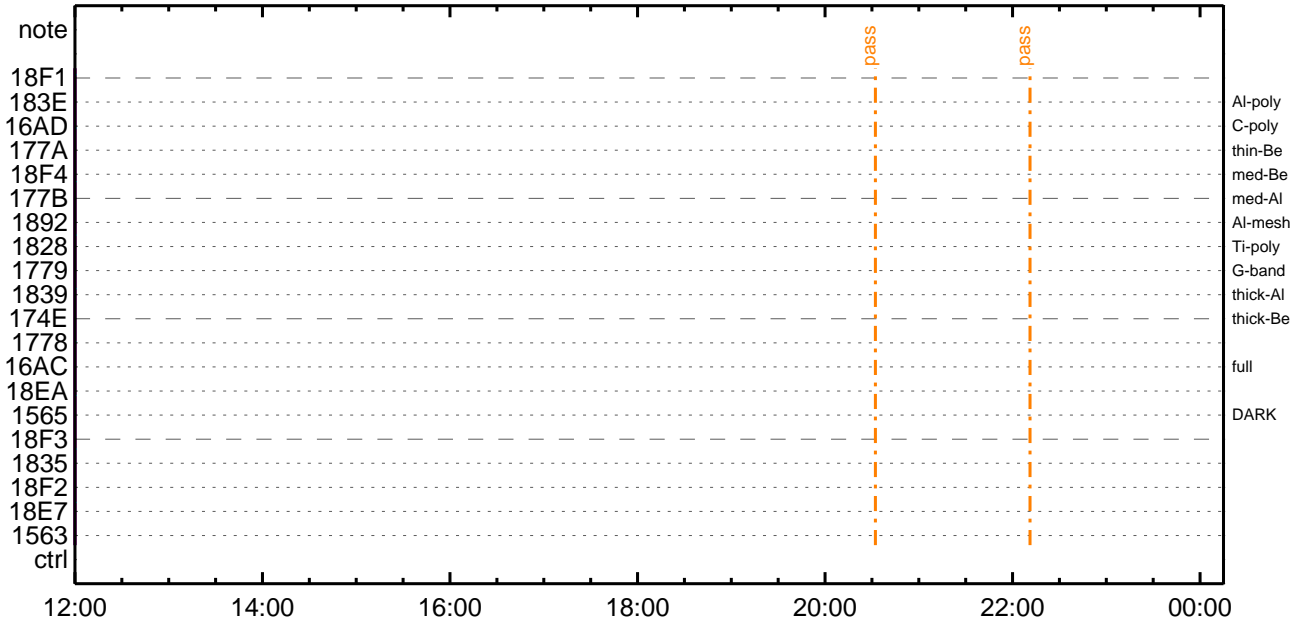
CMDI #0684 2012/06/18



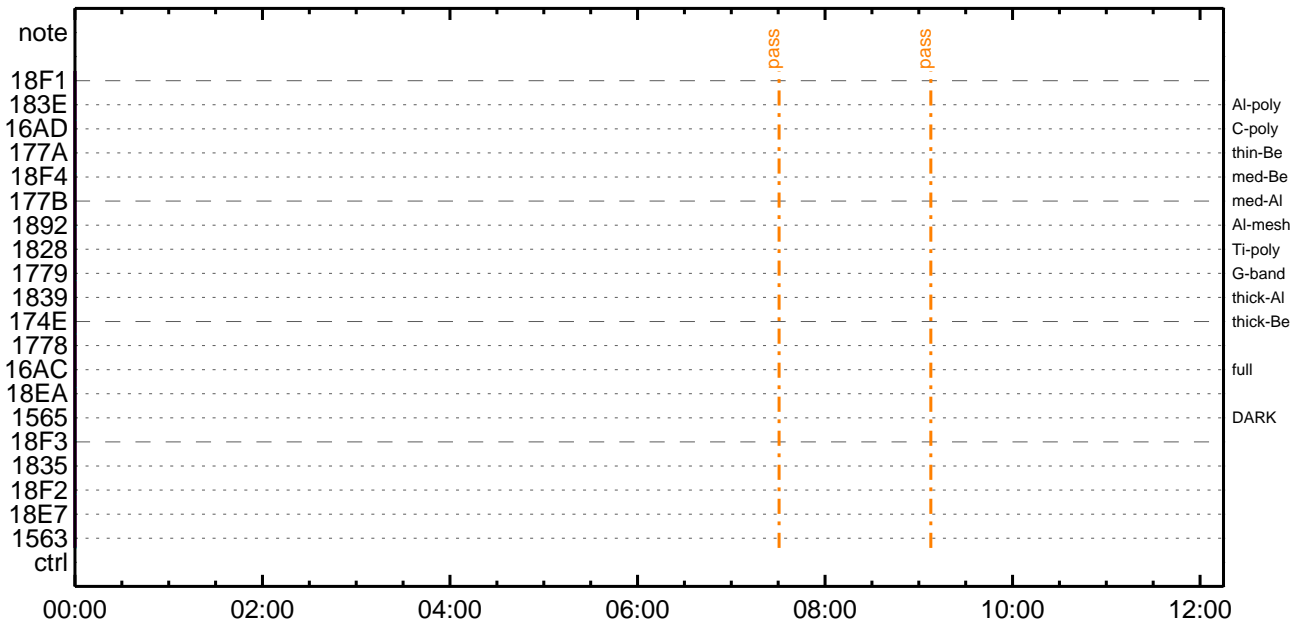
CMDI #0684 2012/06/19



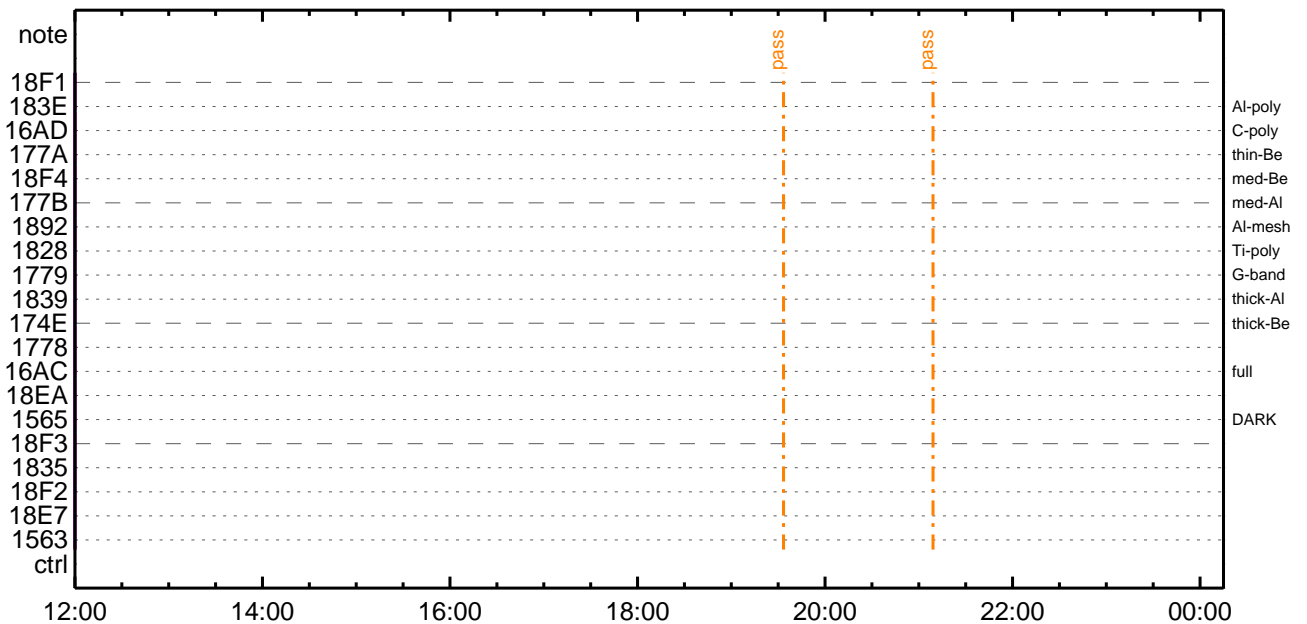
CMDI #0684 2012/06/19



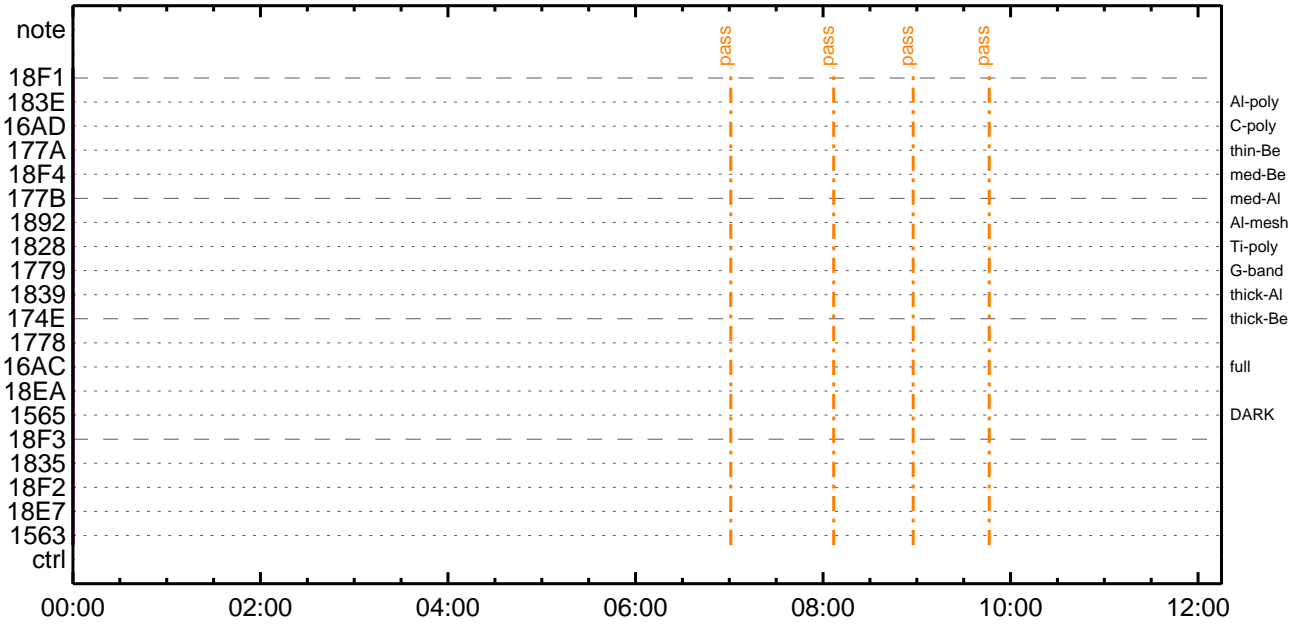
CMDI #0684 2012/06/20



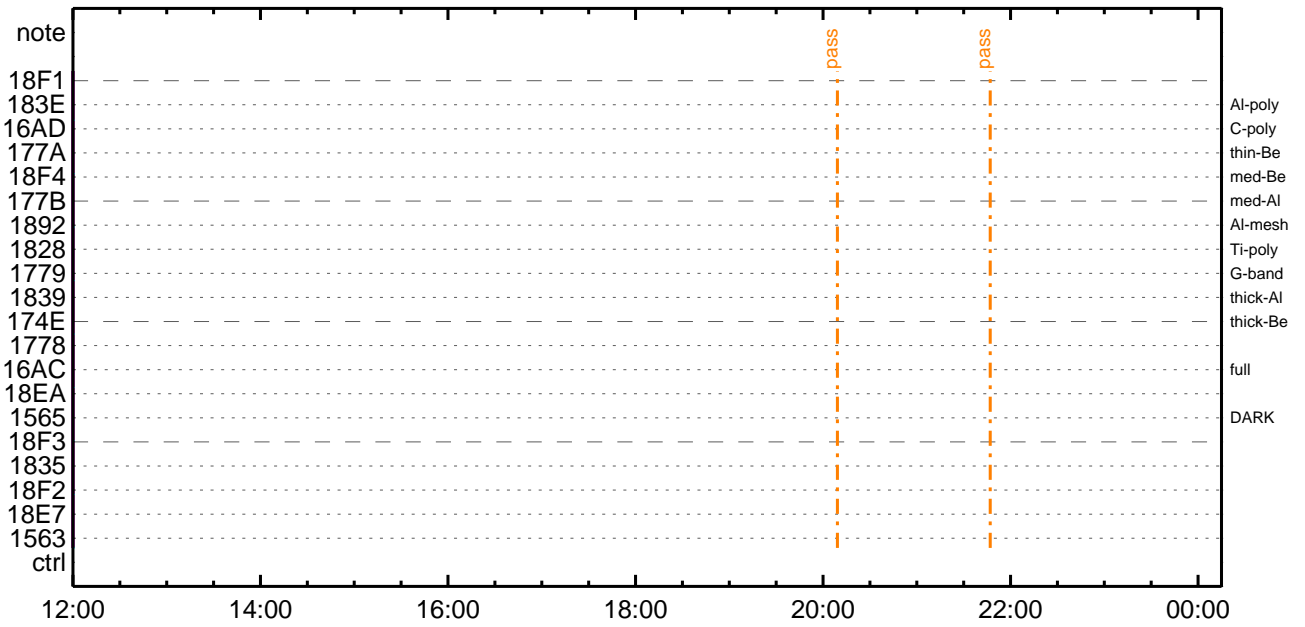
CMDI #0684 2012/06/20



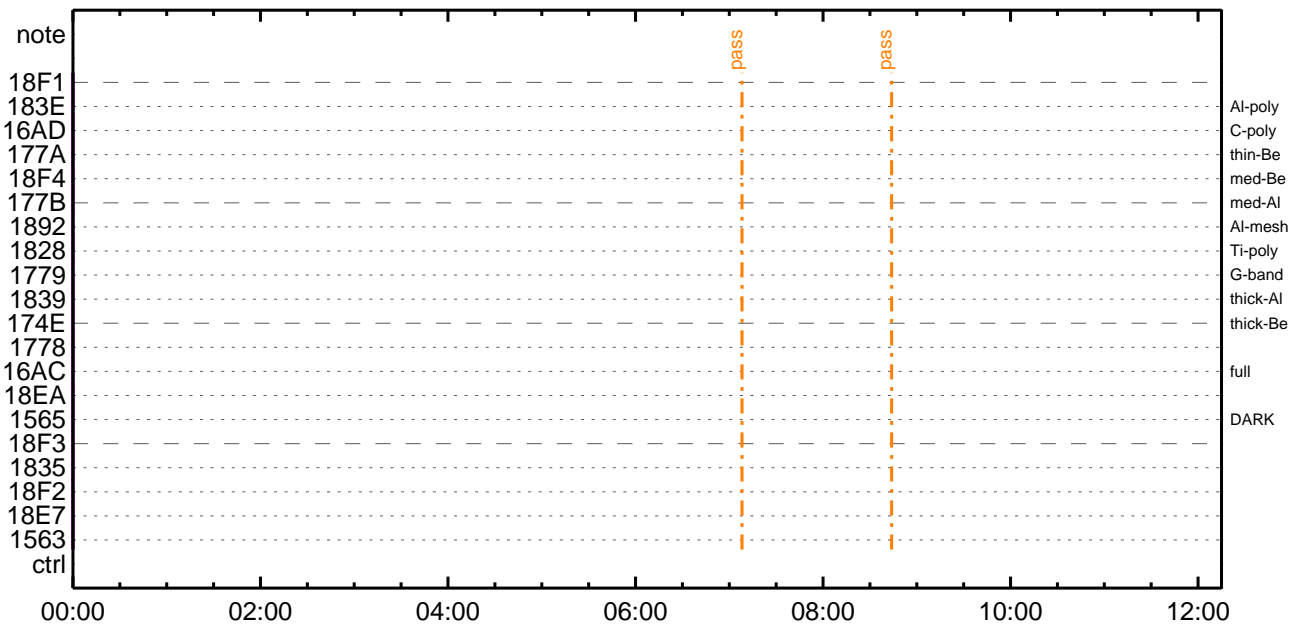
CMDI #0684 2012/06/21



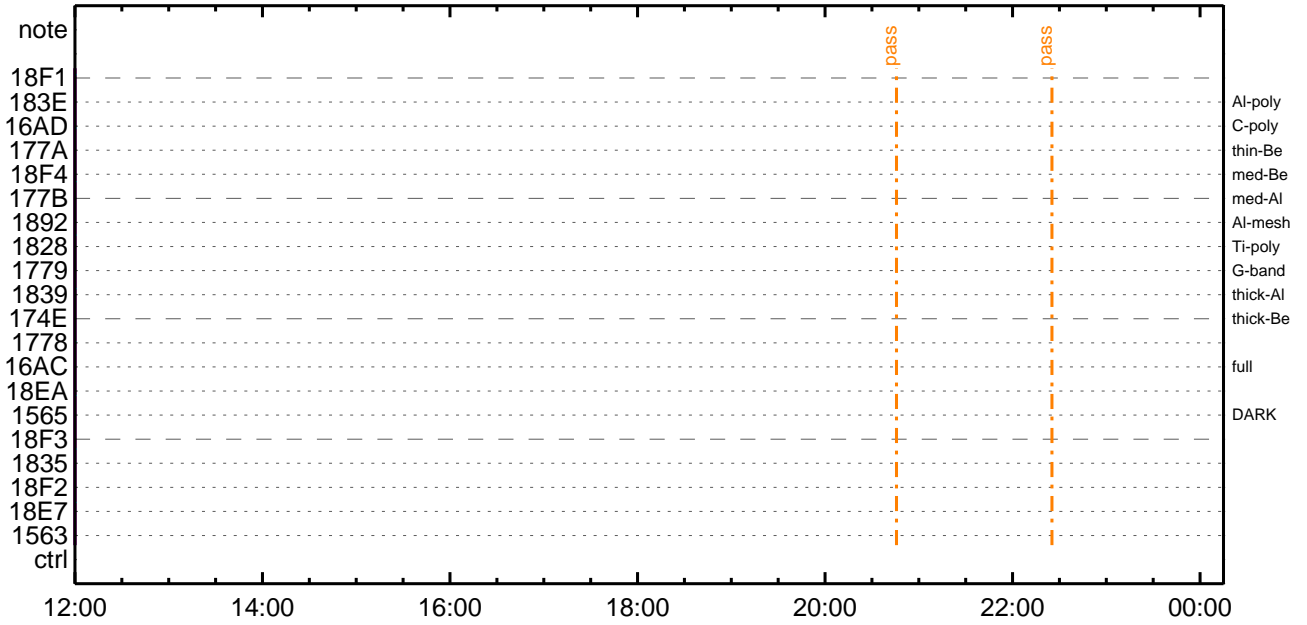
CMDI #0684 2012/06/21



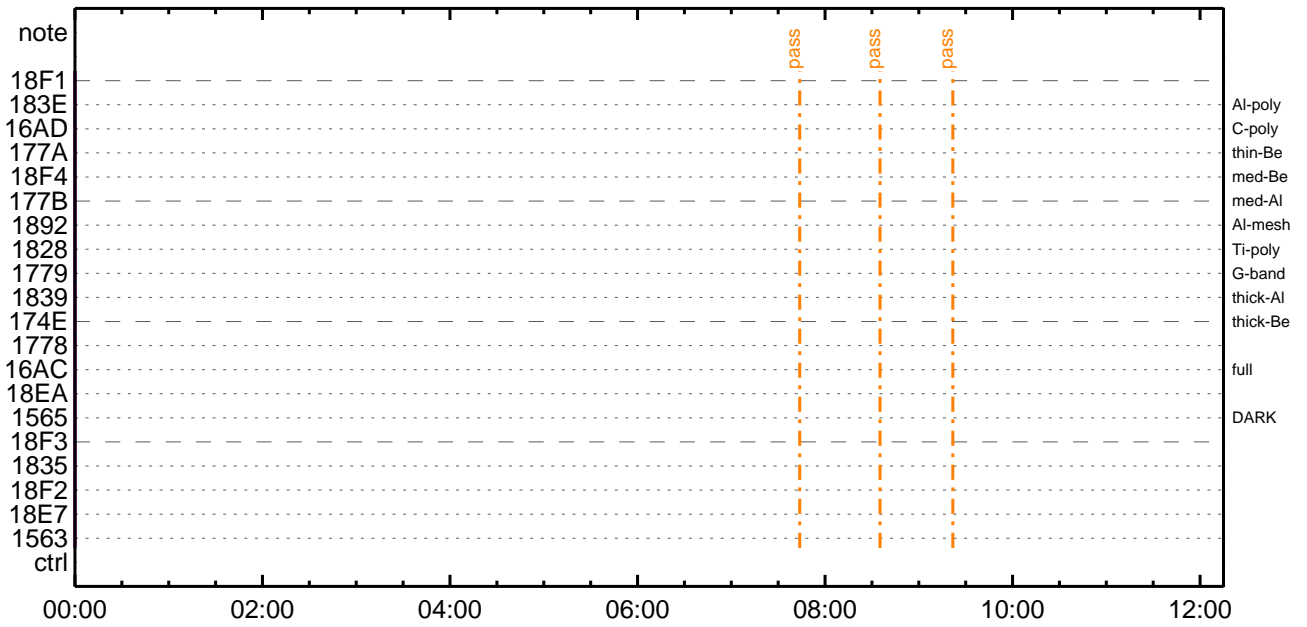
CMDI #0684 2012/06/22



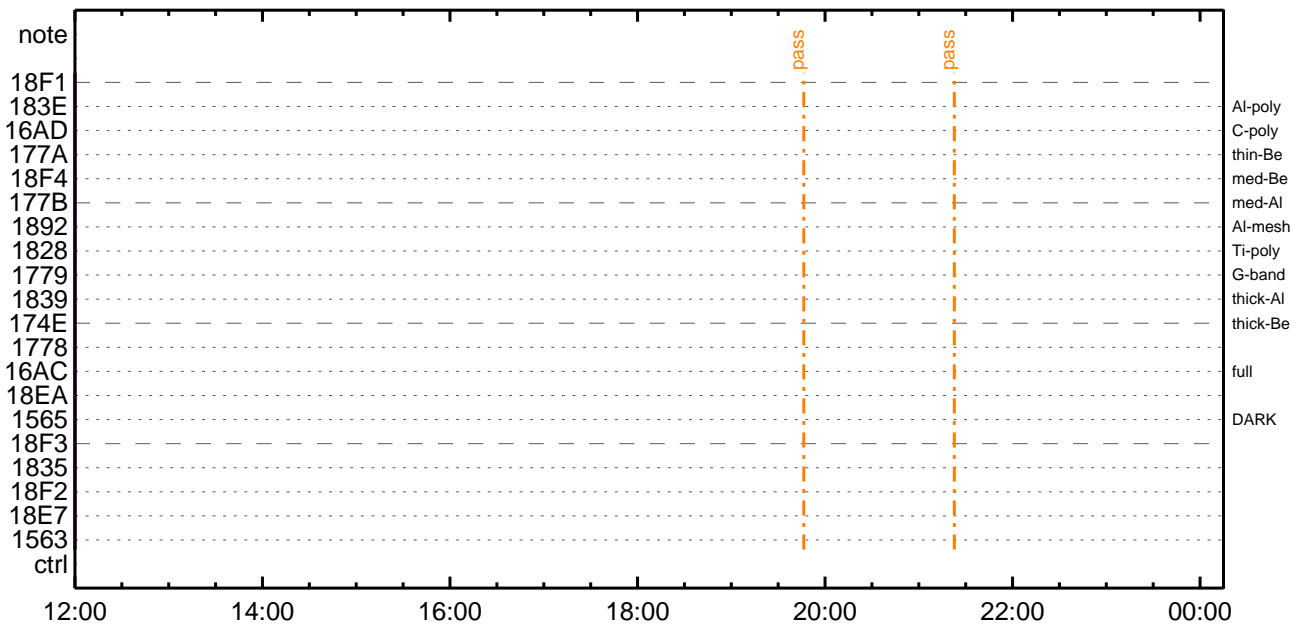
CMDI #0684 2012/06/22



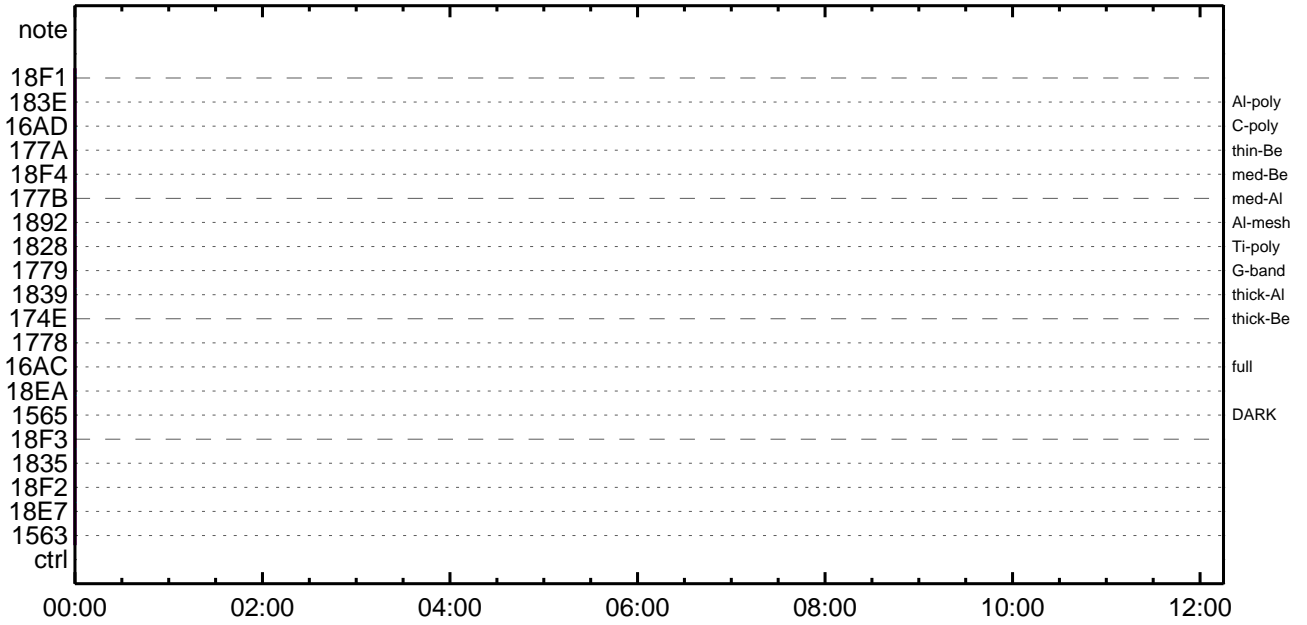
CMDI #0684 2012/06/23



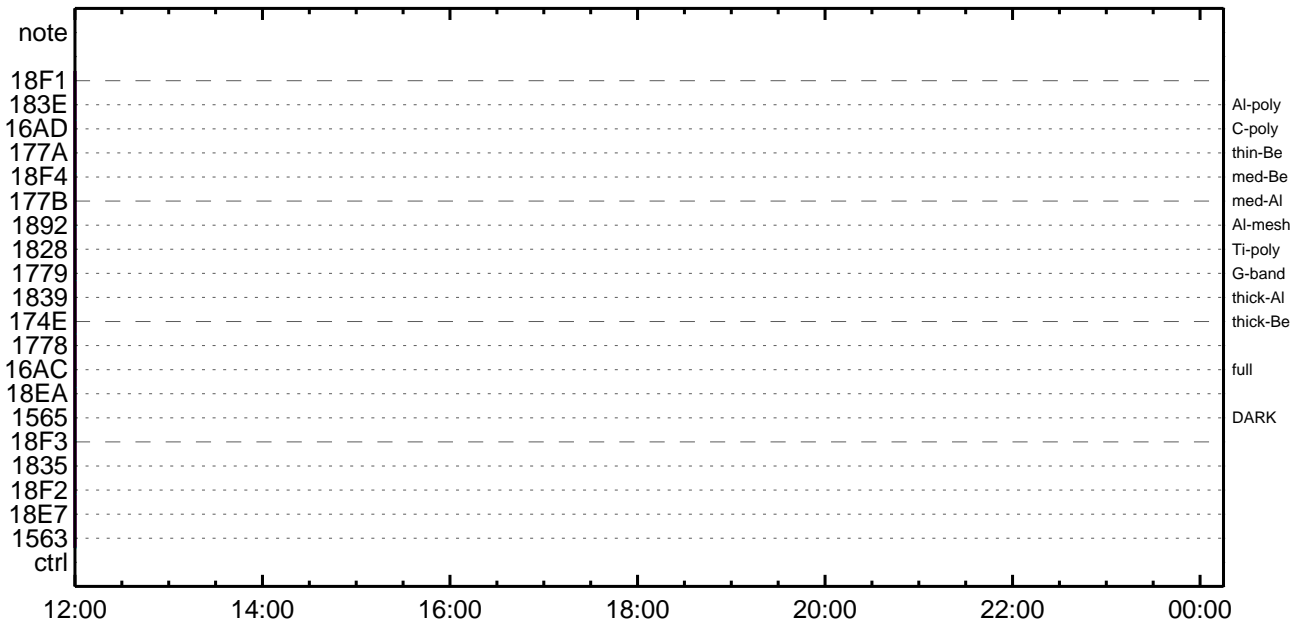
CMDI #0684 2012/06/23



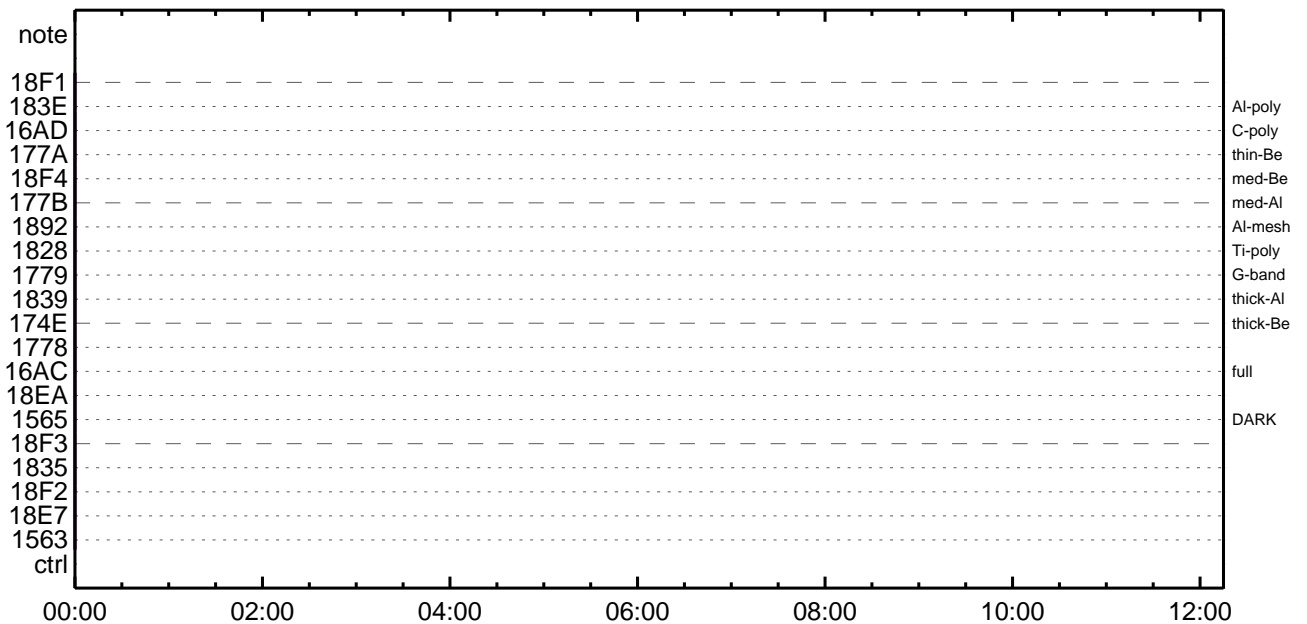
CMDI #0684 2012/06/24



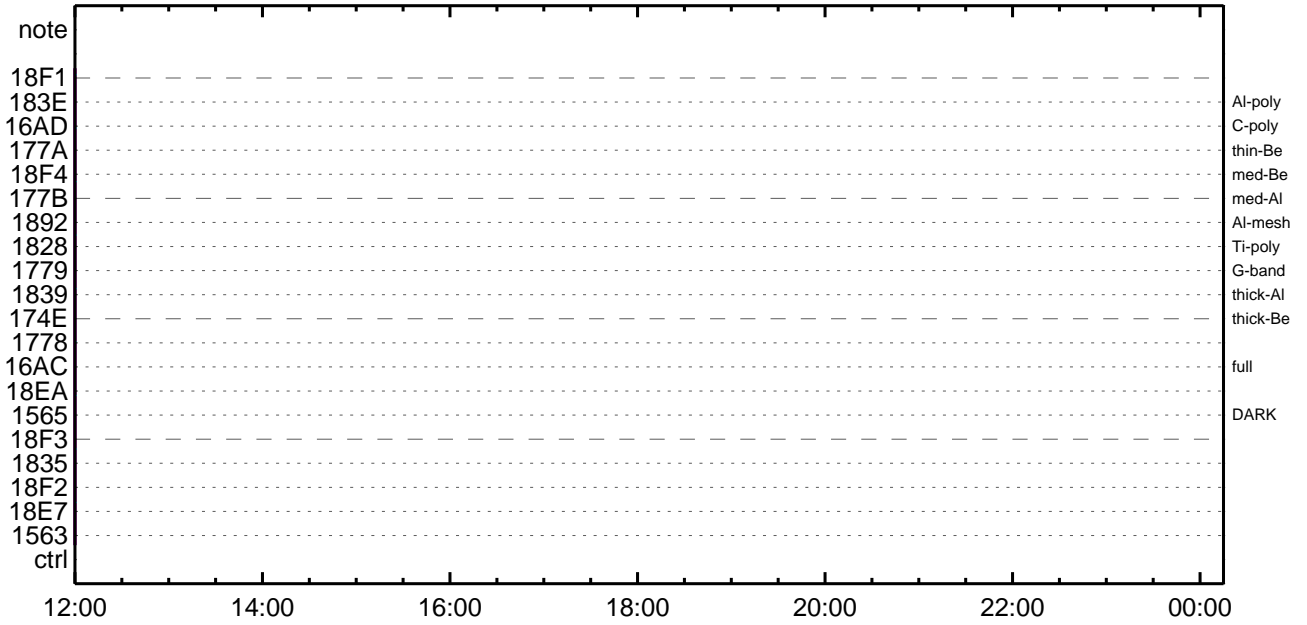
CMDI #0684 2012/06/24



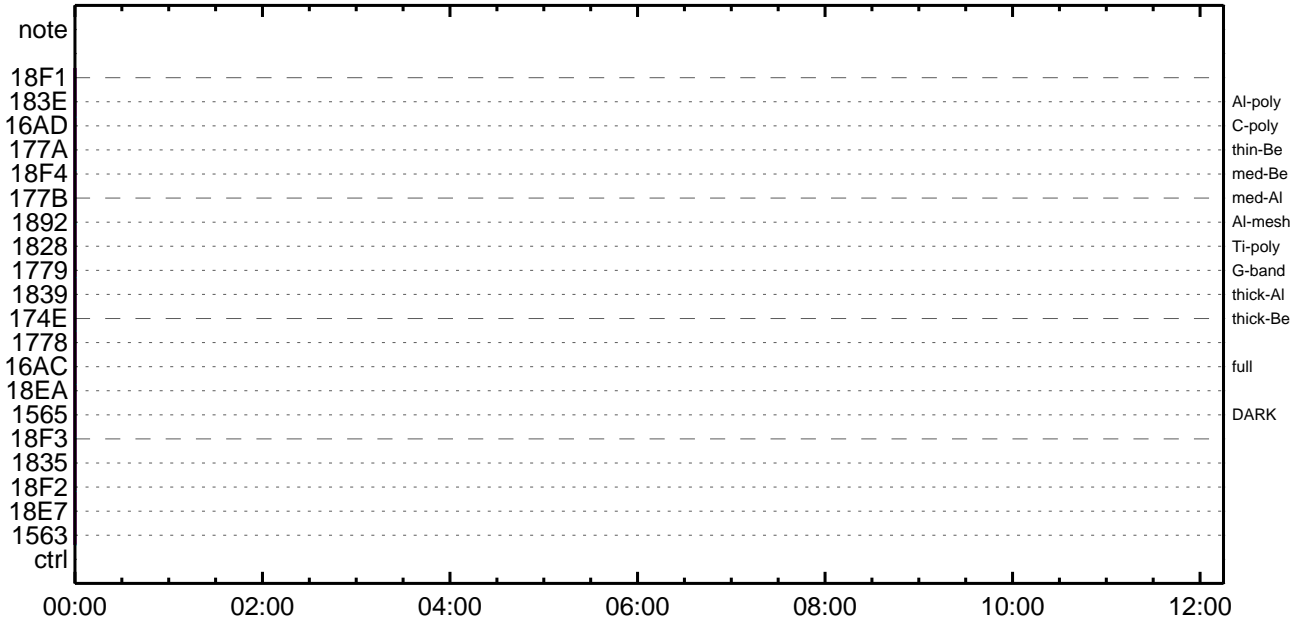
CMDI #0684 2012/06/25



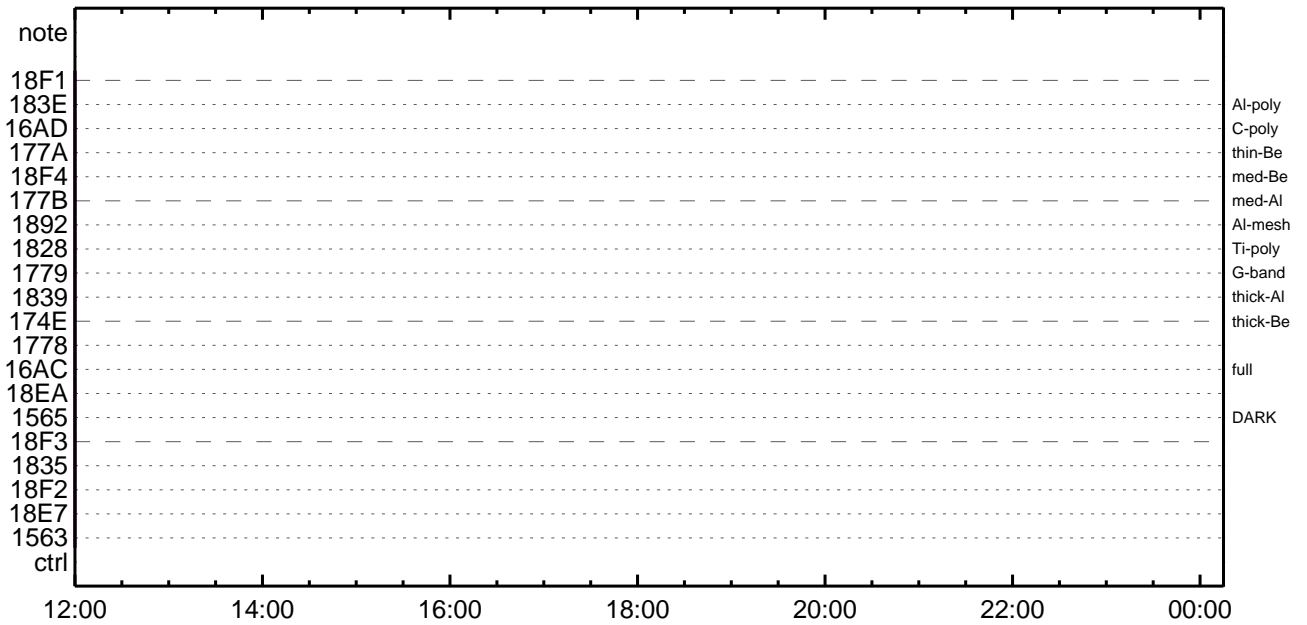
CMDI #0684 2012/06/25



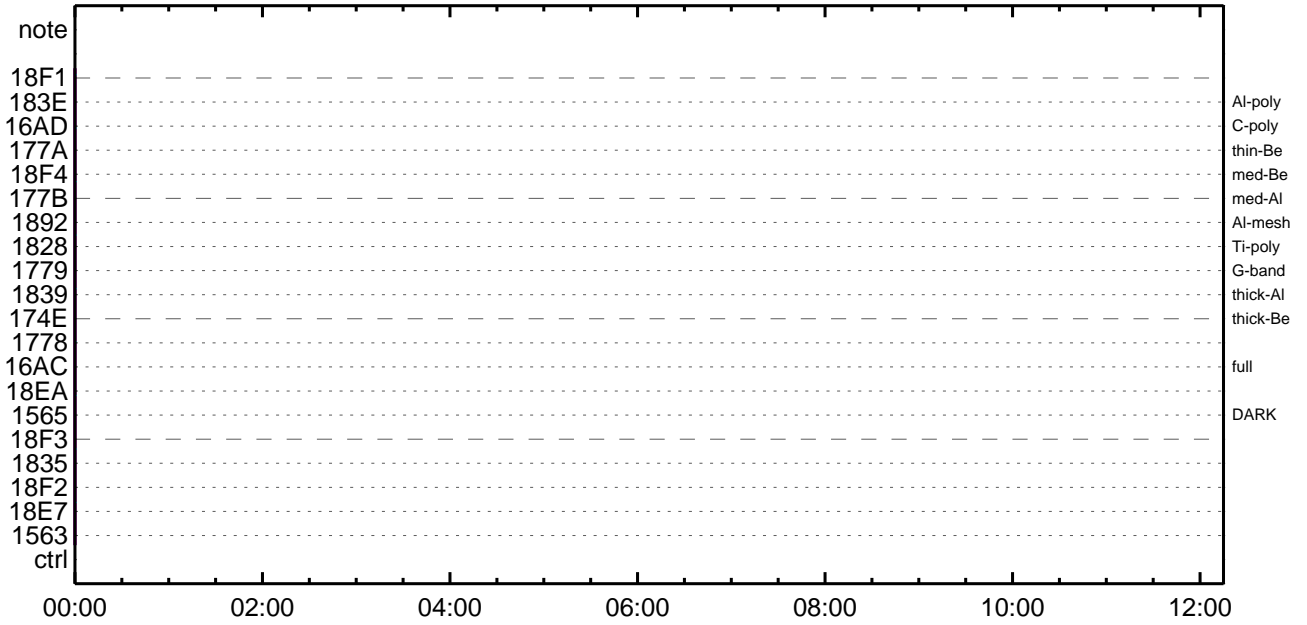
CMDI #0684 2012/06/26



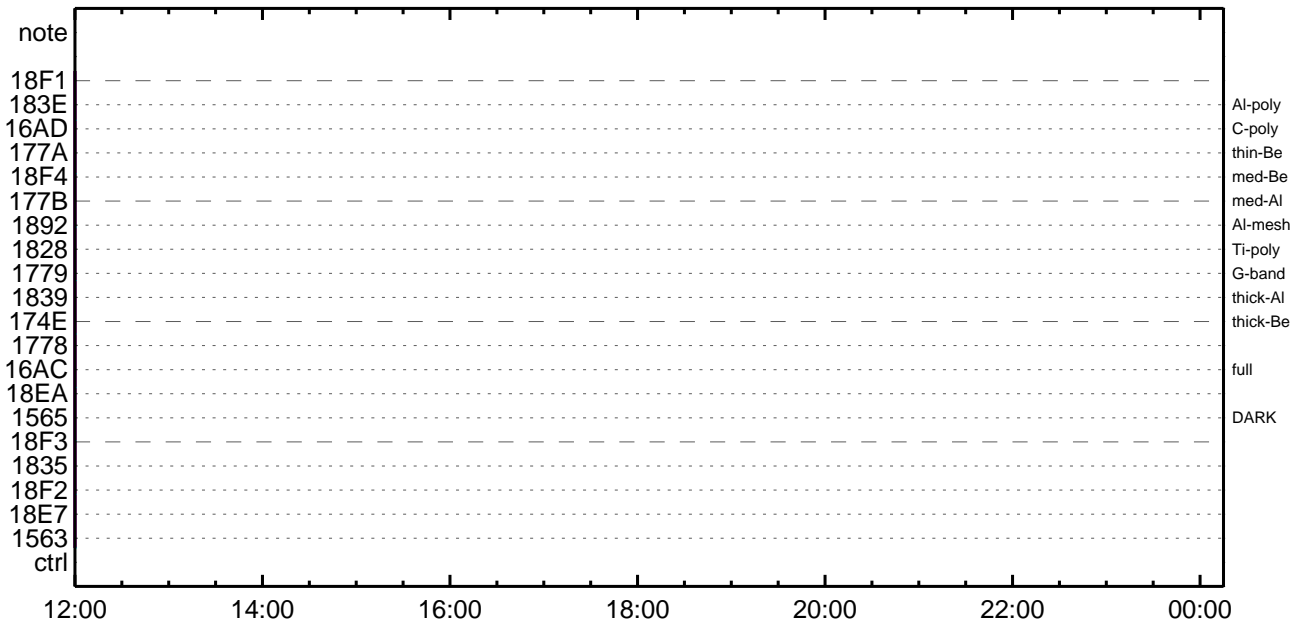
CMDI #0684 2012/06/26



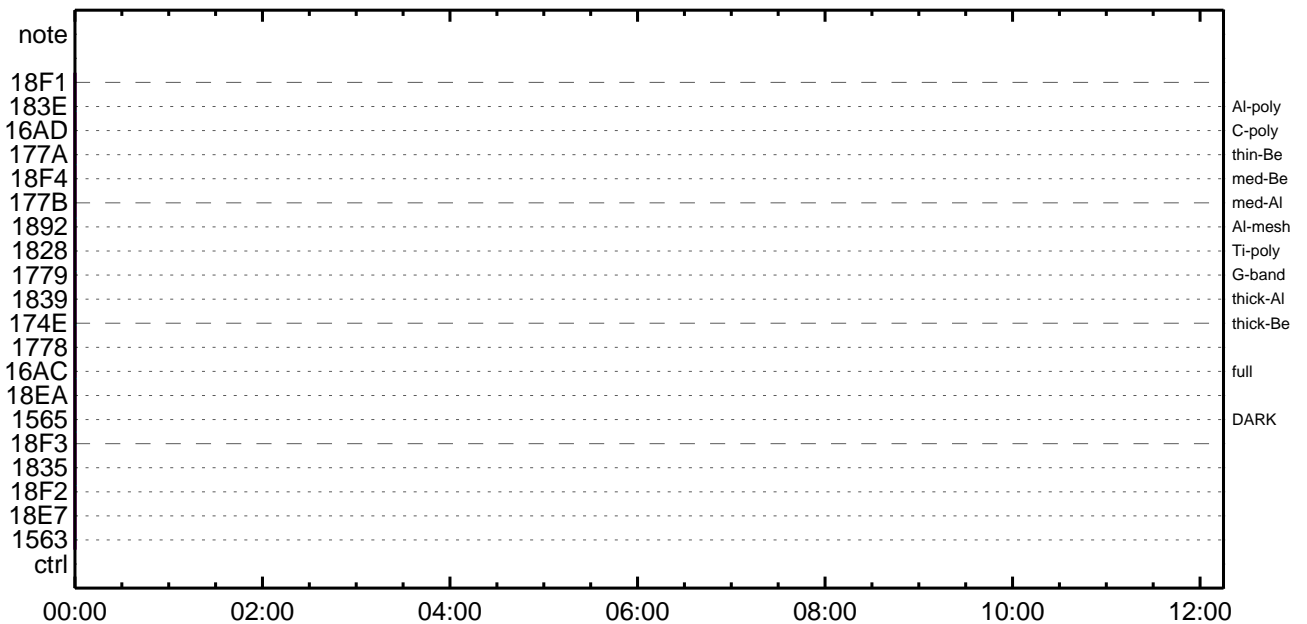
CMDI #0684 2012/06/27



CMDI #0684 2012/06/27

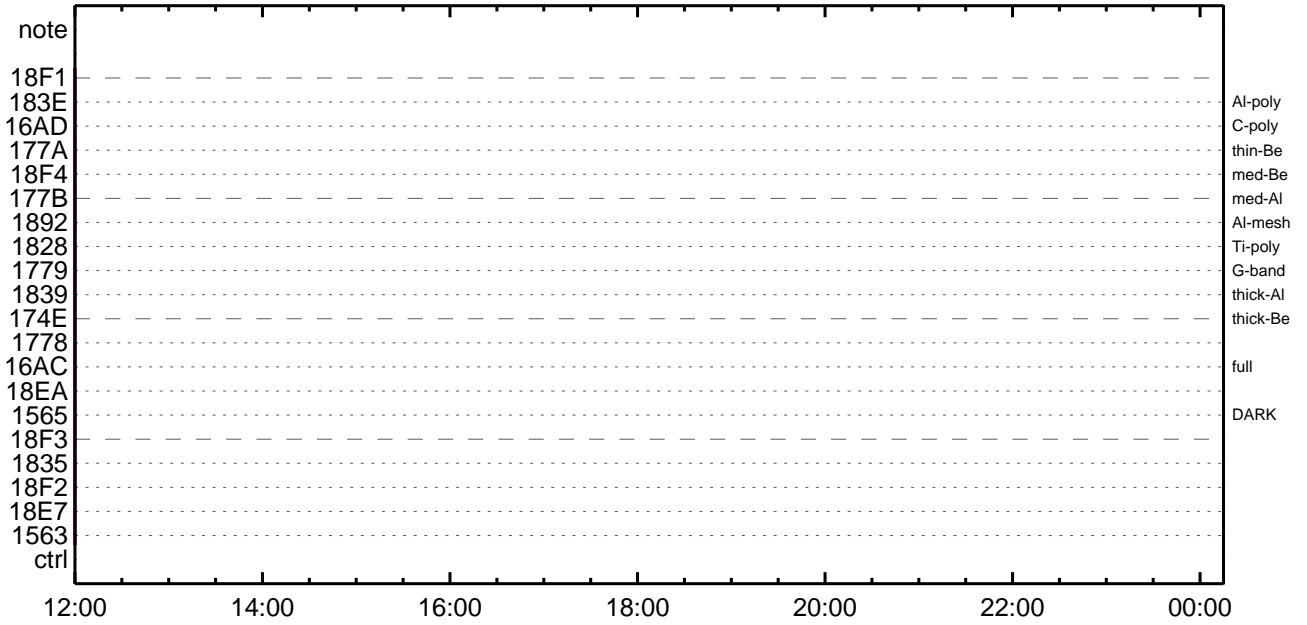


CMDI #0684 2012/06/28

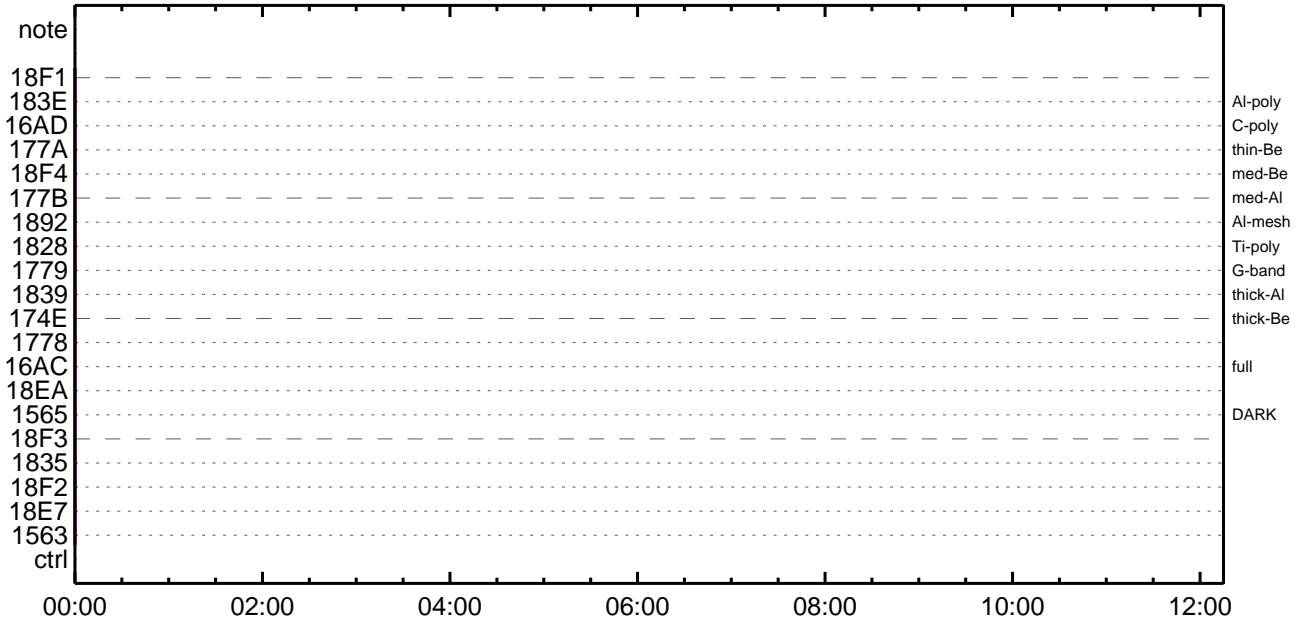




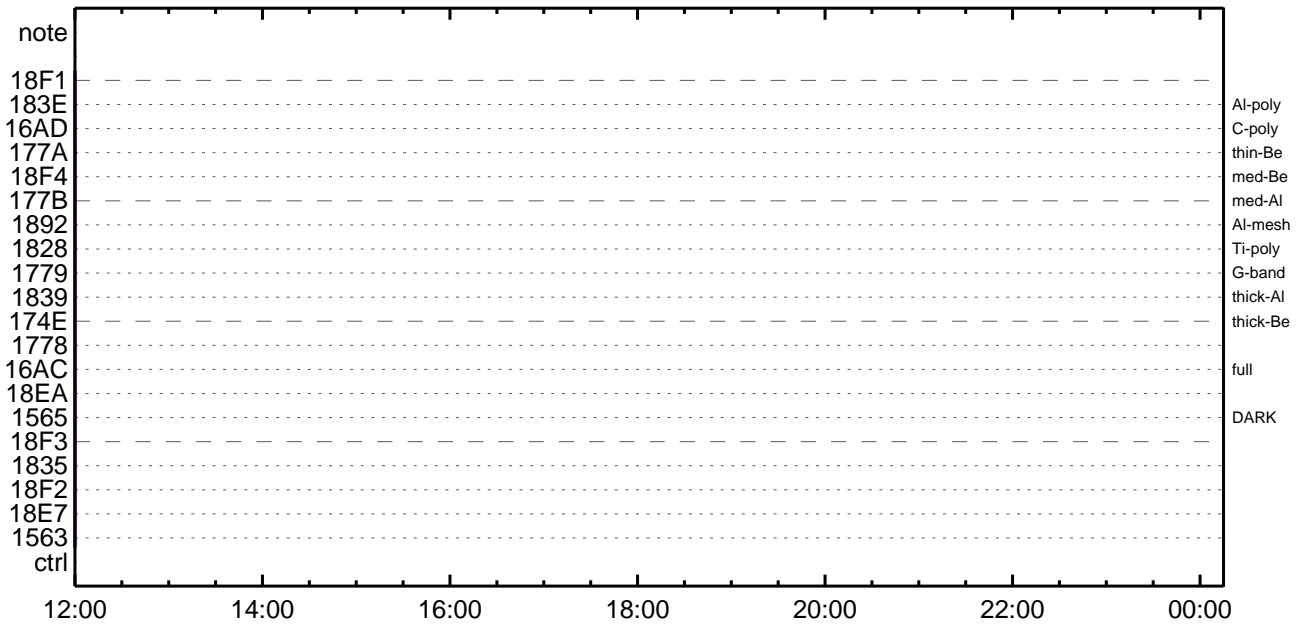
CMDI #0684 2012/06/28



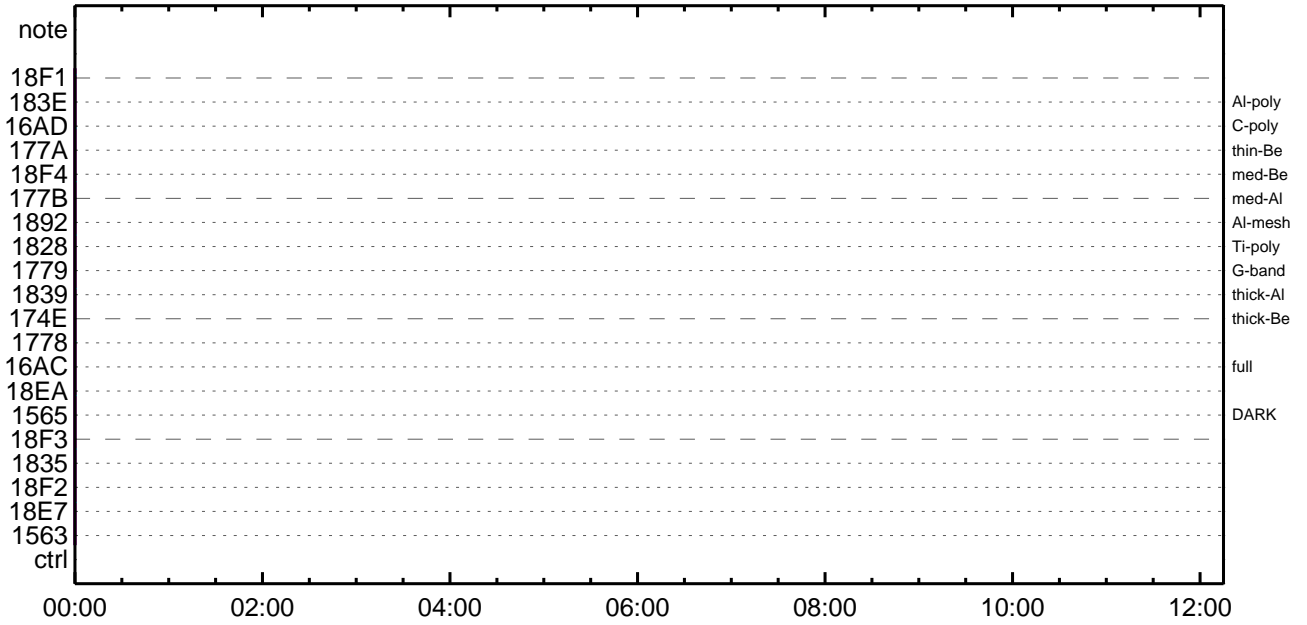
CMDI #0684 2012/06/29



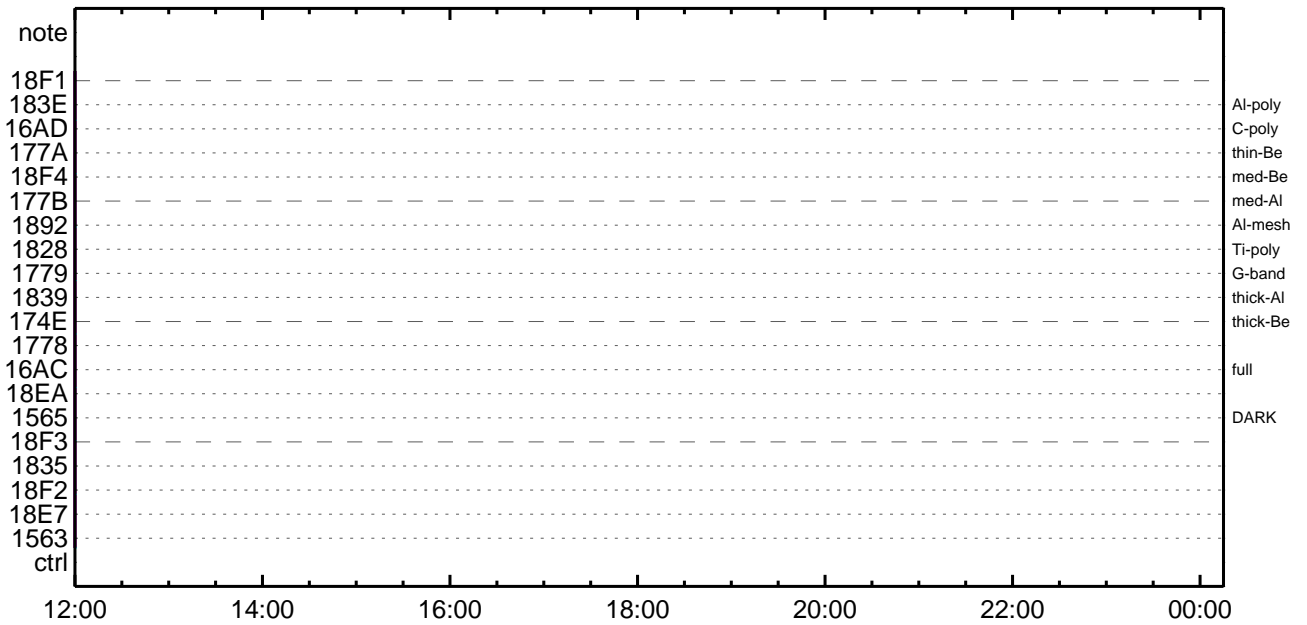
CMDI #0684 2012/06/29



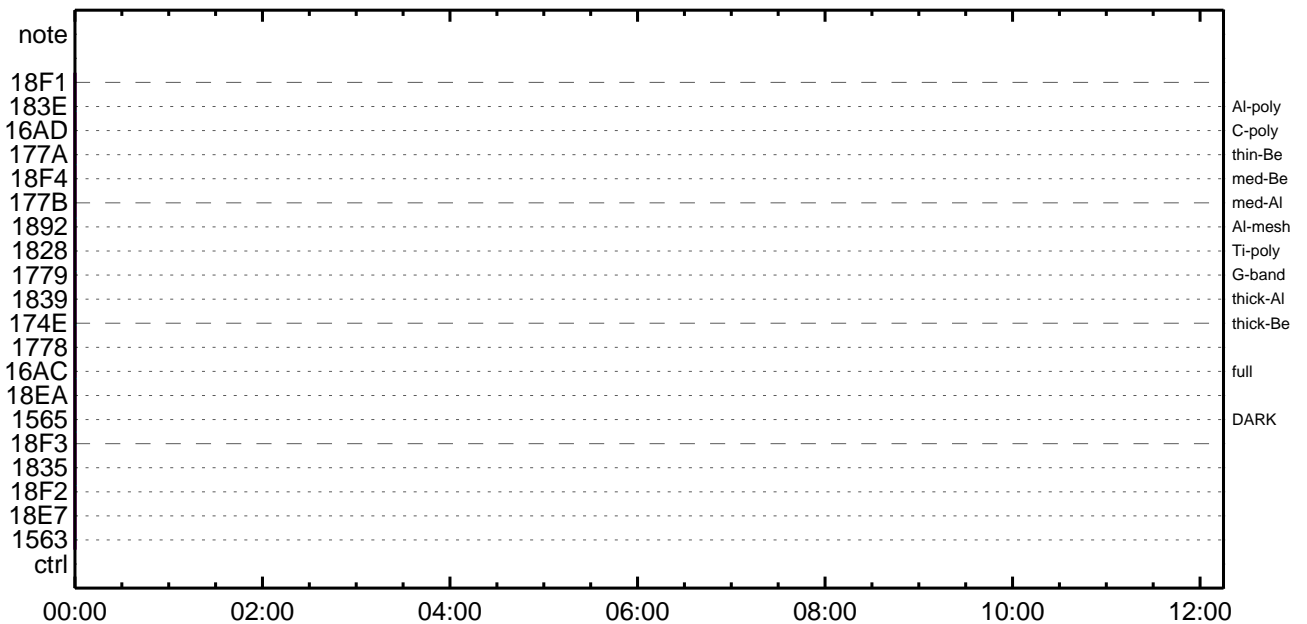
CMDI #0684 2012/06/30



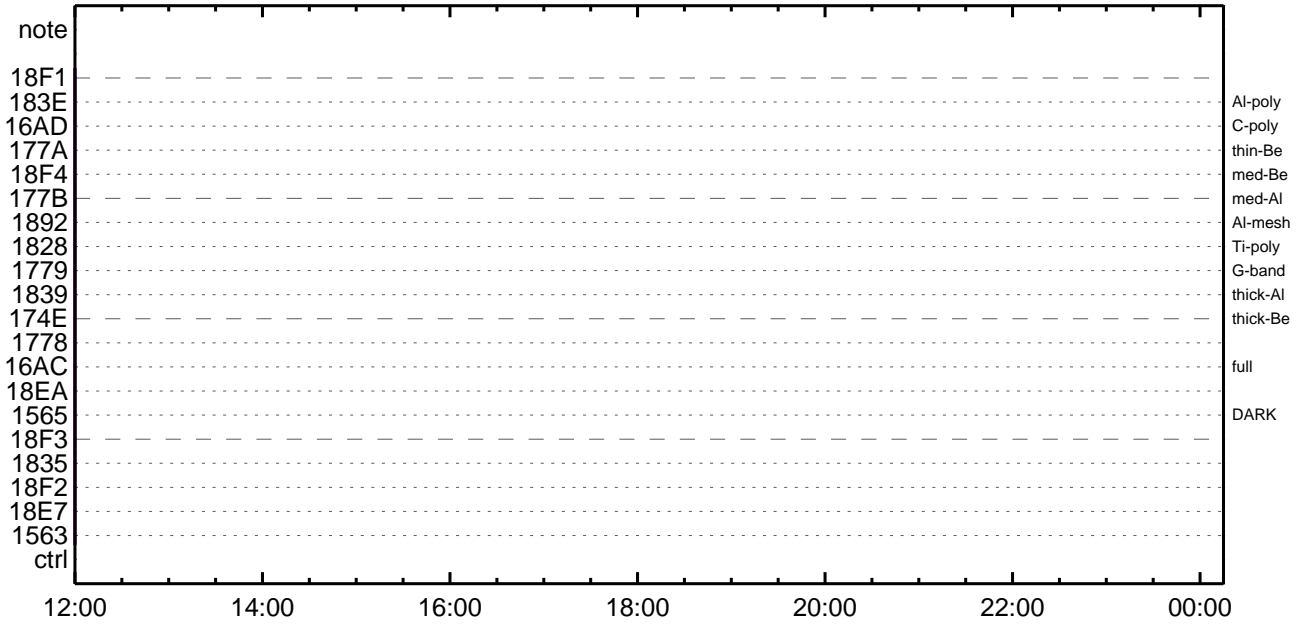
CMDI #0684 2012/06/30



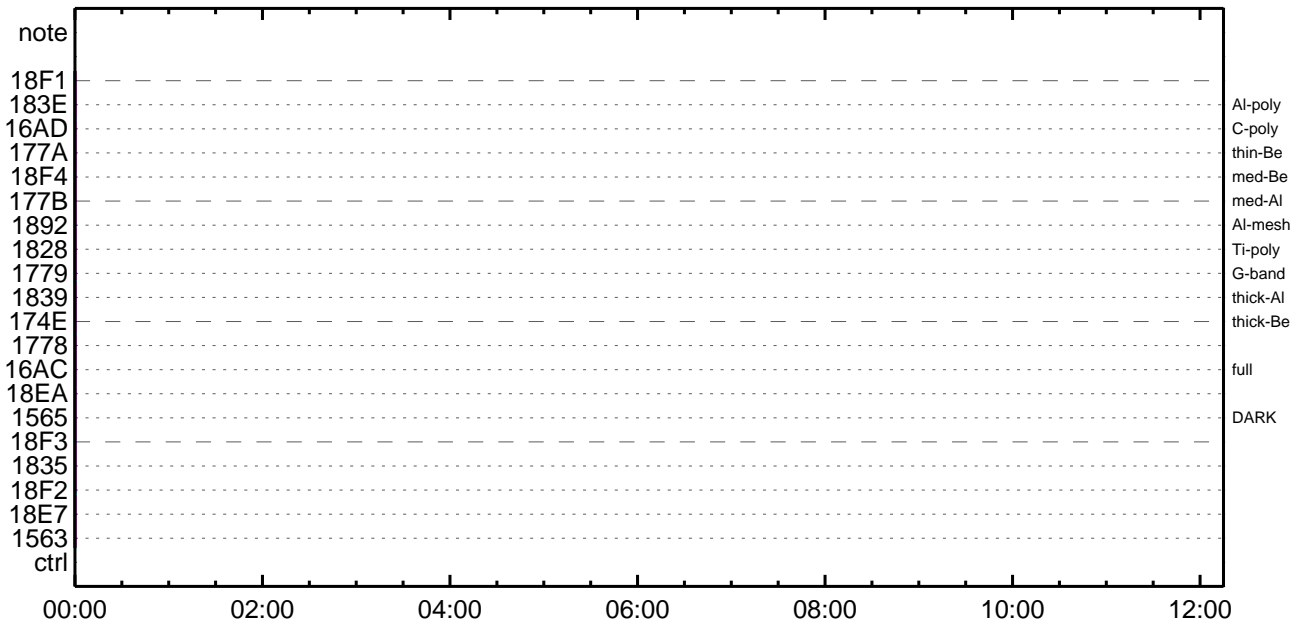
CMDI #0684 2012/07/01



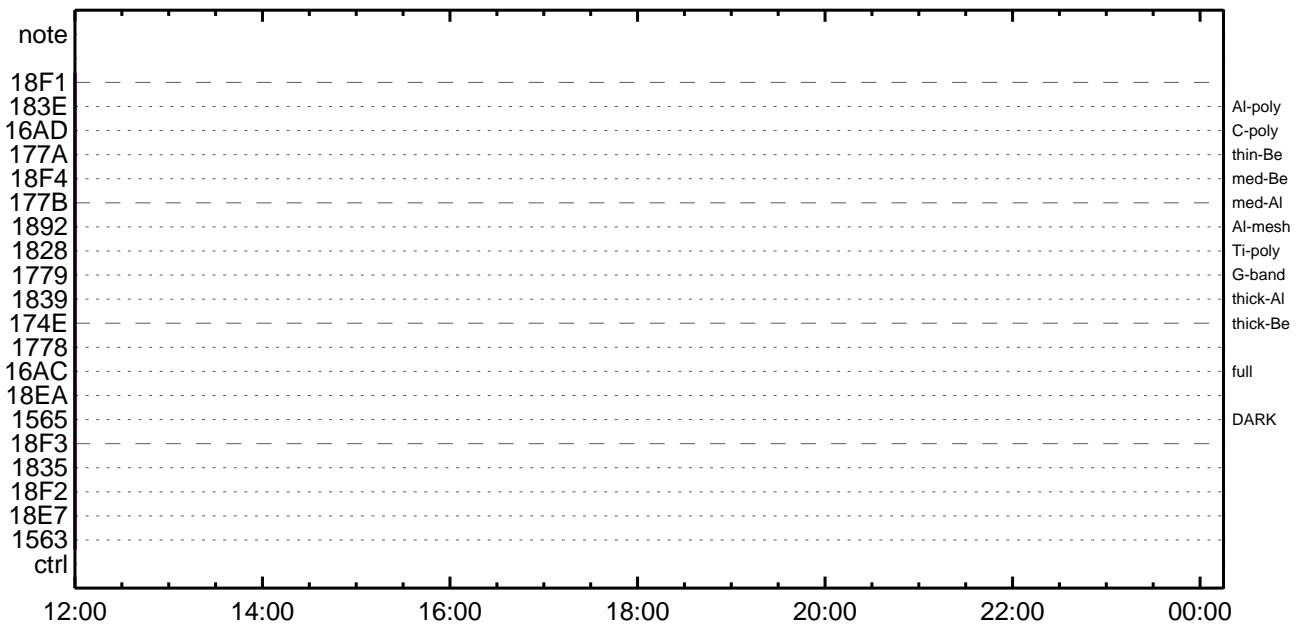
CMDI #0684 2012/07/01



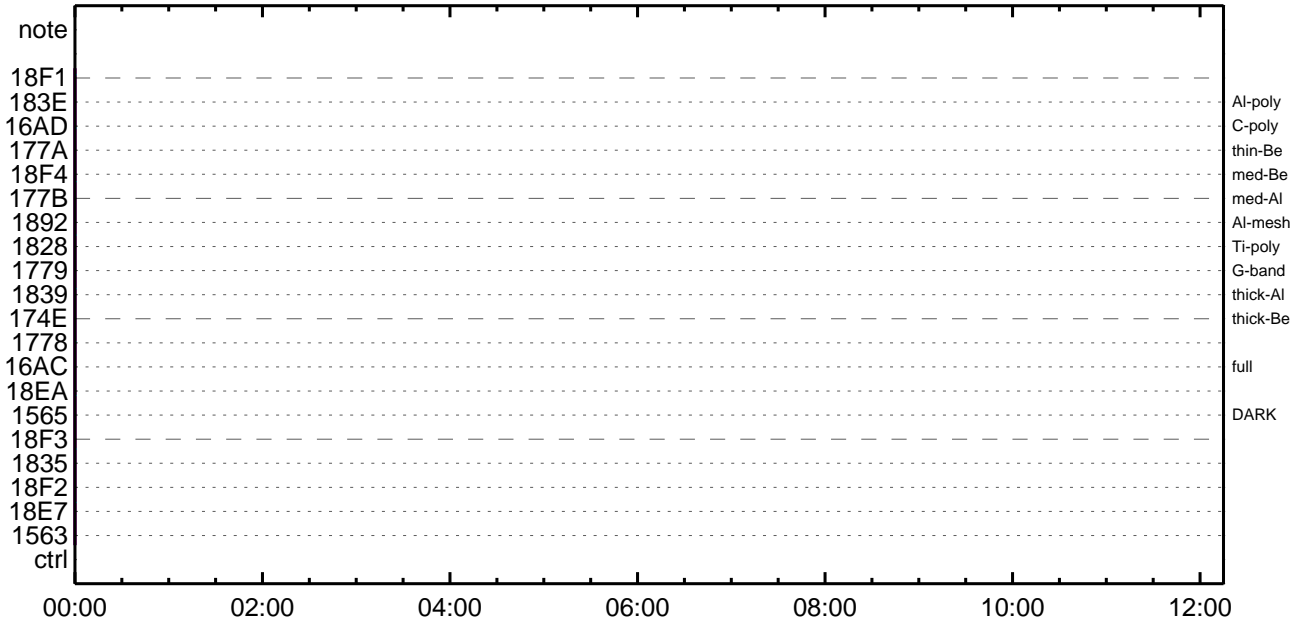
CMDI #0684 2012/07/02



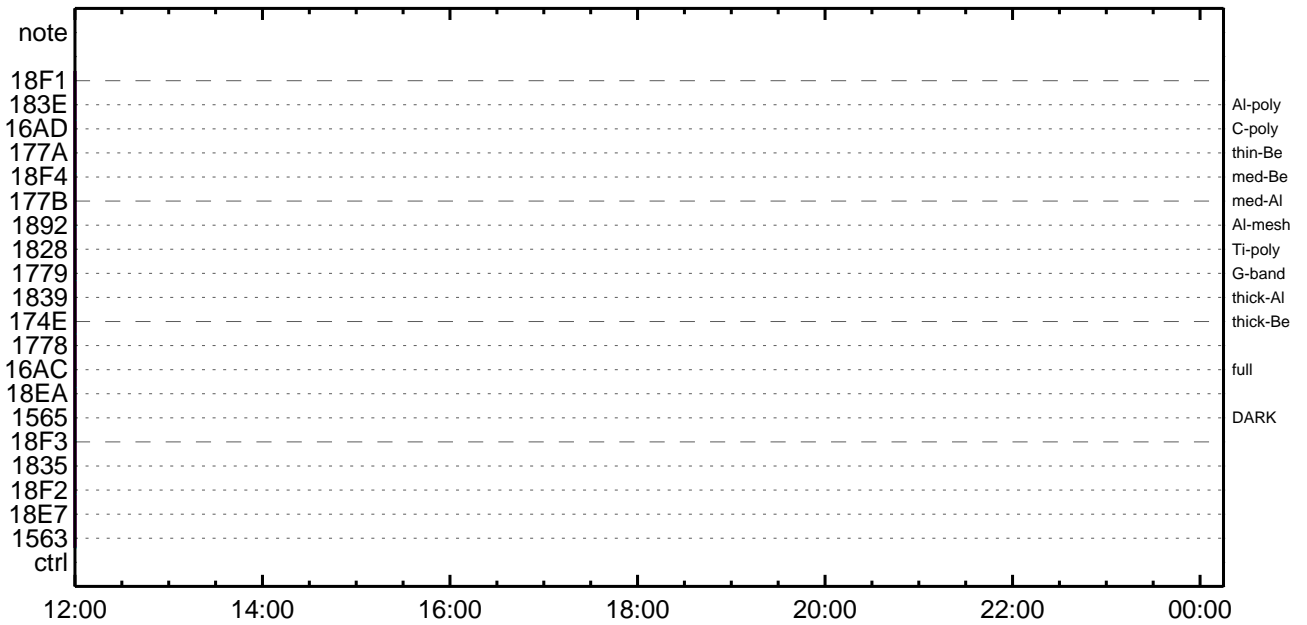
CMDI #0684 2012/07/02



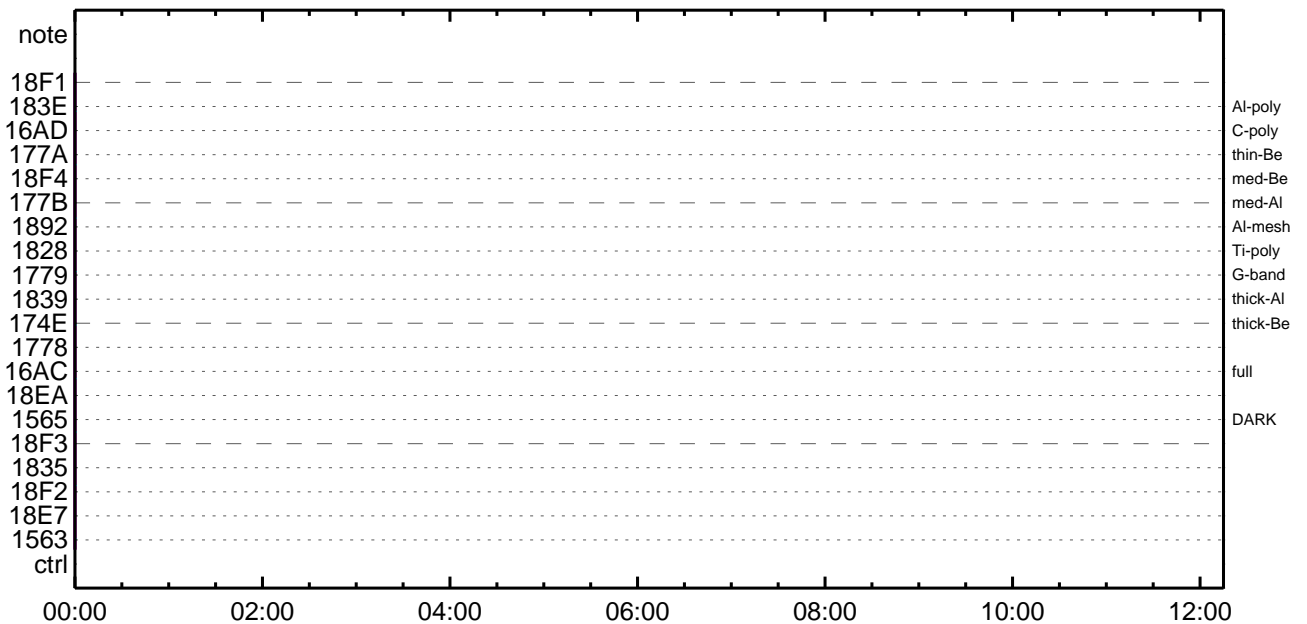
CMDI #0684 2012/07/03



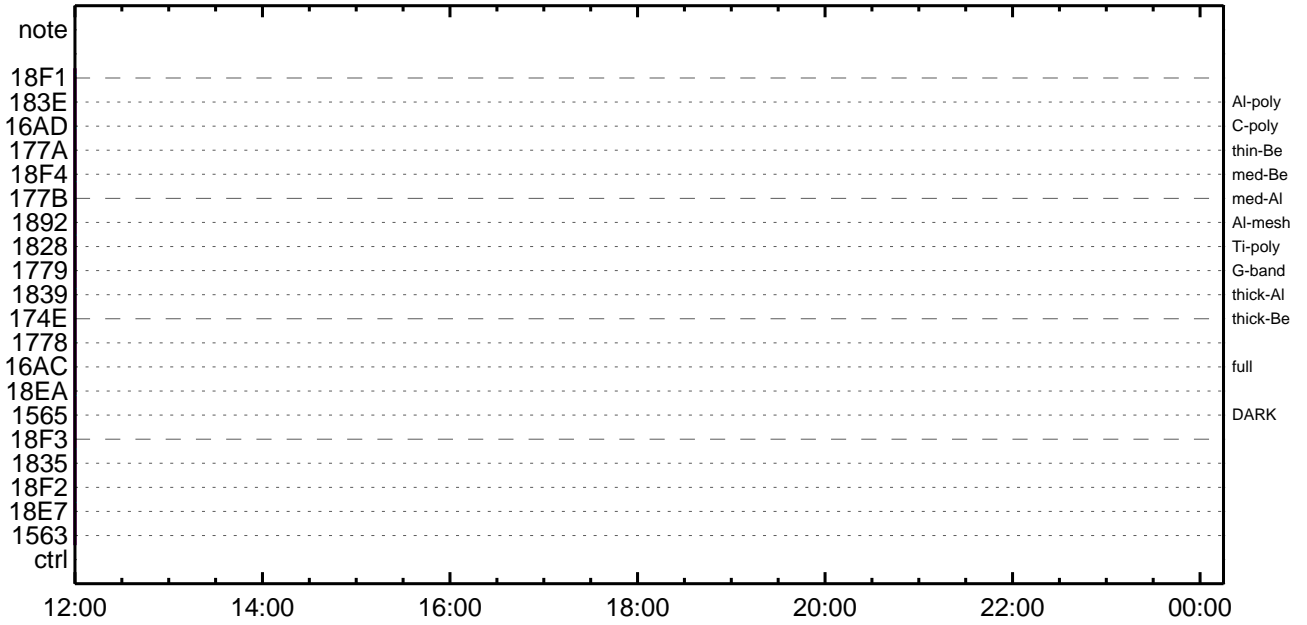
CMDI #0684 2012/07/03



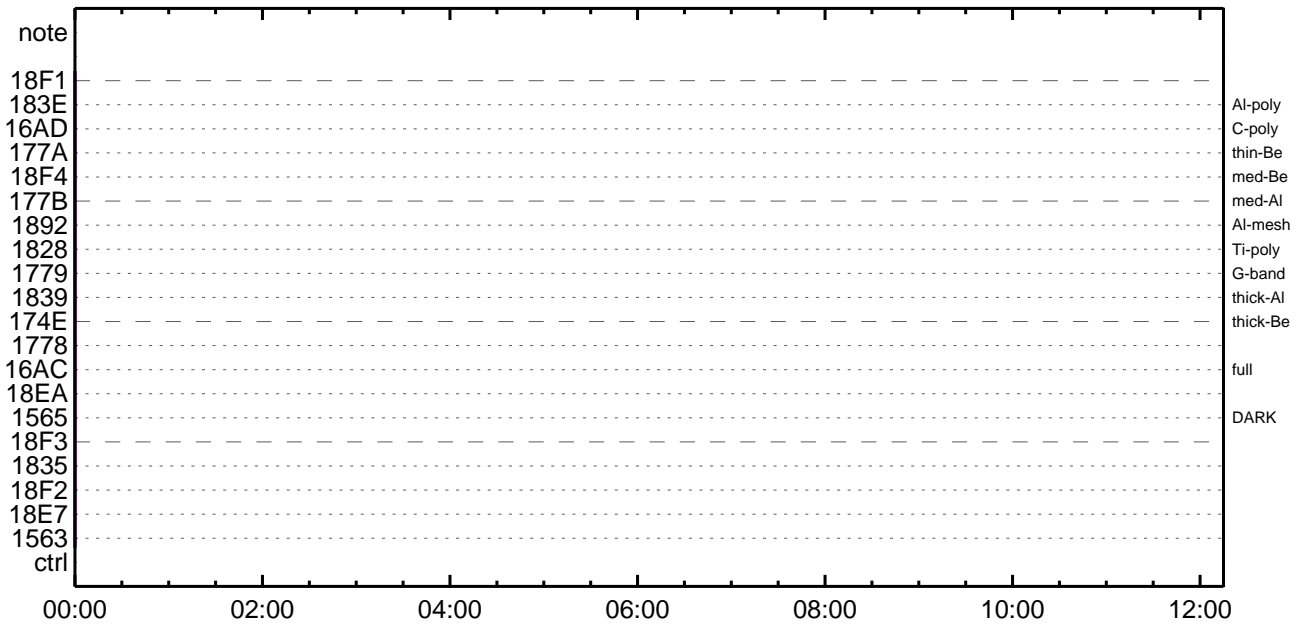
CMDI #0684 2012/07/04



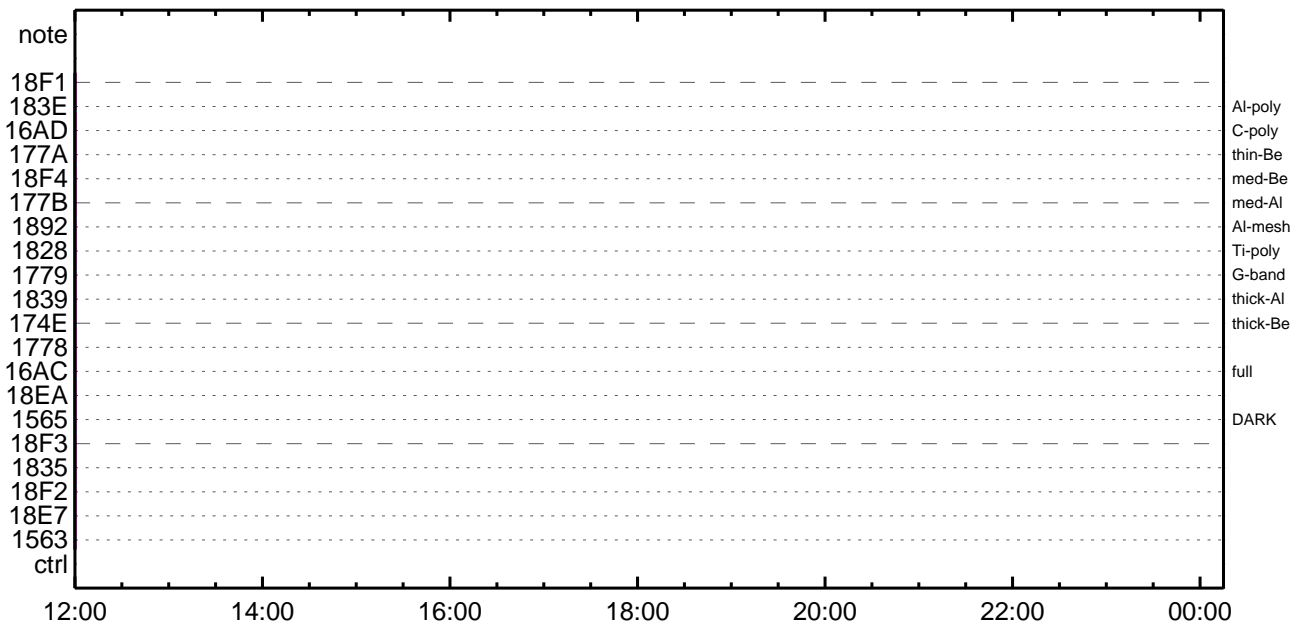
CMDI #0684 2012/07/04



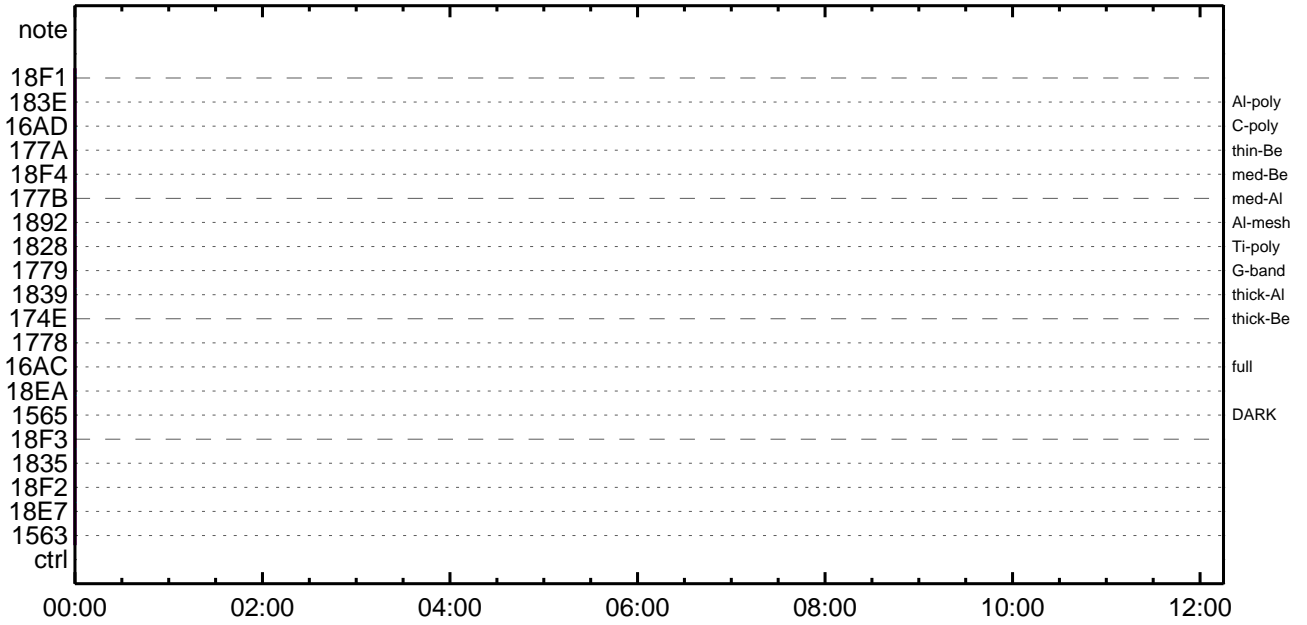
CMDI #0684 2012/07/05



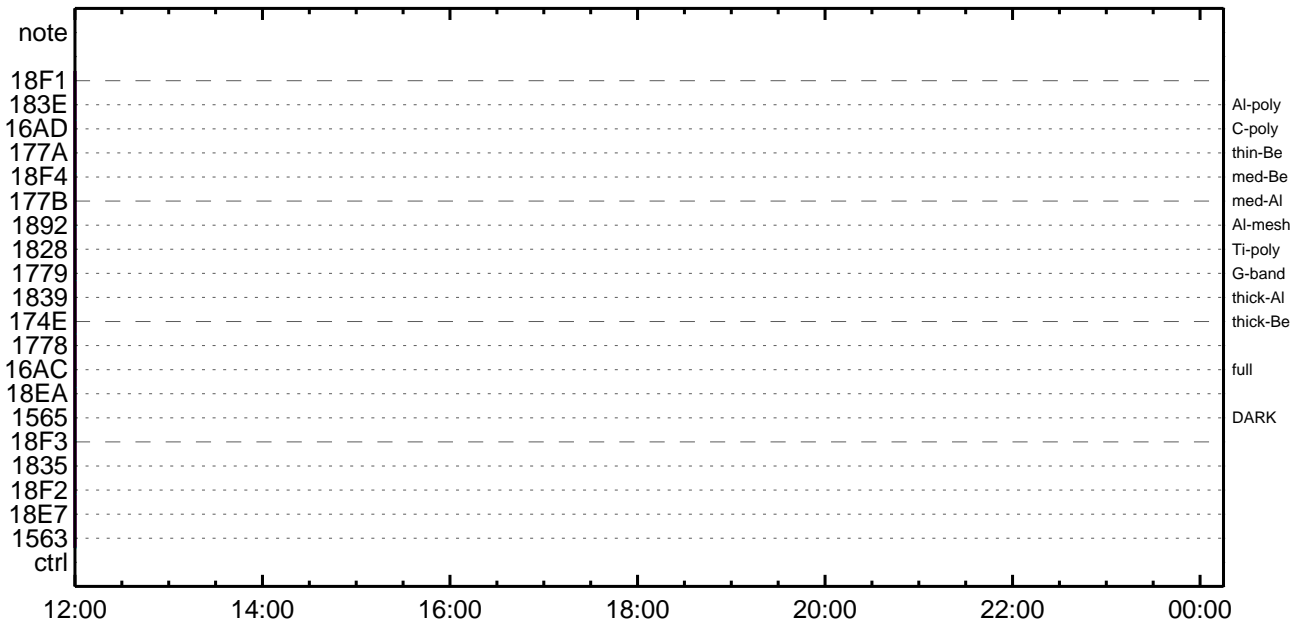
CMDI #0684 2012/07/05



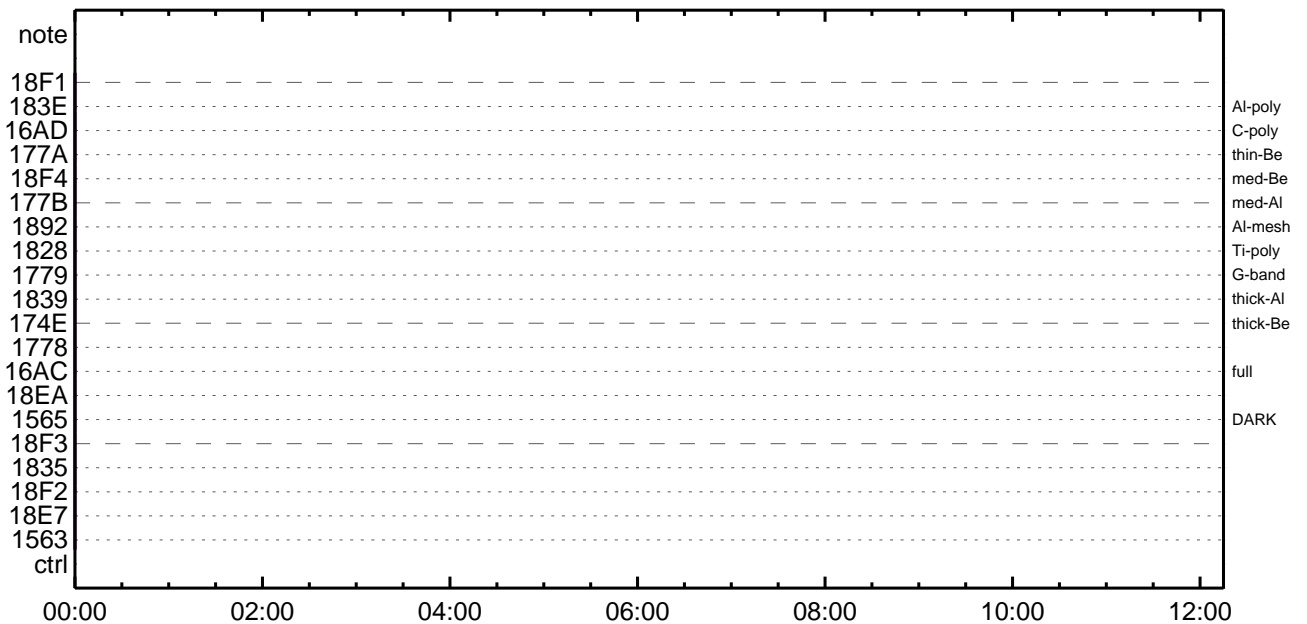
CMDI #0684 2012/07/06



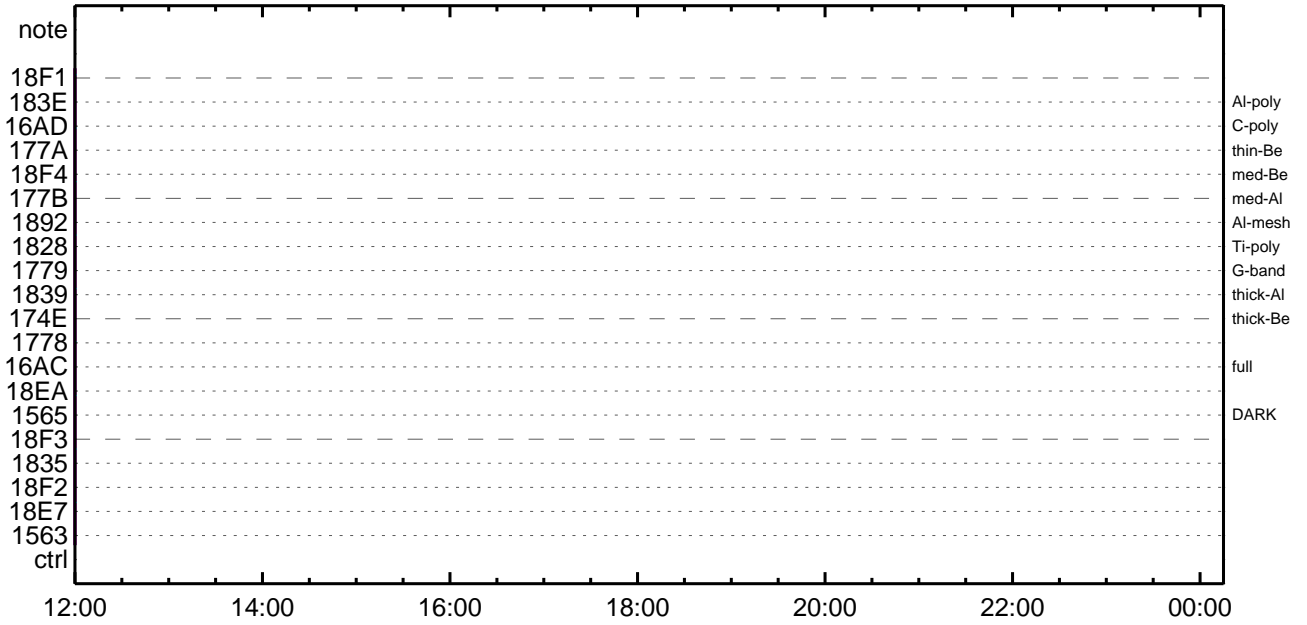
CMDI #0684 2012/07/06



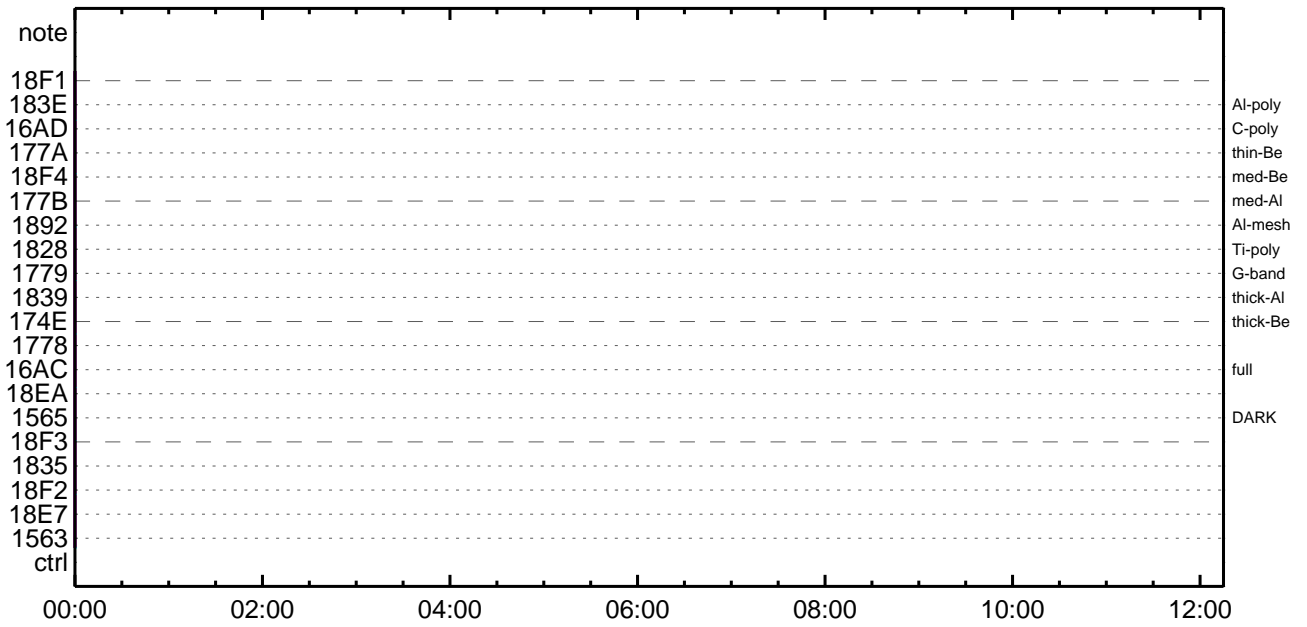
CMDI #0684 2012/07/07



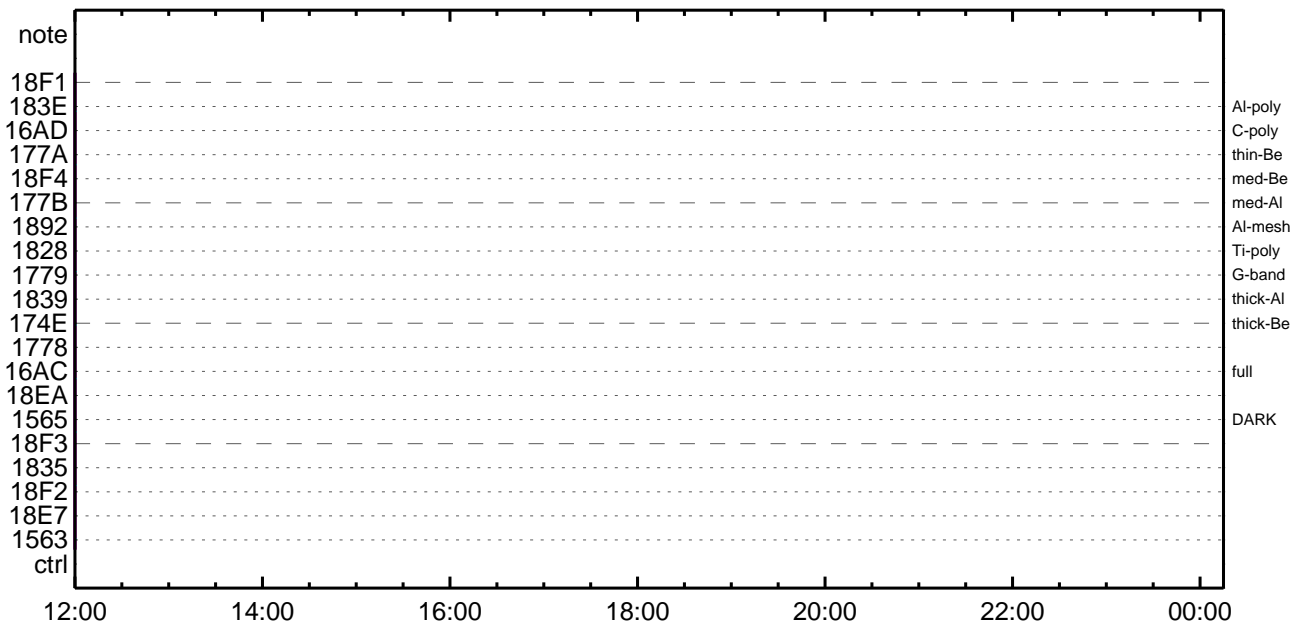
CMDI #0684 2012/07/07



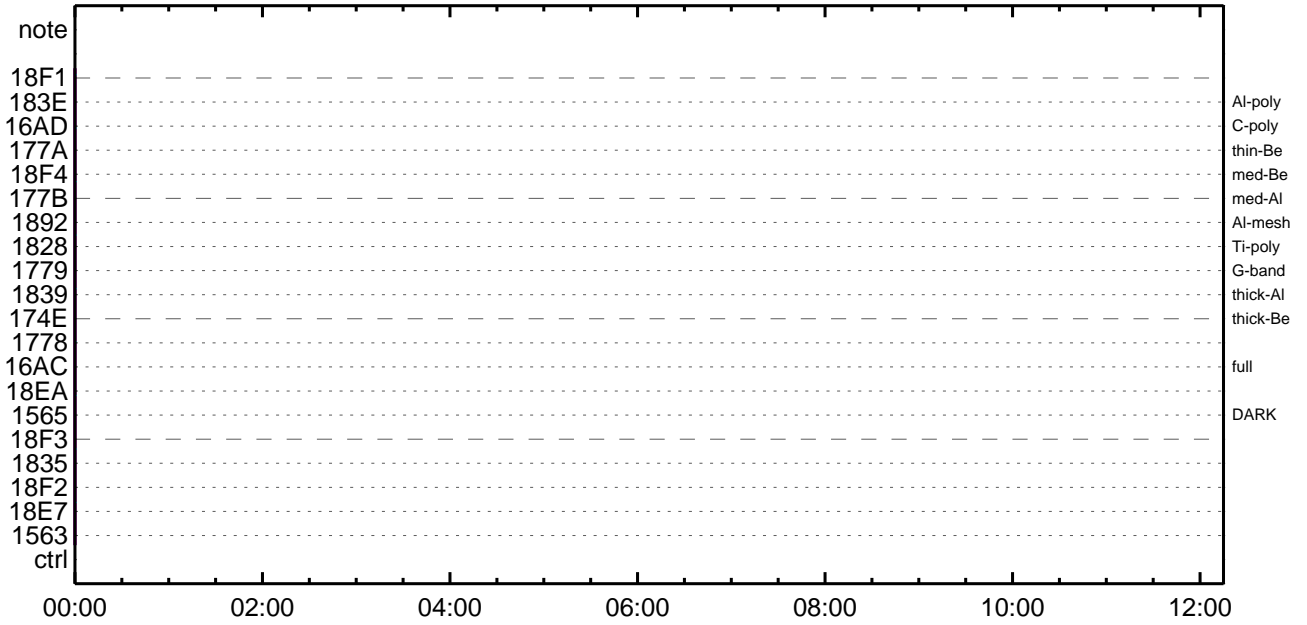
CMDI #0684 2012/07/08



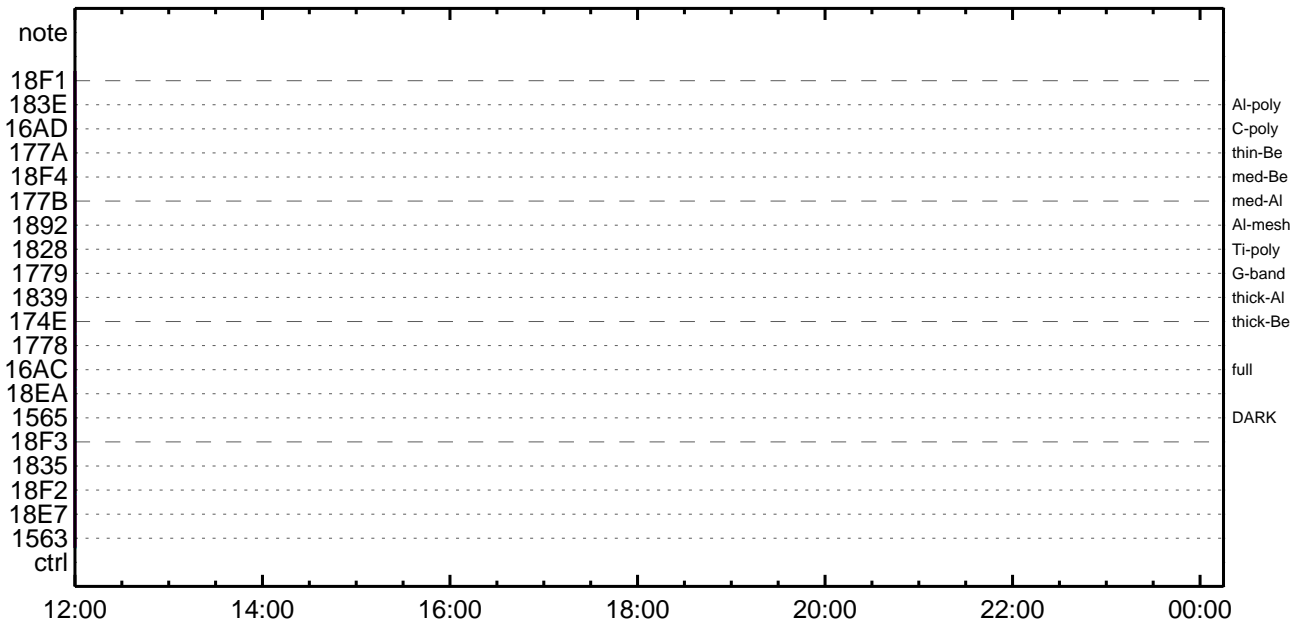
CMDI #0684 2012/07/08



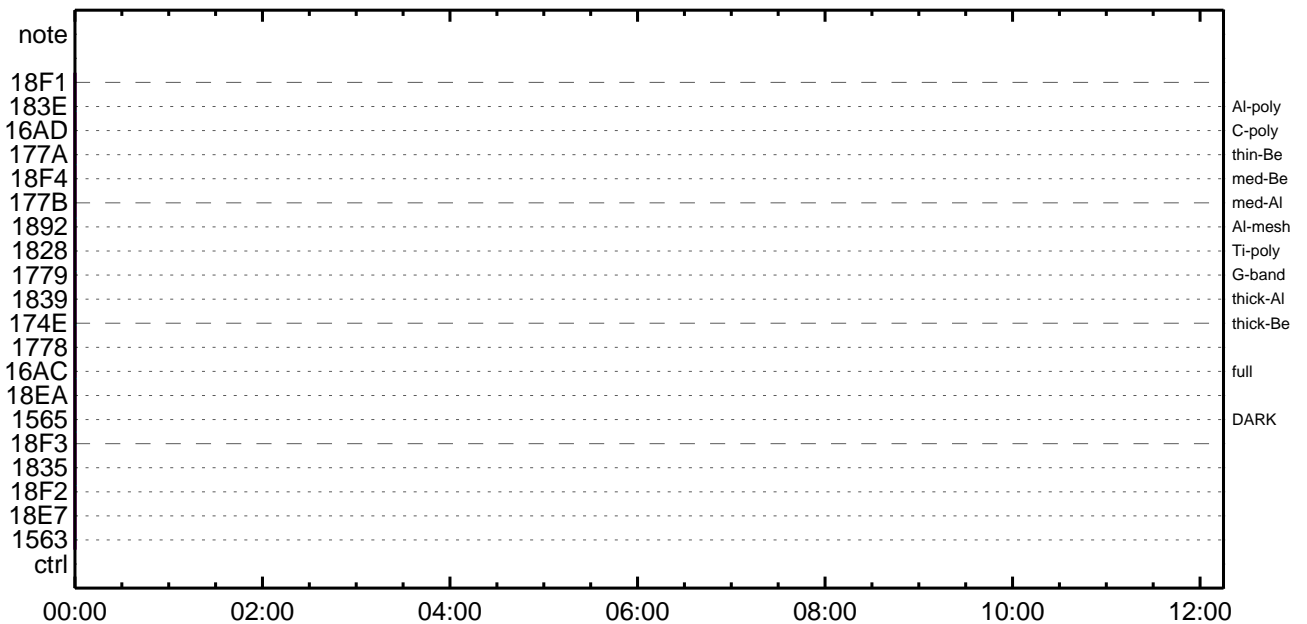
CMDI #0684 2012/07/09



CMDI #0684 2012/07/09

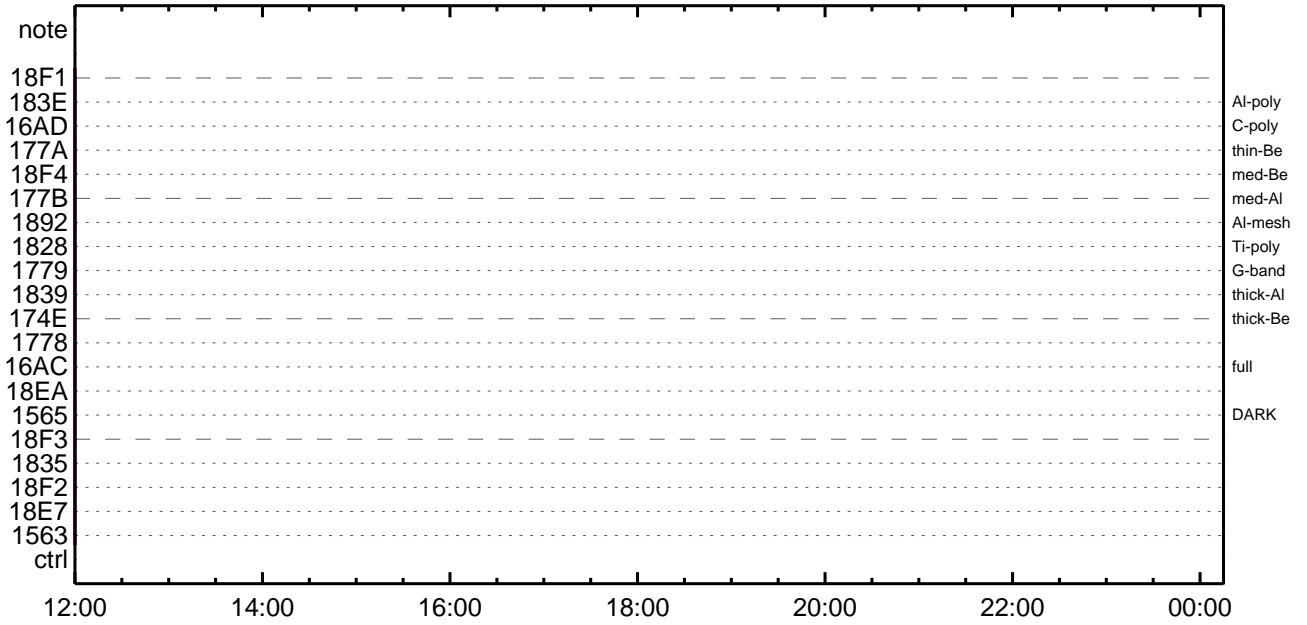


CMDI #0684 2012/07/10

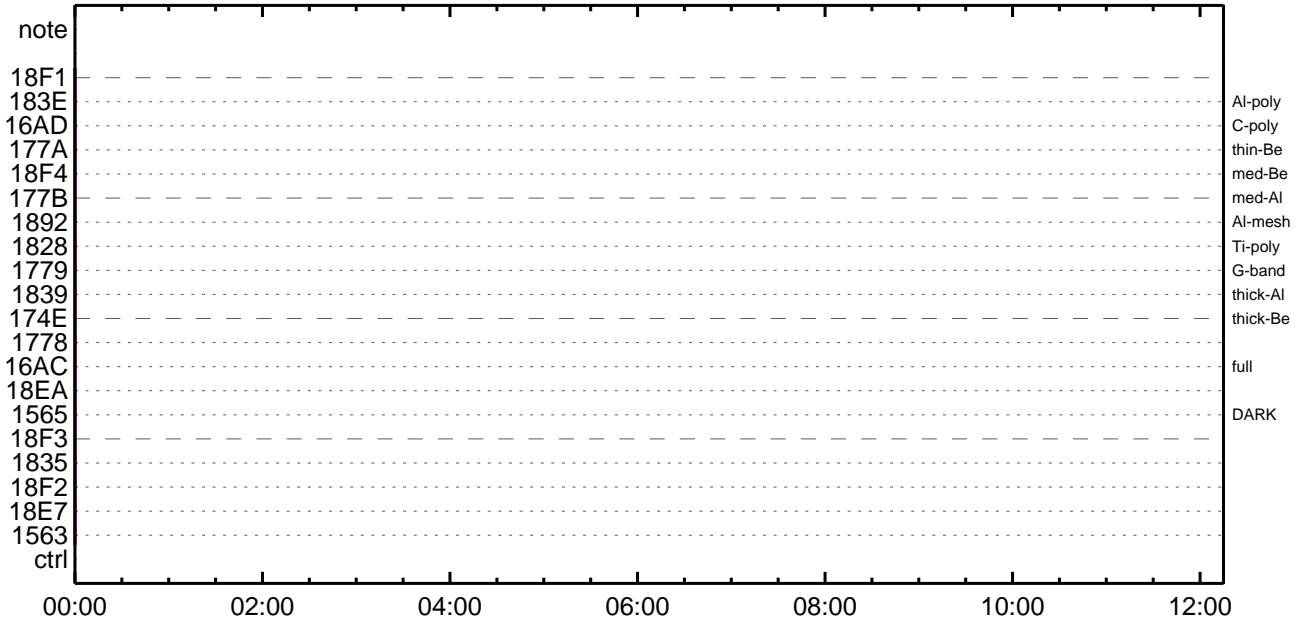




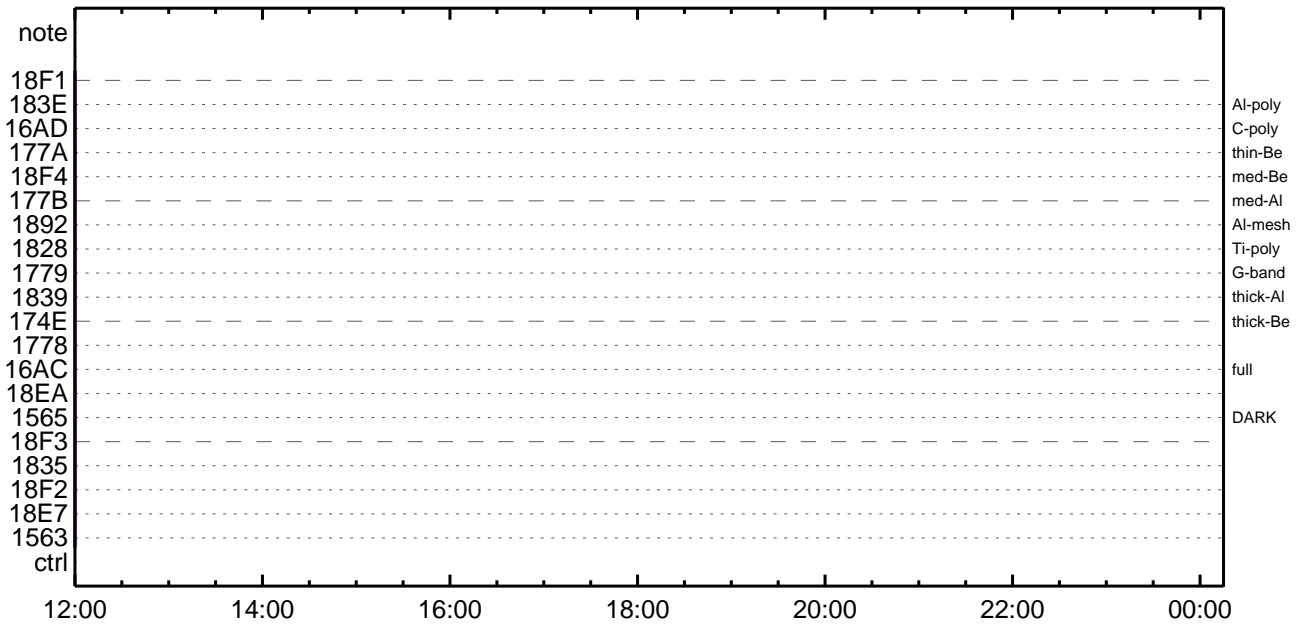
CMDI #0684 2012/07/10



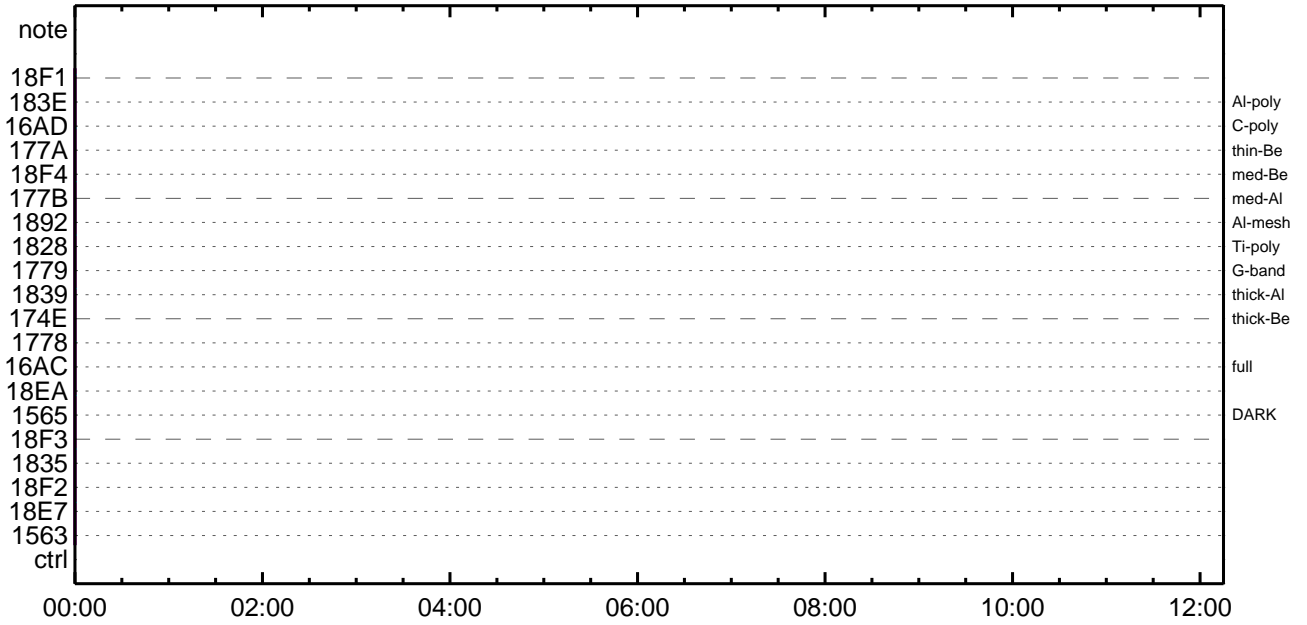
CMDI #0684 2012/07/11



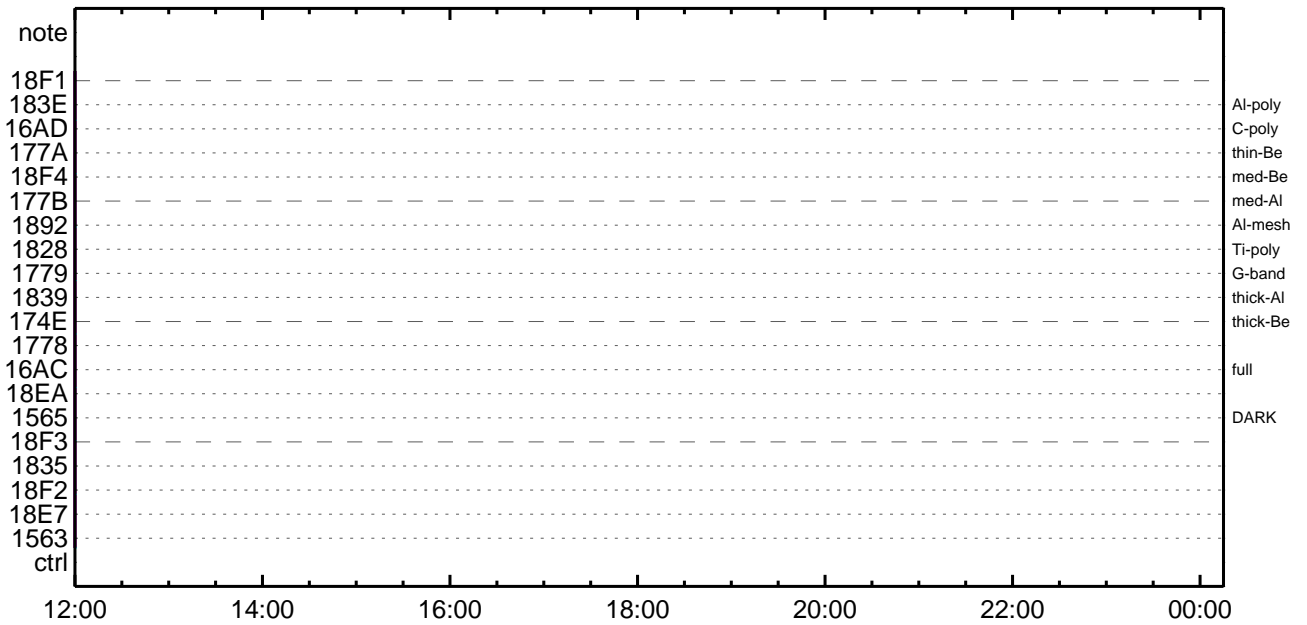
CMDI #0684 2012/07/11



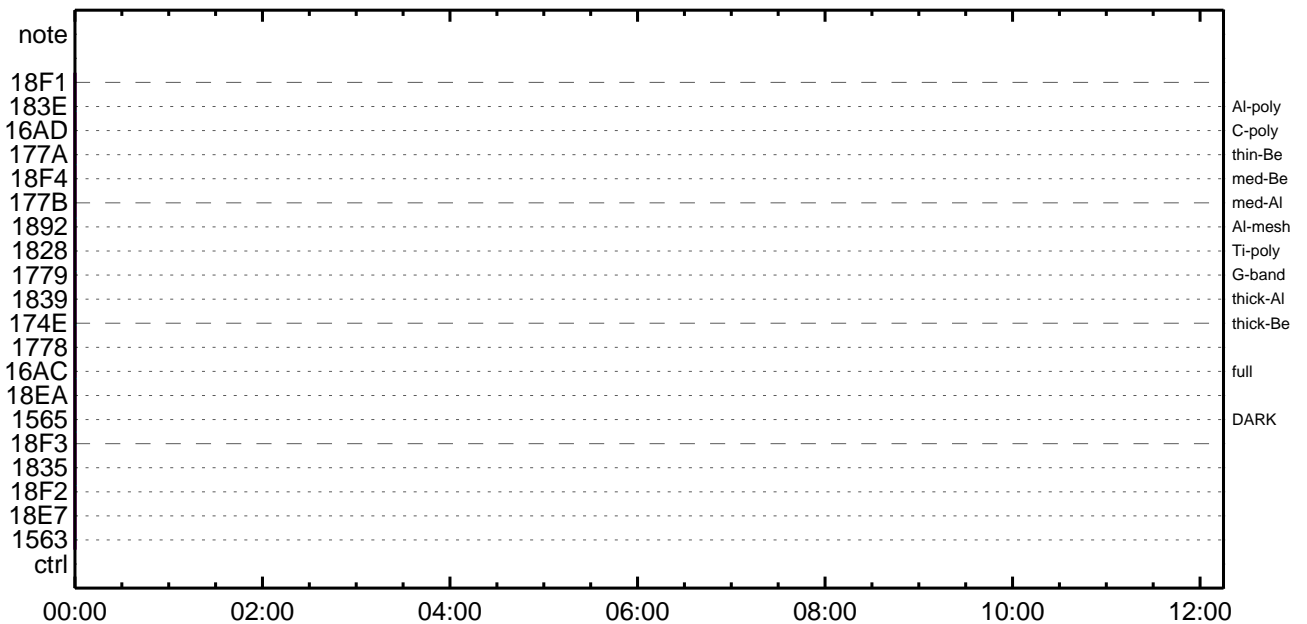
CMDI #0684 2012/07/12



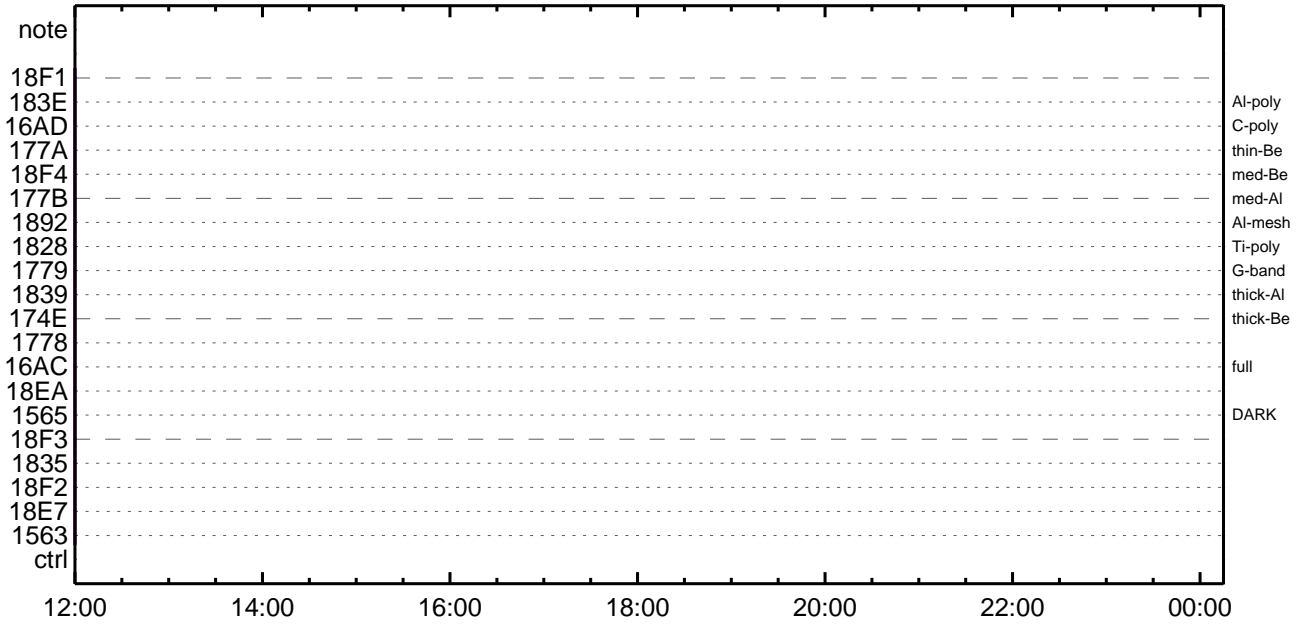
CMDI #0684 2012/07/12



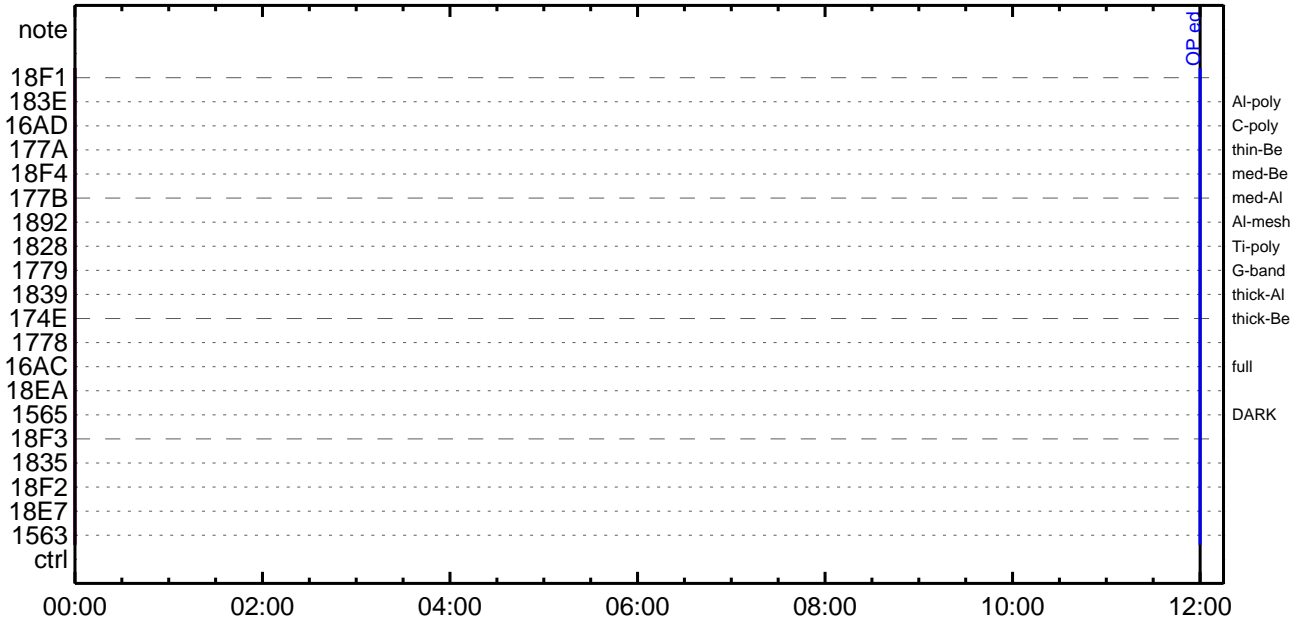
CMDI #0684 2012/07/13



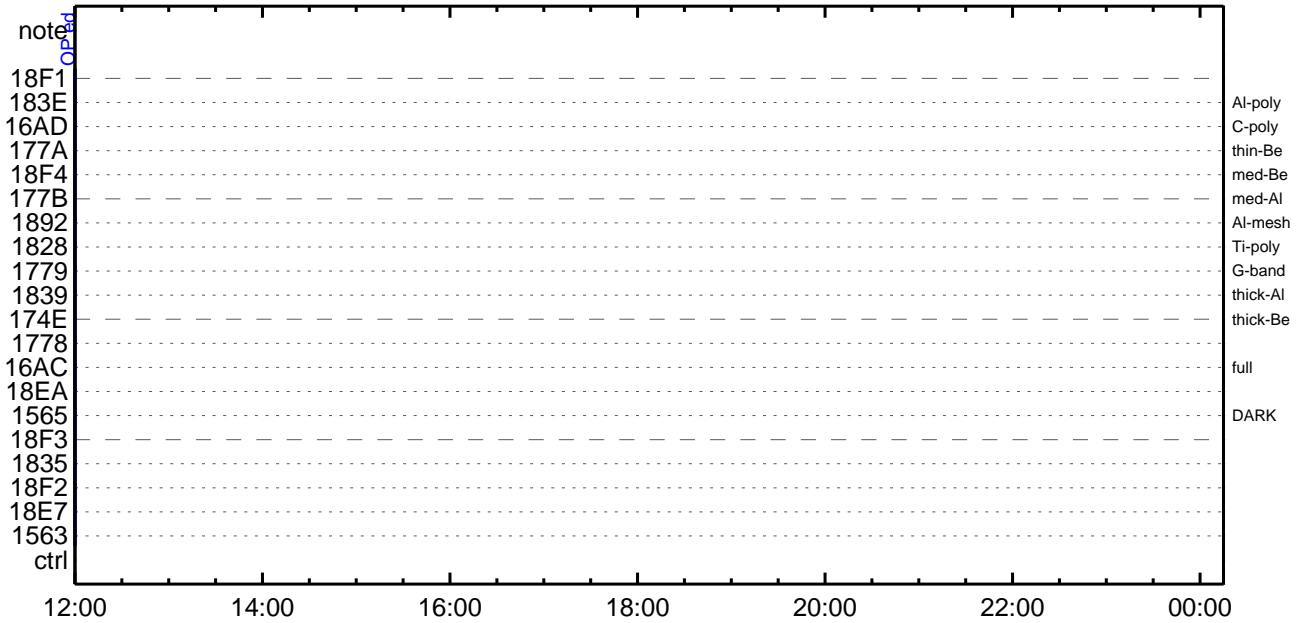
CMDI #0684 2012/07/13



CMDI #0684 2012/07/14



CMDI #0684 2012/07/14





```
0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-926:OP
0104 ( )
0105 S. OG og-926:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îç§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îç§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îç§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îç§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îç§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOK²³îç§
0165 C.
0166 C. ***** °E²¼²î¼E¹ç•è²îOK²³îç§ *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î¼E¹ç•è²îOK²³îç§
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 C. TIY³YpYóYEòòÁDîç(UT)
0183 +. TI 2012-06-09 09:26:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-06-09 09:26:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-06-09 09:26:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

```

0194 C.
0195 +. TI 2012-06-09 09:30:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄë%îíñαîî¥Á¥§¥Á¥-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîî°è¥Á¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ          EXEC
0224 C.
0225 C. ¥Á¥Ö¥×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHU¥â;¼¥É;È¼¥¼. ¥î;¼¥È;Èαòîáα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-06-09 09:30:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-06-09 09:30:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2012-06-09 09:30:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-06-09 09:30:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `úÃîαî»ö¼ÝαÈÄα¹αèDCBC•x²è *****
0276 C. (¼á°îî¥Á¥È¥¥¥¥¥¥¥ç¥èè%¼αα¼Á»Üα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•î Daily±çîñèÈ'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Á¥§¥Á¥-¼Á»Ü;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 20 04)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 c0 c0 10 10)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 40 c0 10 10)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 40 40 10 10)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b c0 40 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c 85 83 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d 80 80 20 08)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0e 80 80 08 20)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0f 80 80 06 06)
0146 + DC 07-F0 MDP_XRT_FLD_DIS
0147 BC (d9)
0148 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0149 BC (c9)
0150 + DC 07-F0 MDP_XRT_AEC_RESET
0151 BC (d0)
0152 + DC 07-F0 MDP_XRT_ARS_DIS
0153 BC (d5)
0154 + DC 07-F0 MDP_XRT_FLD_RESET
0155 BC (da)
0156 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0157 BC (c4 01)
0158 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0159 BC (c5 02)
0160 . C. ----- Success Verify ? OK / NG ____
0161 C.
0162 C.
0163 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0164 C.
0165 +. DC 07-F0 MDP_XRT_MODE_OBSV
0166 BC (c2)
0167 +. TI 2012-06-09 09:30:02.0
0168 DC 07-F0 MDP_XRT_MODE_OBSV
0169 BC (c2)
0170 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0171 BC (c0)
0172 . C. ----- Success Verify ? OK / NG ____
0173 C.
0174 C. ***** XRT END *****
0175 . C. *****
0176 C. SOT table upload
0177 C. *****
0178 . C. < Stop FG table >
0179 +. DC 07-F0 MDP_FG_CTRL_MANU
0180 BC (51)
0181 . C. -----
0182 C. MDP_FG_CTRL_MODE = MANU [ ]
0183 C. -----
0184 C.
0185 . C. <Upload FG Observation Table>
0186 . S. RAM ram-269:MDP_OBS_F
0187 ( )
0188 C.
0189 . C. < Dump RAMID=MDP_OBS_F >
0190 +. DC 07-F0 MDP_DUMP_FGTBL
0191 BC (82 07 00 00 00 38 b8)
0192 C. -----
0193 C. MDP_OBS_F verify = OK/NG [ ]

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0194 C. -----
0195 C.
0196 C. *****
0197 C. SOT TI command set
0198 C. *****
0199 C. Execute, after the success of TBL upload.
0200 +. TI 2012-06-09 09:30:18.0
0201 DC 07-F0 MDP_SOT_MODE_OBSV
0202 BC (40)
0203 . C. -----
0204 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0205 C. -----
0206 C.
0207 C.
0208 . C. ***** MDP 'úÃîâî»ö¼ÝðÊÂð¹æDCBC•x²è *****
0209 C. (¼â°îÝÓÝÄÝËÝÞÝËÝáÝçÝèæ¼¼¼¼¼»Û¹æ)
0210 . S. DC-BC dcbc-402:DCBC
0211 (MDP_known_event)
0212 C.
0213 C.
0214 . C. ***** ÝÐÝ¹•Ï Daily±¿ÎÑæË'Ø¹æDCBC•x²è *****
0215 . S. DC-BC dcbc-153:DCBC
0216 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0217 C.
0218 C.
0219 . C. ;ãLOSÝÁÝ$ÝÃÝ⁻¼Â»Û;ä
0220 C.
0221 . C. ***** LOS *****
0222 C.
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*** OP Sequence for XRT ***
2012/06/09 09:39:30.0 XRT_CTRL_MANU_408_OG [0x198]
                           MDP_XRT_CTRL_MANU      1 07-F0 c1
2012/06/09 09:39:32.0 XRT_FLD_RESET_412_OG [0x19c]
                           MDP_XRT_FLD_RESET      1 07-F0 da
2012/06/09 09:39:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                           MDP_XRT_PREFLR_STRT    1 07-F0 e8
2012/06/09 09:41:00.0 APCS_OrE-point_Start_1_OG [0x097]
                           AOCU_NM                5 02-76 00 57 ce 00 00
2012/06/09 09:42:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                           MDP_XRT_PREFLR_STOP    1 07-F0 e9
2012/06/09 09:43:00.0 XRT_CTRL_MANU_400_OG [0x190]
                           MDP_XRT_CTRL_MANU      1 07-F0 c1
2012/06/09 09:43:02.0 XRT_ARS_DIS_429_OG [0x1ad]
                           MDP_XRT_ARS_DIS        1 07-F0 d5
2012/06/09 09:43:04.0 XRT_FLD_DIS_425_OG [0x1a9]
                           MDP_XRT_FLD_DIS        1 07-F0 d9
2012/06/09 09:43:06.0 XRT_FLRCTRL_DIS_430_OG [0x1ae]
                           MDP_XRT_FLRCTRL_DIS    1 07-F0 c9
2012/06/09 09:46:04.0 XRT_QT_PROG_SET_444_OG [0x1bc]
                           MDP_XRT_QT_PROG_SET    2 07-F0 c4 01
2012/06/09 10:14:00.0 XRT_Custom_418_OG [0x1a2]
2012/06/09 10:15:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                           MDP_XRT_CTRL_AUTO      1 07-F0 c0
2012/06/09 10:59:54.0 XRT_CTRL_MANU_400_OG [0x190]
                           MDP_XRT_CTRL_MANU      1 07-F0 c1
2012/06/09 10:59:56.0 XRT_ARS_DIS_429_OG [0x1ad]
                           MDP_XRT_ARS_DIS        1 07-F0 d5
2012/06/09 10:59:58.0 XRT_FLD_DIS_425_OG [0x1a9]
                           MDP_XRT_FLD_DIS        1 07-F0 d9
2012/06/09 11:00:00.0 APCS_OrE-point_Start_2_OG [0x098]
                           AOCU_NM                5 02-76 00 00 00 00 00
2012/06/09 11:00:00.5 XRT_FLRCTRL_DIS_430_OG [0x1ae]
                           MDP_XRT_FLRCTRL_DIS    1 07-F0 c9
2012/06/09 11:02:58.5 XRT_QT_PROG_SET_444_OG [0x1bc]
                           MDP_XRT_QT_PROG_SET    2 07-F0 c4 01
2012/06/09 11:03:00.5 XRT_CTRL_AUTO_406_OG [0x196]
                           MDP_XRT_CTRL_AUTO      1 07-F0 c0
2012/06/09 11:22:30.0 XRT_CTRL_MANU_408_OG [0x198]
                           MDP_XRT_CTRL_MANU      1 07-F0 c1
2012/06/09 11:22:32.0 XRT_FLD_RESET_412_OG [0x19c]
                           MDP_XRT_FLD_RESET      1 07-F0 da
2012/06/09 11:22:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                           MDP_XRT_PREFLR_STRT    1 07-F0 e8
2012/06/09 11:25:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                           MDP_XRT_PREFLR_STOP    1 07-F0 e9
2012/06/09 11:52:30.0 XRT_Custom_418_OG [0x1a2]
2012/06/09 11:53:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                           MDP_XRT_CTRL_AUTO      1 07-F0 c0
2012/06/09 13:01:00.0 XRT_CTRL_MANU_408_OG [0x198]
                           MDP_XRT_CTRL_MANU      1 07-F0 c1
2012/06/09 13:01:02.0 XRT_FLD_RESET_412_OG [0x19c]
                           MDP_XRT_FLD_RESET      1 07-F0 da
2012/06/09 13:01:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                           MDP_XRT_PREFLR_STRT    1 07-F0 e8
2012/06/09 13:04:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                           MDP_XRT_PREFLR_STOP    1 07-F0 e9
2012/06/09 13:30:00.0 XRT_CTRL_MANU_442_OG [0x1ba]
                           MDP_XRT_CTRL_MANU      1 07-F0 c1
2012/06/09 13:30:10.0 XRT_FOCUS_RECALIBRATE_428_OG [0x1ac]
                           XRT_FOCUS_RECAL        2 07-F8 78 00
2012/06/09 13:34:10.0 XRT_FOCUS_POSITION_401_OG [0x191]
                           XRT_FOCUS_POSITION      4 07-F8 22 ff aa 00
2012/06/09 15:29:54.0 XRT_CTRL_MANU_407_OG [0x197]
                           MDP_XRT_CTRL_MANU      1 07-F0 c1
2012/06/09 15:30:00.0 APCS_OrE-point_Start_3_OG [0x099]
                           AOCU_NM                5 02-76 00 2e f9 2e f9
2012/06/09 15:32:32.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                           XRT_FOCUS_POSITION      4 07-F8 22 ff aa 00
2012/06/09 15:32:52.0 XRT_QT_PROG_SET_448_OG [0x1c0]
                           MDP_XRT_QT_PROG_SET    2 07-F0 c4 09
2012/06/09 15:32:54.0 XRT_FLD_DIS_402_OG [0x192]
                           MDP_XRT_FLD_DIS        1 07-F0 d9
2012/06/09 15:32:56.0 XRT_FLRCTRL_DIS_433_OG [0x1b1]
                           MDP_XRT_FLRCTRL_DIS    1 07-F0 c9
2012/06/09 15:32:58.0 XRT_ARS_DIS_431_OG [0x1af]
                           MDP_XRT_ARS_DIS        1 07-F0 d5
2012/06/09 15:33:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                           MDP_XRT_CTRL_AUTO      1 07-F0 c0
2012/06/09 15:39:54.0 XRT_CTRL_MANU_407_OG [0x197]
                           MDP_XRT_CTRL_MANU      1 07-F0 c1
2012/06/09 15:40:00.0 APCS_OrE-point_Start_4_OG [0x09a]
                           AOCU_NM                5 02-76 00 2e f9 d1 07
2012/06/09 15:42:32.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                           XRT_FOCUS_POSITION      4 07-F8 22 ff aa 00
2012/06/09 15:42:52.0 XRT_QT_PROG_SET_404_OG [0x194]
                           MDP_XRT_QT_PROG_SET    2 07-F0 c4 0c
2012/06/09 15:42:54.0 XRT_FLD_DIS_402_OG [0x192]
                           MDP_XRT_FLD_DIS        1 07-F0 d9
2012/06/09 15:42:56.0 XRT_FLRCTRL_DIS_433_OG [0x1b1]
                           MDP_XRT_FLRCTRL_DIS    1 07-F0 c9
2012/06/09 15:42:58.0 XRT_ARS_DIS_431_OG [0x1af]

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2012/06/09	15:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_ARS_DIS	1	07-F0	d5					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/06/09	15:49:54.0	XRT_CTRL_MANU_407_OG [0x197]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/06/09	15:50:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	d1	07	d1	07	
2012/06/09	15:52:32.0	XRT_FOCUS_POSITION_447_OG [0x1bf]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2012/06/09	15:52:52.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11				
2012/06/09	15:52:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9					
2012/06/09	15:52:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2012/06/09	15:52:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/06/09	15:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/06/09	15:59:54.0	XRT_CTRL_MANU_407_OG [0x197]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/06/09	16:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	d1	07	2e	f9	
2012/06/09	16:02:32.0	XRT_FOCUS_POSITION_447_OG [0x1bf]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2012/06/09	16:02:52.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f				
2012/06/09	16:02:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9					
2012/06/09	16:02:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2012/06/09	16:02:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/06/09	16:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/06/09	16:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/06/09	16:10:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	01	00	00	00	00	
2012/06/09	16:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2012/06/09	16:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8					
2012/06/09	16:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2012/06/09	16:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0					
2012/06/09	16:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/06/09	16:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da					
2012/06/09	16:12:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04				
2012/06/09	16:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02				
2012/06/09	16:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/06/09	16:18:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/06/09	16:18:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da					
2012/06/09	16:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/06/09	16:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/06/09	17:04:00.0	XRT_Custom_418_OG [0x1a2]									
2012/06/09	17:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/06/09	17:41:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/06/09	17:41:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2012/06/09	17:41:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00	
2012/06/09	17:41:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9					
2012/06/09	17:41:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2012/06/09	17:41:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/06/09	17:44:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07				
2012/06/09	17:44:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/06/09	17:51:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/06/09	17:51:30.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	01	00	00	00	00	
2012/06/09	17:53:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2012/06/09	17:54:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8					
2012/06/09	17:54:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]									

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2012/06/09	17:54:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2012/06/09	17:54:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/06/09	17:54:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/09	17:54:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/06/09	17:54:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2012/06/09	17:54:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/09	17:56:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/09	17:56:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/09	17:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/09	17:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/09	18:40:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/09	18:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/09	19:35:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/09	19:35:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/09	19:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/09	19:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/09	20:18:00.0	XRT_Custom_418_OG [0x1a2]				
2012/06/09	20:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/09	21:13:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/09	21:13:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/09	21:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/09	21:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/09	21:54:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/09	21:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/09	22:52:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/09	22:52:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/09	22:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/09	22:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/09	23:25:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/09	23:26:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/10	00:30:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/10	00:30:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/10	00:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/10	00:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/10	01:00:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/10	01:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/10	02:09:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/10	02:09:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/10	02:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/10	02:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/10	02:39:00.0	XRT_Custom_418_OG [0x1a2]				
2012/06/10	02:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/10	03:38:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/10	03:38:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/10	03:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/10	03:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/10	04:17:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/10	04:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/10	05:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/10	05:17:02.0	XRT_FLD_RESET_412_OG [0x19c]				

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2012/06/10	05:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/10	05:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/10	05:56:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/10	05:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/10	06:01:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/10	06:01:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/06/10	06:01:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2012/06/10	06:01:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/06/10	06:01:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/06/10	06:01:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/06/10	06:04:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2012/06/10	06:04:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/10	06:11:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/10	06:11:30.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	01 00 00 00 00
2012/06/10	06:13:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/06/10	06:14:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/06/10	06:14:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/06/10	06:14:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/06/10	06:14:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/06/10	06:14:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/10	06:14:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/06/10	06:14:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2012/06/10	06:14:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/10	06:57:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/10	06:57:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/10	06:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/10	07:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/10	07:34:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/10	07:35:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/10	07:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/10	08:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 57 ce 00 00
2012/06/10	10:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/10	11:00:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	01 00 00 00 00
2012/06/10	11:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/06/10	11:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/06/10	11:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/06/10	11:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/06/10	11:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/06/10	11:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/10	11:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/06/10	11:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2012/06/10	11:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/10	11:59:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/10	11:59:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/10	11:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/10	12:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/10	12:30:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/10	12:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2012/06/10	13:38:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/10	13:38:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/10	13:38:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/10	13:41:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/10	14:13:30.0	XRT_Custom_418_OG [0x1a2]								
2012/06/10	14:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/10	15:16:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/10	15:16:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/10	15:16:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/10	15:19:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/10	16:04:00.0	XRT_Custom_418_OG [0x1a2]								
2012/06/10	16:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/10	16:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/10	16:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/10	16:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/10	16:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/10	17:40:30.0	XRT_Custom_418_OG [0x1a2]								
2012/06/10	17:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/10	17:57:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/10	17:57:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/06/10	17:58:00.0	AOCs_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2012/06/10	17:58:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/06/10	17:58:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/06/10	17:58:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/10	18:00:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2012/06/10	18:01:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/10	18:07:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/10	18:08:00.0	AOCs_Orе-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	01 00 00 00 00				
2012/06/10	18:10:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/06/10	18:10:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/06/10	18:10:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/06/10	18:10:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/06/10	18:10:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/10	18:10:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/10	18:10:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/06/10	18:10:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2012/06/10	18:11:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/10	18:33:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/10	18:33:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/10	18:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/10	18:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/10	19:17:30.0	XRT_Custom_418_OG [0x1a2]								
2012/06/10	19:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/10	20:12:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/10	20:12:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/10	20:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/10	20:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/10	20:55:00.0	XRT_Custom_418_OG [0x1a2]								
2012/06/10	20:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								

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2012/06/10	21:50:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/10	21:50:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/10	21:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/10	21:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/10	22:30:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/10	22:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/10	23:29:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/10	23:29:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/10	23:29:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/10	23:32:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/10	23:59:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/11	00:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/11	01:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/11	01:07:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/11	01:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/11	01:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/11	01:38:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/11	01:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/11	02:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/11	02:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/11	02:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/11	02:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/11	03:16:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/11	03:17:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/11	04:14:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/11	04:14:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/11	04:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/11	04:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/11	04:55:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/11	04:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/11	05:27:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/11	05:27:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/06/11	05:27:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2012/06/11	05:27:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/06/11	05:27:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/06/11	05:27:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/06/11	05:30:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07			
2012/06/11	05:30:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/11	05:37:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/11	05:37:30.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	01 00 00 00 00			
2012/06/11	05:39:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/06/11	05:40:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/06/11	05:40:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/06/11	05:40:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/06/11	05:40:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/06/11	05:40:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/11	05:40:26.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2012/06/11	05:40:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2012/06/11	05:40:30.5	XRT_CTRL_AUTO_406_OG [0x196]							



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2012/06/11	05:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/11	05:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/11	05:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/11	05:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/11	06:33:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/11	06:34:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/11	07:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/11	07:35:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/11	07:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/11	07:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/11	07:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/11	08:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 57 ce 00 00
2012/06/11	11:00:00.0	AOCS_OrE-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	01 00 00 00 00
2012/06/11	11:01:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/11	11:04:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/06/11	11:04:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/06/11	11:04:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/06/11	11:04:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/06/11	11:04:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/06/11	11:04:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/11	11:04:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2012/06/11	11:04:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2012/06/11	11:29:00.0	XRT_Custom_418_OG [0x1a2]				
2012/06/11	11:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/11	12:37:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/11	12:37:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/11	12:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/11	12:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/11	13:07:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/11	13:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/11	14:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/11	14:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/11	14:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/11	14:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/11	14:53:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/11	14:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/11	15:54:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/11	15:54:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/11	15:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/11	15:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/11	16:40:30.0	XRT_Custom_418_OG [0x1a2]				
2012/06/11	16:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/06/11	17:32:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/11	17:32:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/06/11	17:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/06/11	17:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/06/11	18:17:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/11	18:17:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/06/11	18:17:30.0	AOCS_OrE-point_Start_2_OG [0x098]				

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2012/06/11	18:17:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00	00	00	00	00
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/06/11	18:17:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/06/11	18:17:50.0	XRT_ARS_DIS_438_OG [0x1b6]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/11	18:20:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2012/06/11	18:20:30.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/11	18:27:24.0	XRT_CTRL_MANU_439_OG [0x1b7]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/11	18:27:30.0	AOCS_OrE-point_Start_7_OG [0x09d]								
			AOCU_NM	5	02-76	01	00	00	00	00
2012/06/11	18:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/06/11	18:30:16.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/06/11	18:30:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/06/11	18:30:20.0	XRT_AEC_RESET_443_OG [0x1bb]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/06/11	18:30:22.0	XRT_ARS_DIS_431_OG [0x1af]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/11	18:30:24.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/11	18:30:26.0	XRT_QT_PROG_SET_415_OG [0x19f]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2012/06/11	18:30:28.0	XRT_FL_PROG_SET_414_OG [0x19e]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2012/06/11	18:30:30.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/11	19:11:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/11	19:11:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/11	19:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/11	19:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/11	19:54:30.0	XRT_Custom_418_OG [0x1a2]								
2012/06/11	19:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/11	20:49:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/11	20:49:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/11	20:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/11	20:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/11	21:31:30.0	XRT_Custom_418_OG [0x1a2]								
2012/06/11	21:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/11	22:28:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/11	22:28:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/11	22:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/11	22:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/11	23:06:30.0	XRT_Custom_418_OG [0x1a2]								
2012/06/11	23:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/12	00:06:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/12	00:06:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/12	00:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/12	00:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/12	00:37:00.0	XRT_Custom_418_OG [0x1a2]								
2012/06/12	00:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/12	01:45:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/12	01:45:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/12	01:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/12	01:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/12	02:15:30.0	XRT_Custom_418_OG [0x1a2]								
2012/06/12	02:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/12	03:22:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/12	03:22:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/12	03:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								

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2012/06/12	03:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/12	03:54:00.0	XRT_Custom_418_OG [0x1a2]					
2012/06/12	03:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/12	04:52:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/12	04:52:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/12	04:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/12	04:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/12	05:32:30.0	XRT_Custom_418_OG [0x1a2]					
2012/06/12	05:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/12	06:03:24.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/12	06:03:26.0	XRT_FOCUS_POSITION_401_OG [0x191]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/06/12	06:03:30.0	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00	
2012/06/12	06:03:46.0	XRT_FLD_DIS_402_OG [0x192]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/06/12	06:03:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/06/12	06:03:50.0	XRT_ARS_DIS_438_OG [0x1b6]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/06/12	06:06:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2012/06/12	06:06:30.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/12	06:13:24.0	XRT_CTRL_MANU_439_OG [0x1b7]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/12	06:13:30.0	AOCS_Ore-point_Start_7_OG [0x09d]					
			AOCU_NM	5	02-76	01 00 00 00	
2012/06/12	06:15:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/06/12	06:16:16.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/06/12	06:16:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/06/12	06:16:20.0	XRT_AEC_RESET_443_OG [0x1bb]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/06/12	06:16:22.0	XRT_ARS_DIS_431_OG [0x1af]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/06/12	06:16:24.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/12	06:16:26.0	XRT_QT_PROG_SET_415_OG [0x19f]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2012/06/12	06:16:28.0	XRT_FL_PROG_SET_414_OG [0x19e]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2012/06/12	06:16:30.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/12	06:33:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/12	06:33:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/12	06:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/12	06:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/12	07:11:00.0	XRT_Custom_418_OG [0x1a2]					
2012/06/12	07:12:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/12	07:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/12	08:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	00 57 ce 00 00	
2012/06/12	09:54:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00	