

XRT Timeline to be uploaded on 2012/06/12

Period: 2012/06/12 09:44:00 - 2012/07/16 12:00:00

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Normal mode

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XOB #1835: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 40sec-cad											
Term	Pointing (x, y)						Comment				
06/12 11:03:00 - 06/12 17:54:54	Track (857.2, 210.4) ^{Ⓜ 06/12 11:00:00}						track AR11493/11496				
06/12 22:09:00 - 06/13 06:15:24	Fixed (888.0, 211.0)						# fix AR 11493/11496 (cont)				
PROG= 04 Inf.-time(s)											
└─ Subr= 2 1-time(s) 2.0sec											
└─ Seqn= 19 1-time(s) 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec											
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec											
└─ Seqn= 41 4-time(s) 2.0sec											
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
└─ Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
└─ Subr= 1 1-time(s) 2.0sec											
└─ Seqn= 52 45-time(s) 24.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 6.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec											
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #18EA: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-band(16)											
Term	Pointing (x, y)						Comment				
06/12 17:58:00 - 06/12 18:04:54	Fixed (0.0, 0.0)						synoptic, shifted -5.0 min + SOT flat field				
06/13 06:18:30 - 06/13 06:25:24	Fixed (0.0, 0.0)						synoptic, shifted 15.5 min				
06/13 17:59:00 - 06/13 18:05:54	Fixed (0.0, 0.0)						synoptic, shifted -4.0 min				
06/14 05:44:30 - 06/14 05:51:24	Fixed (0.0, 0.0)						synoptic, shifted -18.5 min				

PROG= 07 1-time(s)											
└─ Subr= 1 1-time(s) 12.0sec											
└─ Seqn= 46 1-time(s) 4.0sec											
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec											
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec											
└─ Seqn= 5 1-time(s) 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec											
└─ Seqn= 69 1-time(s) 4.0sec											
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec											
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec											
└─ Seqn= 4 1-time(s) 2.0sec											
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec											
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #183E: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad											
Term	Pointing (x, y)						Comment				
06/13 06:28:30 - 06/13 07:59:54	Fixed (888.0, 211.0)						# AR11493/496 fix				
06/13 11:06:01 - 06/13 17:09:00	Track (-344.5, -334.0) ^{Ⓜ 06/13 11:00:00}						track AR11504				
06/13 18:09:00 - 06/14 05:41:24	Track (-288.3, -334.9) ^{Ⓜ 06/13 18:06:00}						# AR11504				
06/14 05:54:30 - 06/14 07:49:30	Track (-192.4, -336.2) ^{Ⓜ 06/14 05:51:30}						# ar11504				

PROG= 19 Inf.-time(s)											
└─ Subr= 2 1-time(s) 2.0sec											
└─ Seqn= 19 1-time(s) 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec											
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec											
└─ Seqn= 41 4-time(s) 2.0sec											
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
└─ Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
└─ Subr= 1 1-time(s) 2.0sec											
└─ Seqn= 16 30-time(s) 80.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 20.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 20.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 20.0sec											
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 20.0sec											
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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Flare mode

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XOB #18E7: Flare obs. dynamics - Ti_poly high cadence + context (thick-Al-384x384)-15 loops											
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Term	Pointing (x, y)	Comment
06/12 11:03:00 - 06/12 17:54:54	Track (857.2, 210.4) ^{Ⓢ 06/12 11:00:00}	track AR11493/11496
06/12 22:09:00 - 06/13 06:15:24	Fixed (888.0, 211.0)	# fix AR 11493/11496 (cont)
06/13 06:28:30 - 06/13 07:59:54	Fixed (888.0, 211.0)	# AR11493/496 fix
06/13 11:06:01 - 06/13 17:09:00	Track (-344.5, -334.0) ^{Ⓢ 06/13 11:00:00}	track AR11504
06/13 18:09:00 - 06/14 05:41:24	Track (-288.3, -334.9) ^{Ⓢ 06/13 18:06:00}	# AR11504
06/14 05:54:30 - 06/14 07:49:30	Track (-192.4, -336.2) ^{Ⓢ 06/14 05:51:30}	# ar11504
PROG= 02 15-time(s)		
└─ Subr= 1 45-time(s) 10.0sec		
└─ Seqn= 92 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Norm 4ms Obs 1x1 384x384 (1024, 1024)	DPCM 2 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Norm 4ms Obs 1x1 384x384 (1024, 1024)	DPCM 3 0 2.0sec
└─ Subr= 2 1-time(s) 10.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
Open/thick-Al	Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024)	DPCM 2 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024)	DPCM 3 0 2.0sec
└─ Seqn= 79 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024)	Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024)	Q=98 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

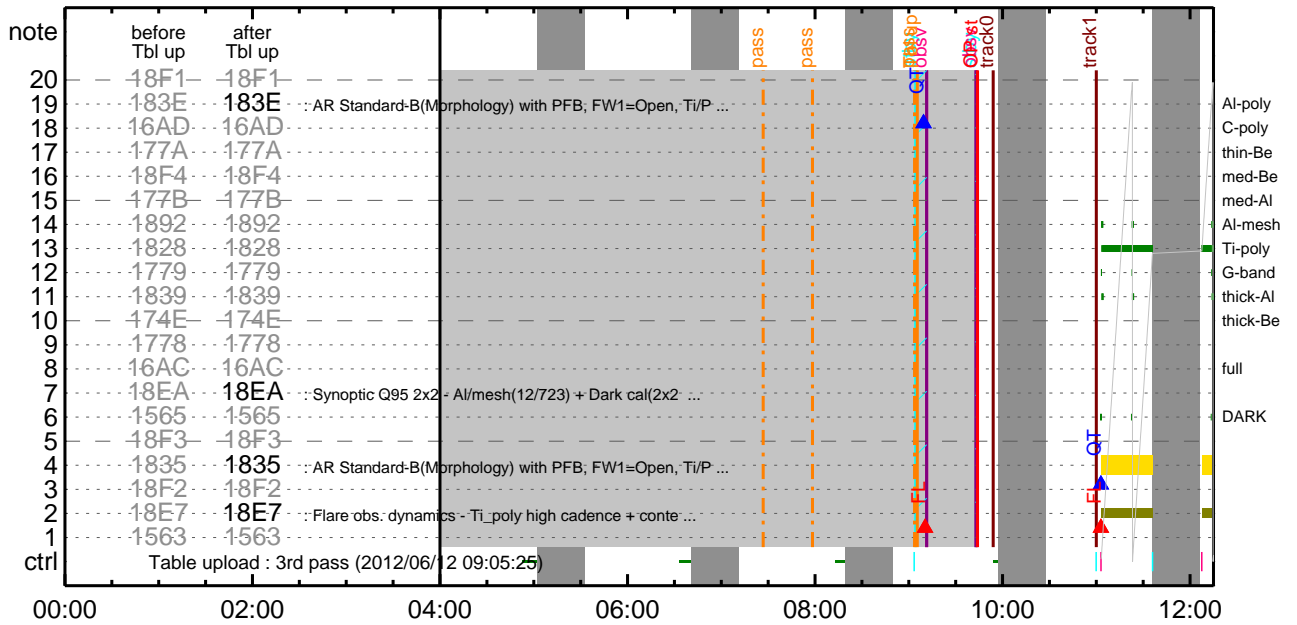
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Flare Detection

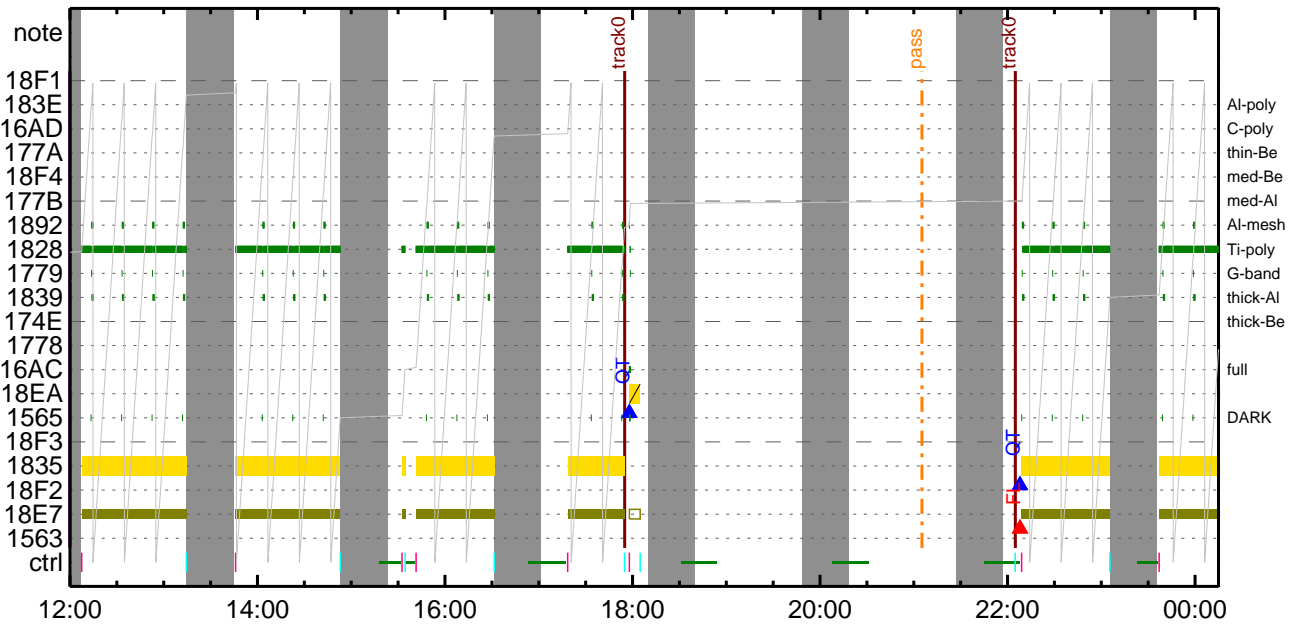
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FLD Patrol											
Term	Pointing (x, y)	Comment									
06/12 11:02:46 - 06/12 17:55:16	Track (857.2, 210.4) ^{Ⓢ 06/12 11:00:00}	track AR11493/11496									
06/12 22:07:46 - 06/13 06:15:46	Fixed (888.0, 211.0)	# fix AR 11493/11496 (cont)									
06/13 06:28:16 - 06/13 17:56:16	Fixed (888.0, 211.0)	# AR11493/496 fix									
06/13 18:08:46 - 06/14 05:41:46	Track (-288.3, -334.9) ^{Ⓢ 06/13 18:06:00}	# AR11504									
06/14 05:54:16 - 07/16 12:00:00	Track (-192.4, -336.2) ^{Ⓢ 06/14 05:51:30}	# ar11504									
Open/Ti-poly	Open/thick-Al close Safe Norm 8ms Obs 8x8	Q=50 30sec									
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval									

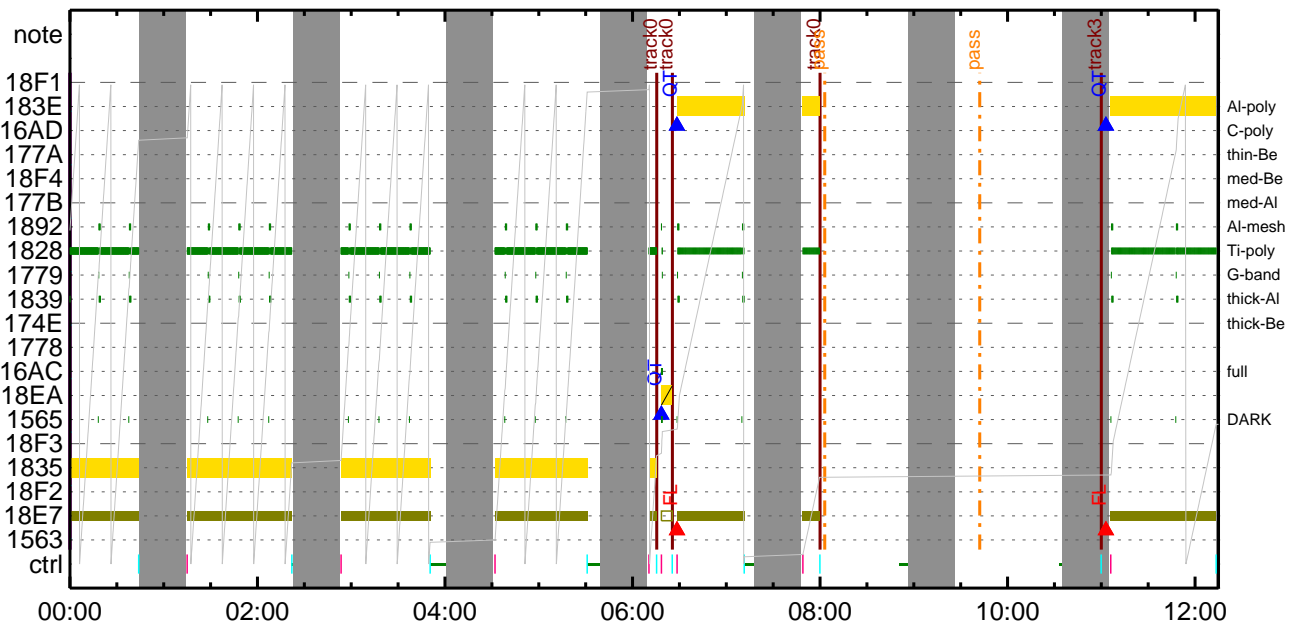
CMDI #0690 2012/06/12



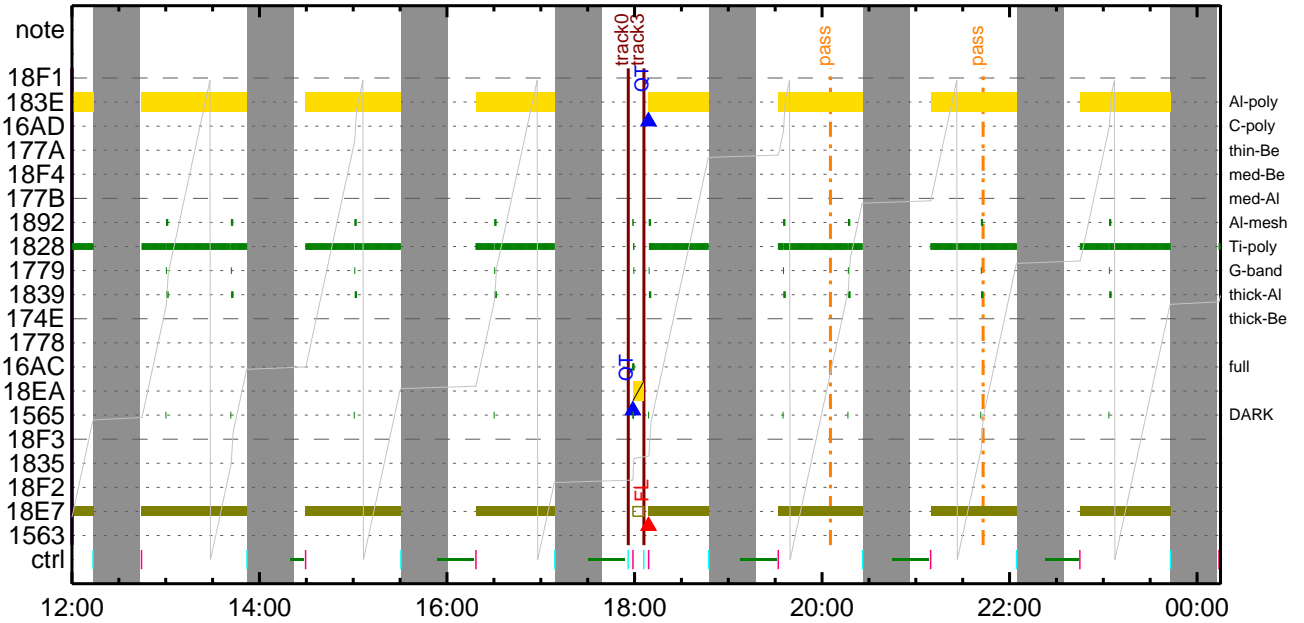
CMDI #0690 2012/06/12



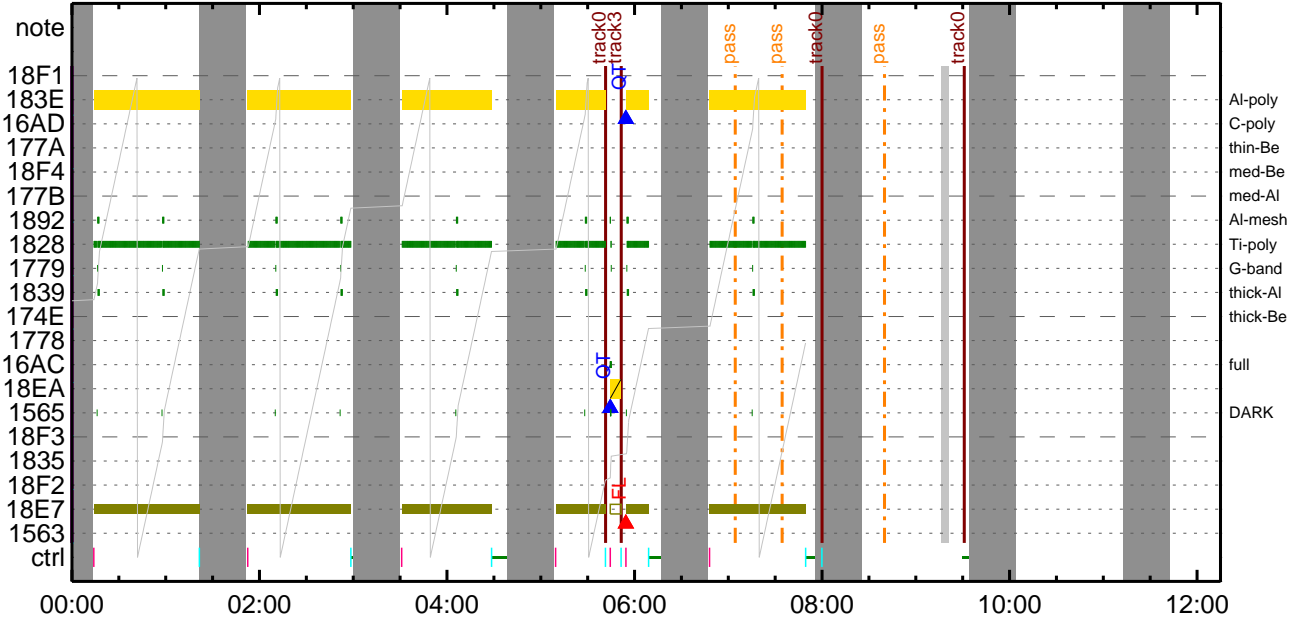
CMDI #0690 2012/06/13



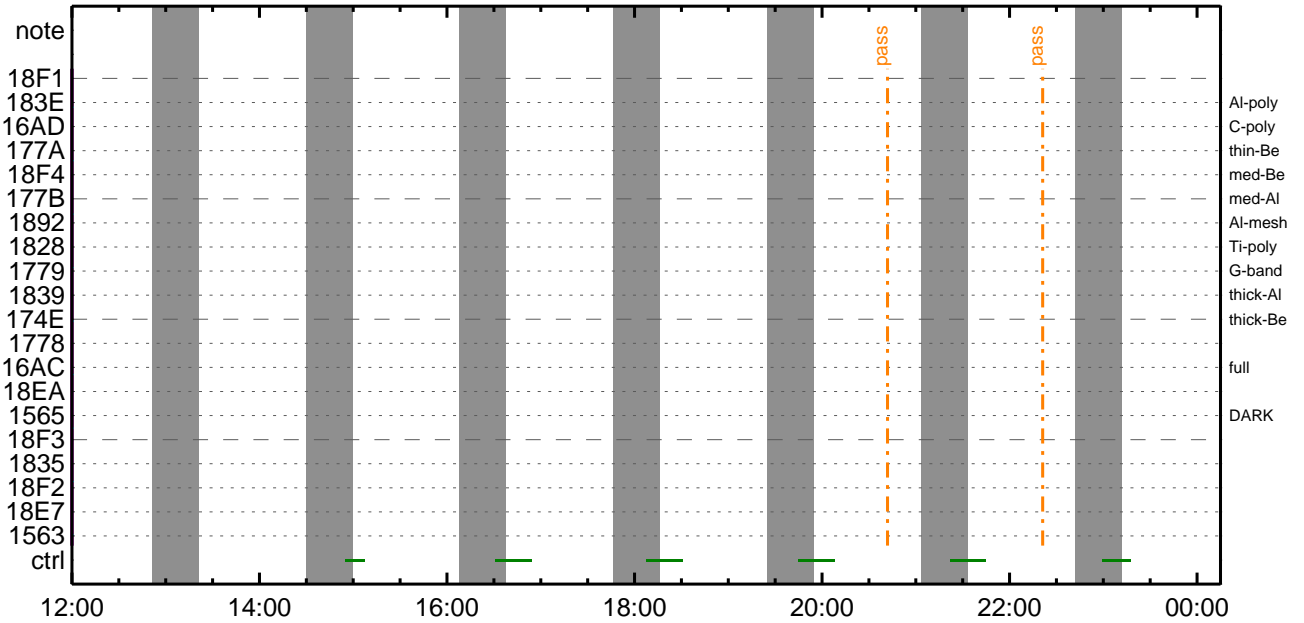
CMDI #0690 2012/06/13



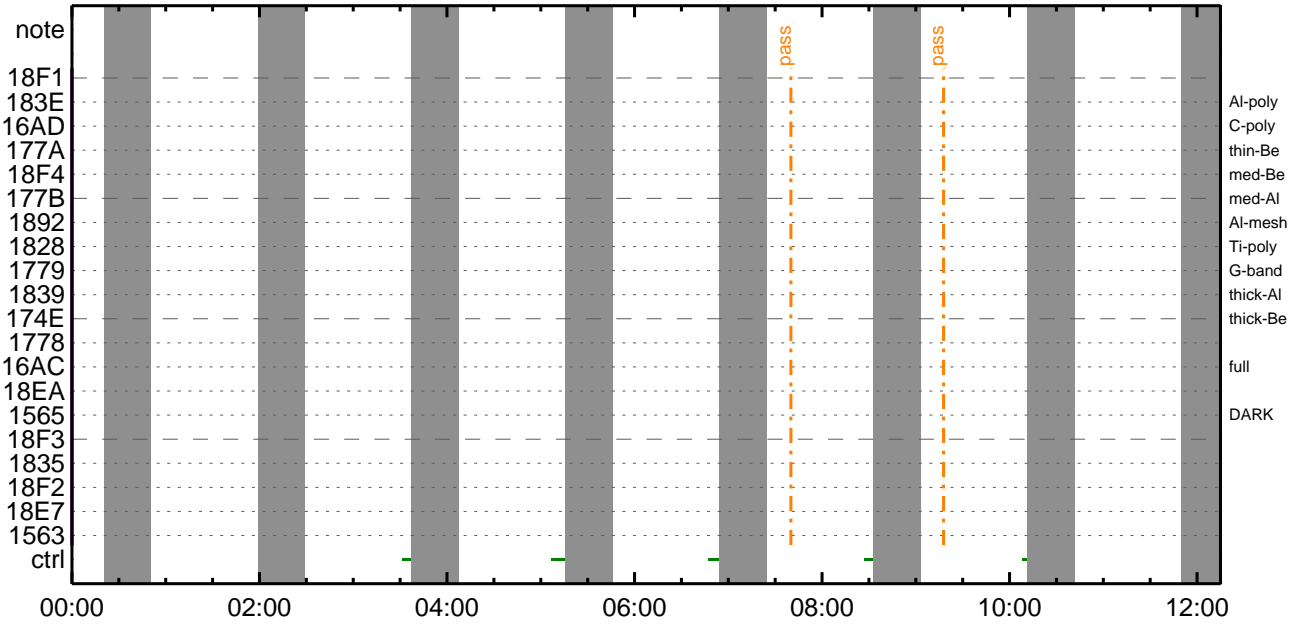
CMDI #0690 2012/06/14



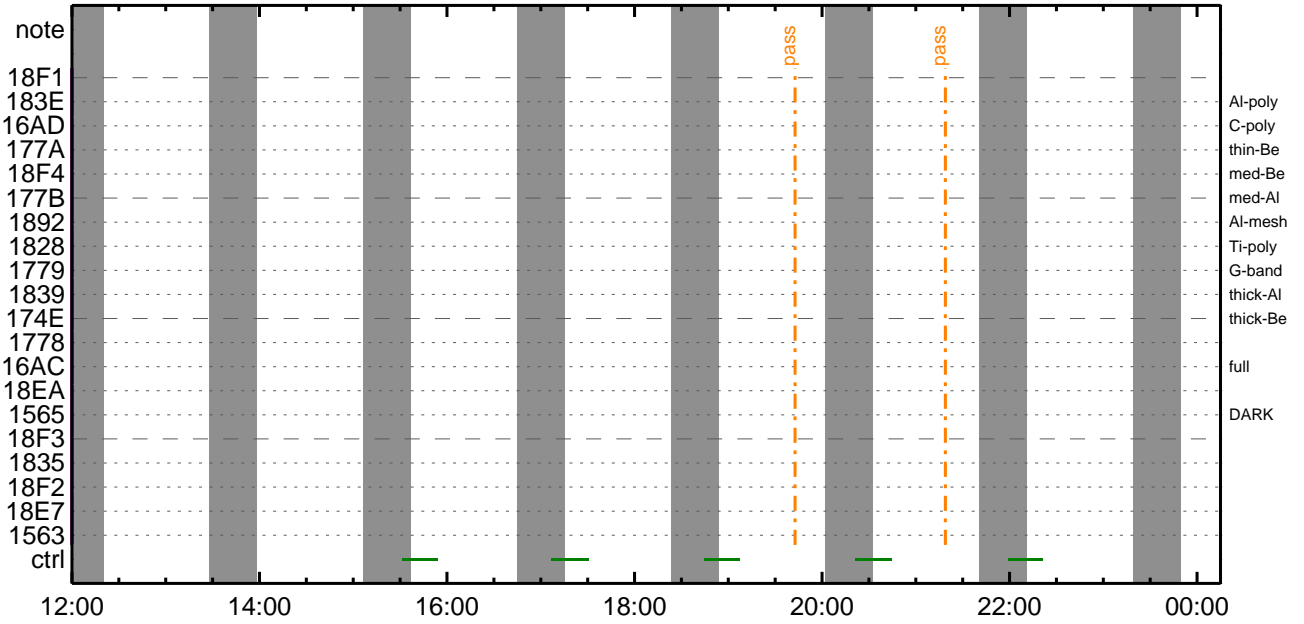
CMDI #0690 2012/06/14



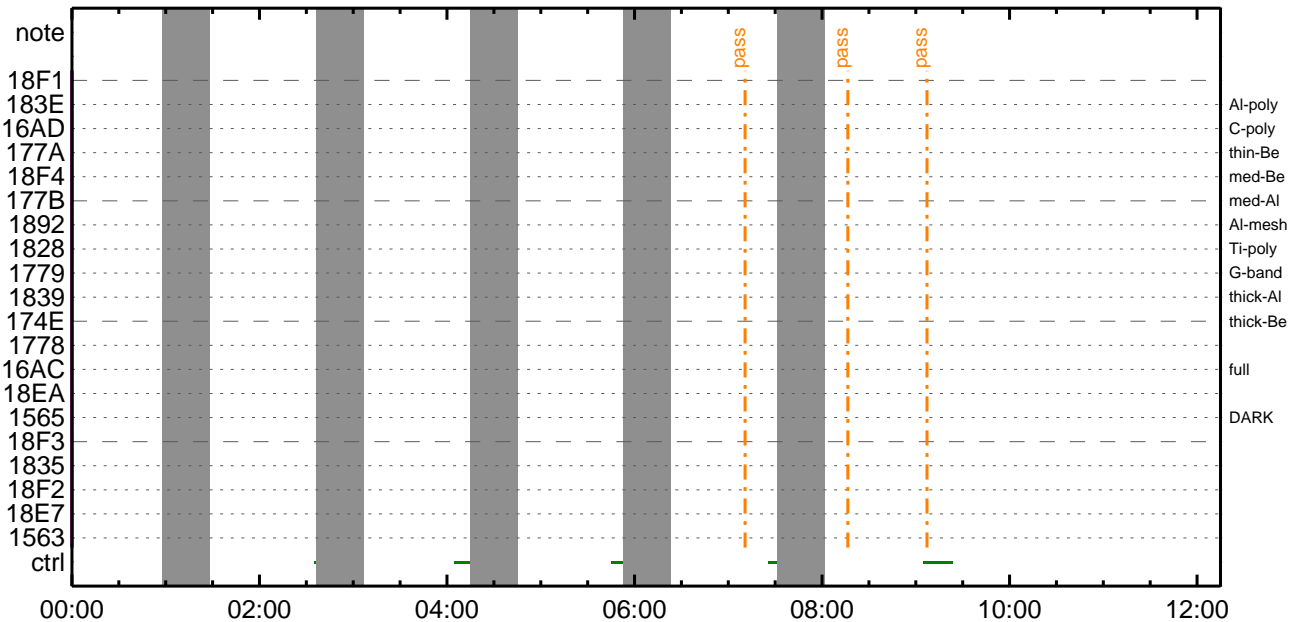
CMDI #0690 2012/06/15



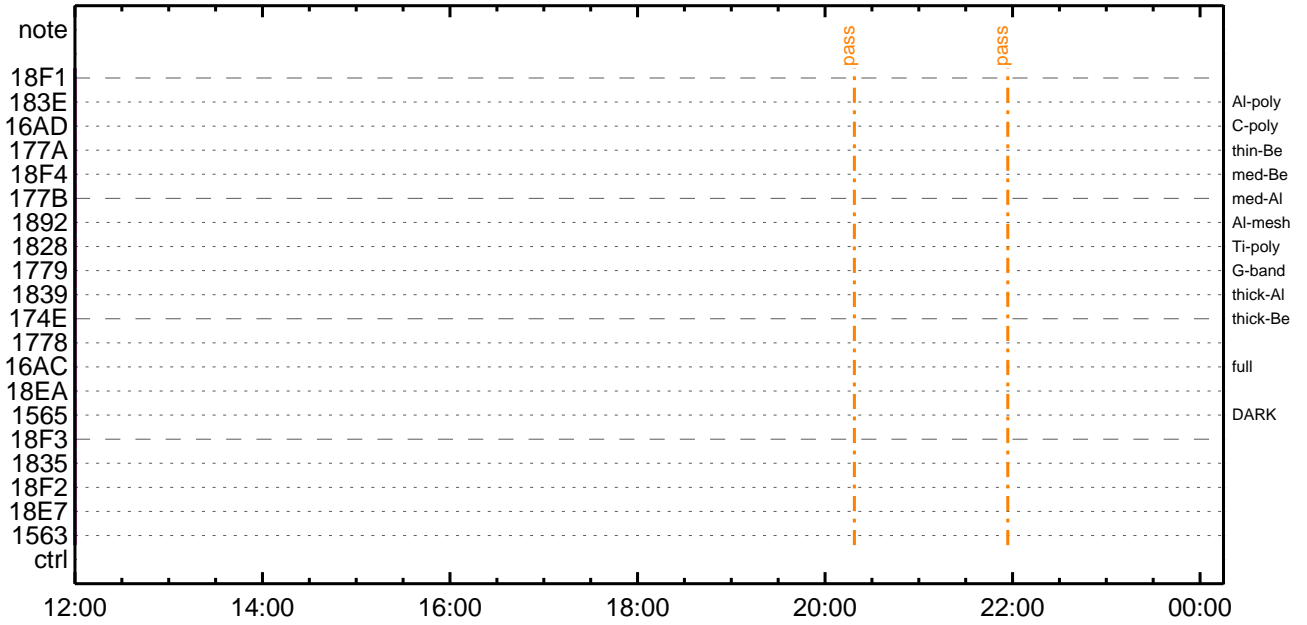
CMDI #0690 2012/06/15



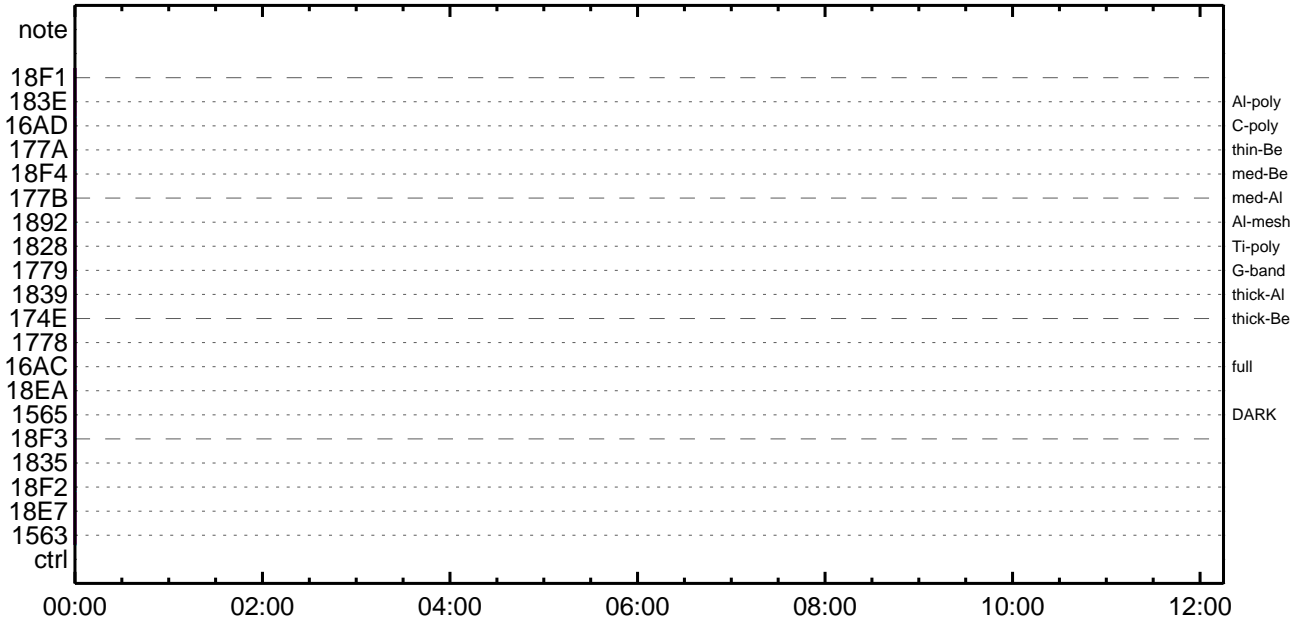
CMDI #0690 2012/06/16



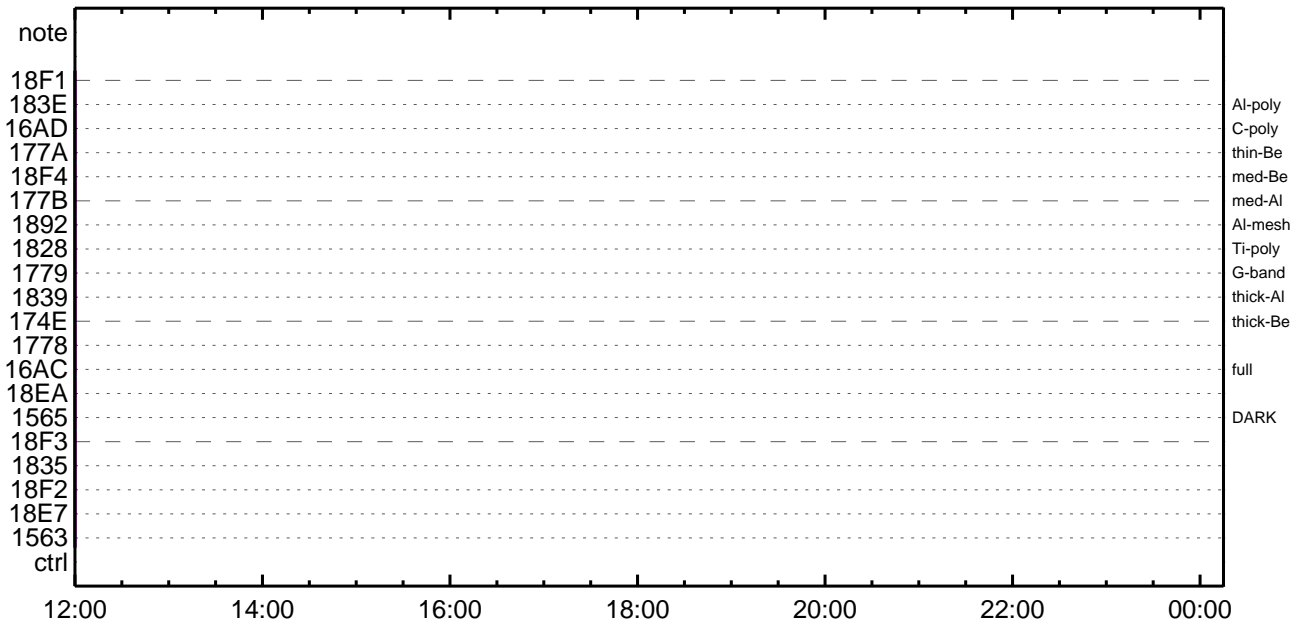
CMDI #0690 2012/06/16



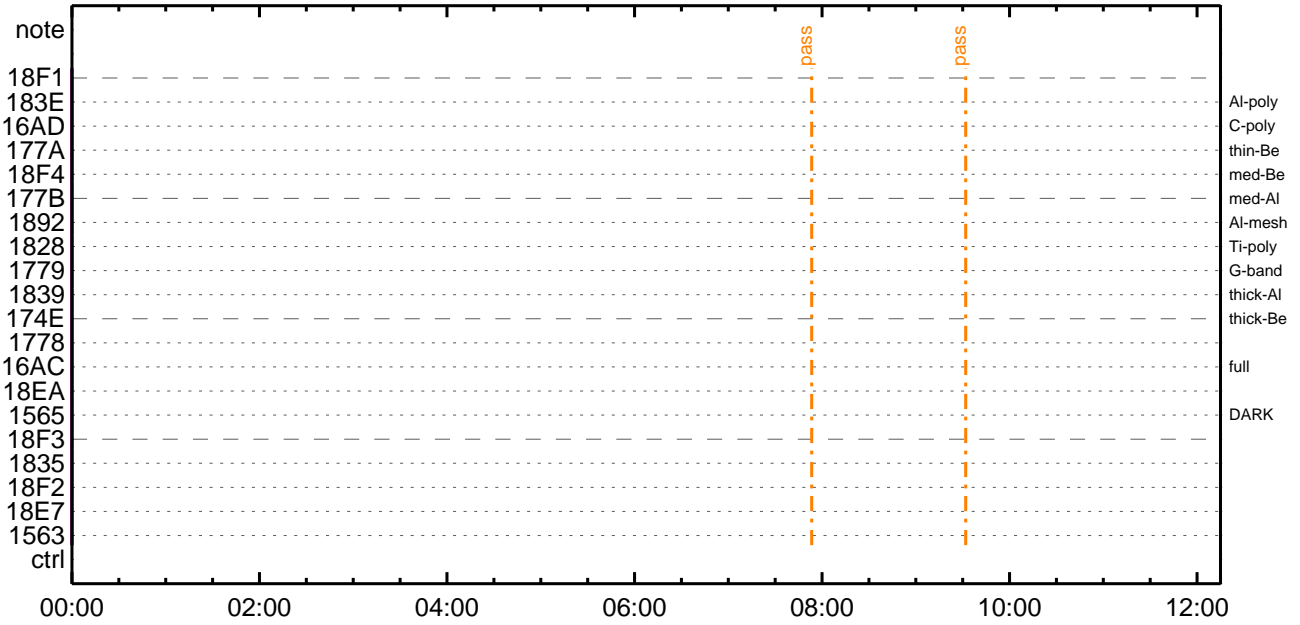
CMDI #0690 2012/06/17



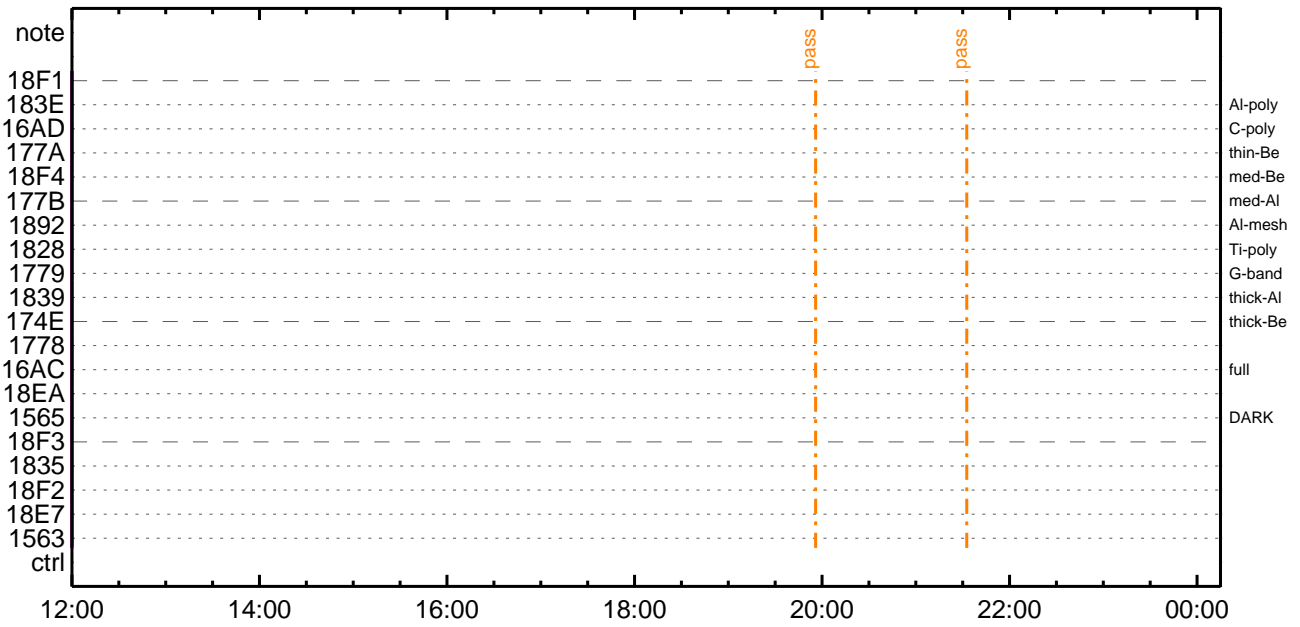
CMDI #0690 2012/06/17



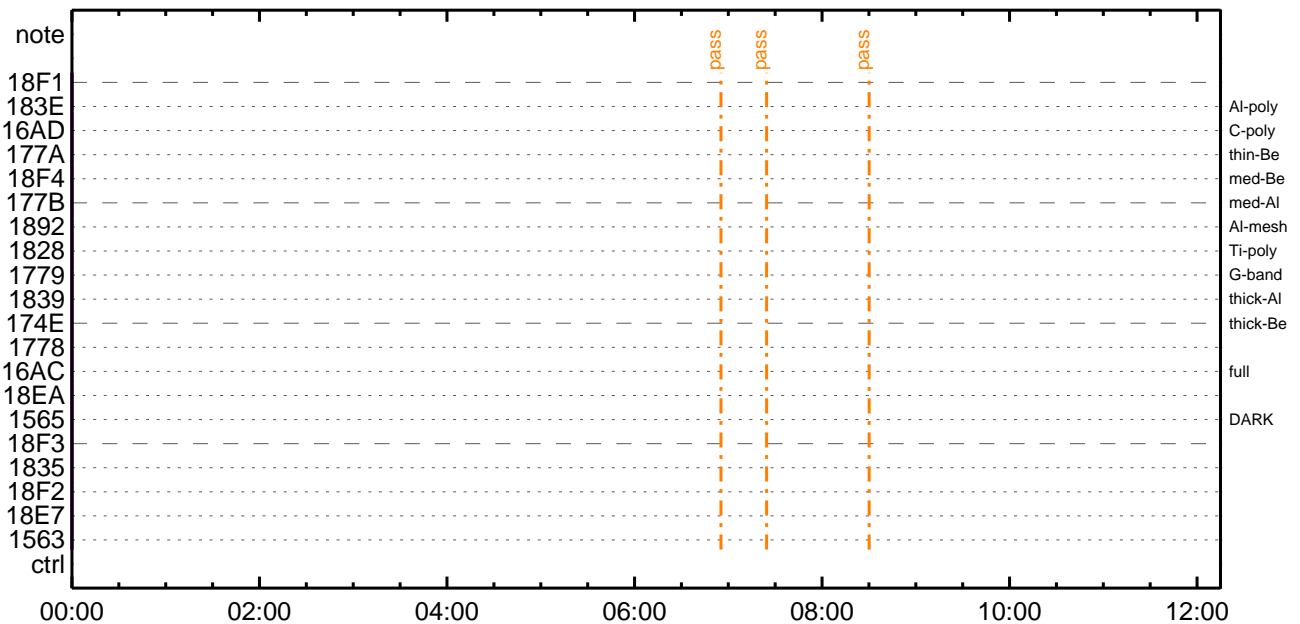
CMDI #0690 2012/06/18



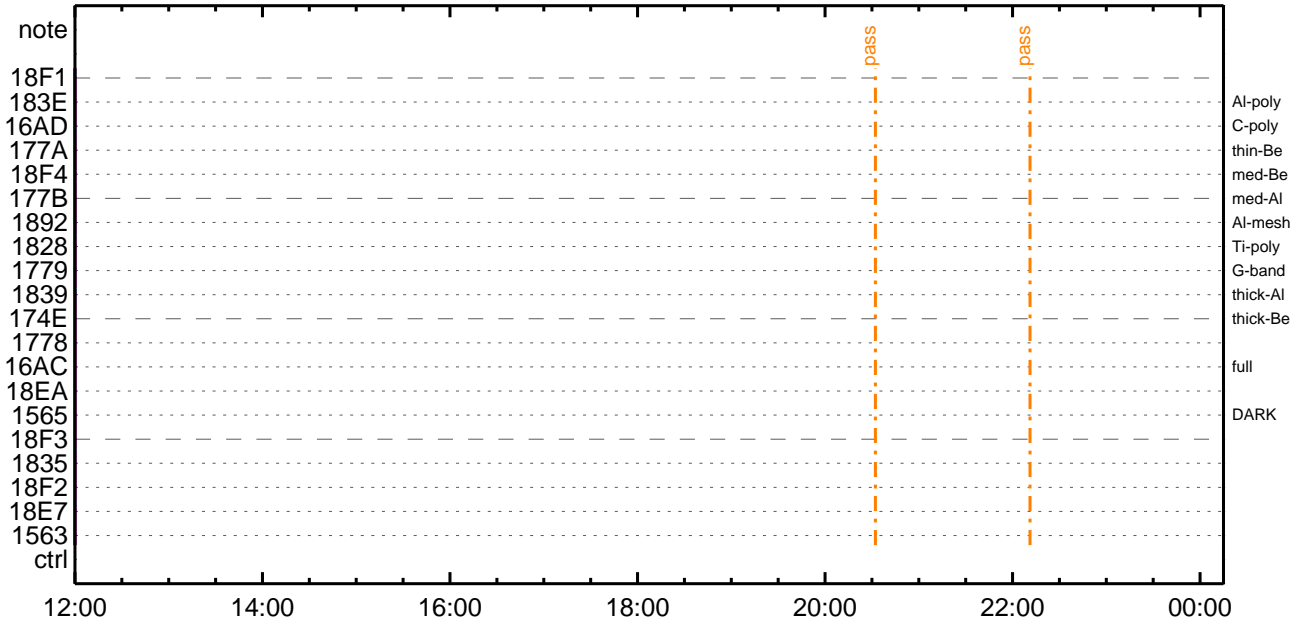
CMDI #0690 2012/06/18



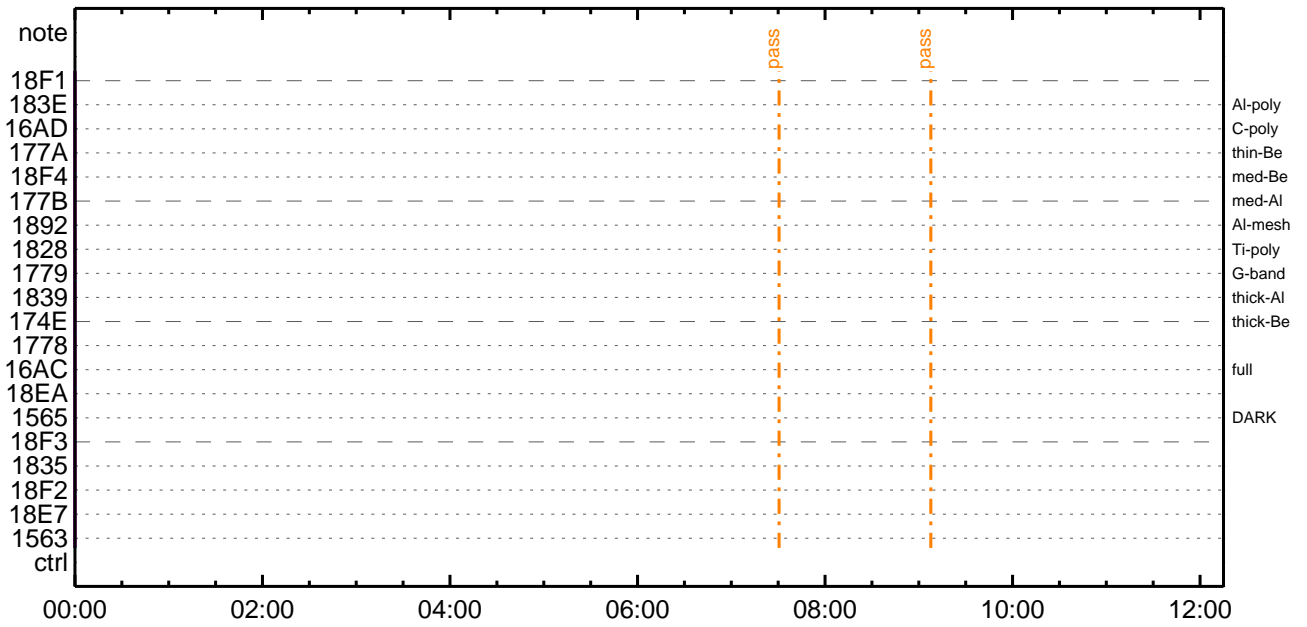
CMDI #0690 2012/06/19



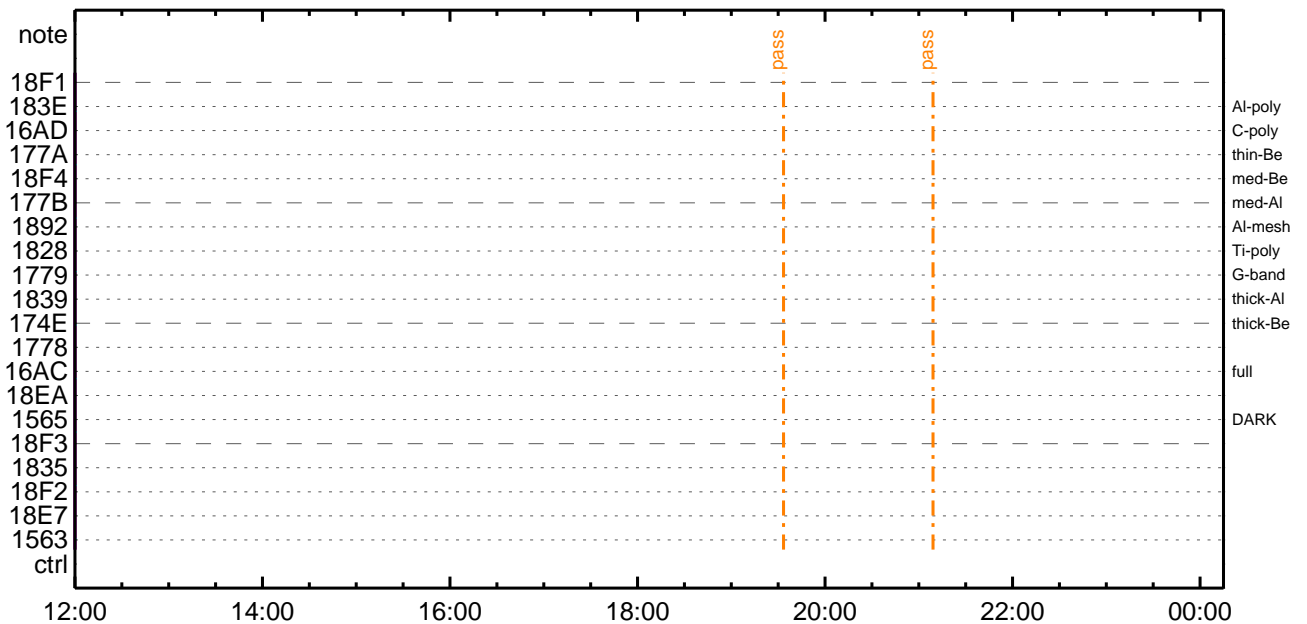
CMDI #0690 2012/06/19



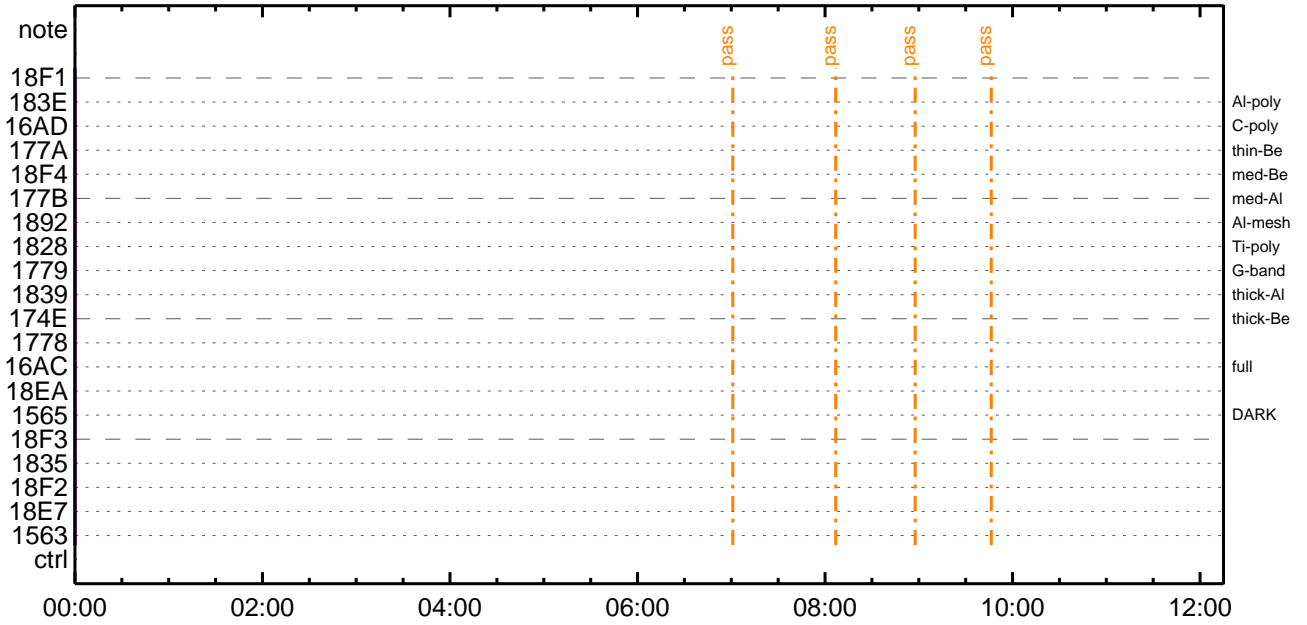
CMDI #0690 2012/06/20



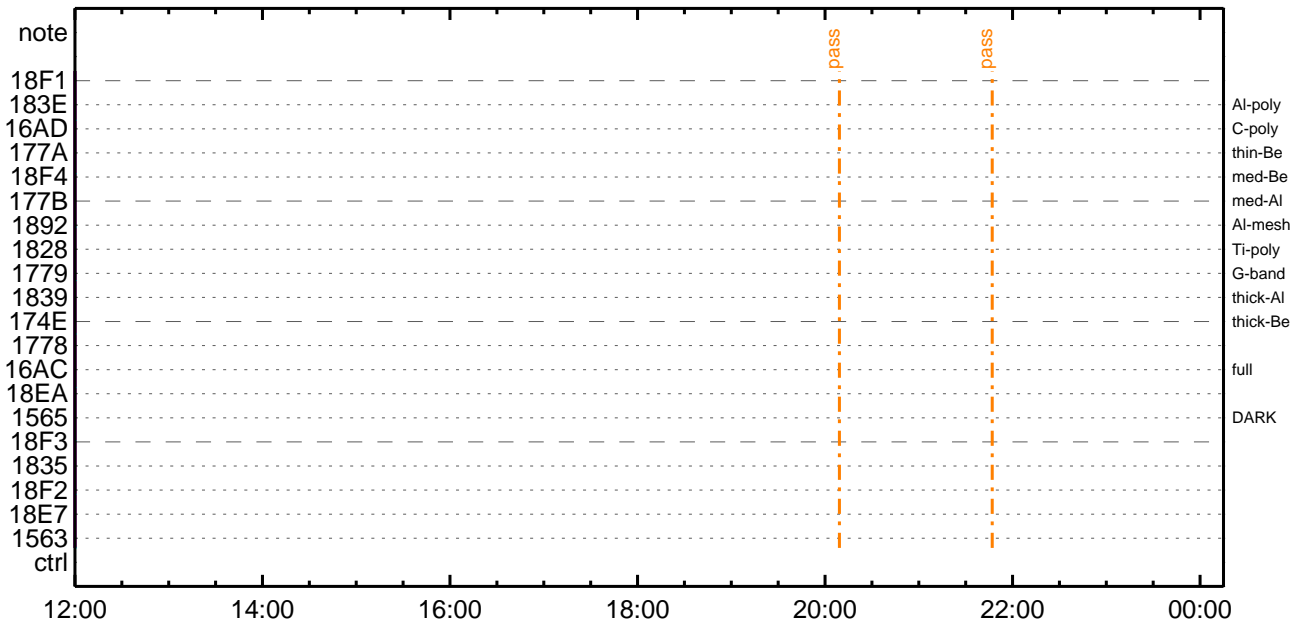
CMDI #0690 2012/06/20



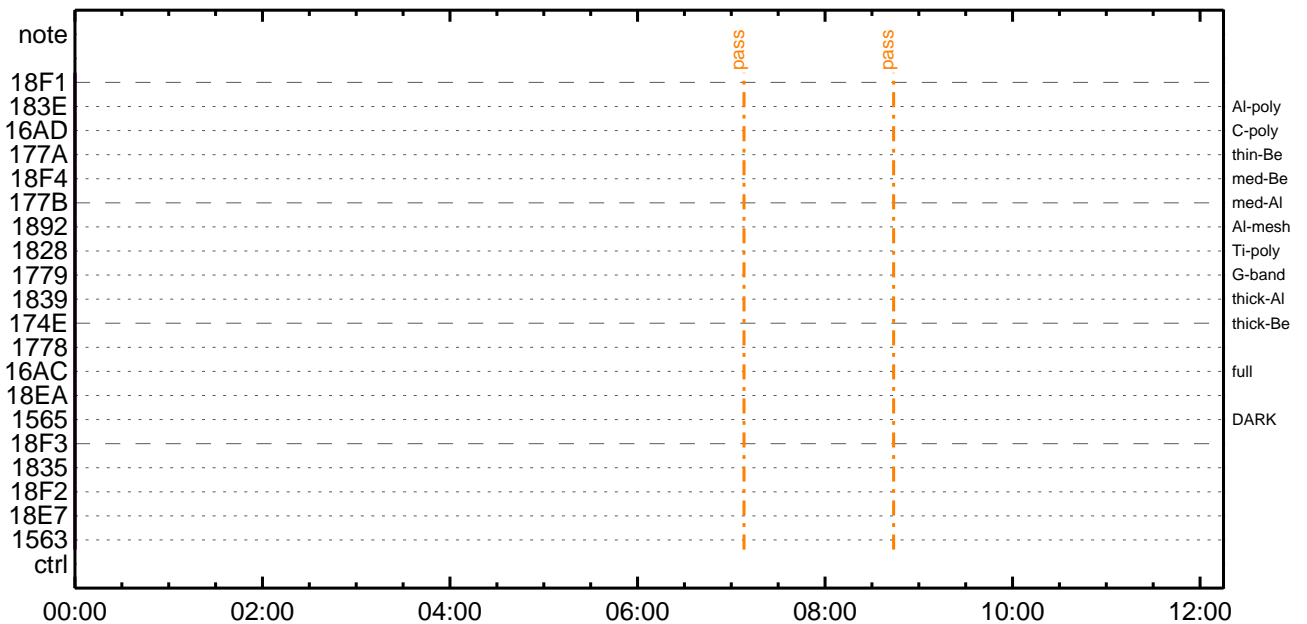
CMDI #0690 2012/06/21



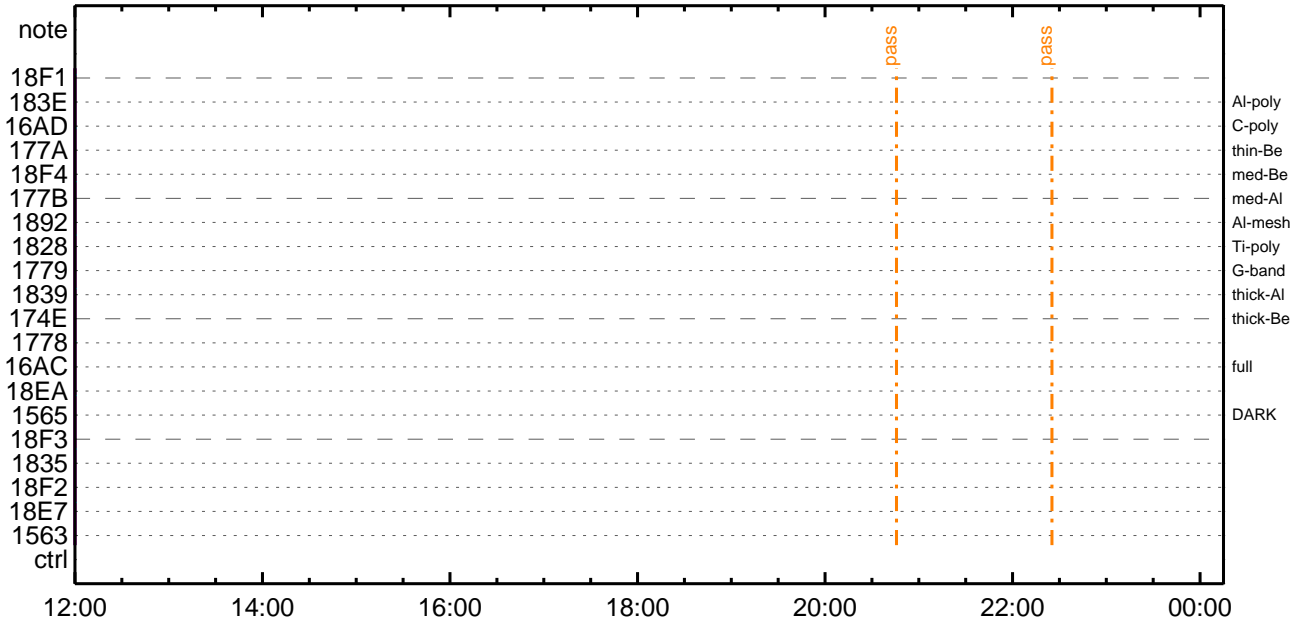
CMDI #0690 2012/06/21



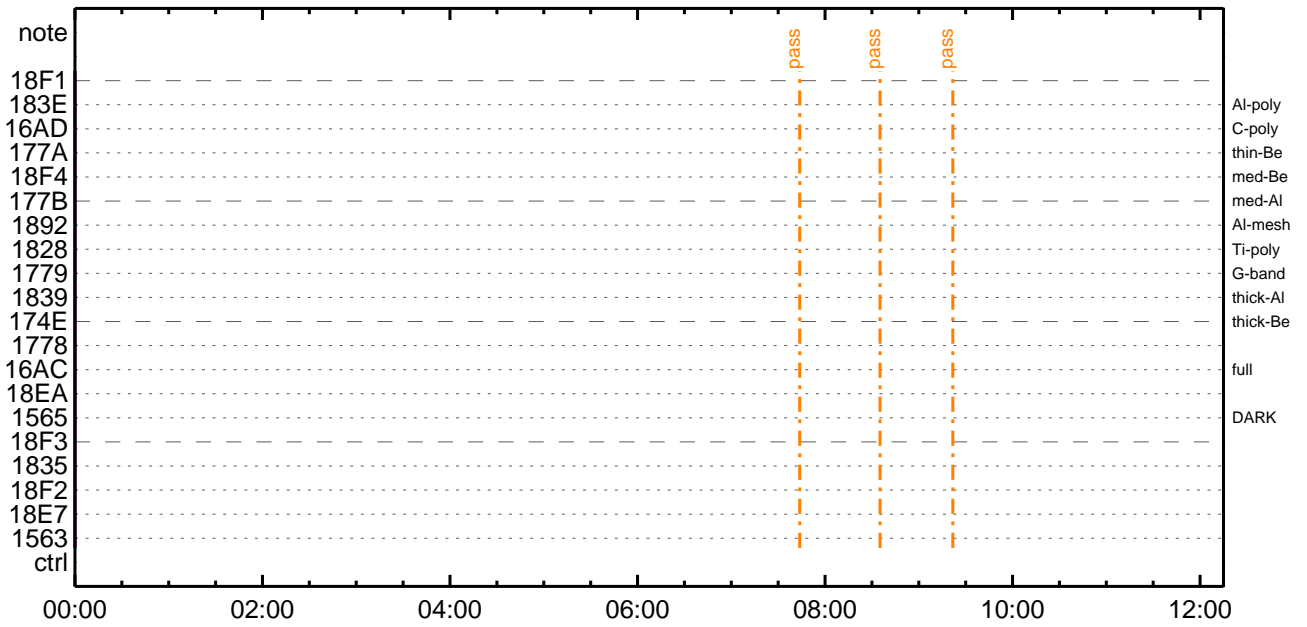
CMDI #0690 2012/06/22



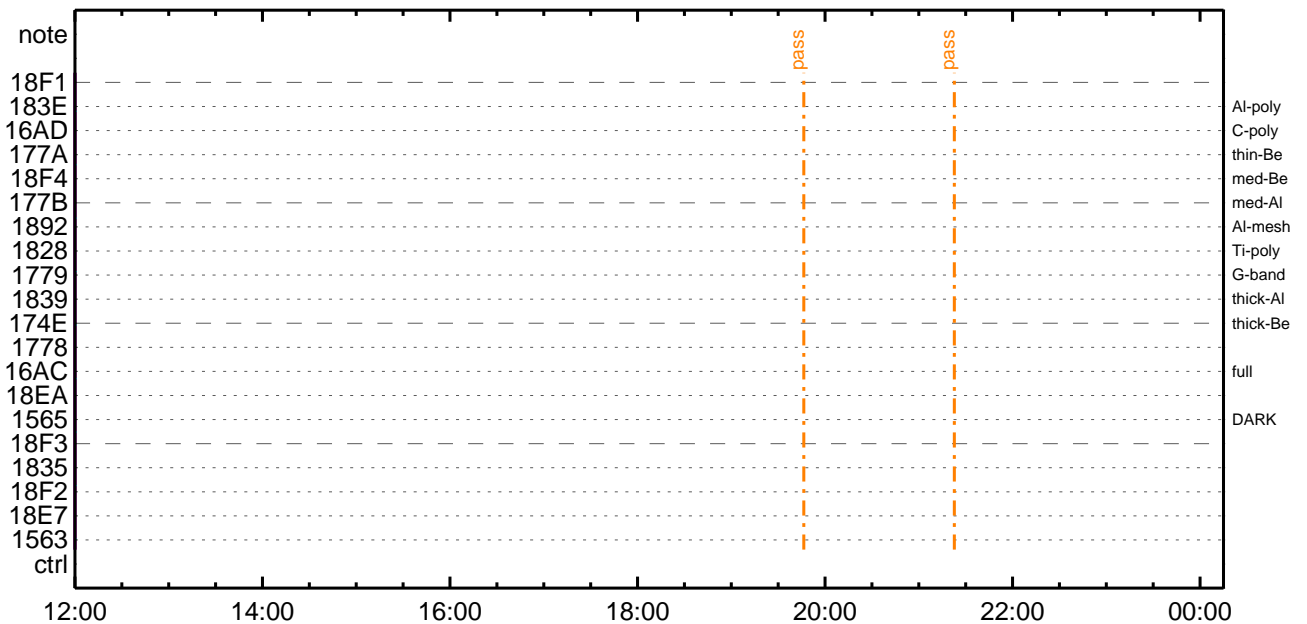
CMDI #0690 2012/06/22



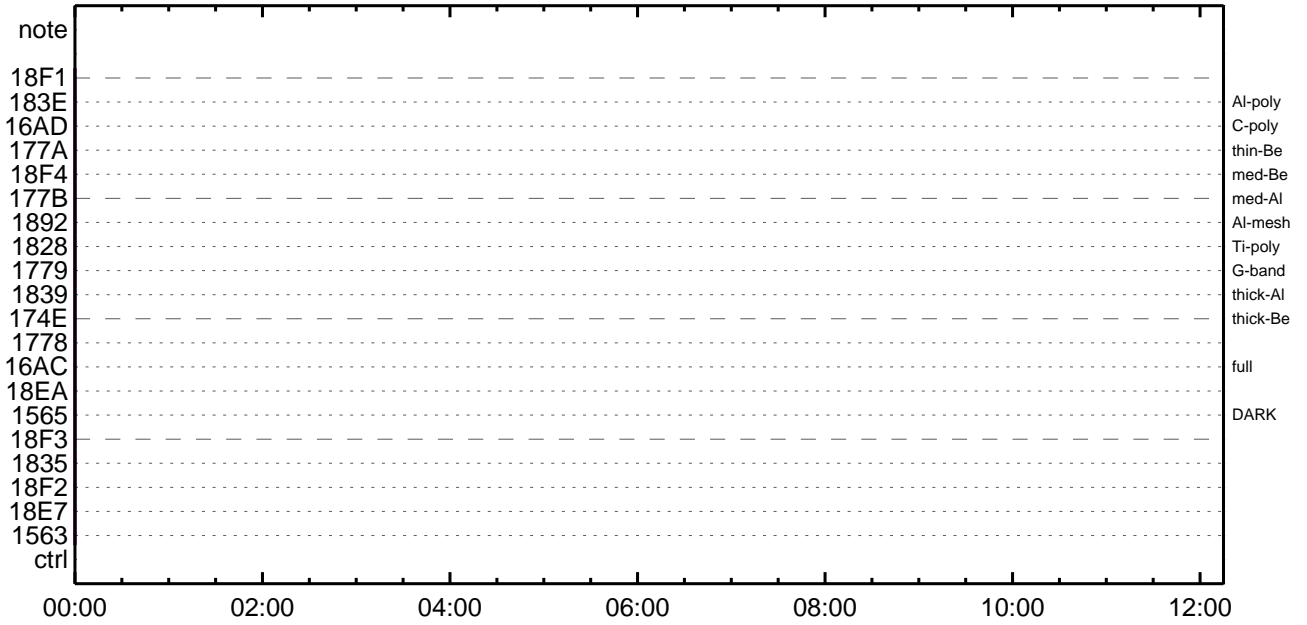
CMDI #0690 2012/06/23



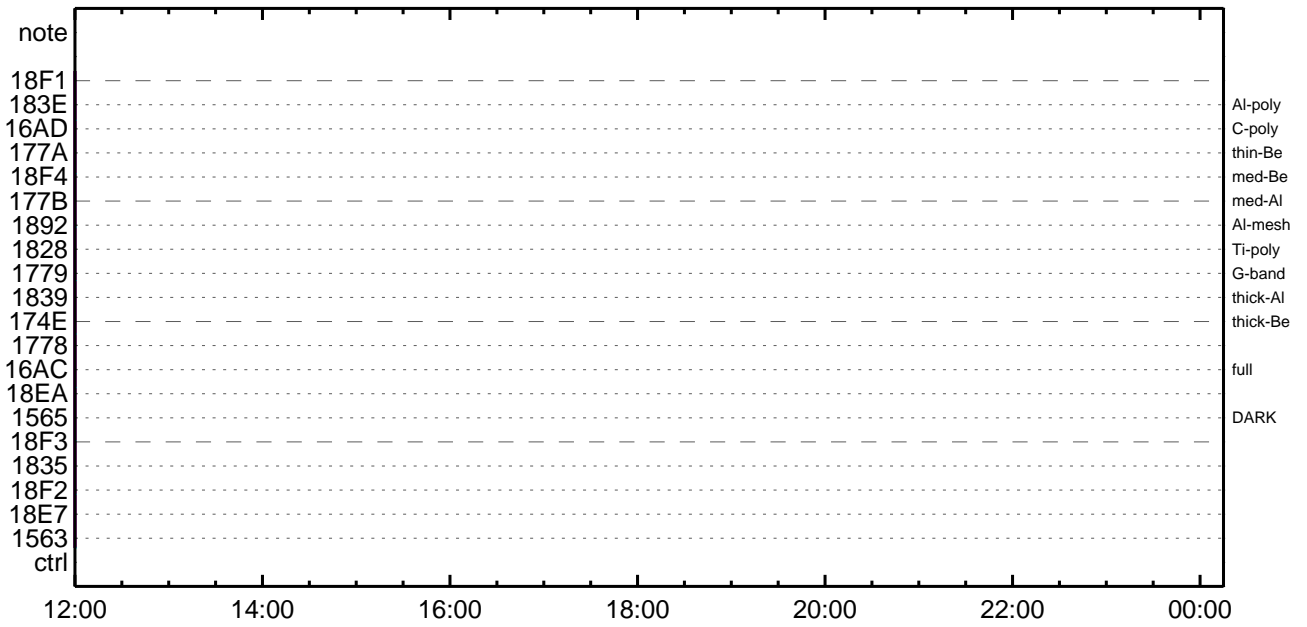
CMDI #0690 2012/06/23



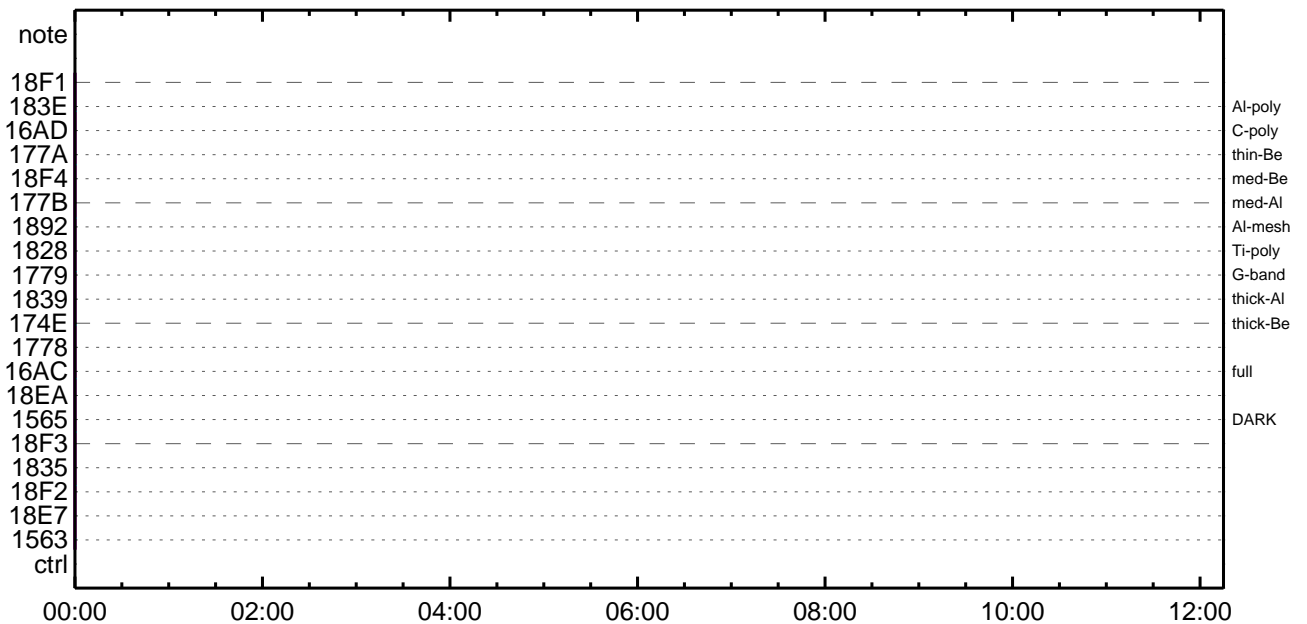
CMDI #0690 2012/06/24



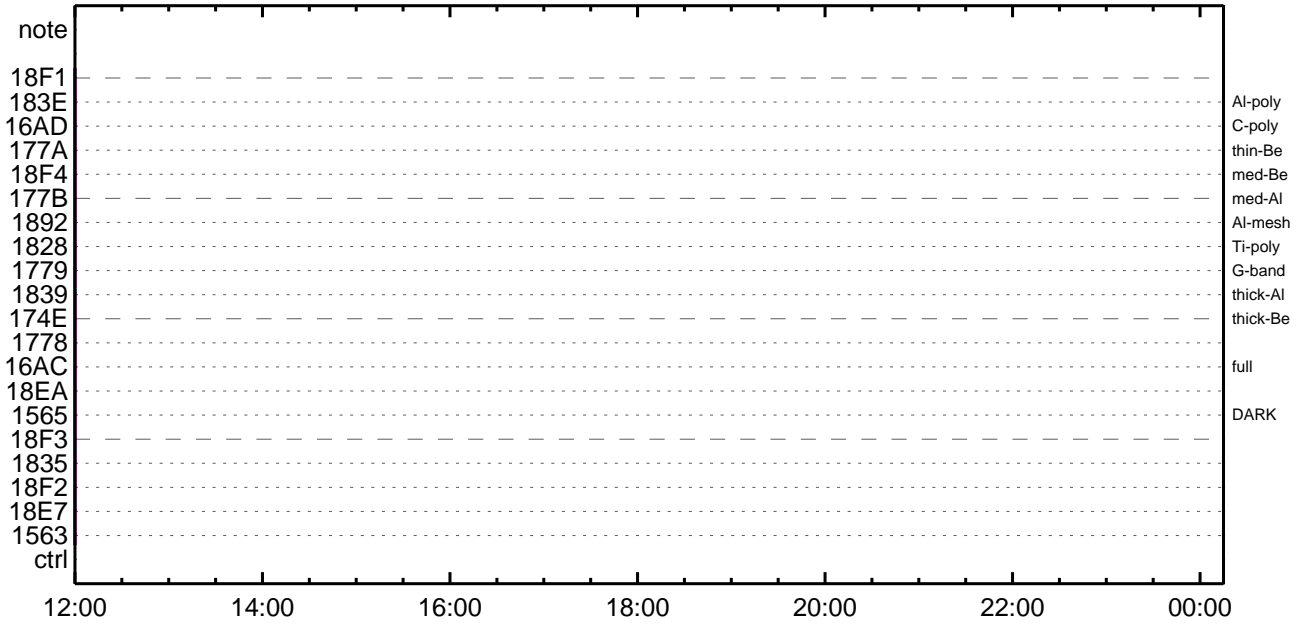
CMDI #0690 2012/06/24



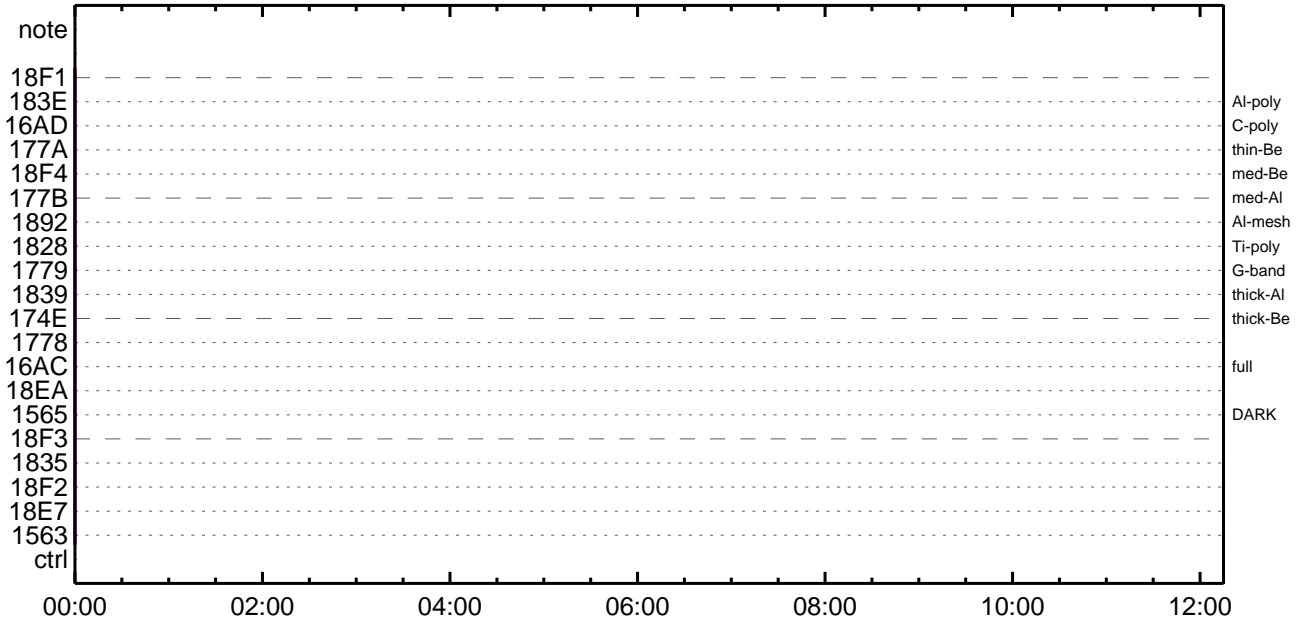
CMDI #0690 2012/06/25



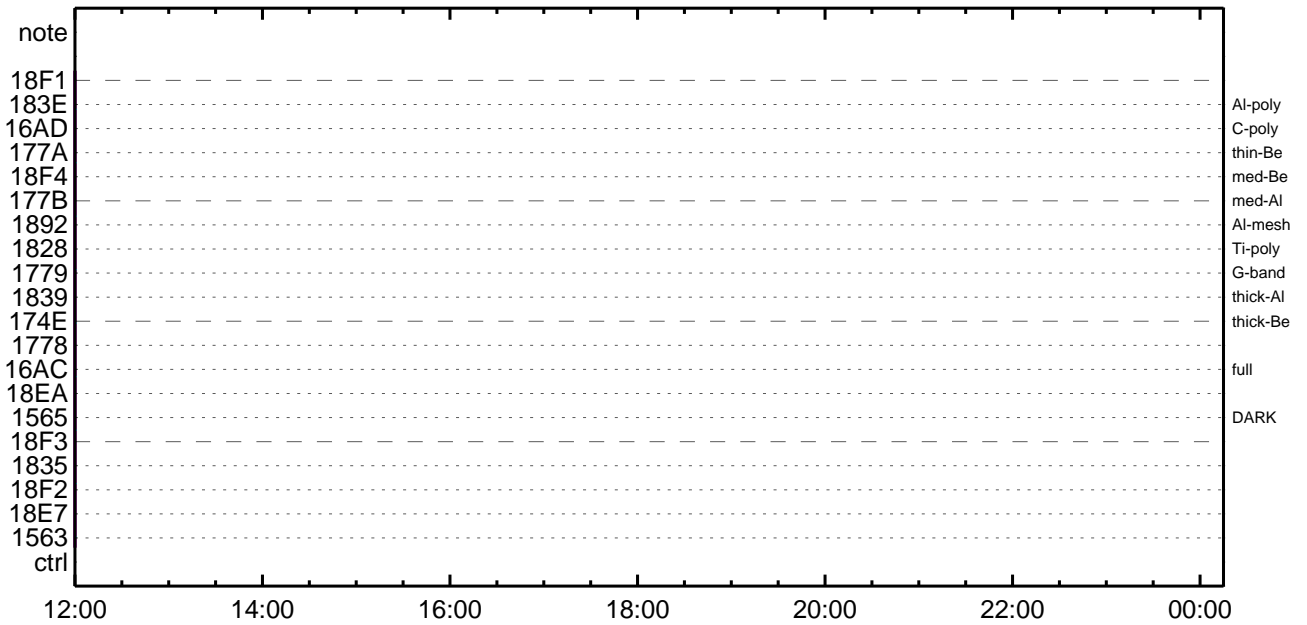
CMDI #0690 2012/06/25



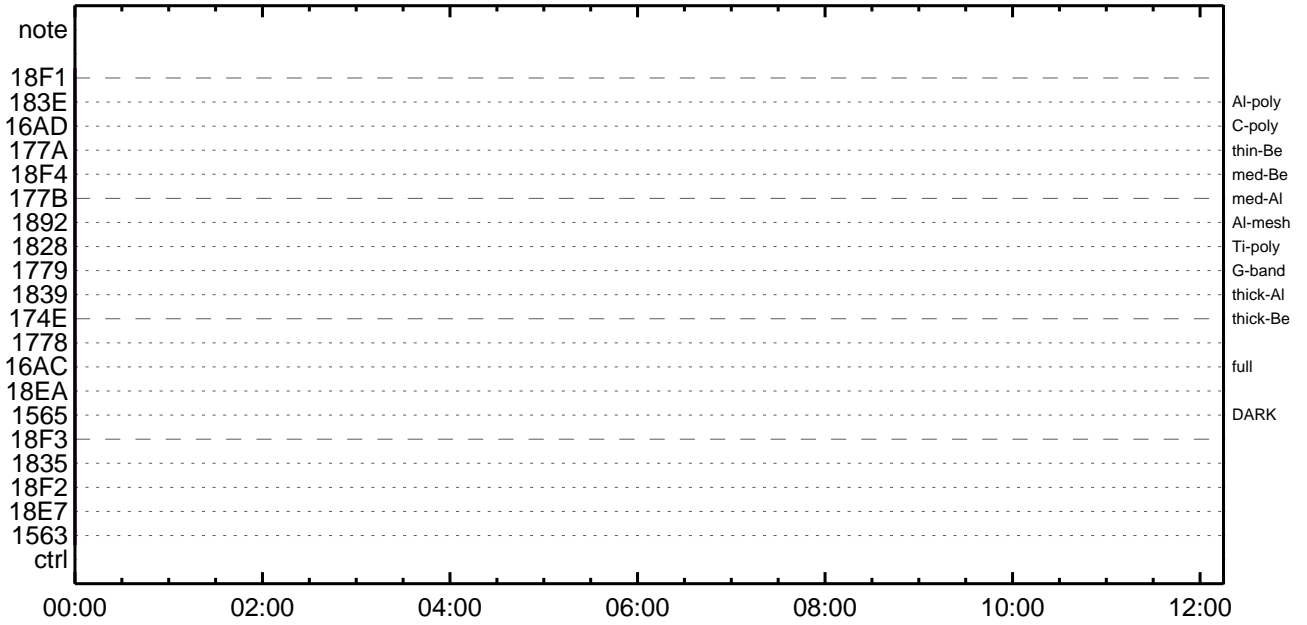
CMDI #0690 2012/06/26



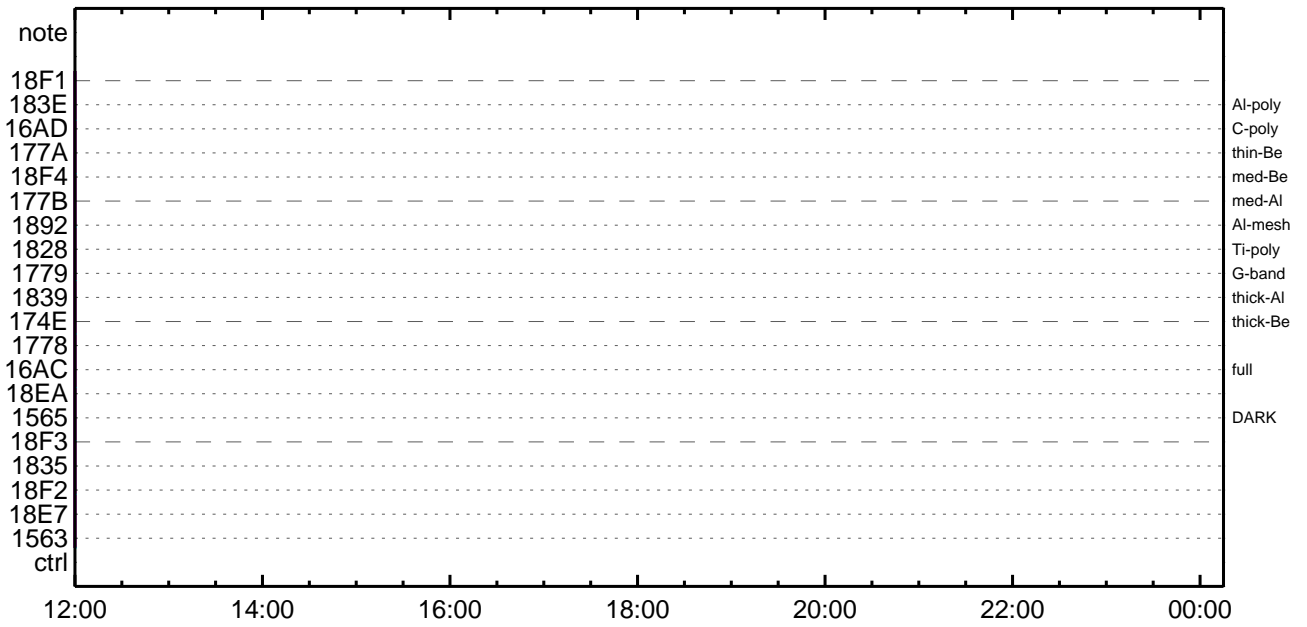
CMDI #0690 2012/06/26



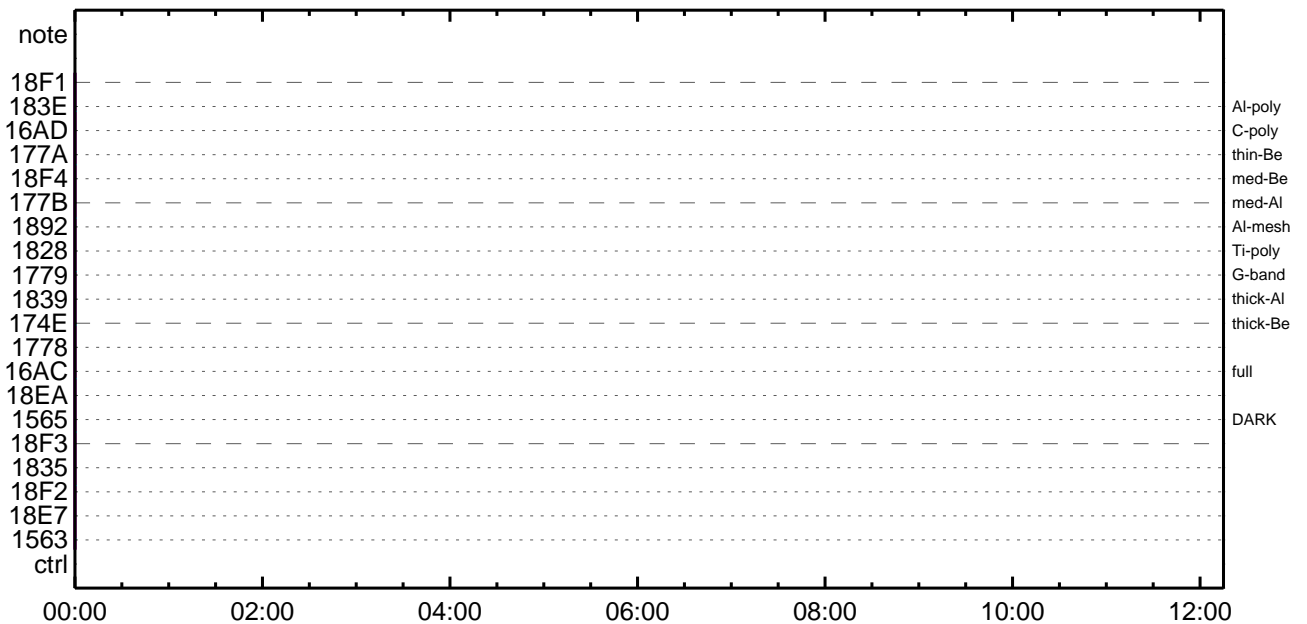
CMDI #0690 2012/06/27



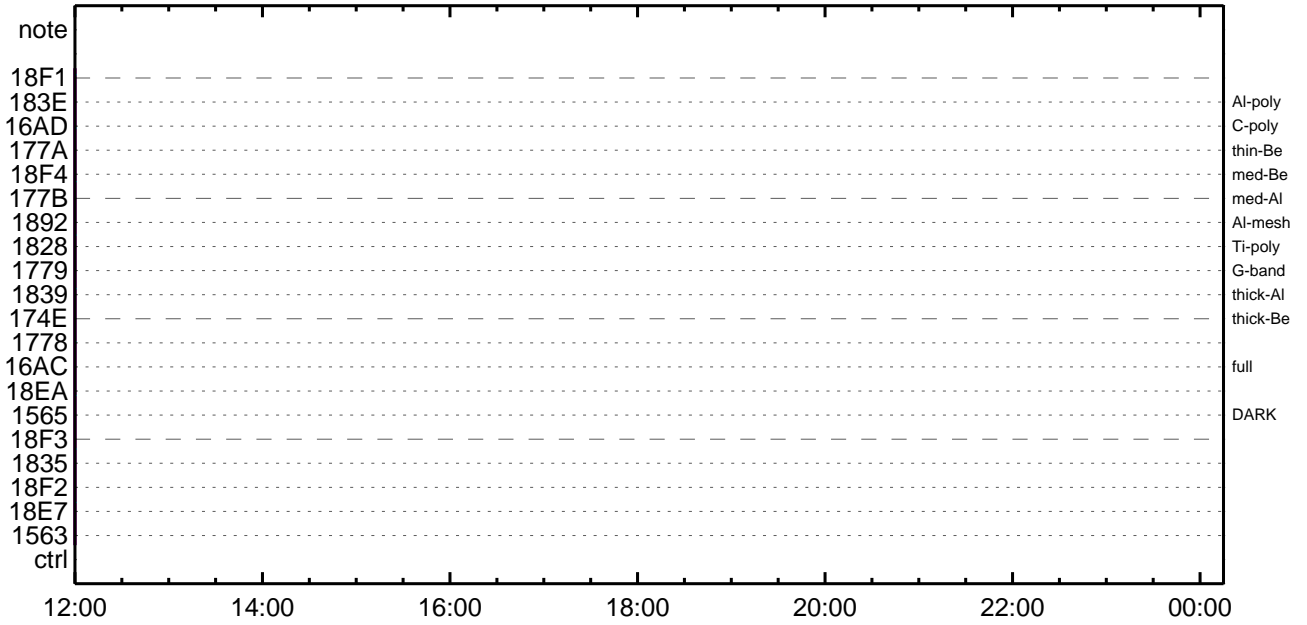
CMDI #0690 2012/06/27



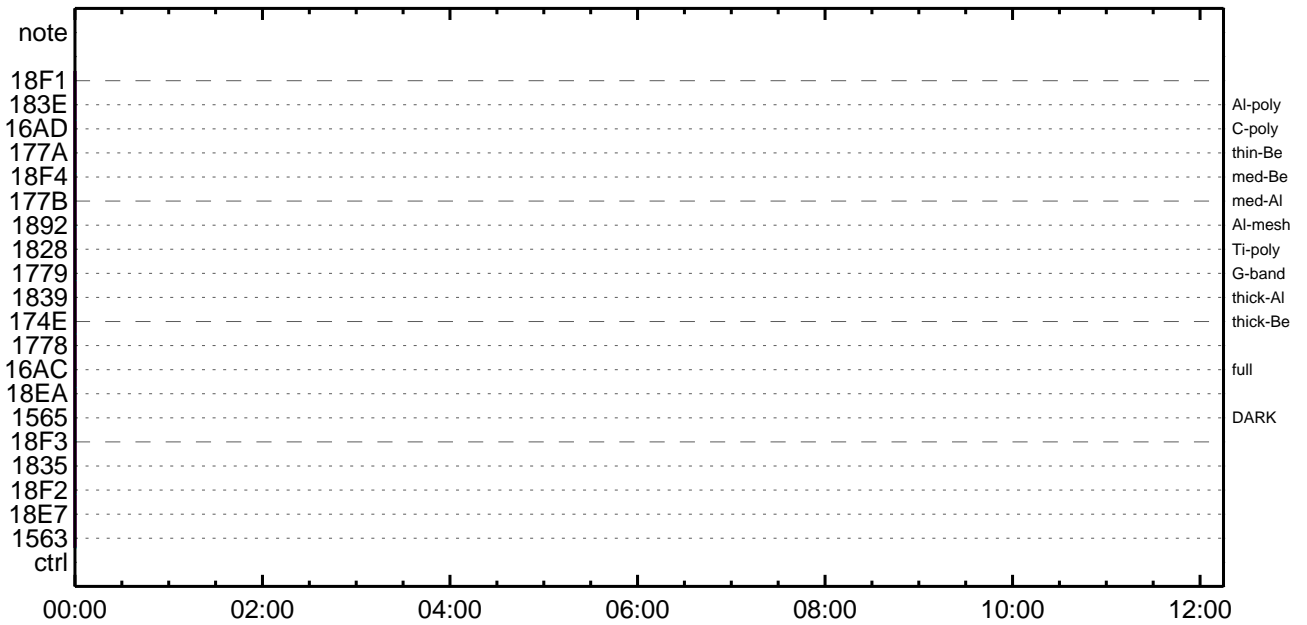
CMDI #0690 2012/06/28



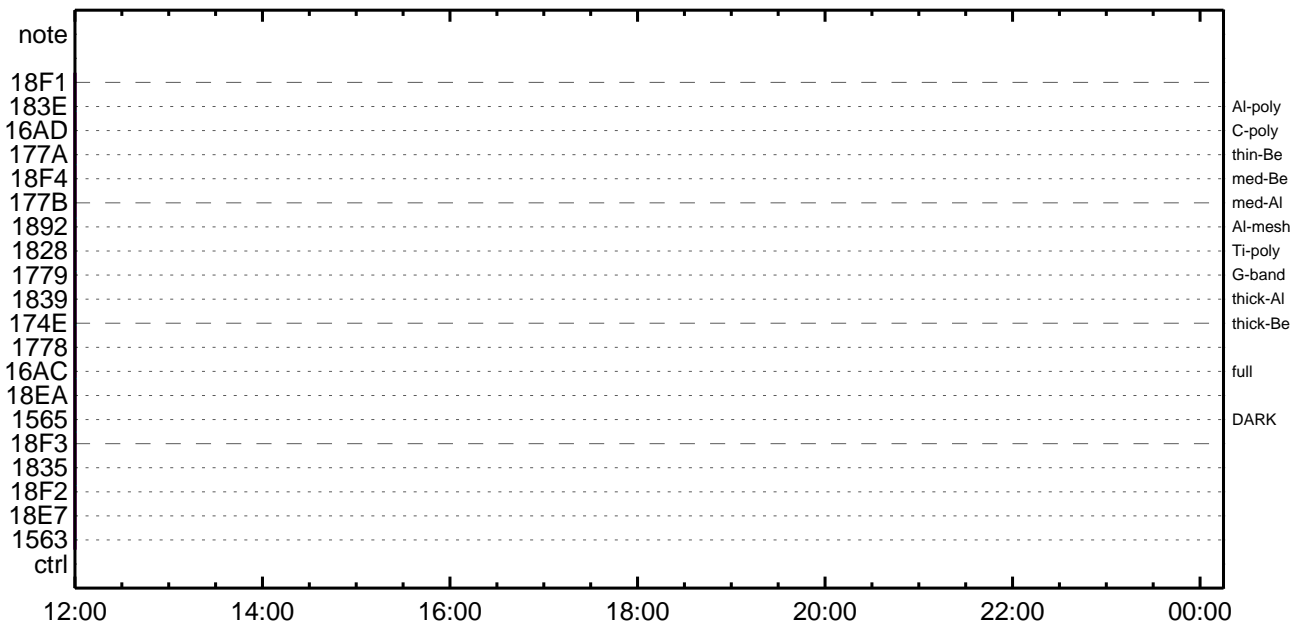
CMDI #0690 2012/06/28



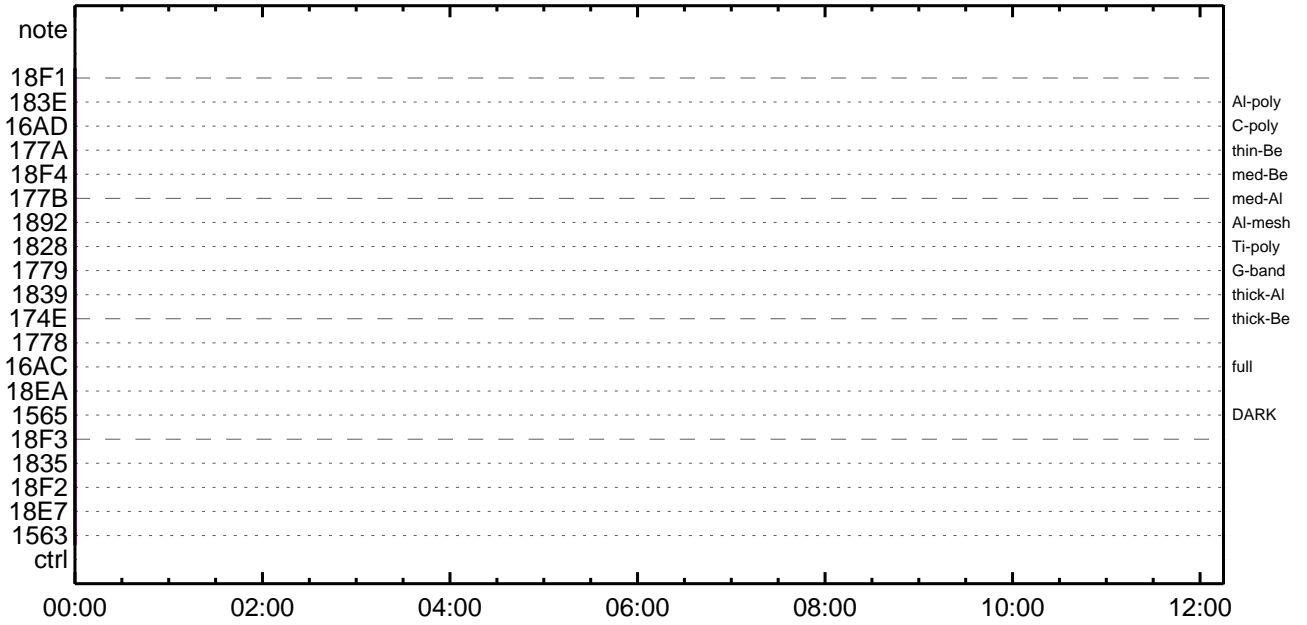
CMDI #0690 2012/06/29



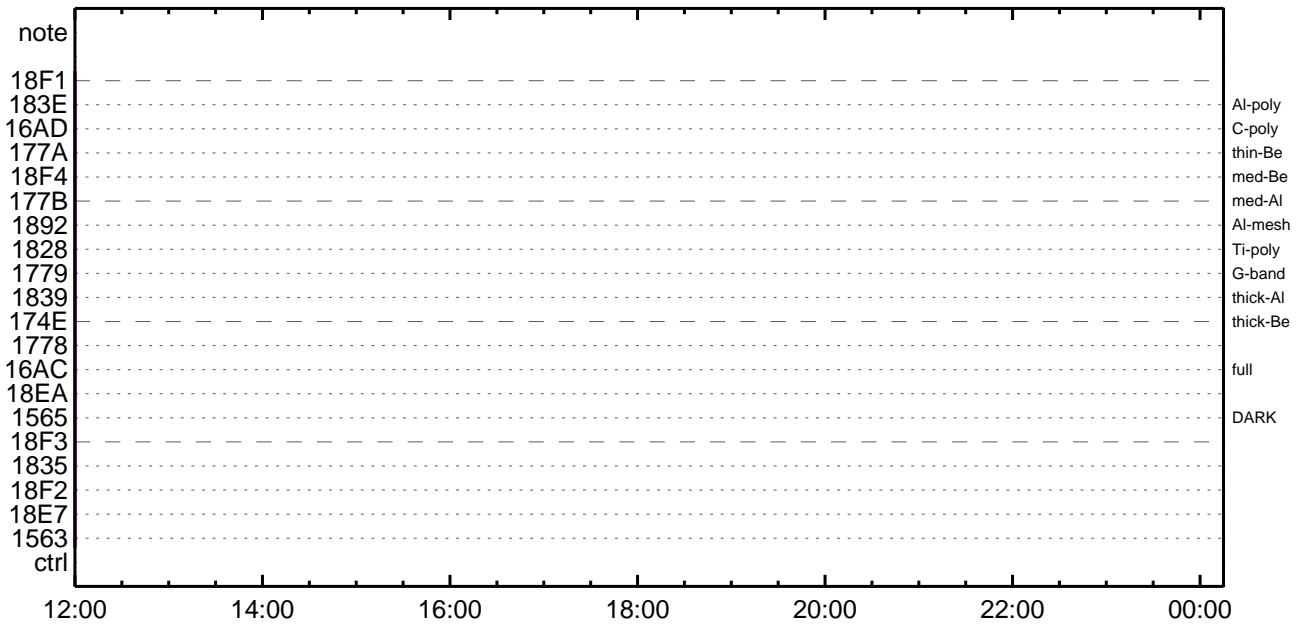
CMDI #0690 2012/06/29



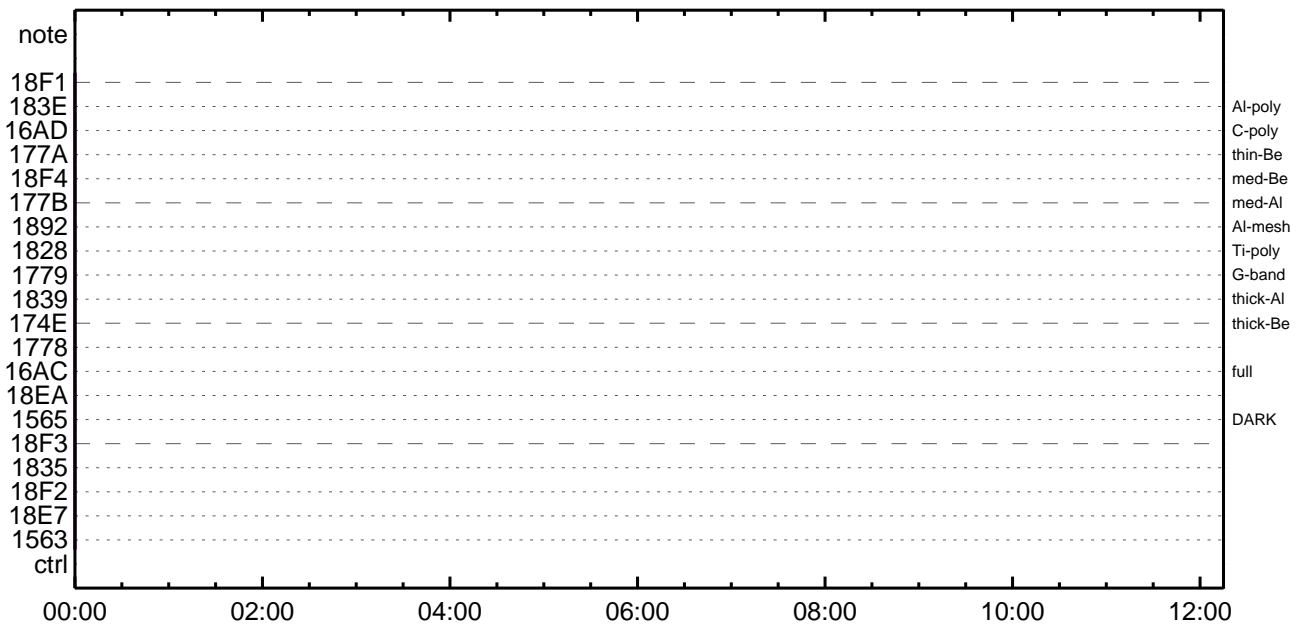
CMDI #0690 2012/06/30



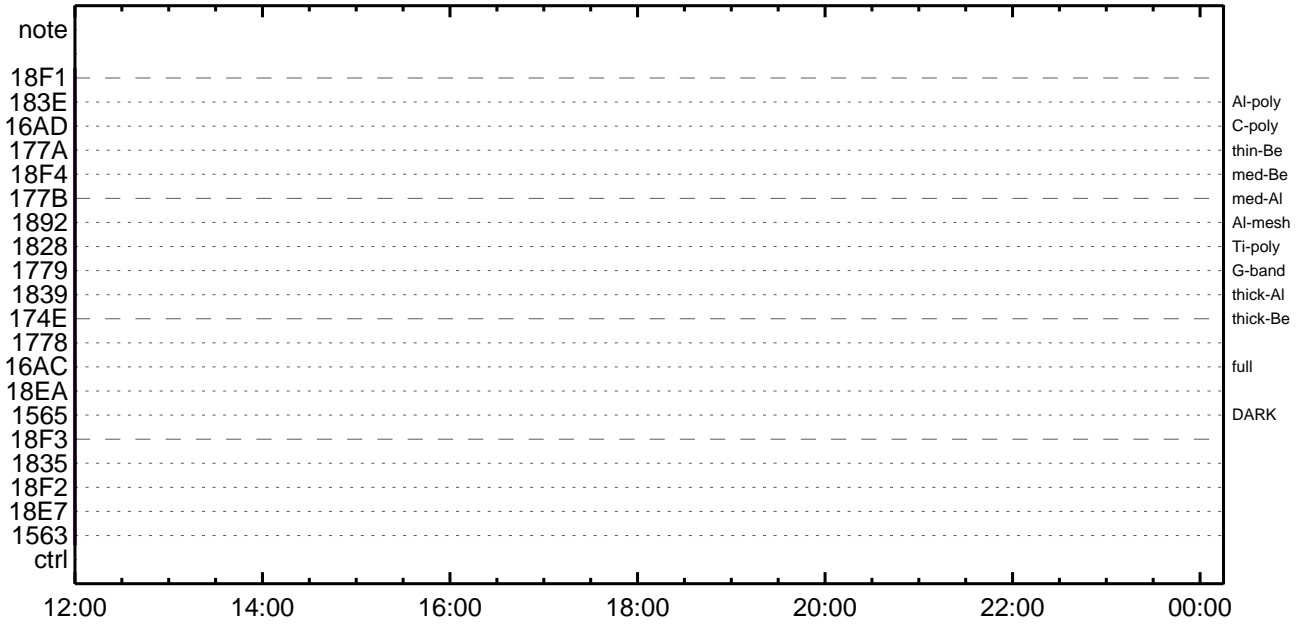
CMDI #0690 2012/06/30



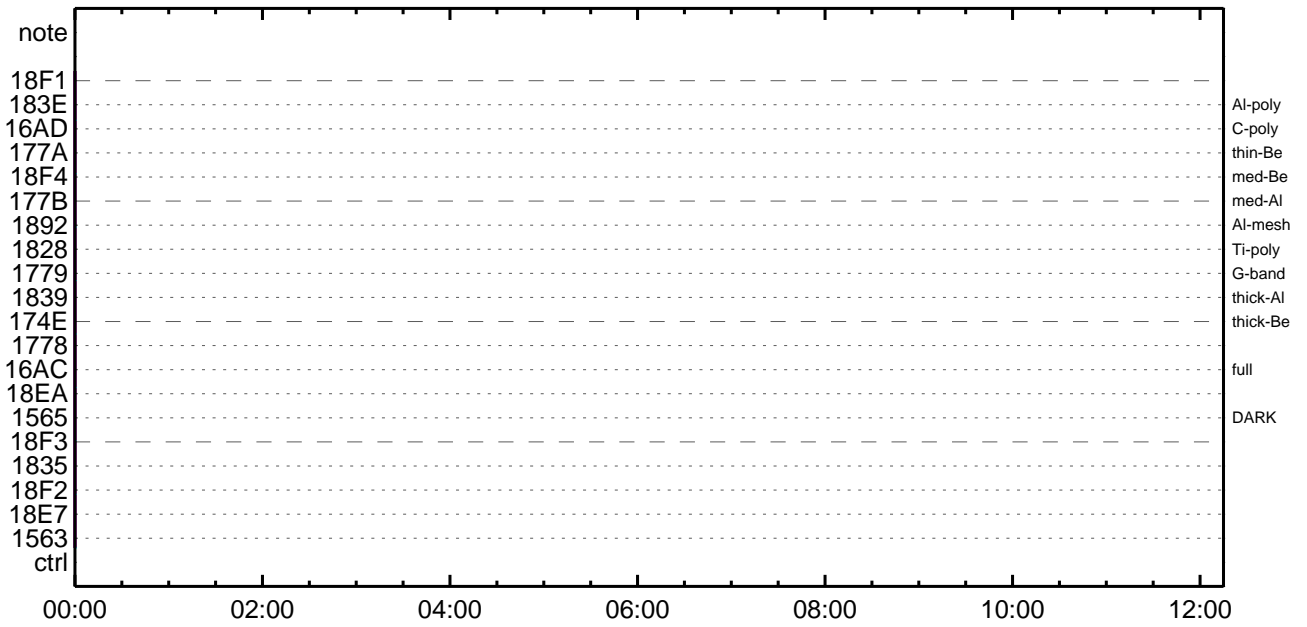
CMDI #0690 2012/07/01



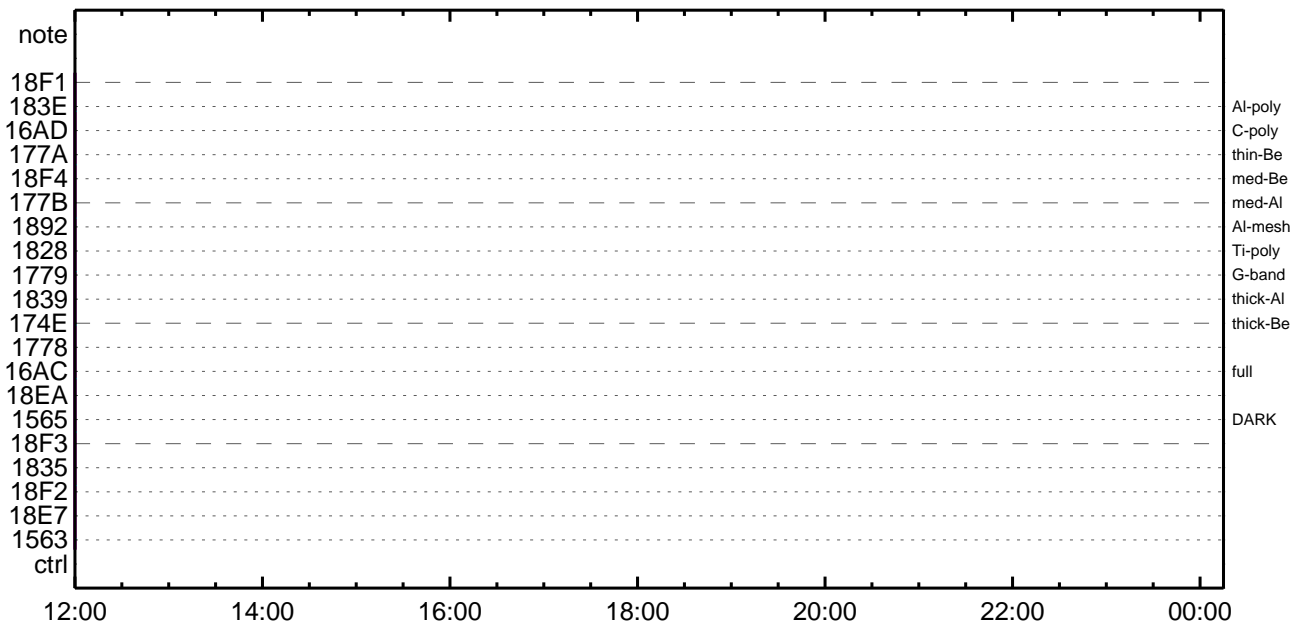
CMDI #0690 2012/07/01



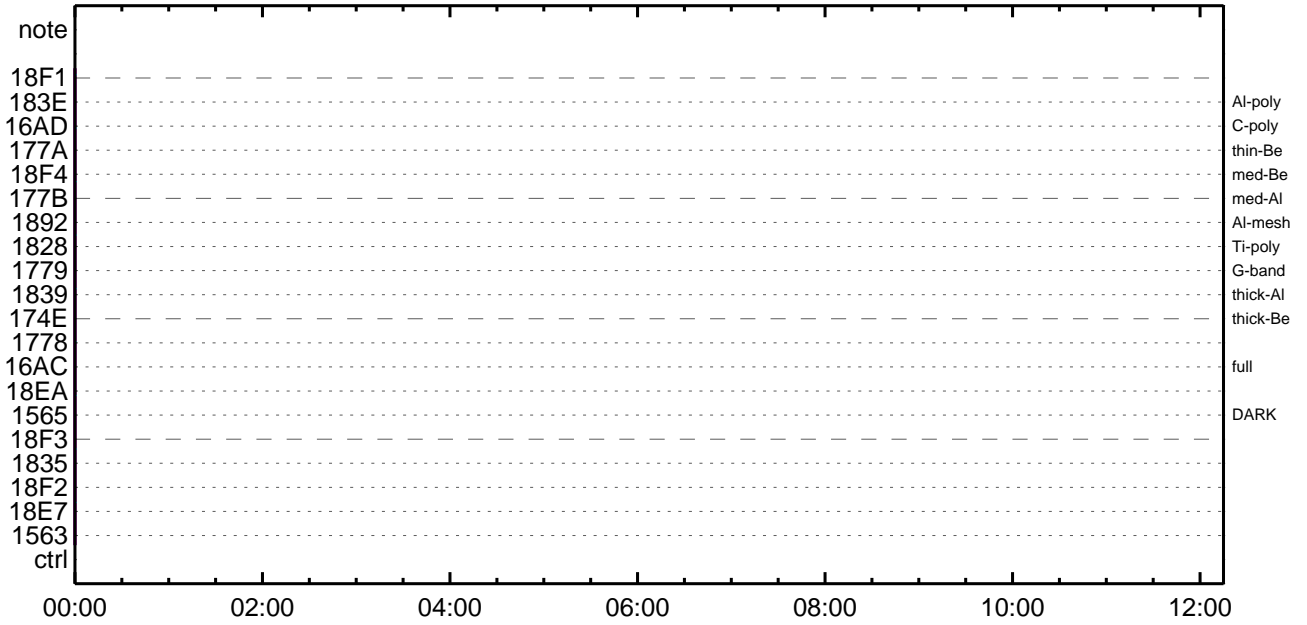
CMDI #0690 2012/07/02



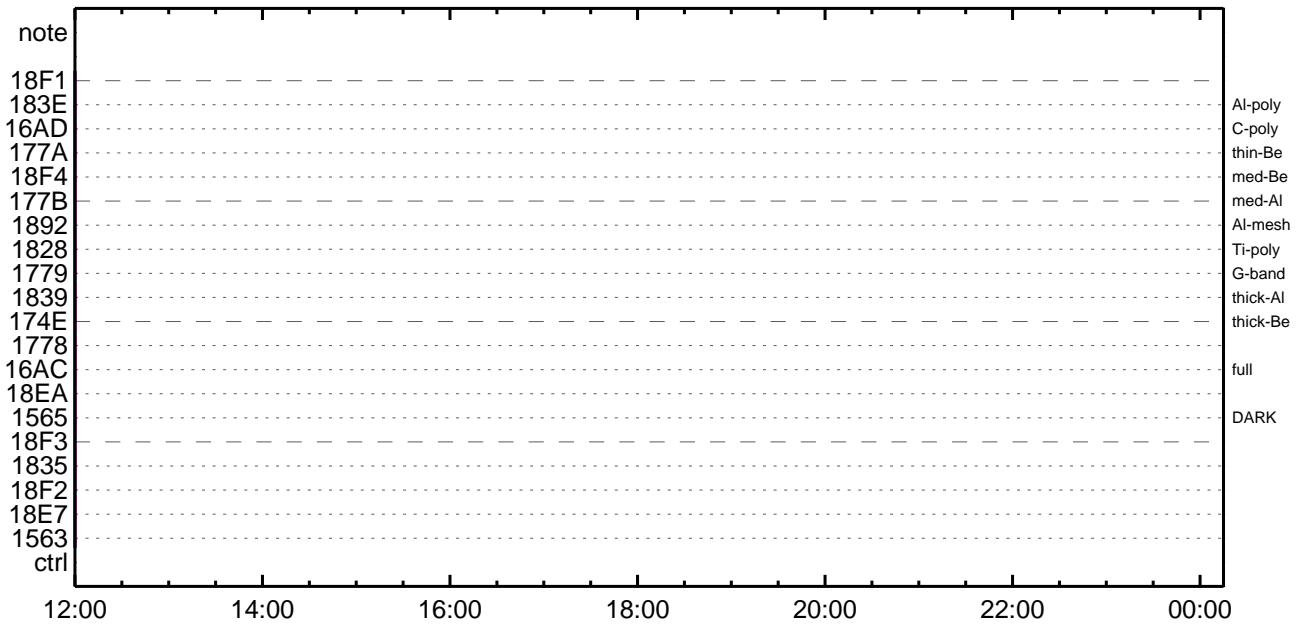
CMDI #0690 2012/07/02



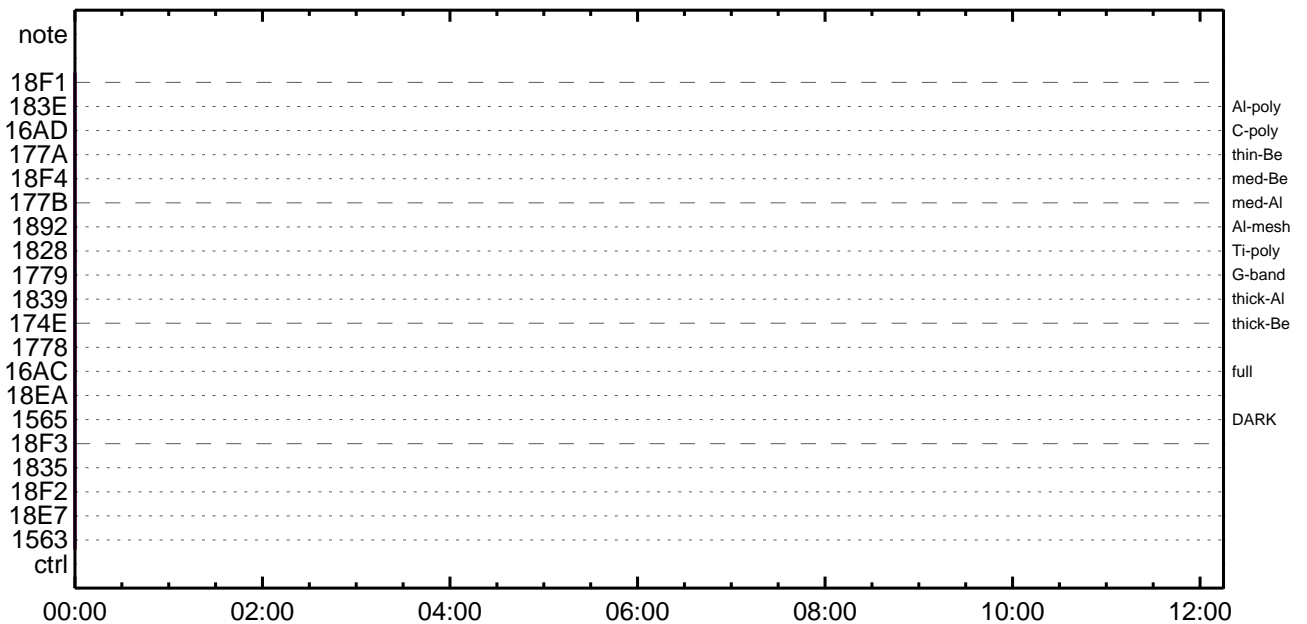
CMDI #0690 2012/07/03



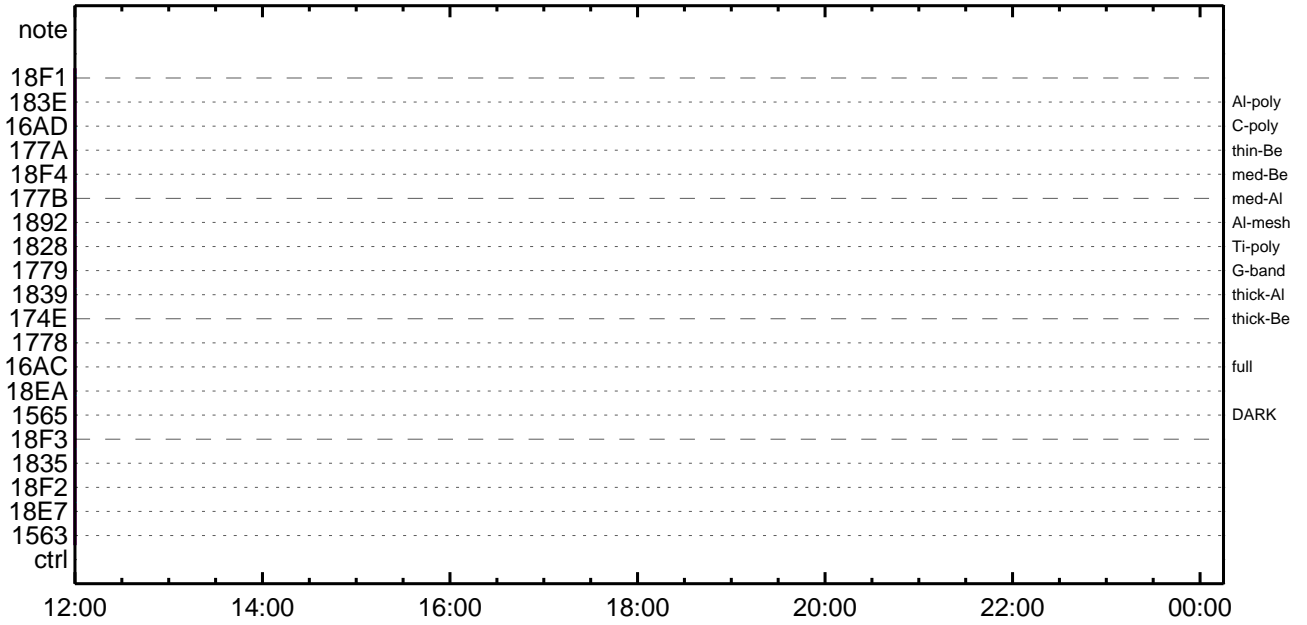
CMDI #0690 2012/07/03



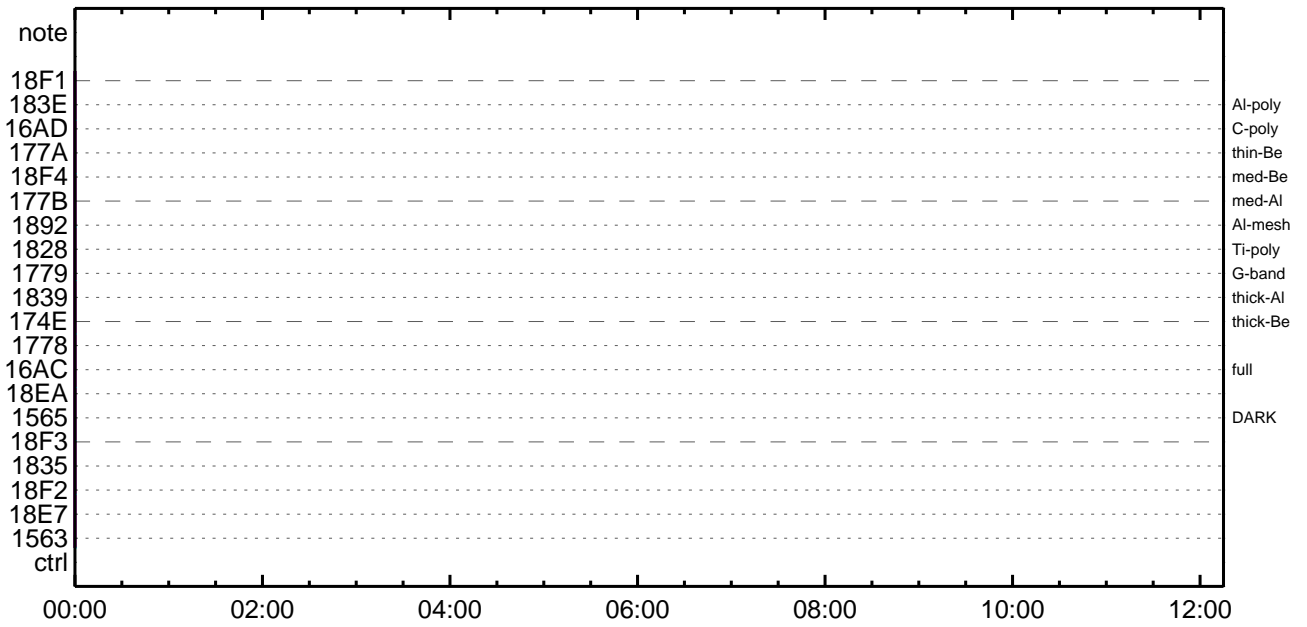
CMDI #0690 2012/07/04



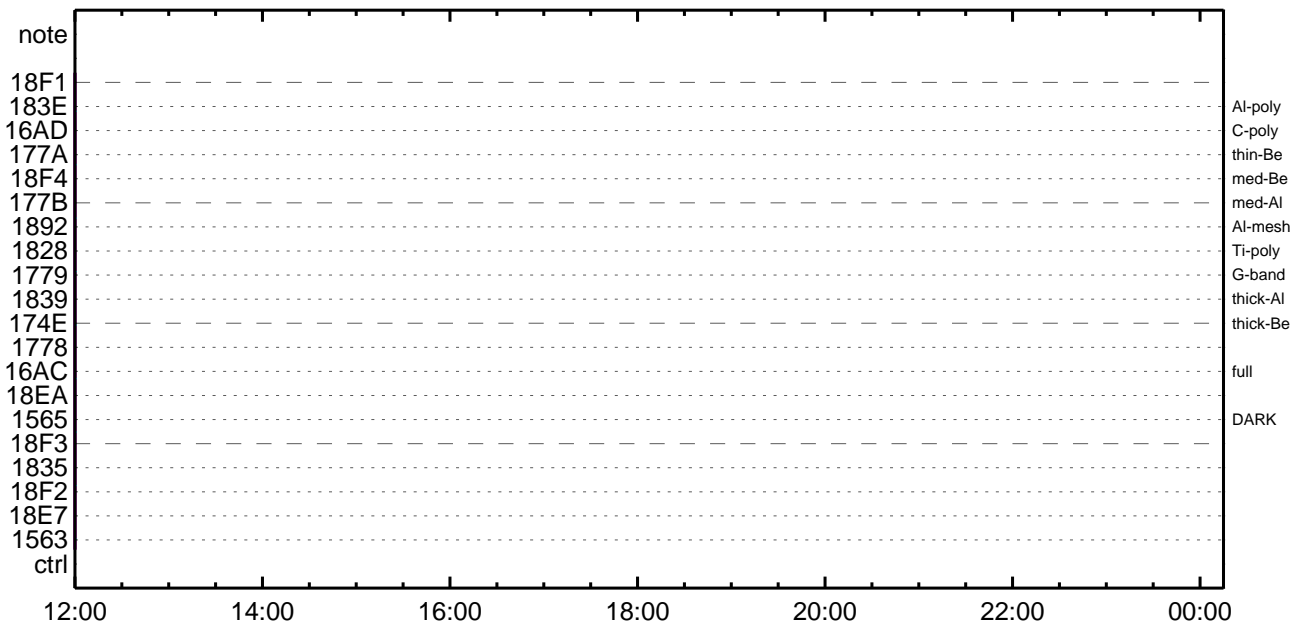
CMDI #0690 2012/07/04



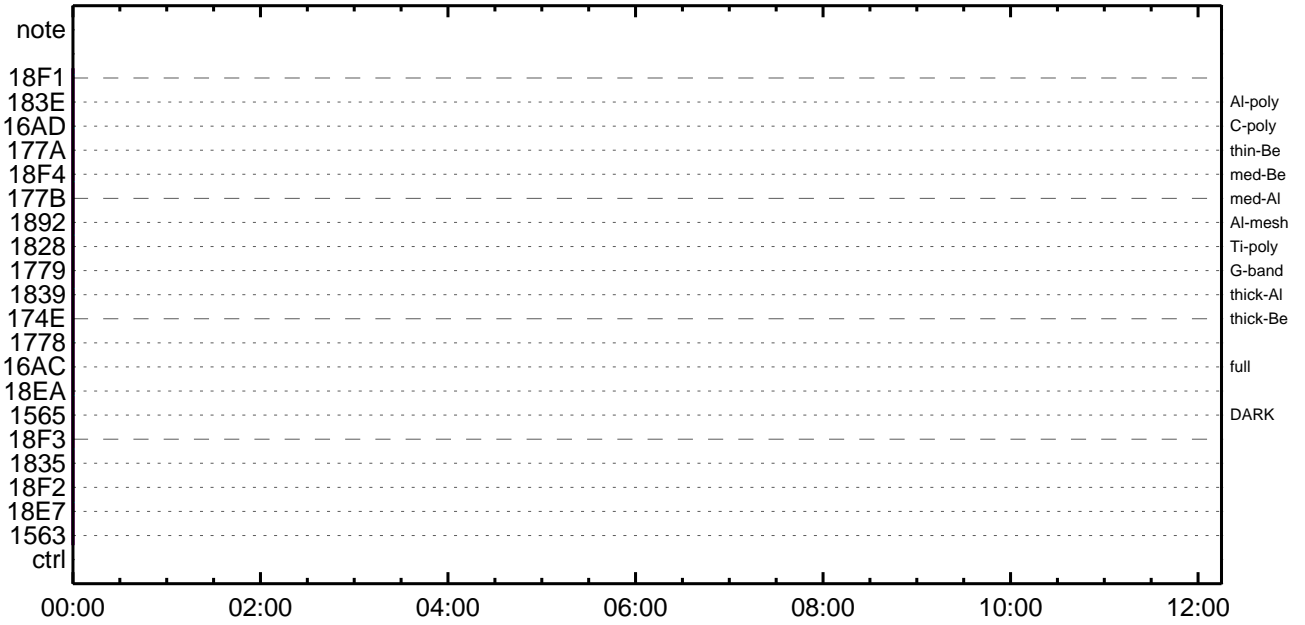
CMDI #0690 2012/07/05



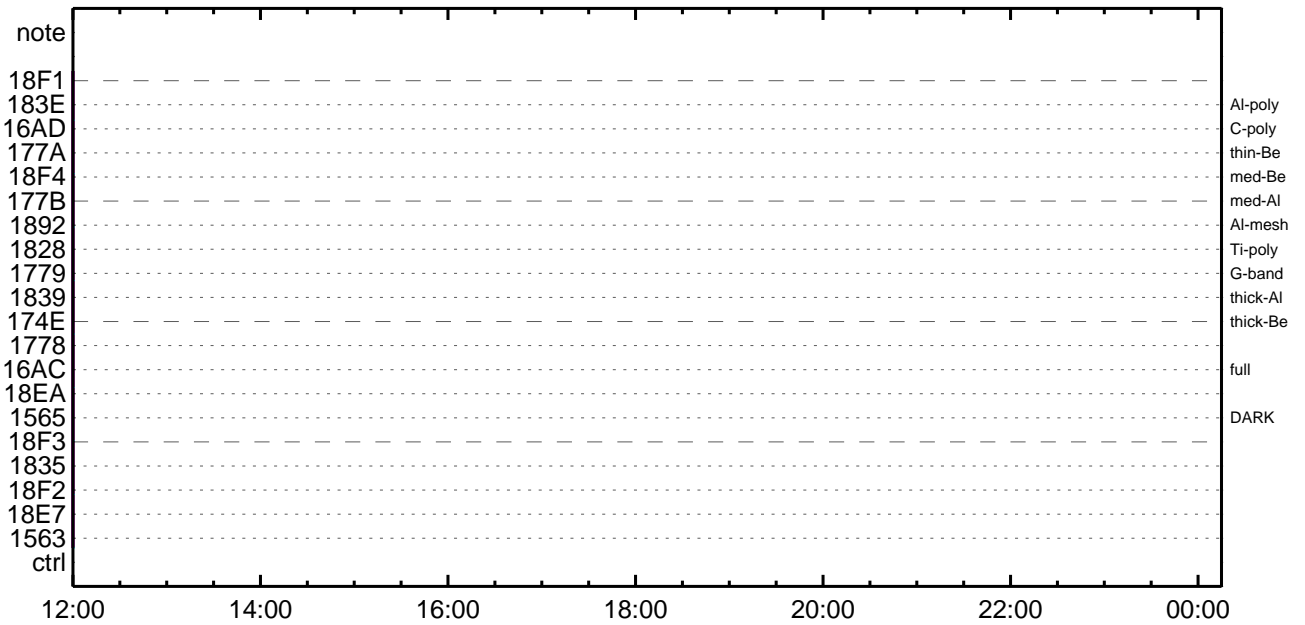
CMDI #0690 2012/07/05



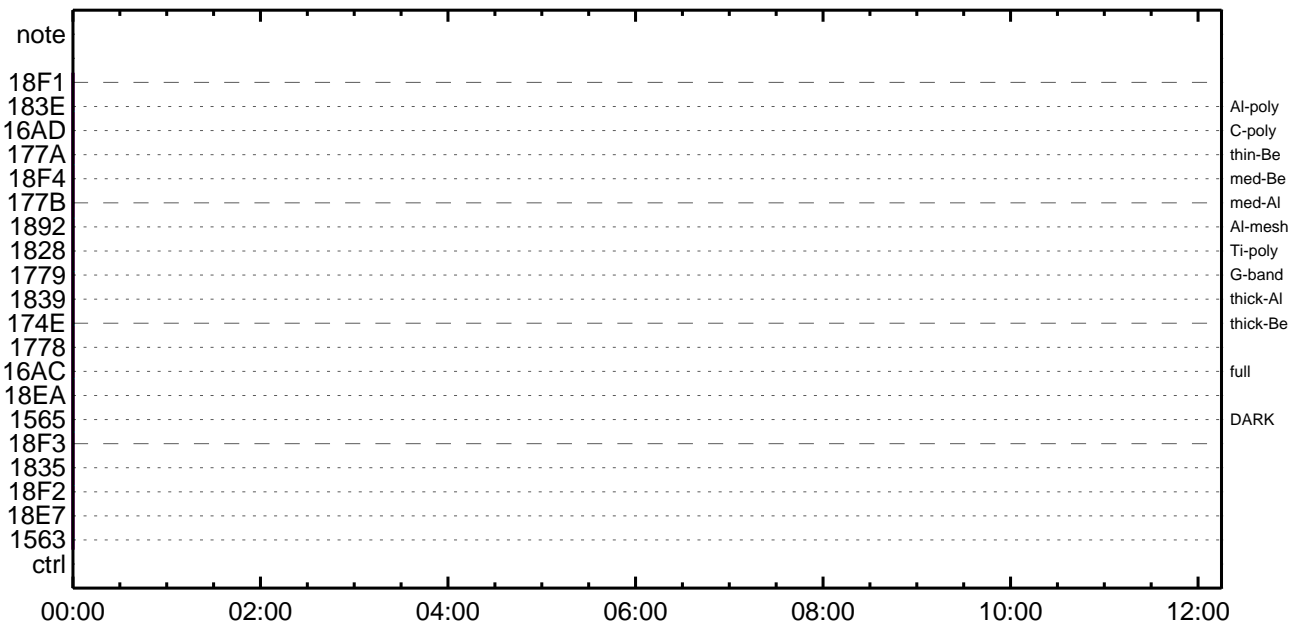
CMDI #0690 2012/07/06



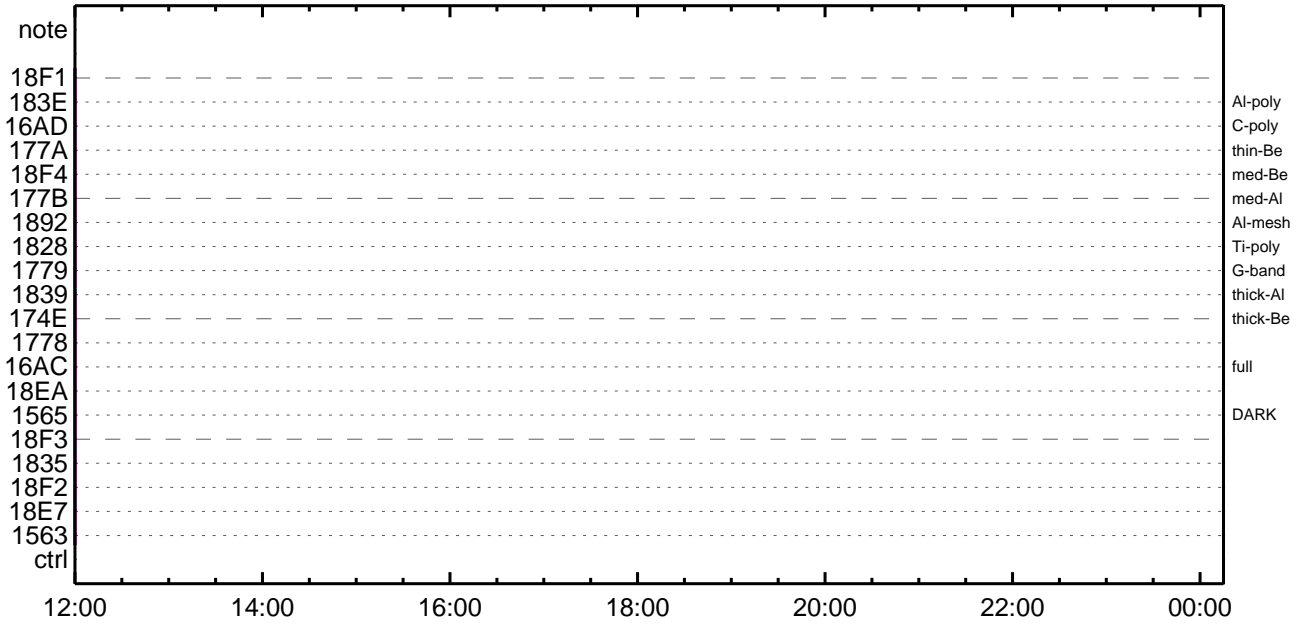
CMDI #0690 2012/07/06



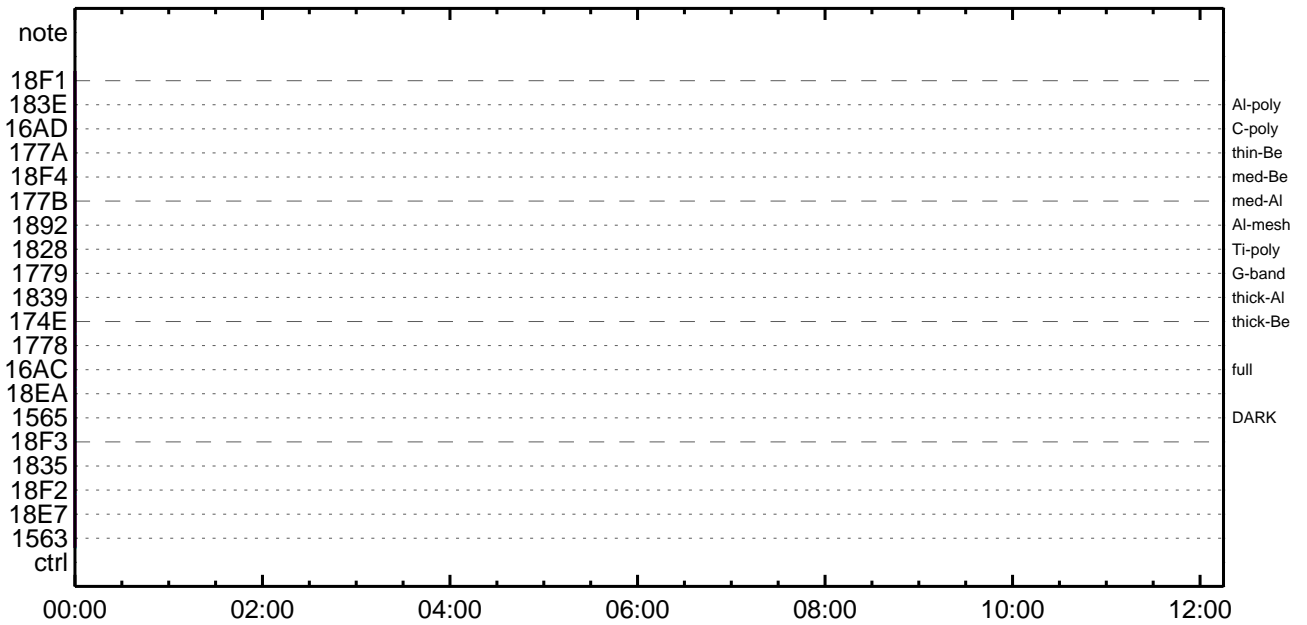
CMDI #0690 2012/07/07



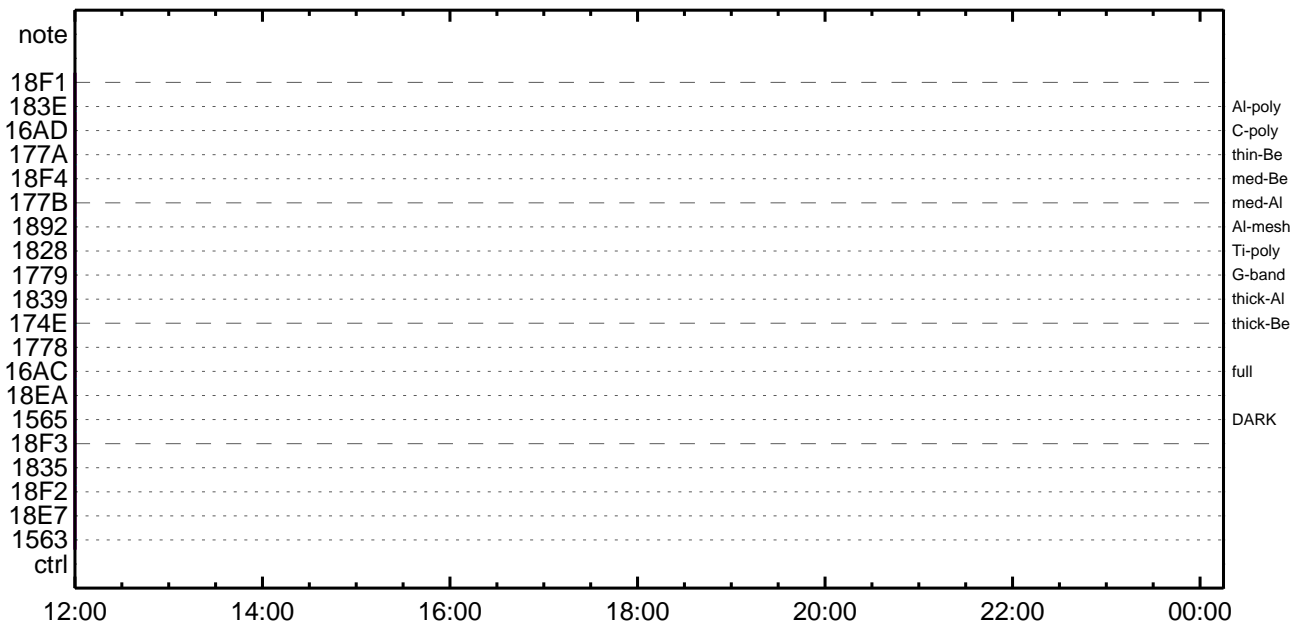
CMDI #0690 2012/07/07



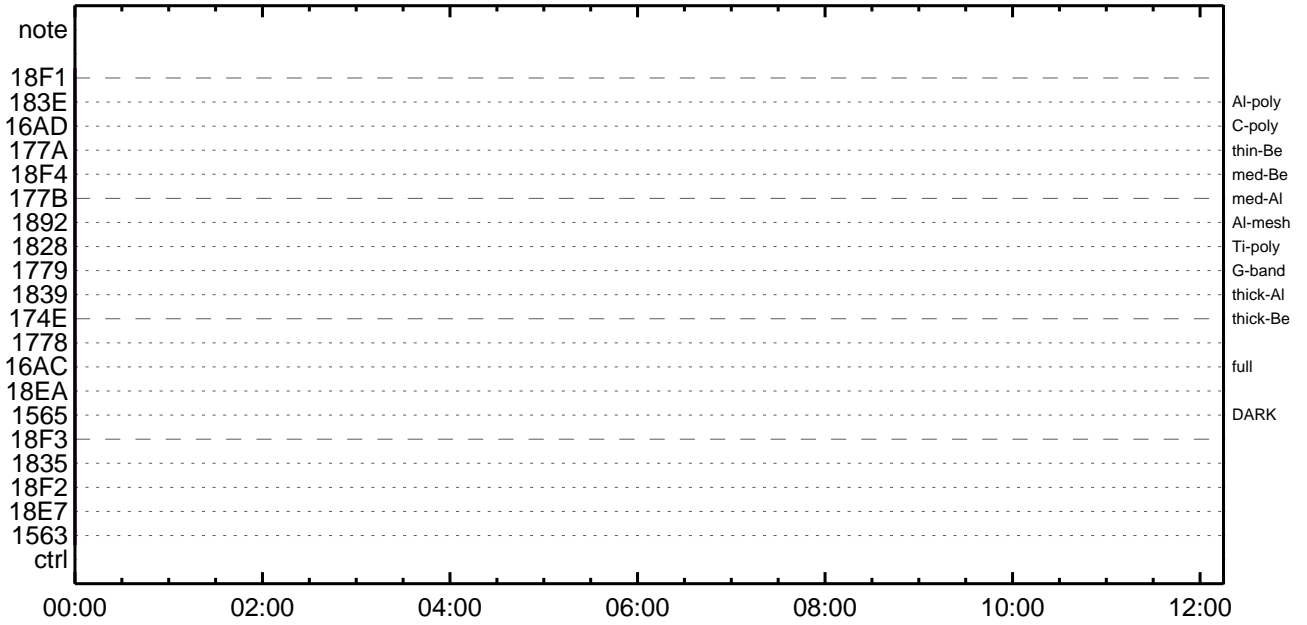
CMDI #0690 2012/07/08



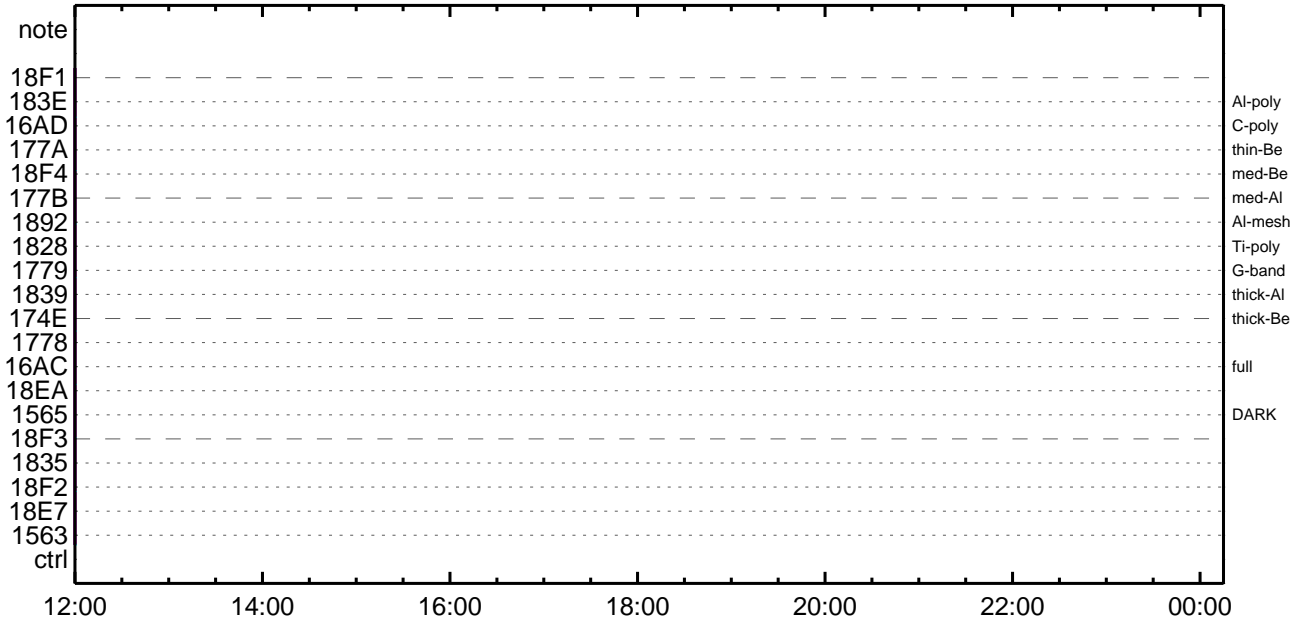
CMDI #0690 2012/07/08



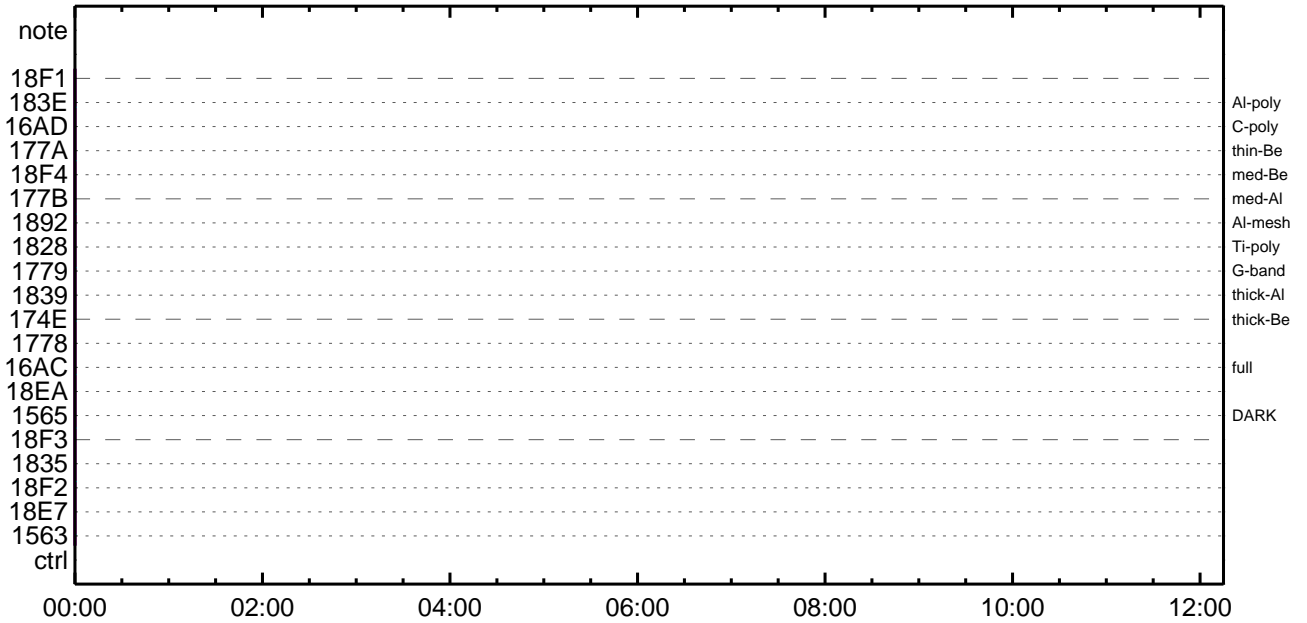
CMDI #0690 2012/07/09



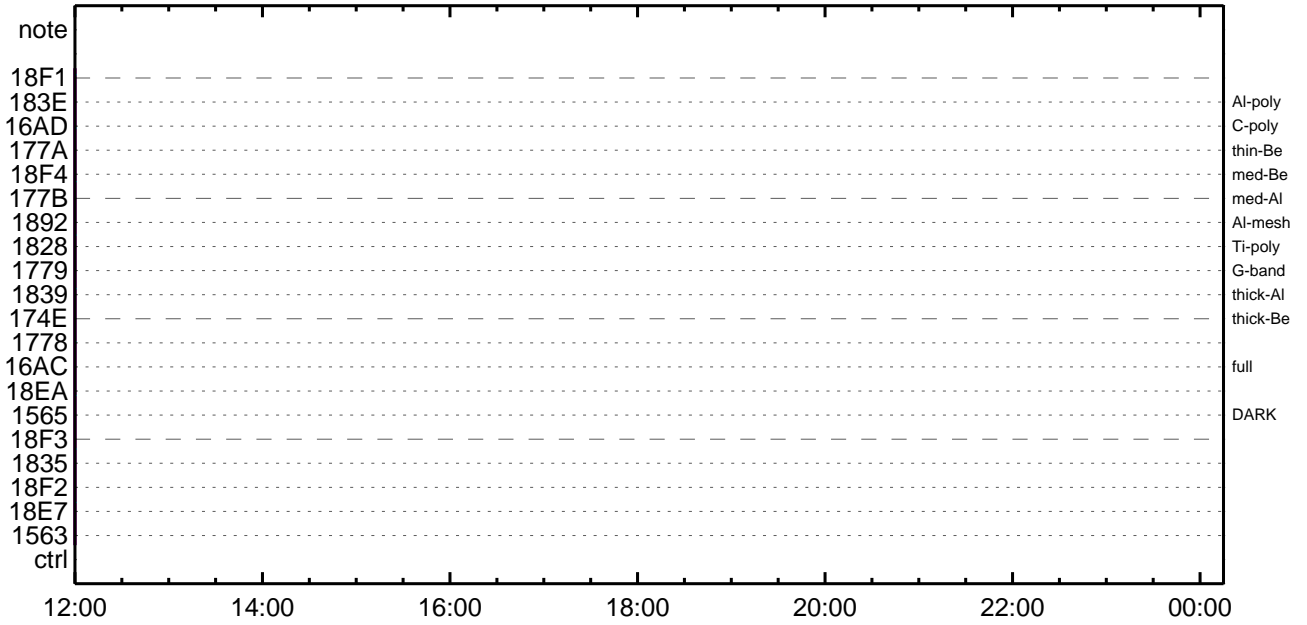
CMDI #0690 2012/07/09



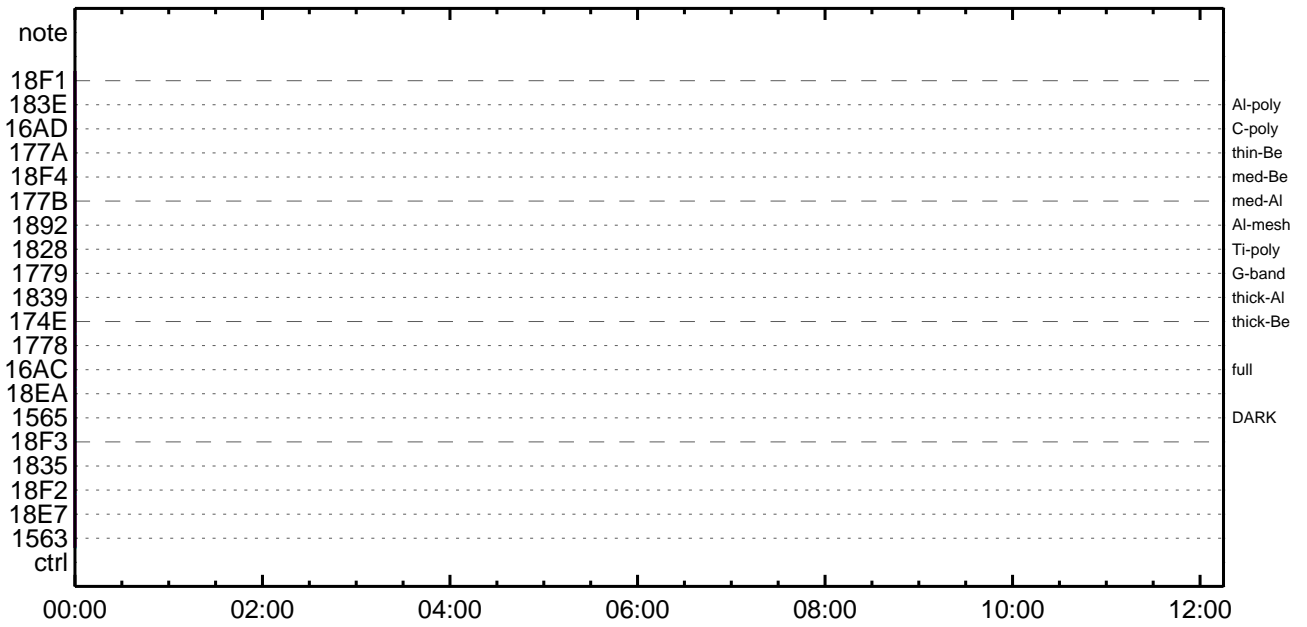
CMDI #0690 2012/07/10



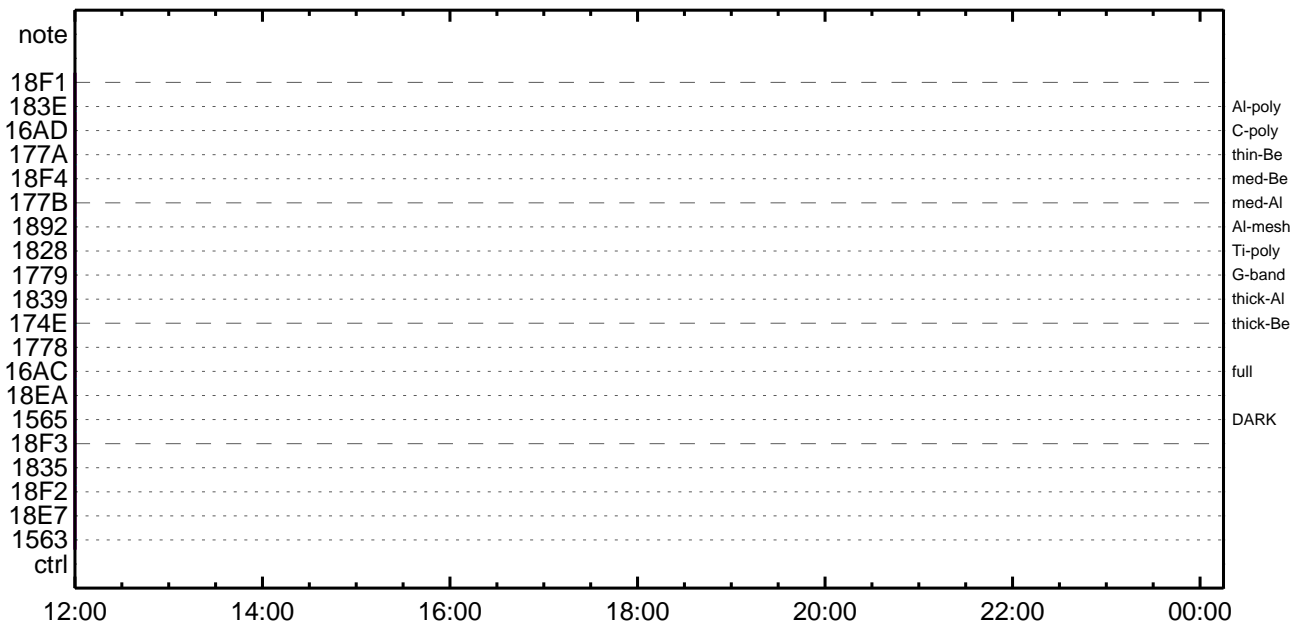
CMDI #0690 2012/07/10



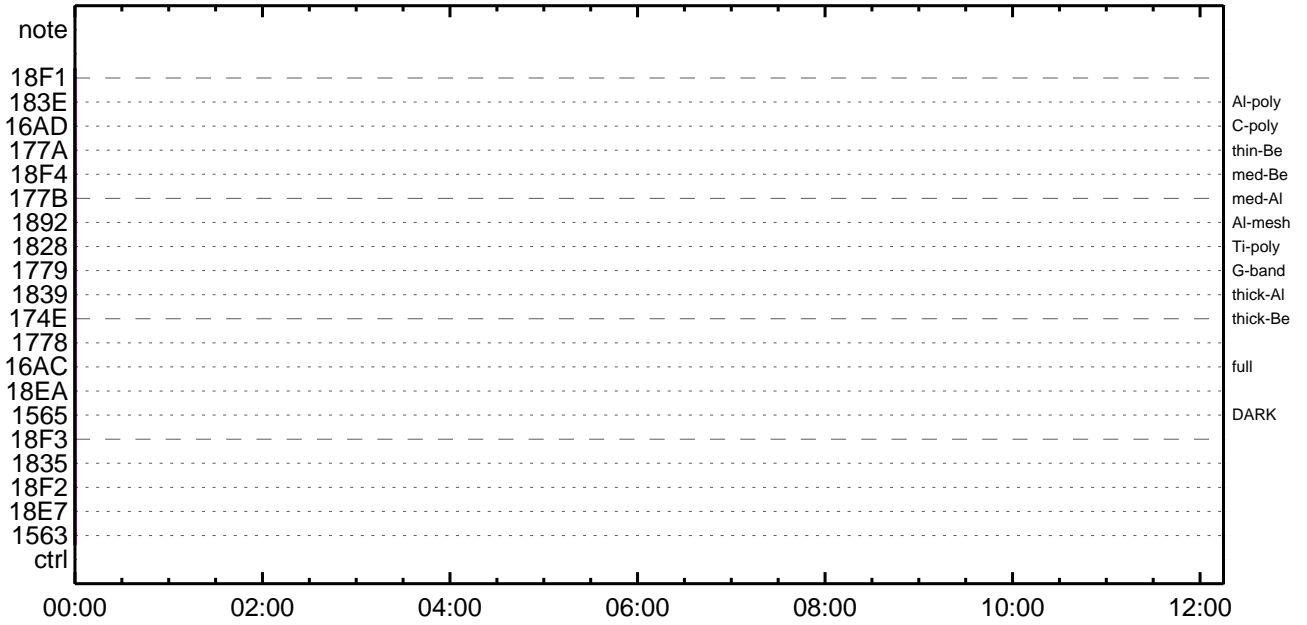
CMDI #0690 2012/07/11



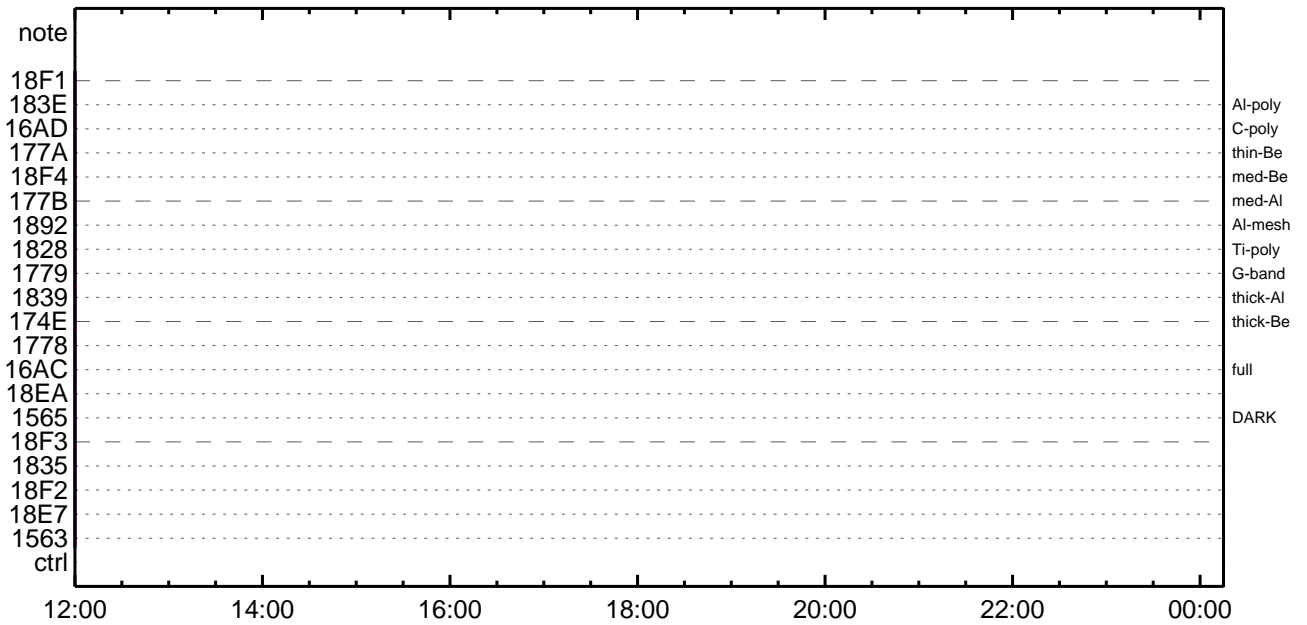
CMDI #0690 2012/07/11



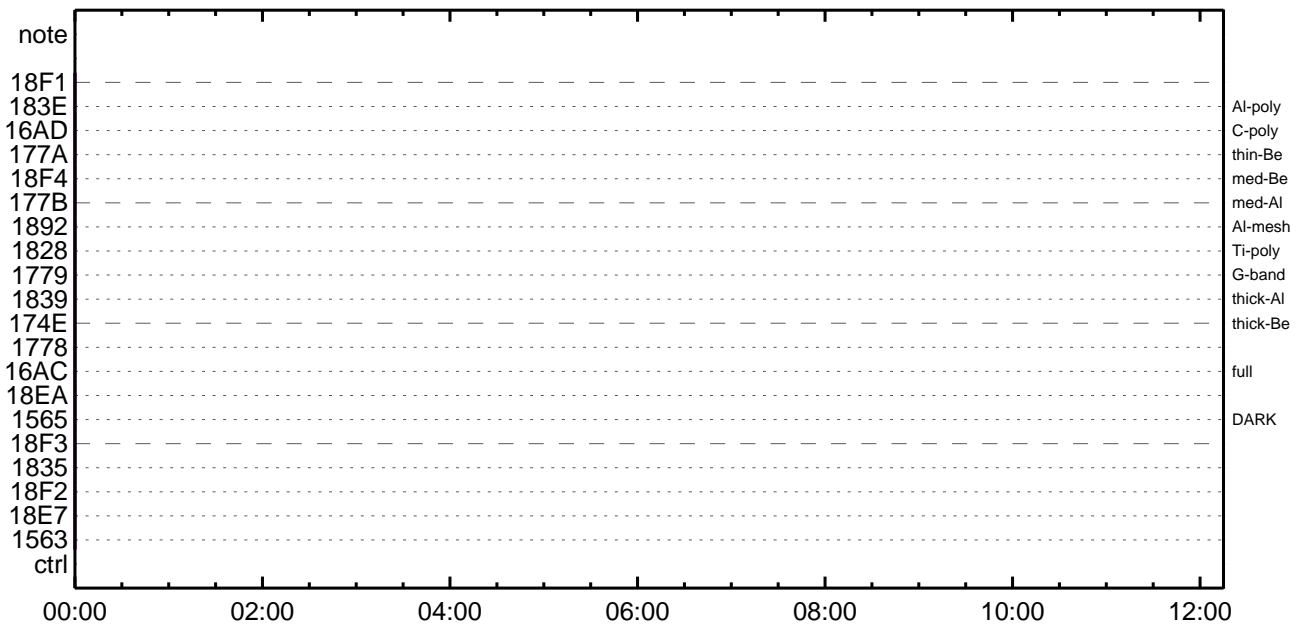
CMDI #0690 2012/07/12



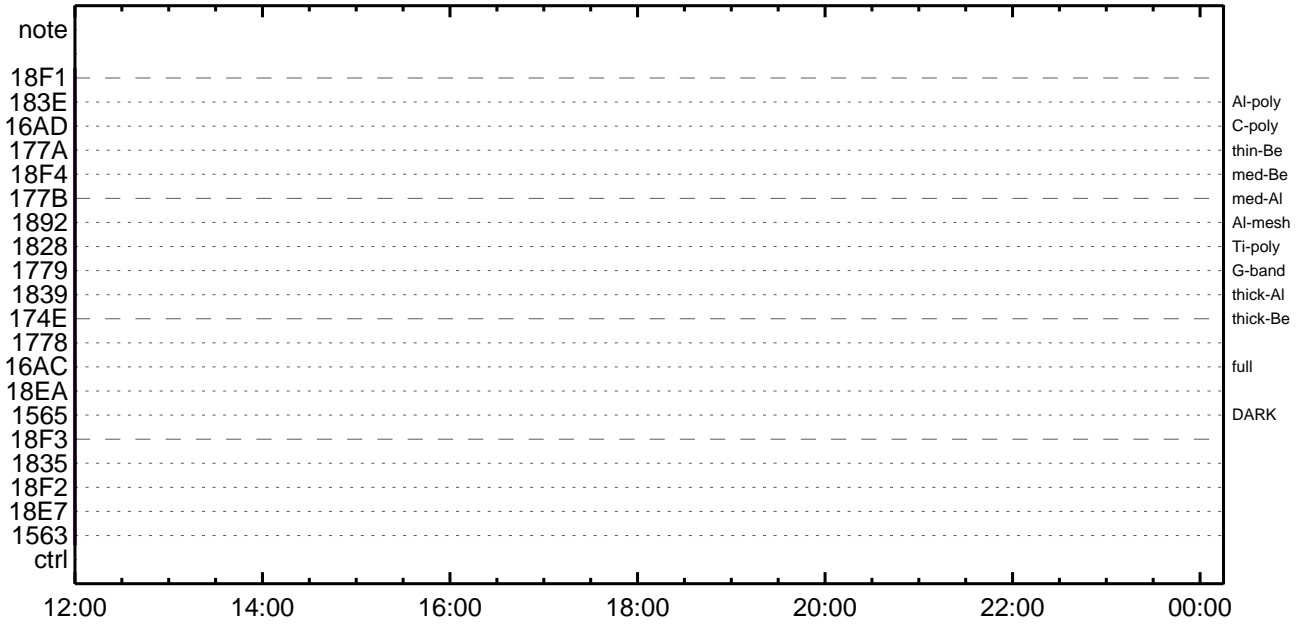
CMDI #0690 2012/07/12



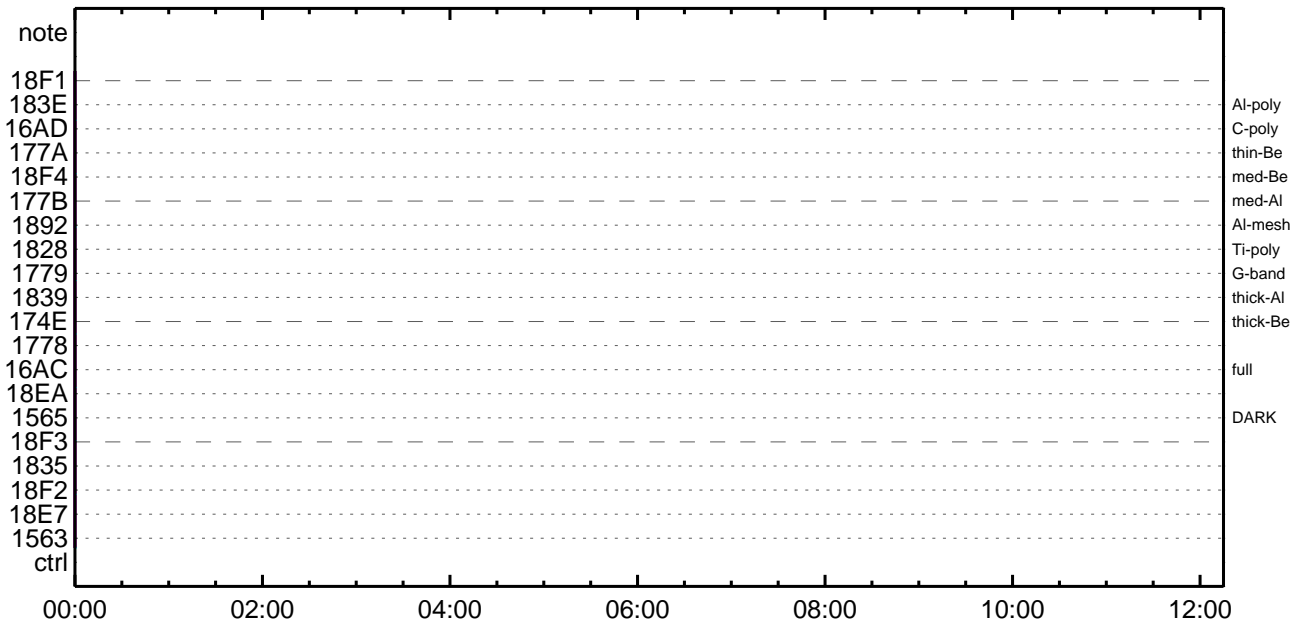
CMDI #0690 2012/07/13



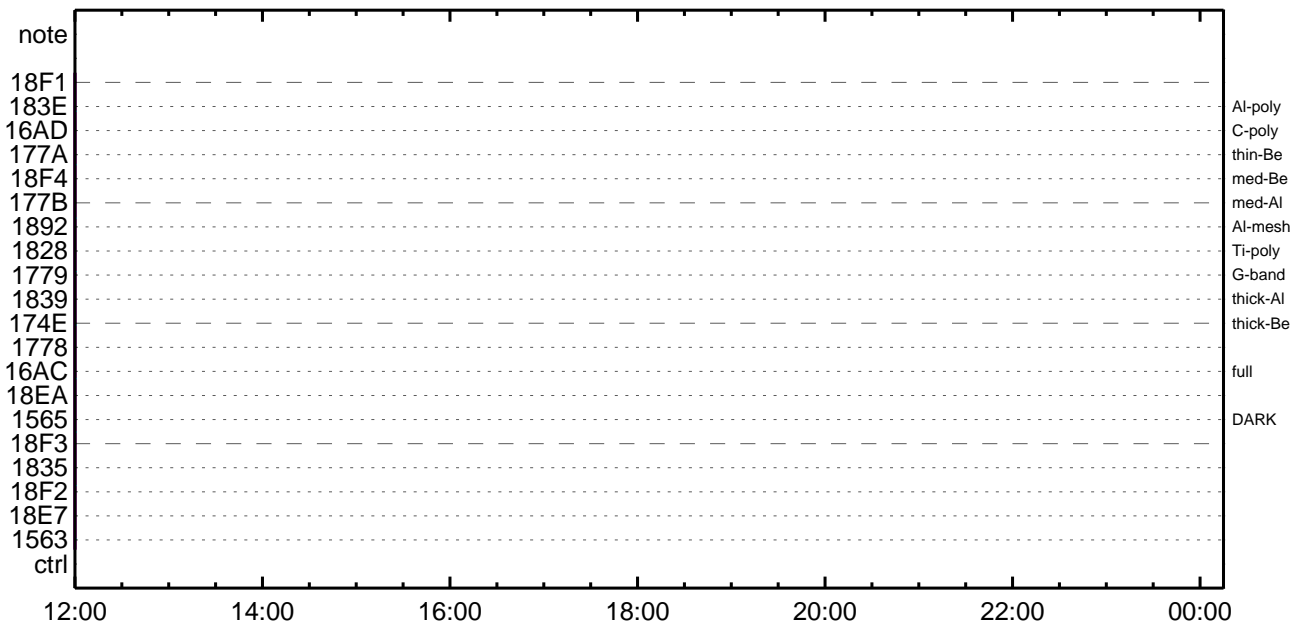
CMDI #0690 2012/07/13



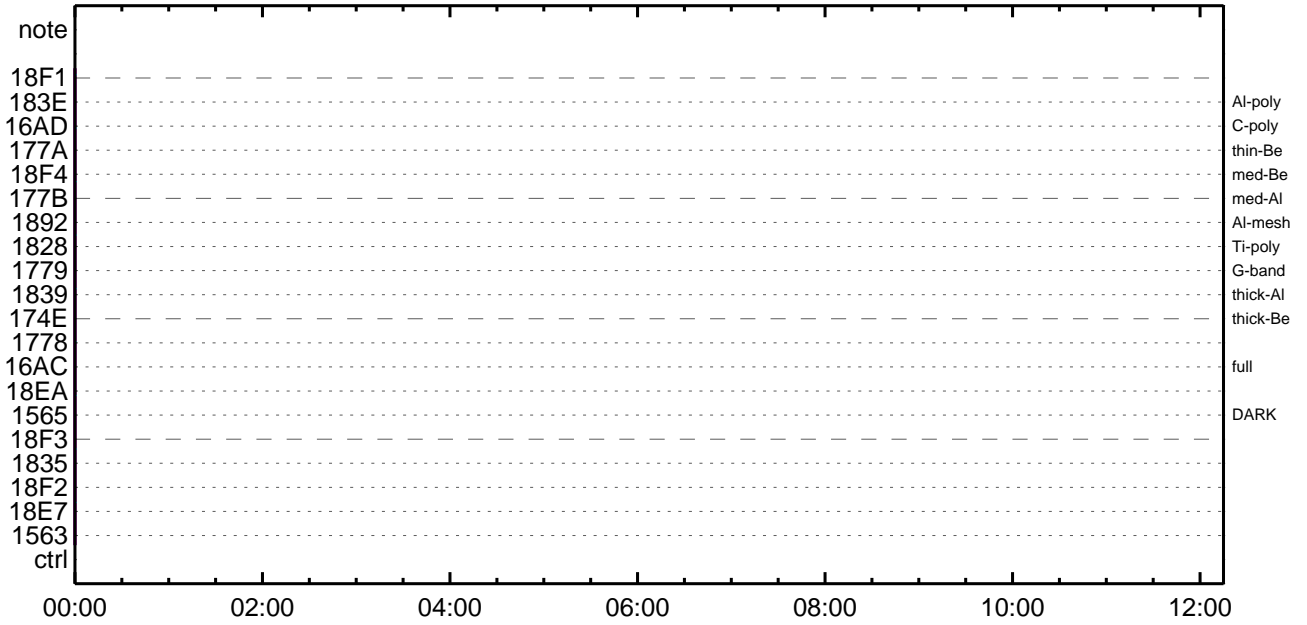
CMDI #0690 2012/07/14



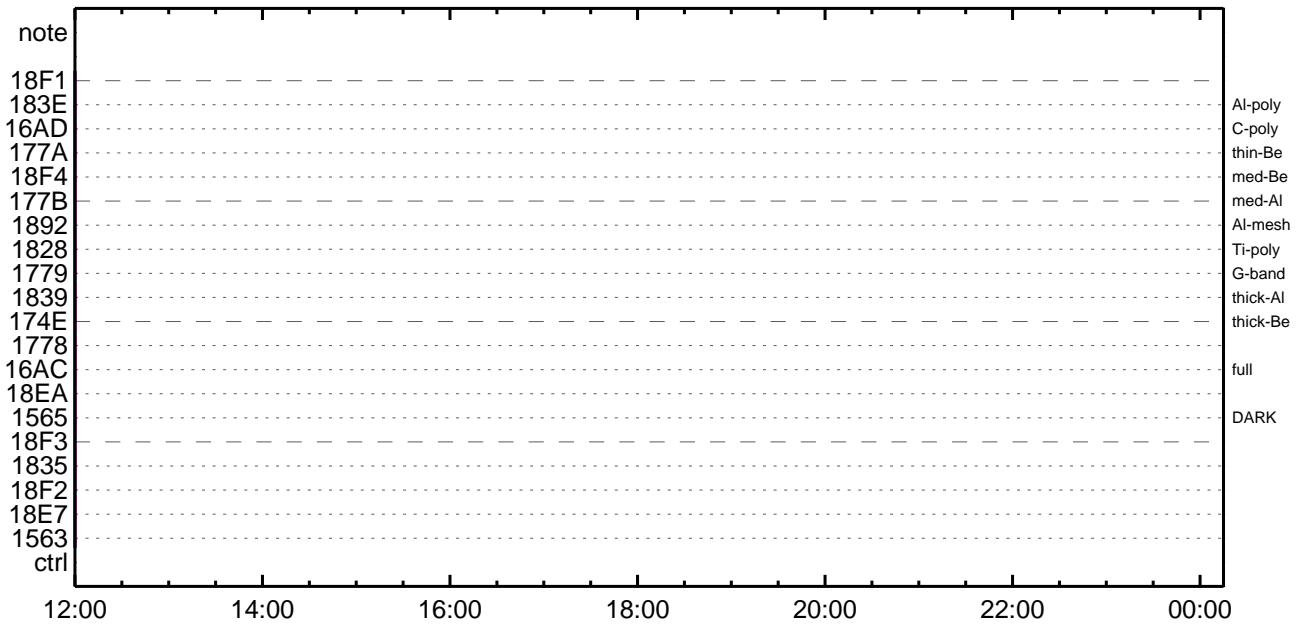
CMDI #0690 2012/07/14



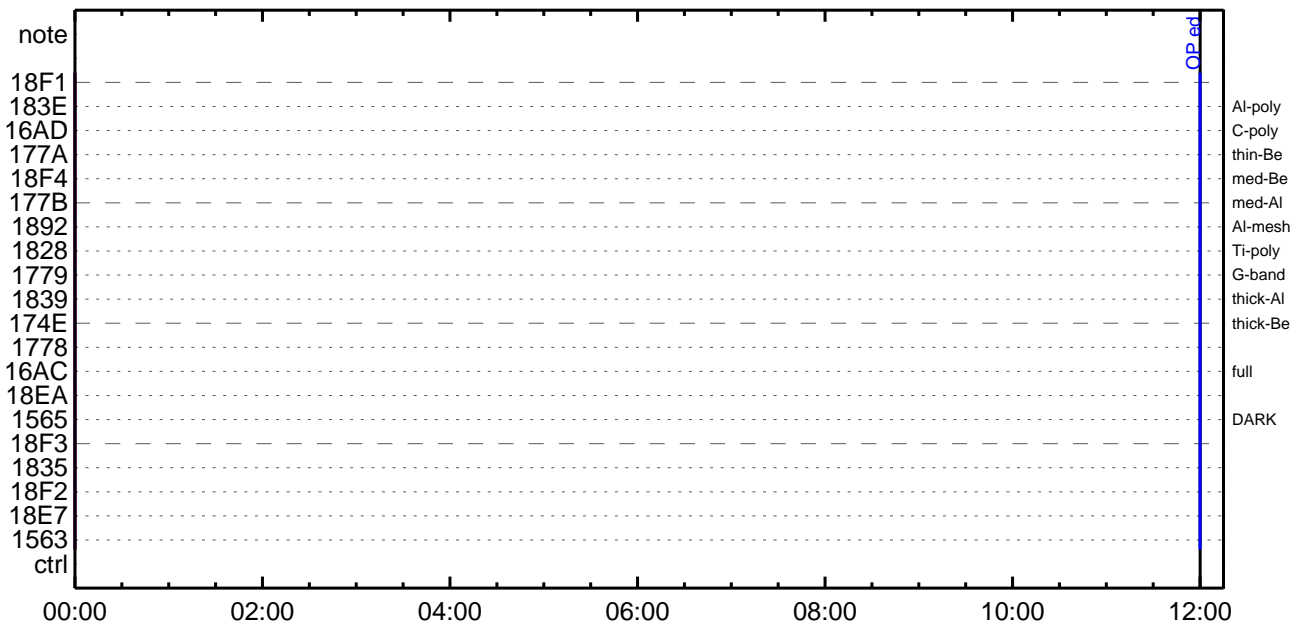
CMDI #0690 2012/07/15

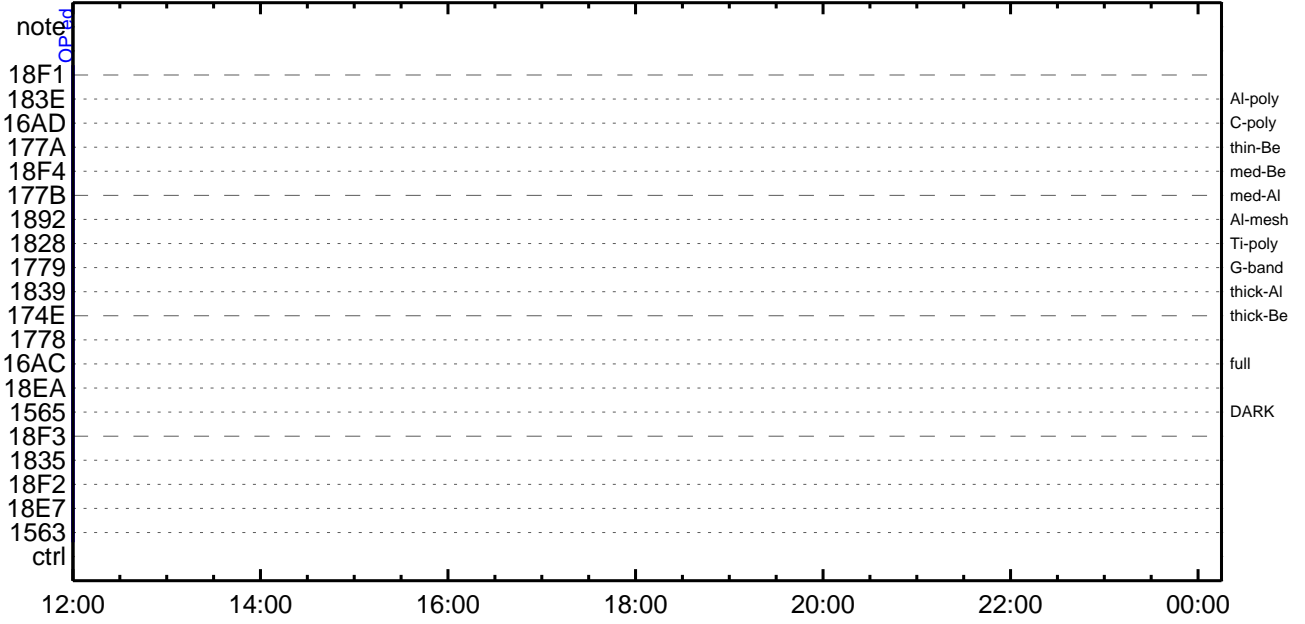


CMDI #0690 2012/07/15



CMDI #0690 2012/07/16






```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0f 80 80 06 06)
0136 + DC 07-F0 MDP_XRT_FLD_DIS
0137 BC (d9)
0138 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0139 BC (c9)
0140 + DC 07-F0 MDP_XRT_AEC_RESET
0141 BC (d0)
0142 + DC 07-F0 MDP_XRT_ARS_DIS
0143 BC (d5)
0144 + DC 07-F0 MDP_XRT_FLD_RESET
0145 BC (da)
0146 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0147 BC (c4 13)
0148 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0149 BC (c5 02)
0150 . C. ----- Success Verify ? OK / NG ____
0151 C.
0152 C.
0153 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0154 C.
0155 +. DC 07-F0 MDP_XRT_MODE_OBSV
0156 BC (c2)
0157 +. TI 2012-06-12 09:43:02.0
0158 DC 07-F0 MDP_XRT_MODE_OBSV
0159 BC (c2)
0160 . C. ----- Success Verify ? OK / NG ____
0161 C.
0162 C. ***** XRT END *****
0163 . C. *****
0164 C. SOT table upload
0165 C. *****
0166 . C. < Stop FG table >
0167 +. DC 07-F0 MDP_FG_CTRL_MANU
0168 BC (51)
0169 . C. -----
0170 C. MDP_FG_CTRL_MODE = MANU [ ]
0171 C. -----
0172 C.
0173 . C. <Upload FG Observation Table>
0174 . S. RAM ram-261:MDP_OBS_F
0175 ( )
0176 C.
0177 . C. < Dump RAMID=MDP_OBS_F >
0178 +. DC 07-F0 MDP_DUMP_FGTBL
0179 BC (82 07 00 00 00 38 b8)
0180 C. -----
0181 C. MDP_OBS_F verify = OK/NG [ ]
0182 C. -----
0183 C.
0184 . C. < Upload DPL table >
0185 C.
0186 C. ¥¢¥Ã¥×¥í;¥É°îÁ°°ÈSTS_CHKαδOFFαÈα¹αē
0187 C.
0188 . S. RAM ram-271:MDP_DPL
0189 ( )
0190 C.
0191 . C. < Dump RAMID=MDP_DPL >
0192 +. DC 07-F0 MDP_DUMP_FGTBL
0193 BC (82 07 00 38 b8 00 40)

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*** OP Sequence for XRT ***

2012/06/12	09:54:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	57	ce	00	72
2012/06/12	10:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/12	11:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	00	00	00	00
2012/06/12	11:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97		00
2012/06/12	11:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0					d8
2012/06/12	11:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2012/06/12	11:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0					d0
2012/06/12	11:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2012/06/12	11:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/06/12	11:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 04
2012/06/12	11:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0					c5 02
2012/06/12	11:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/06/12	11:36:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/12	11:36:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/06/12	11:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/06/12	11:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/06/12	12:06:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/12	12:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/06/12	13:14:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/12	13:14:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/06/12	13:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/06/12	13:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/06/12	13:45:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/12	13:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/06/12	14:53:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/12	14:53:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/06/12	14:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/06/12	14:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/06/12	15:31:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/12	15:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/06/12	15:34:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/12	15:34:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/06/12	15:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/06/12	15:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/06/12	15:40:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/12	15:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/06/12	16:31:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/12	16:31:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/06/12	16:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/06/12	16:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/06/12	17:17:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/12	17:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/06/12	17:54:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/12	17:54:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2012/06/12	17:55:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2012/06/12	17:55:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2012/06/12	17:55:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2012/06/12	17:55:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0					d5

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2012/06/12	17:57:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2012/06/12	17:58:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/12	18:04:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/12	22:04:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/12	22:05:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	ed 40 b1 0f			
2012/06/12	22:07:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2012/06/12	22:07:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/06/12	22:07:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/06/12	22:07:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/06/12	22:07:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/12	22:07:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/12	22:07:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2012/06/12	22:07:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2012/06/12	22:08:00.5	XRT_Custom_418_OG [0x1a2]							
2012/06/12	22:09:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/12	23:05:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/12	23:05:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/12	23:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/12	23:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/12	23:36:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/12	23:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/13	00:44:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/13	00:44:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/13	00:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/13	00:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/13	01:14:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/13	01:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/13	02:22:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/13	02:22:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/13	02:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/13	02:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/13	02:52:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/13	02:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/13	03:50:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/13	03:50:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/13	03:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/13	03:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/13	04:31:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/13	04:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/13	05:31:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/13	05:31:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/13	05:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/13	05:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/13	06:09:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/13	06:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/13	06:15:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/13	06:15:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2012/06/13	06:15:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00 00 00 00			
2012/06/13	06:15:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/06/13	06:15:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							

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2012/06/13	06:15:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/06/13	06:18:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07		
2012/06/13	06:18:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/13	06:25:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/13	06:25:30.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	ed	40	b1 0f
2012/06/13	06:27:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2012/06/13	06:28:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/06/13	06:28:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/06/13	06:28:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/06/13	06:28:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/06/13	06:28:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/13	06:28:26.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2012/06/13	06:28:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2012/06/13	06:28:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/13	07:11:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/13	07:11:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/13	07:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/13	07:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/13	07:48:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/13	07:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/13	07:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/13	08:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00	57	ce	00 72
2012/06/13	10:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/13	11:00:00.0	AOCS_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	03	00	00	00 00
2012/06/13	11:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2012/06/13	11:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/06/13	11:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/06/13	11:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/06/13	11:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/06/13	11:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/13	11:02:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2012/06/13	11:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2012/06/13	11:05:01.0	XRT_Custom_418_OG [0x1a2]							
2012/06/13	11:06:01.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/13	12:13:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/13	12:13:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/13	12:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/13	12:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/13	12:43:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/13	12:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/13	13:52:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/13	13:52:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/13	13:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/13	13:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/13	14:28:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/13	14:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/13	15:30:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/13	15:30:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			

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2012/06/13	15:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/13	15:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/13	16:17:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/13	16:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/13	17:09:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/13	17:09:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/13	17:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/13	17:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/13	17:55:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/13	17:55:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/06/13	17:56:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/06/13	17:56:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/06/13	17:56:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/06/13	17:56:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/13	17:58:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2012/06/13	17:59:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/13	18:05:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/13	18:06:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/06/13	18:08:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/06/13	18:08:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/06/13	18:08:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/06/13	18:08:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/06/13	18:08:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/13	18:08:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/13	18:08:56.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/06/13	18:08:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2012/06/13	18:09:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/13	18:47:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/13	18:47:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/13	18:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/13	18:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/13	19:31:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/13	19:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/13	20:26:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/13	20:26:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/13	20:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/13	20:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/13	21:08:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/13	21:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/13	22:04:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/13	22:04:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/13	22:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/13	22:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/13	22:44:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/13	22:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/13	23:43:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/13	23:43:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/13	23:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/13	23:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							

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2012/06/14	00:13:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/14	00:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2012/06/14	01:21:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/14	01:21:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/14	01:21:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/14	01:24:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/14	01:51:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/14	01:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2012/06/14	02:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/14	02:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/14	02:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/14	02:58:34.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/14	03:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/14	03:30:00.0	XRT_Custom_418_OG [0x1a2]								
2012/06/14	03:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2012/06/14	04:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/14	04:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/14	04:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/14	04:28:34.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/14	04:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/14	05:08:30.0	XRT_Custom_418_OG [0x1a2]								
2012/06/14	05:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2012/06/14	05:41:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/14	05:41:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/14	05:41:30.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/06/14	05:41:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00				
2012/06/14	05:41:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/06/14	05:41:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/06/14	05:44:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/14	05:44:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2012/06/14	05:51:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/14	05:51:30.0	AOCS_Ore-point_Start_5_OG [0x09b]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/14	05:53:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	03 00 00 00 00				
2012/06/14	05:54:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/06/14	05:54:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/06/14	05:54:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/06/14	05:54:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/06/14	05:54:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/14	05:54:26.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/14	05:54:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/06/14	05:54:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2012/06/14	06:09:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/14	06:09:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/14	06:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/14	06:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/14	06:47:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/14	06:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
2012/06/14	07:49:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/14	07:49:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/14	07:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/14	07:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				

2012/06/14	07:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/06/14	08:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 57 ce 00 72
2012/06/14	09:31:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00