

XRT Timeline to be uploaded on 2012/06/14

Period: 2012/06/14 09:21:00 - 2012/07/19 12:00:00

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Normal mode

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XOB #1835: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 40sec-cad												
Term	Pointing (x, y)							Comment				
06/14 11:03:00 - 06/14 17:31:24	Track (7.7, -468.2) @ 06/14 11:00:00							AR 11507				
PROG= 04 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 41 4-time(s) 2.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 52 45-time(s) 24.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 6.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #18EA: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-band(16)												
Term	Pointing (x, y)							Comment				
06/14 17:34:30 - 06/14 17:41:24	Fixed (0.0, 0.0)							synoptic, shifted -28.5 min, SOT flat field				
06/15 06:03:00 - 06/15 06:09:54	Fixed (0.0, 0.0)							synoptic				
06/15 18:05:30 - 06/15 18:12:24	Fixed (0.0, 0.0)							synoptic, shifted 2.5 min				
06/16 06:32:30 - 06/16 06:39:24	Fixed (0.0, 0.0)							synoptic, shifted 29.5 min				

PROG= 07 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 46 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 69 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 4 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #183E: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad												
Term	Pointing (x, y)							Comment				
06/14 21:46:00 - 06/15 05:59:54	Track (87.3, -468.8) @ 06/14 21:20:00							# AR11507				
06/15 06:13:00 - 06/15 07:59:54	Track (154.7, -469.2) @ 06/15 06:10:00							# AR11507				
06/15 11:03:00 - 06/15 18:02:24	Track (-27.7, 97.9) @ 06/15 11:00:00							AR11506				
06/15 18:15:30 - 06/16 06:29:24	Track (37.9, 97.3) @ 06/15 18:12:30							#AR11506				
06/16 06:42:30 - 06/16 07:25:30	Track (150.7, 96.5) @ 06/16 06:39:30							#AR11506				

PROG= 19 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 41 4-time(s) 2.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 16 30-time(s) 80.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 20.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 20.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 20.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 20.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

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Flare mode

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XOB #18E7: Flare obs. dynamics - Ti_poly high cadence + context (thick-Al-384x384)-15 loops												
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Term	Pointing (x, y)	Comment
06/14 11:03:00 - 06/14 17:31:24	Track (7.7, -468.2) @ 06/14 11:00:00	AR 11507
06/14 21:46:00 - 06/15 05:59:54	Track (87.3, -468.8) @ 06/14 21:20:00	# AR11507
06/15 06:13:00 - 06/15 07:59:54	Track (154.7, -469.2) @ 06/15 06:10:00	# AR11507
06/15 11:03:00 - 06/15 18:02:24	Track (-27.7, 97.9) @ 06/15 11:00:00	AR11506
06/15 18:15:30 - 06/16 06:29:24	Track (37.9, 97.3) @ 06/15 18:12:30	#AR11506
06/16 06:42:30 - 06/16 07:25:30	Track (150.7, 96.5) @ 06/16 06:39:30	#AR11506
PROG= 02 15-time(s)		
└─ Subr= 1 45-time(s) 10.0sec		
└─ Seqn= 92 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Norm 4ms Obs 1x1 384x384 (1024, 1024) DPCM 2 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Norm 4ms Obs 1x1 384x384 (1024, 1024) DPCM 3 0 2.0sec	
└─ Subr= 2 1-time(s) 10.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
Open/thick-Al	Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) DPCM 2 0 2.0sec	
Open/thick-Al	Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) DPCM 3 0 2.0sec	
└─ Seqn= 79 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec	
Open/thick-Al	Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

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Active Region Search

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NOT USED

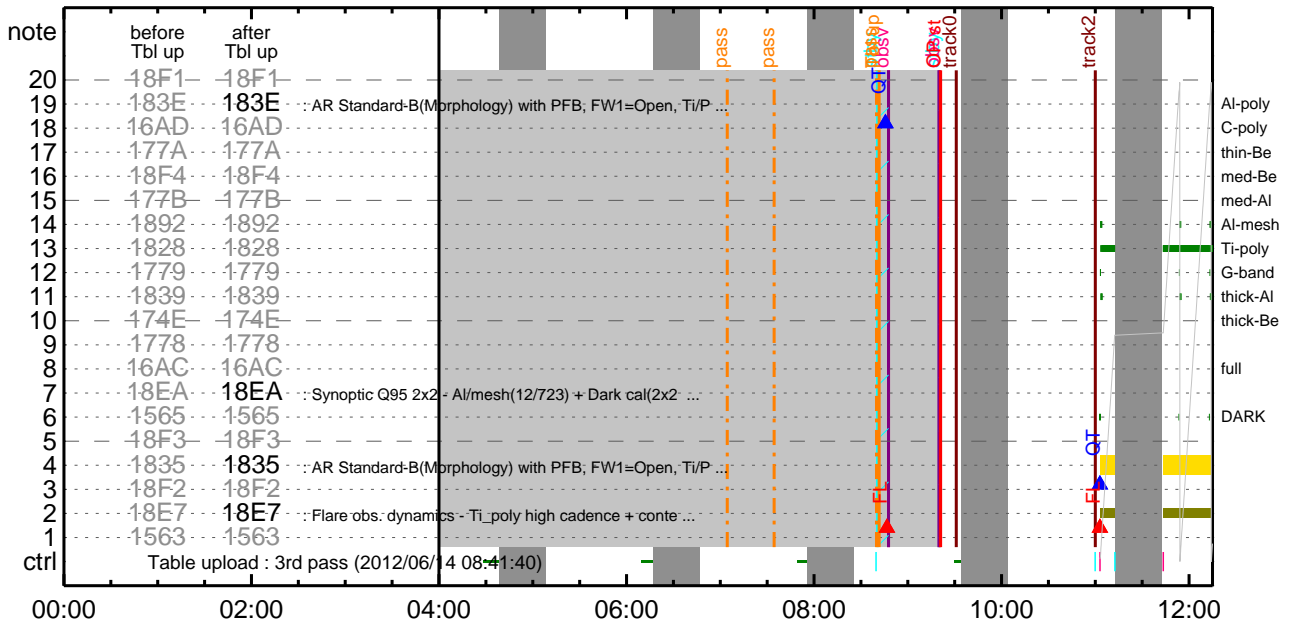
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Flare Detection

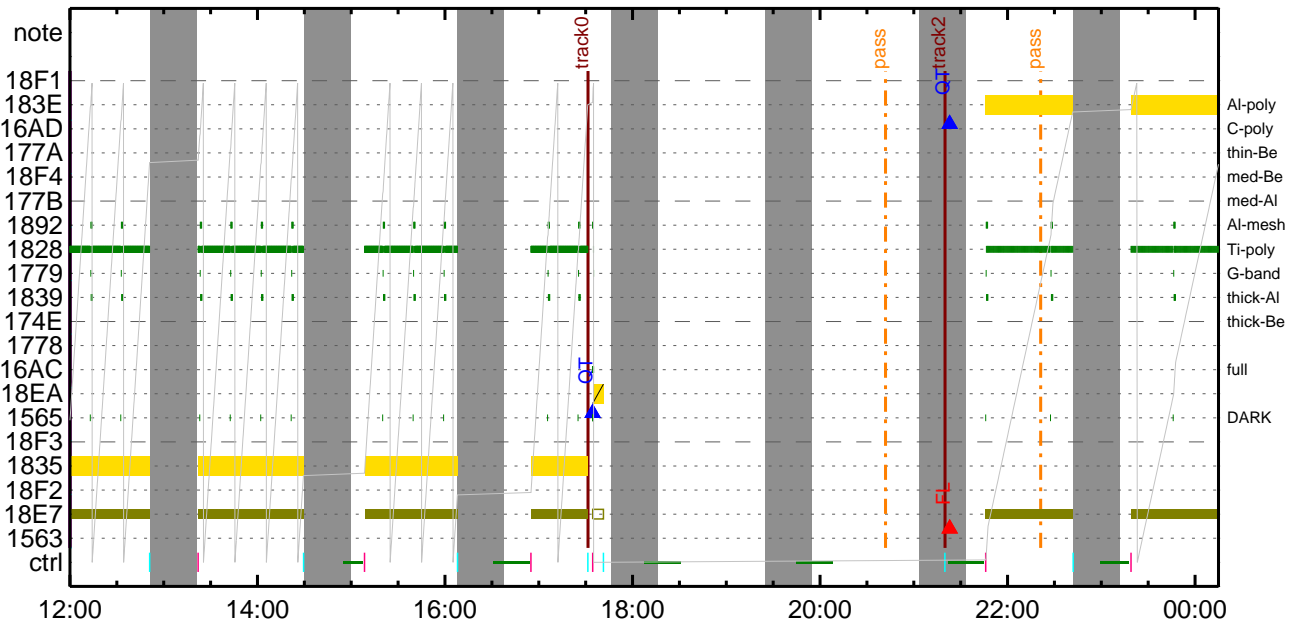
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FLD Patrol											
Term	Pointing (x, y)	Comment									
06/14 11:02:46 - 06/14 17:31:46	Track (7.7, -468.2) @ 06/14 11:00:00	AR 11507									
06/14 21:22:46 - 06/15 06:00:16	Track (87.3, -468.8) @ 06/14 21:20:00	# AR11507									
06/15 06:12:46 - 06/15 18:02:46	Track (154.7, -469.2) @ 06/15 06:10:00	# AR11507									
06/15 18:15:16 - 06/16 06:29:46	Track (37.9, 97.3) @ 06/15 18:12:30	#AR11506									
06/16 06:42:16 - 07/19 12:00:00	Track (150.7, 96.5) @ 06/16 06:39:30	#AR11506									
Open/Ti-poly	Open/thick-Al close Safe Norm 8ms Obs 8x8	Q=50 30sec									
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval										

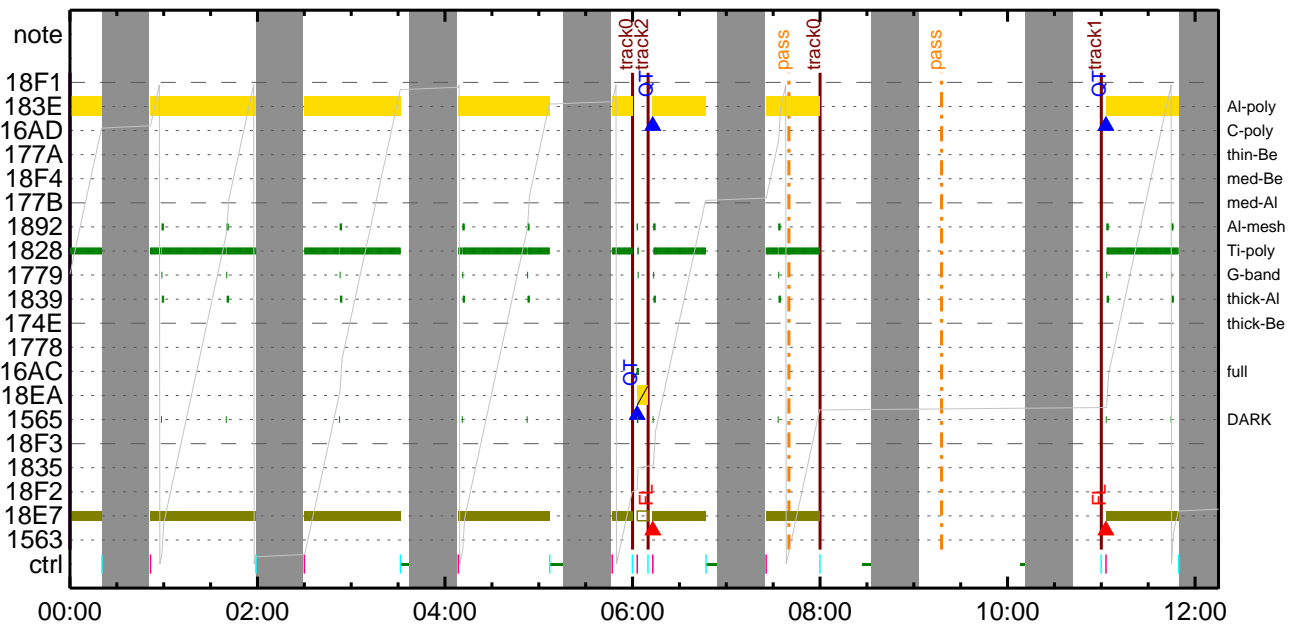
CMDI #0697 2012/06/14



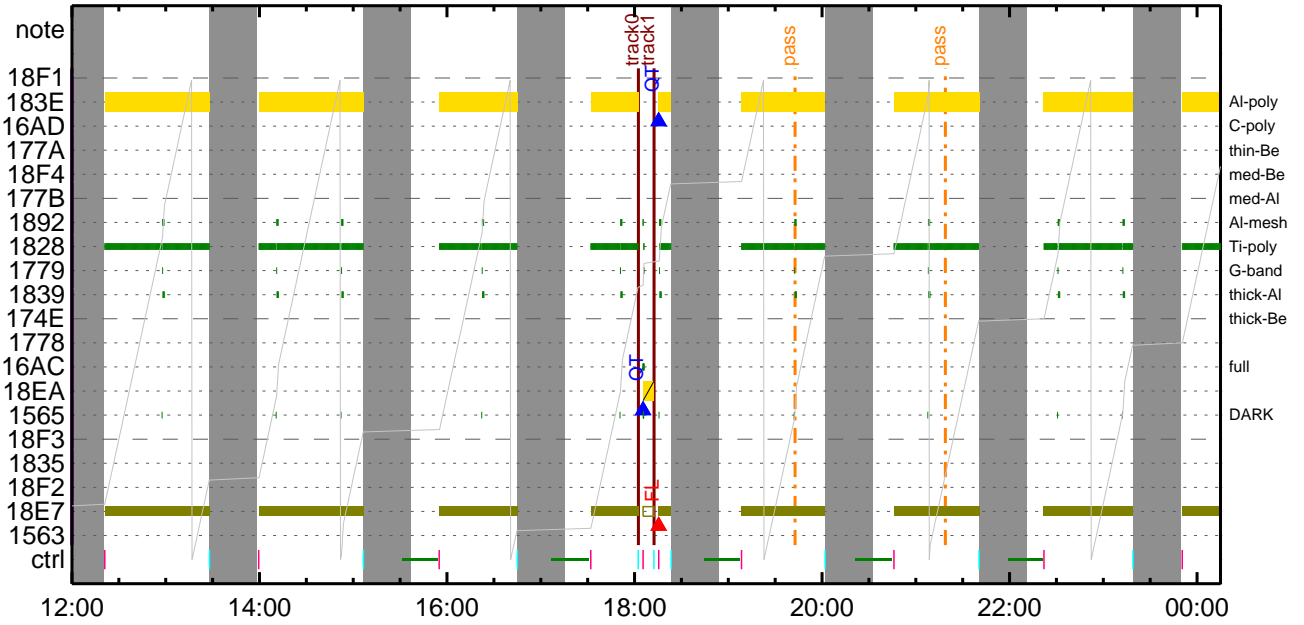
CMDI #0697 2012/06/14



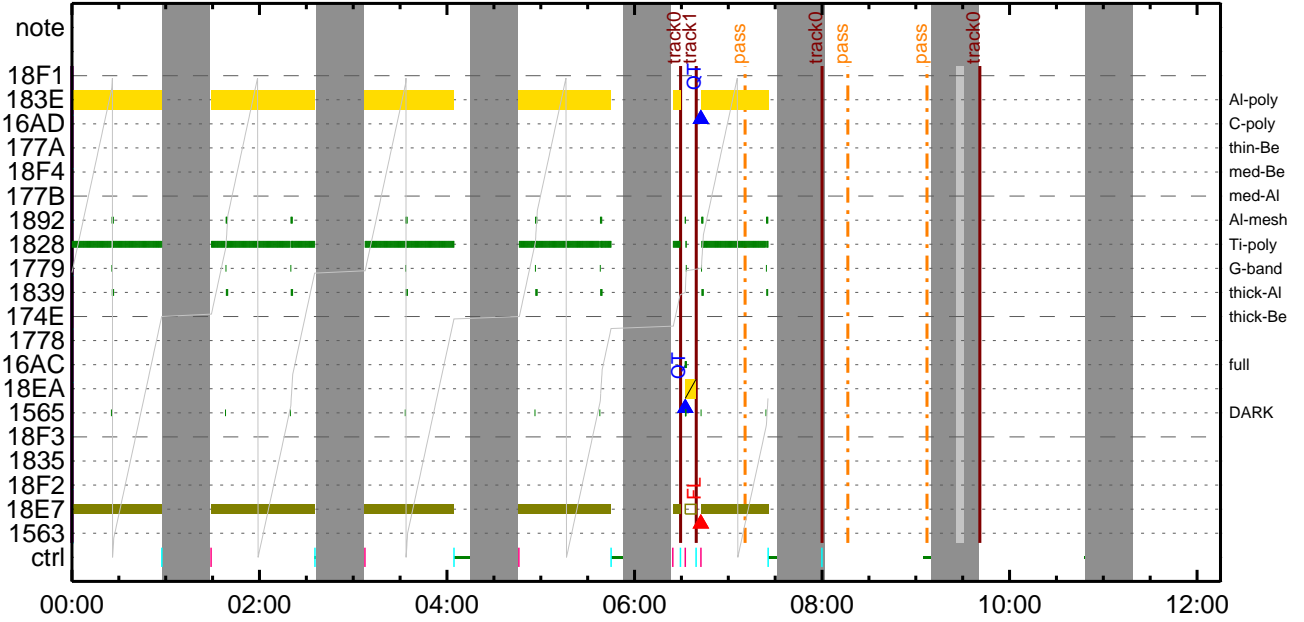
CMDI #0697 2012/06/15



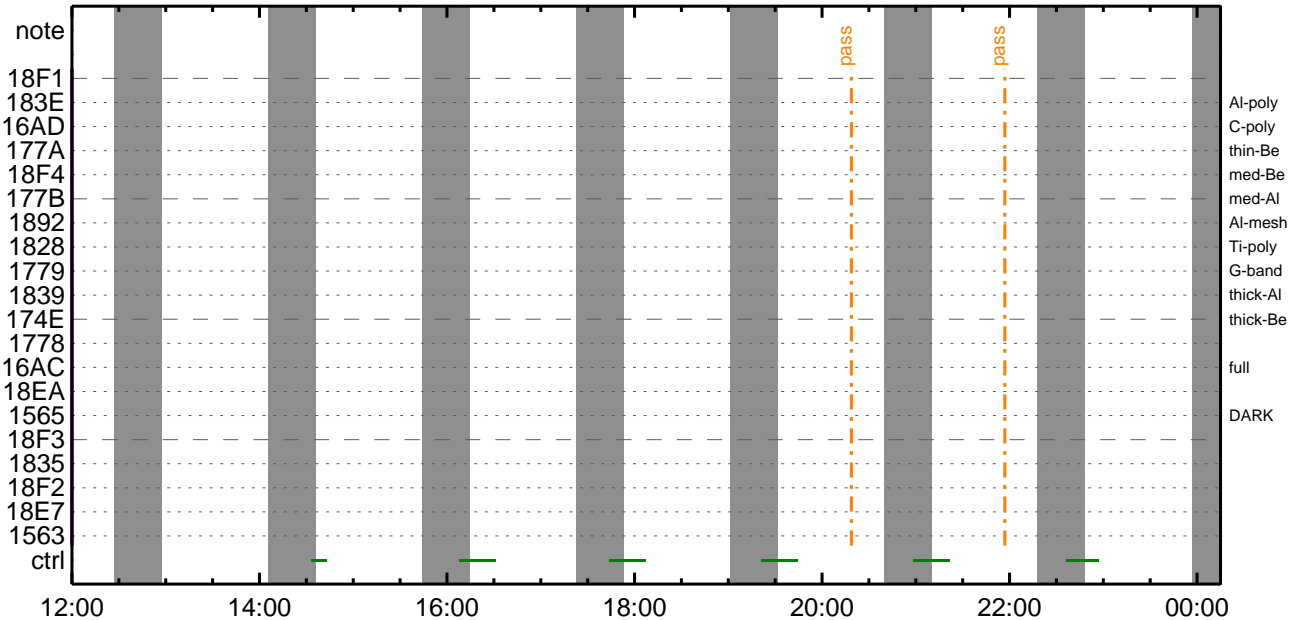
CMDI #0697 2012/06/15



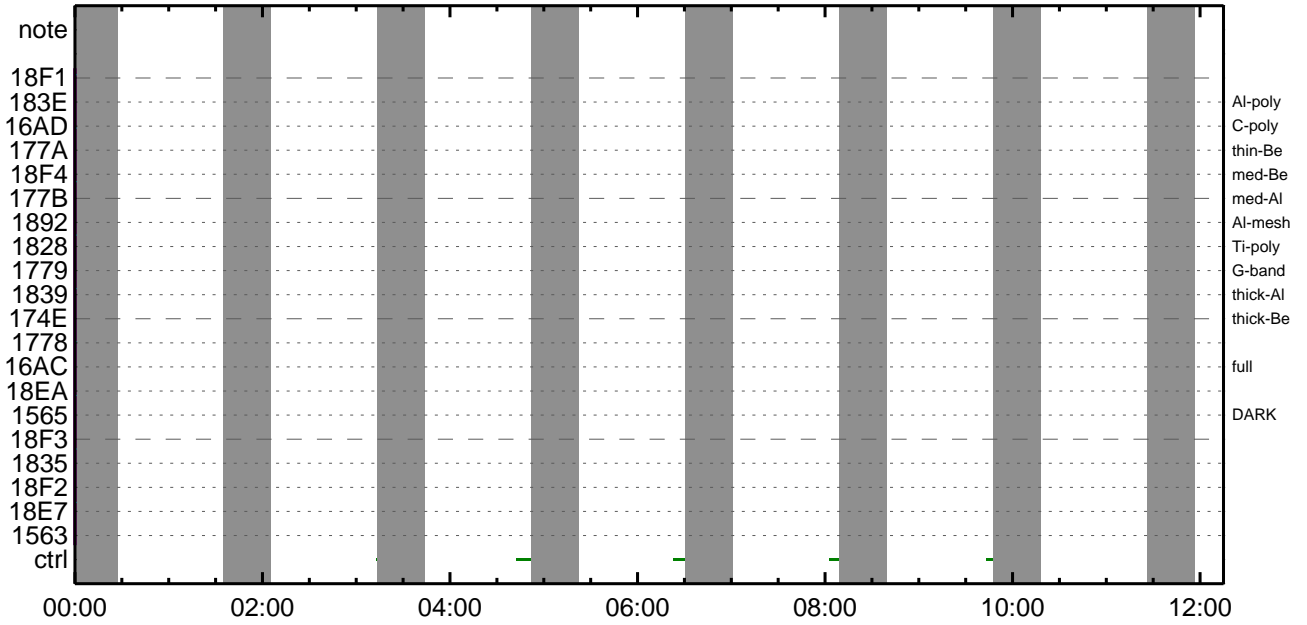
CMDI #0697 2012/06/16



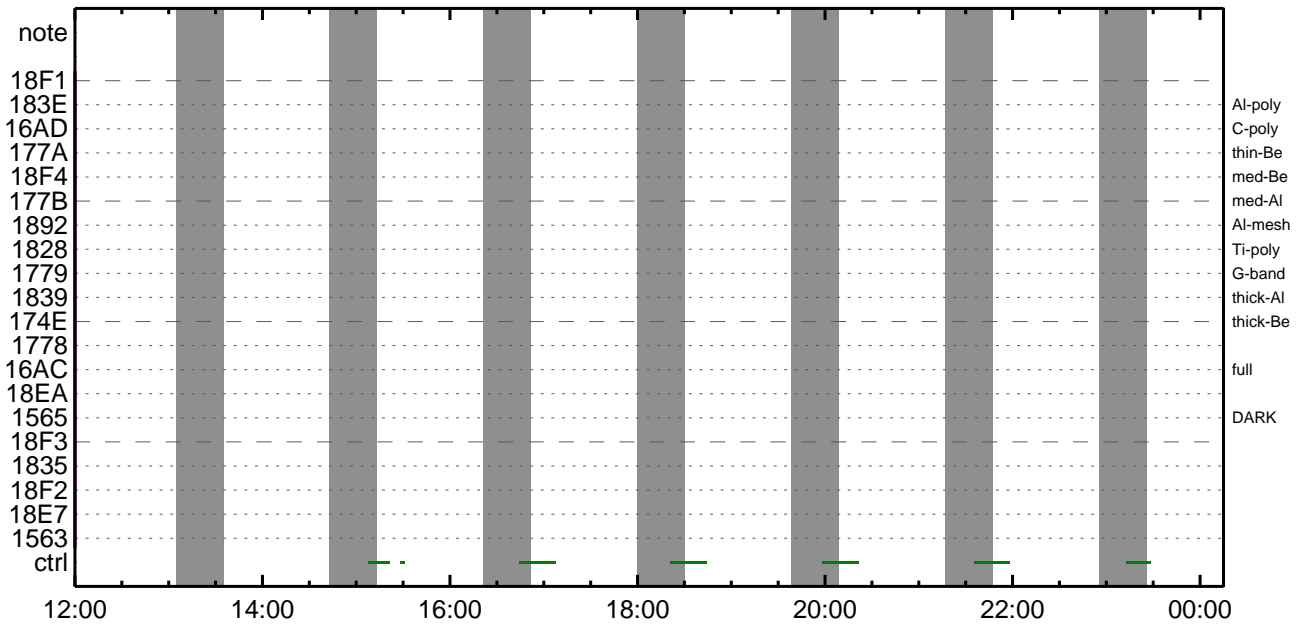
CMDI #0697 2012/06/16



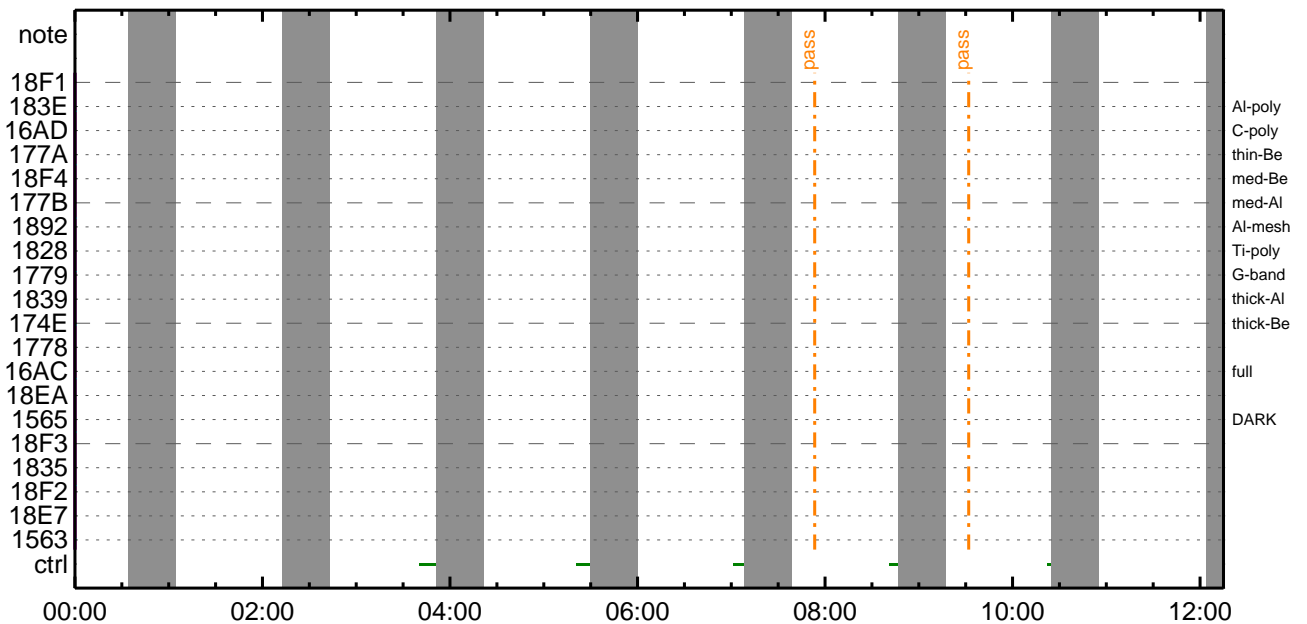
CMDI #0697 2012/06/17



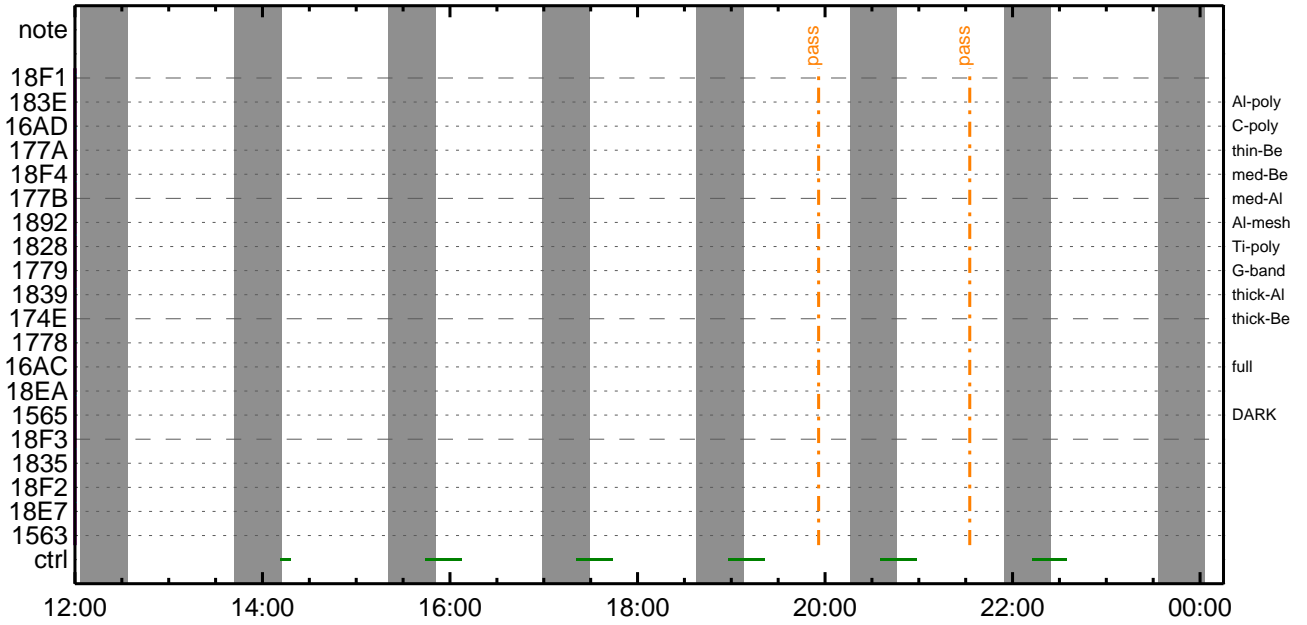
CMDI #0697 2012/06/17



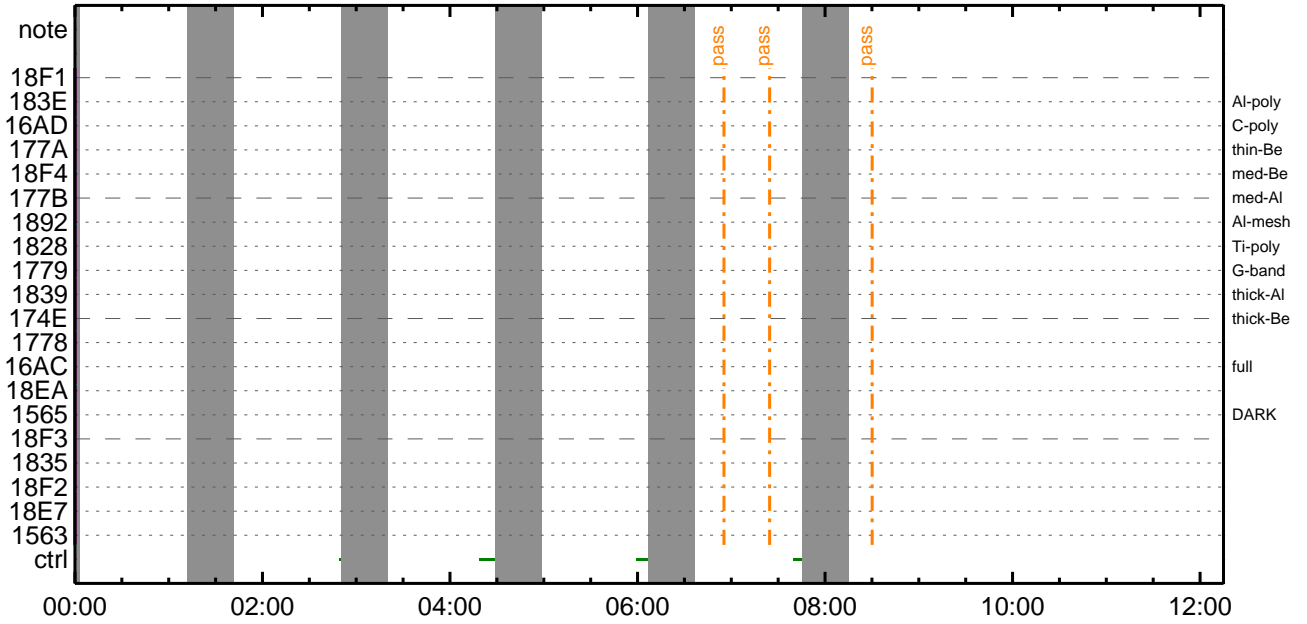
CMDI #0697 2012/06/18



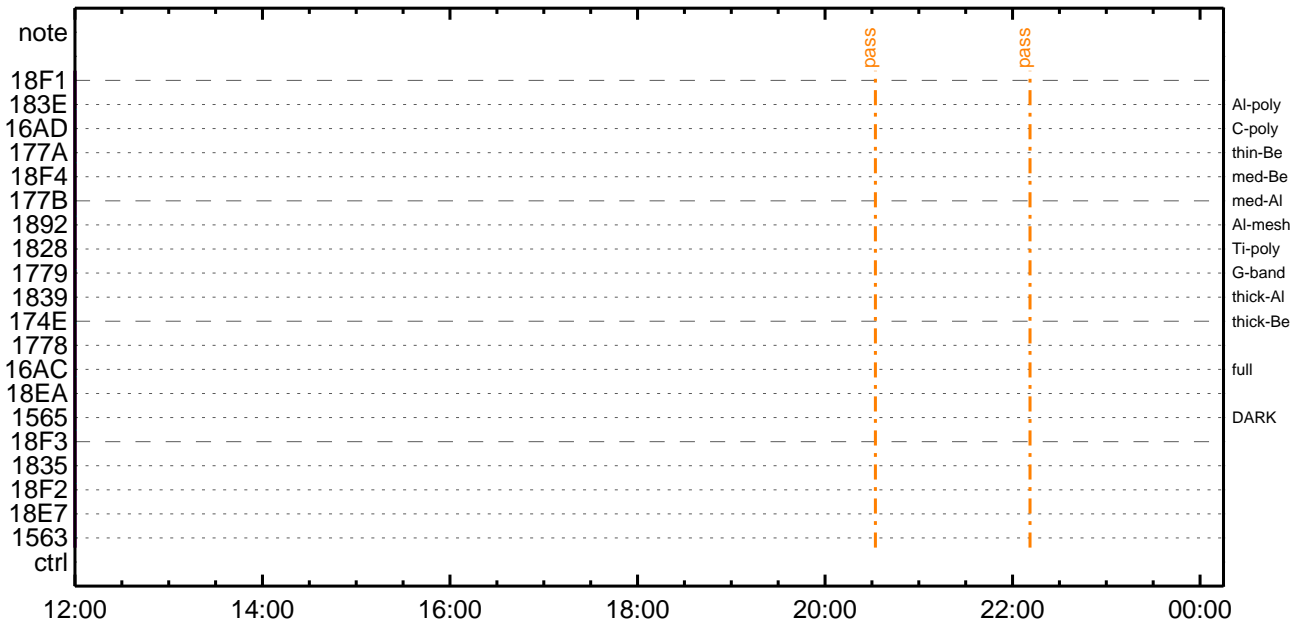
CMDI #0697 2012/06/18



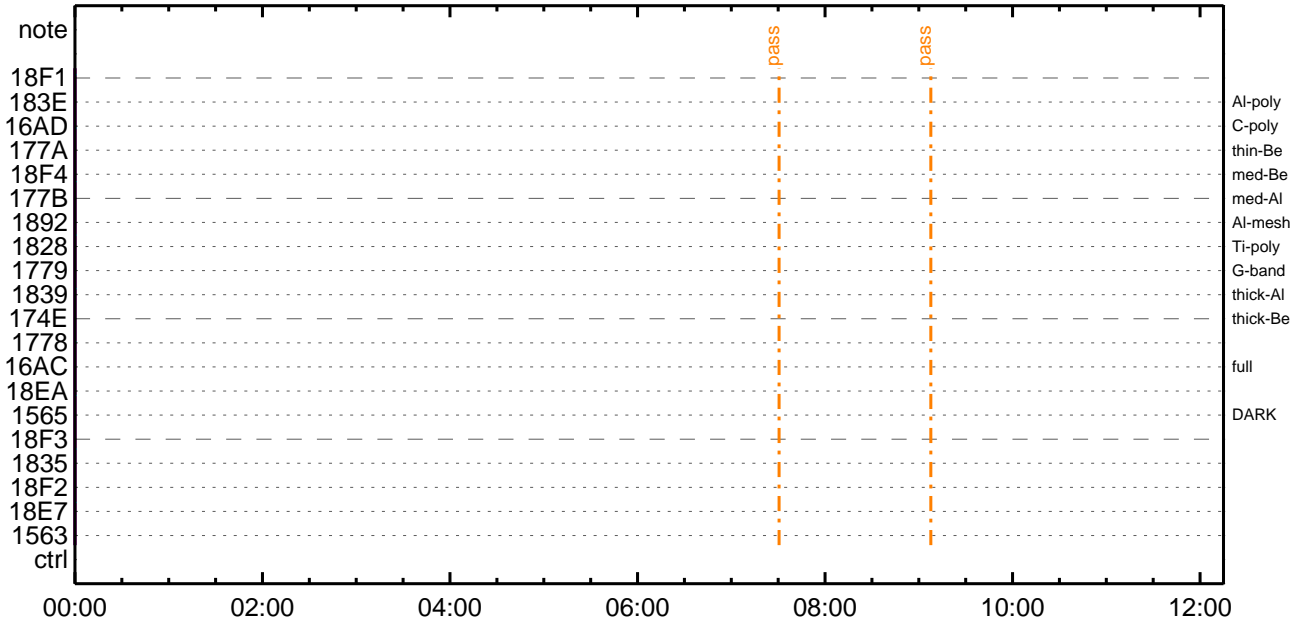
CMDI #0697 2012/06/19



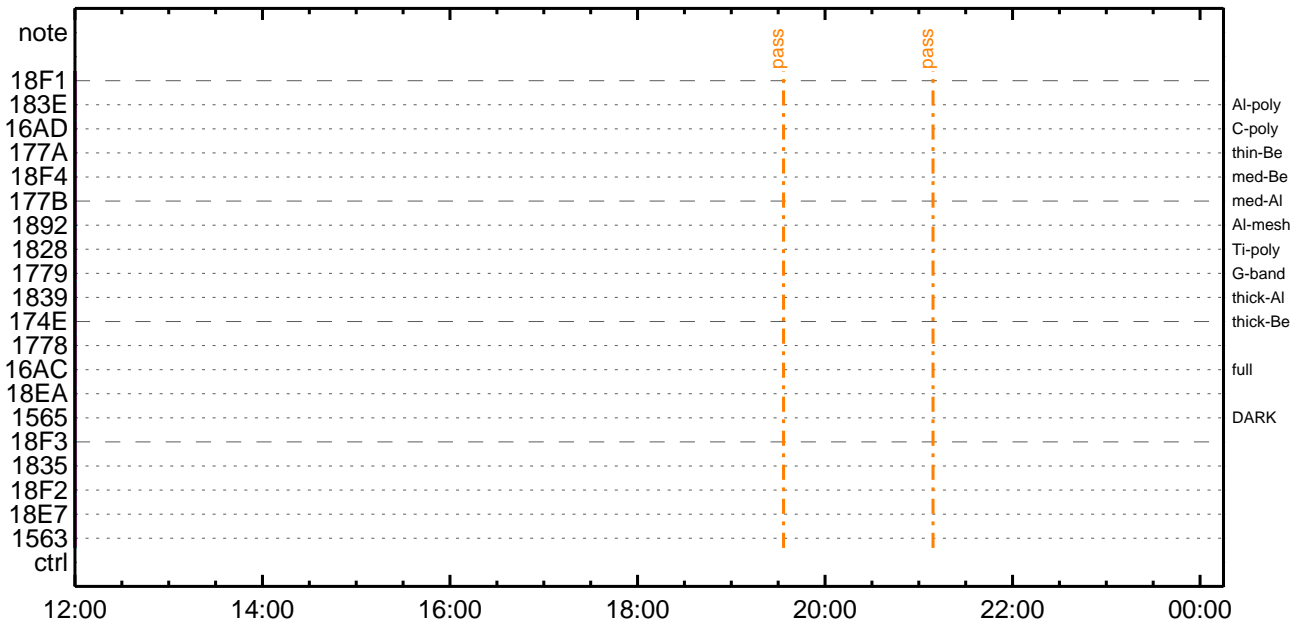
CMDI #0697 2012/06/19



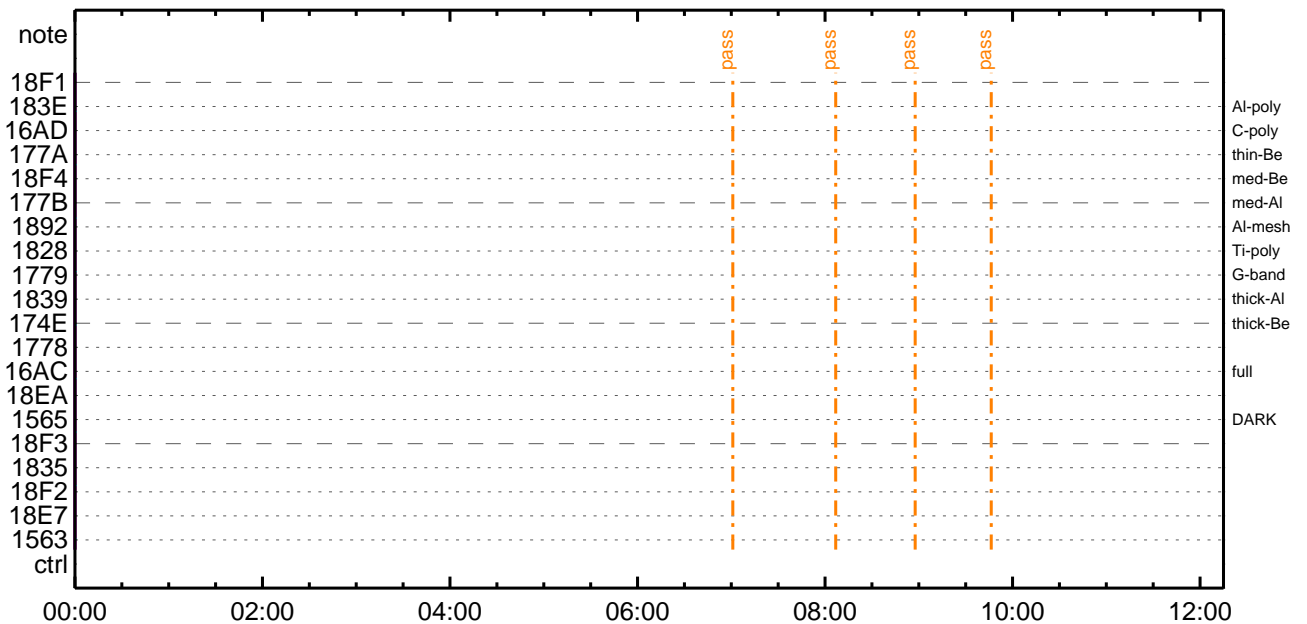
CMDI #0697 2012/06/20



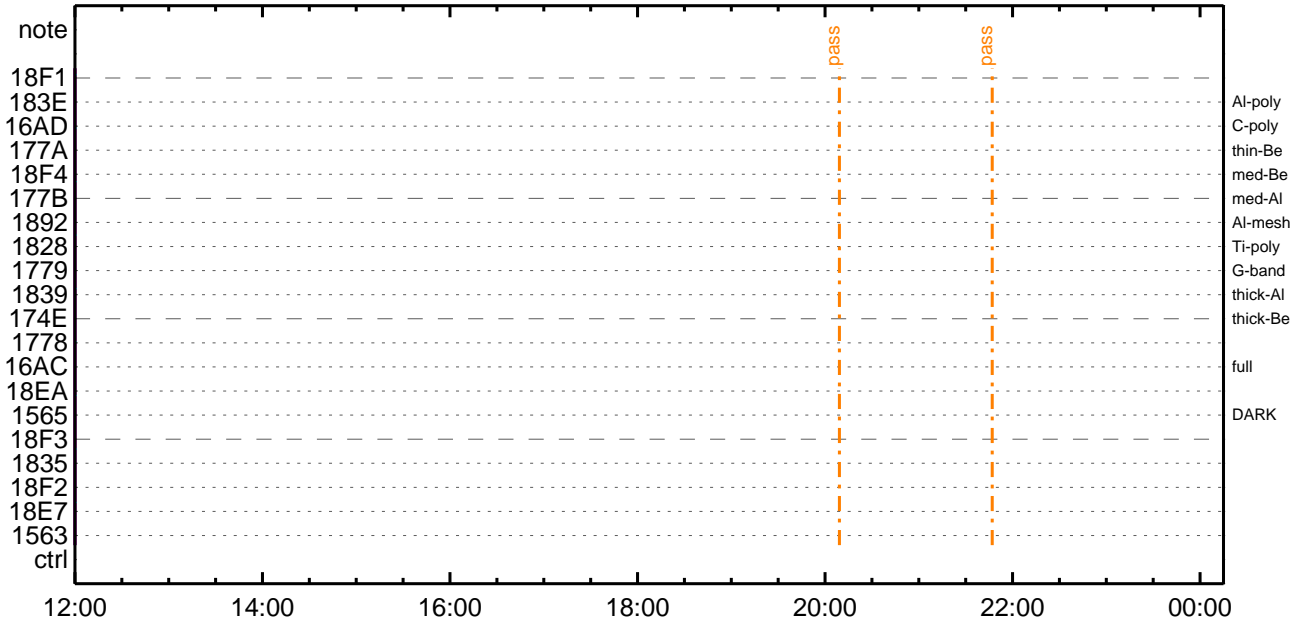
CMDI #0697 2012/06/20



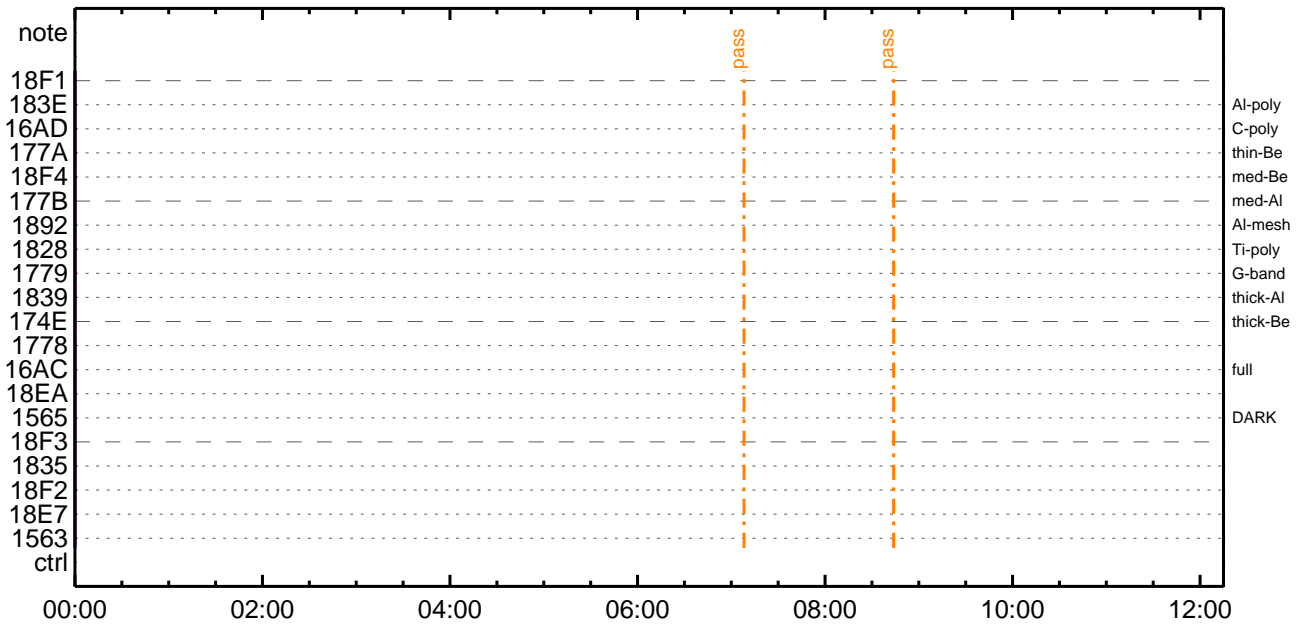
CMDI #0697 2012/06/21



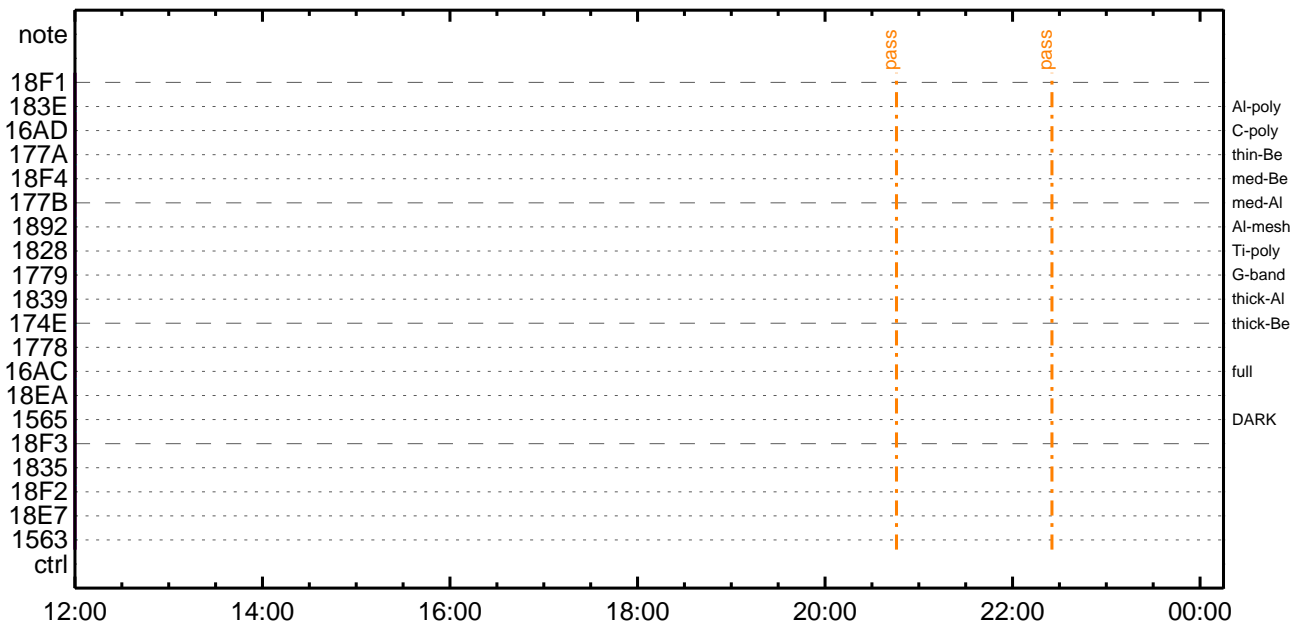
CMDI #0697 2012/06/21



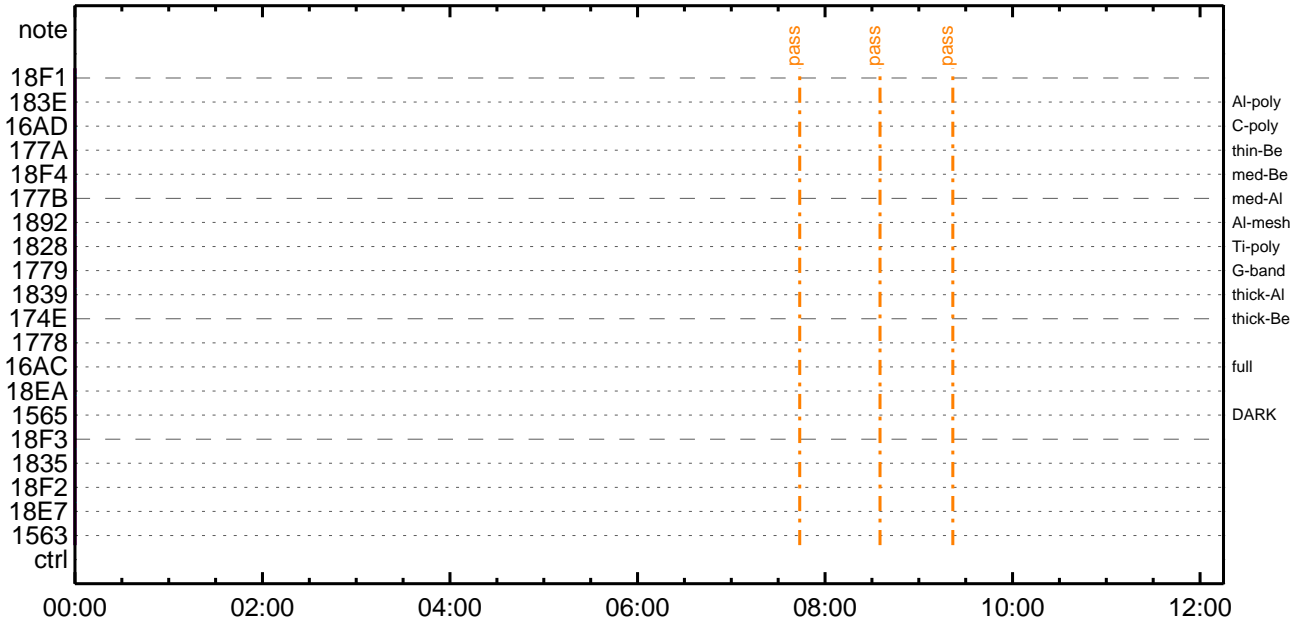
CMDI #0697 2012/06/22



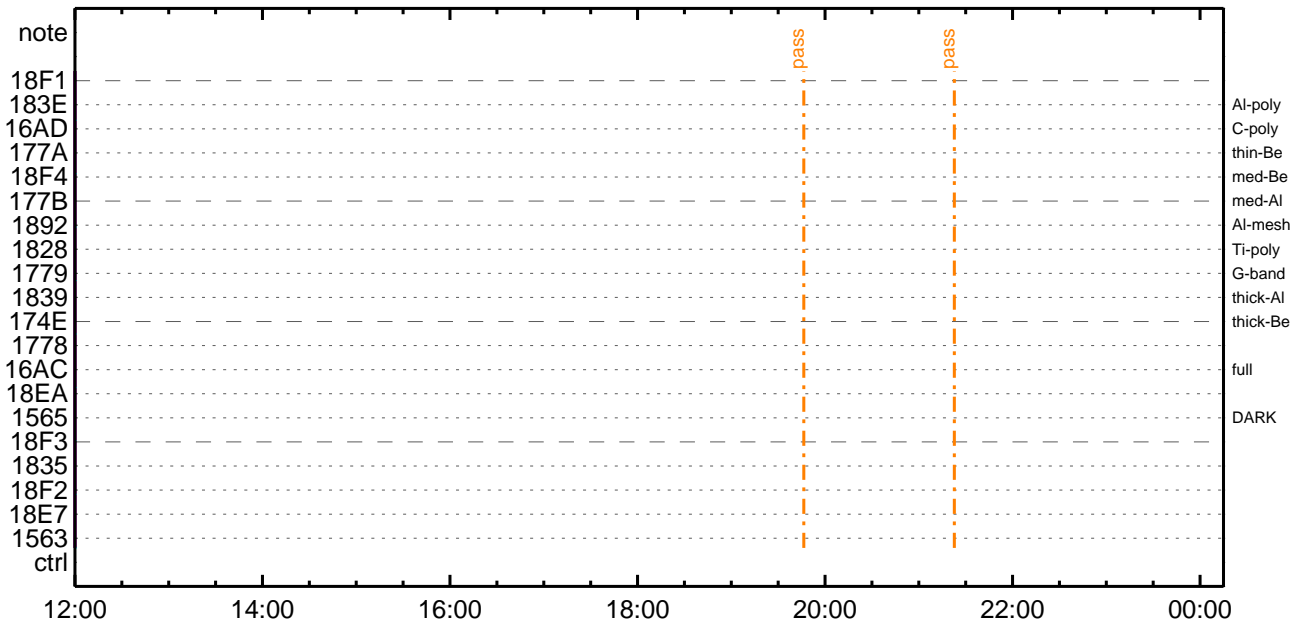
CMDI #0697 2012/06/22



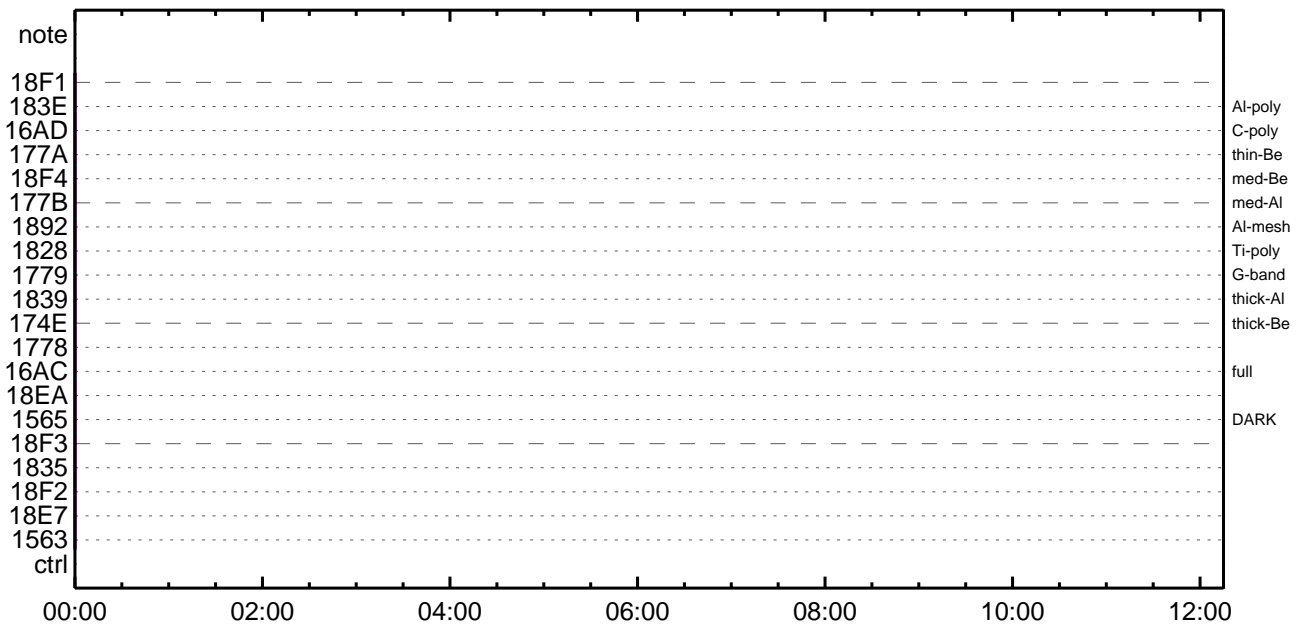
CMDI #0697 2012/06/23



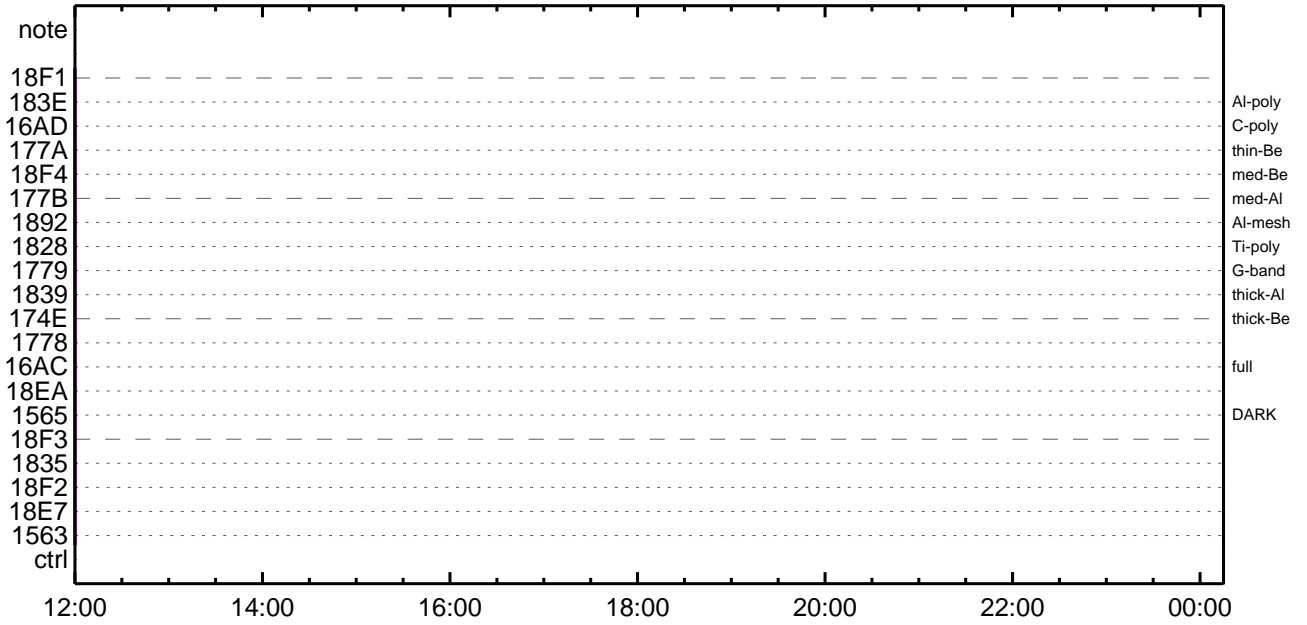
CMDI #0697 2012/06/23



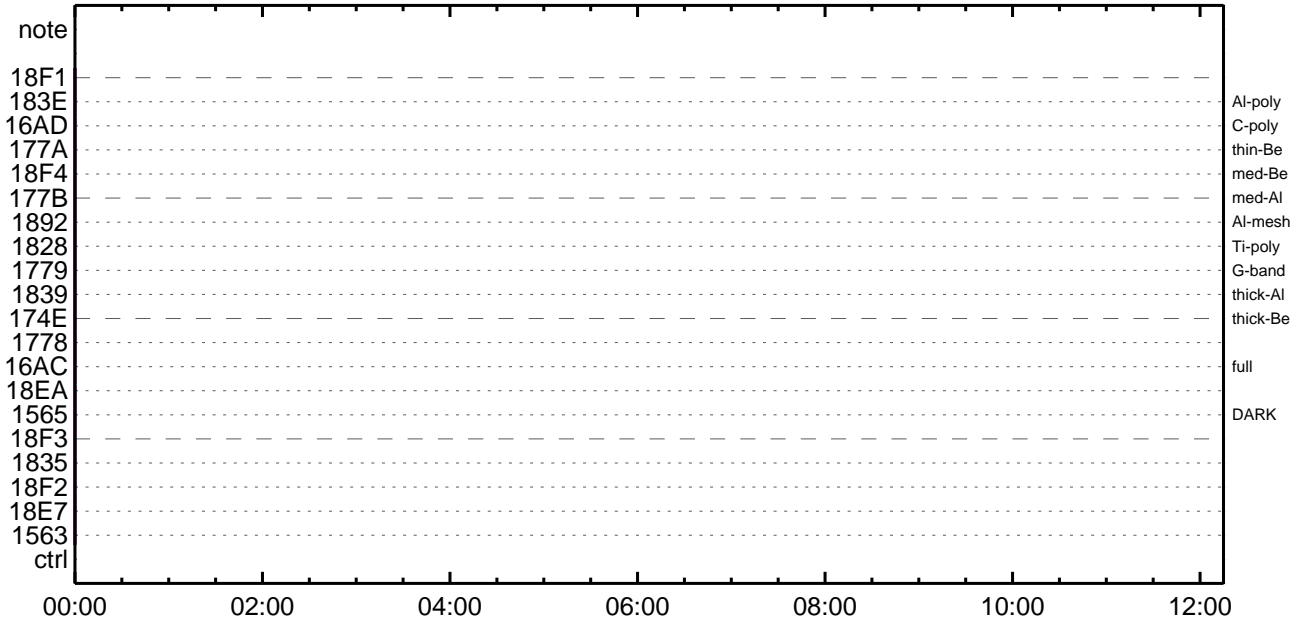
CMDI #0697 2012/06/24



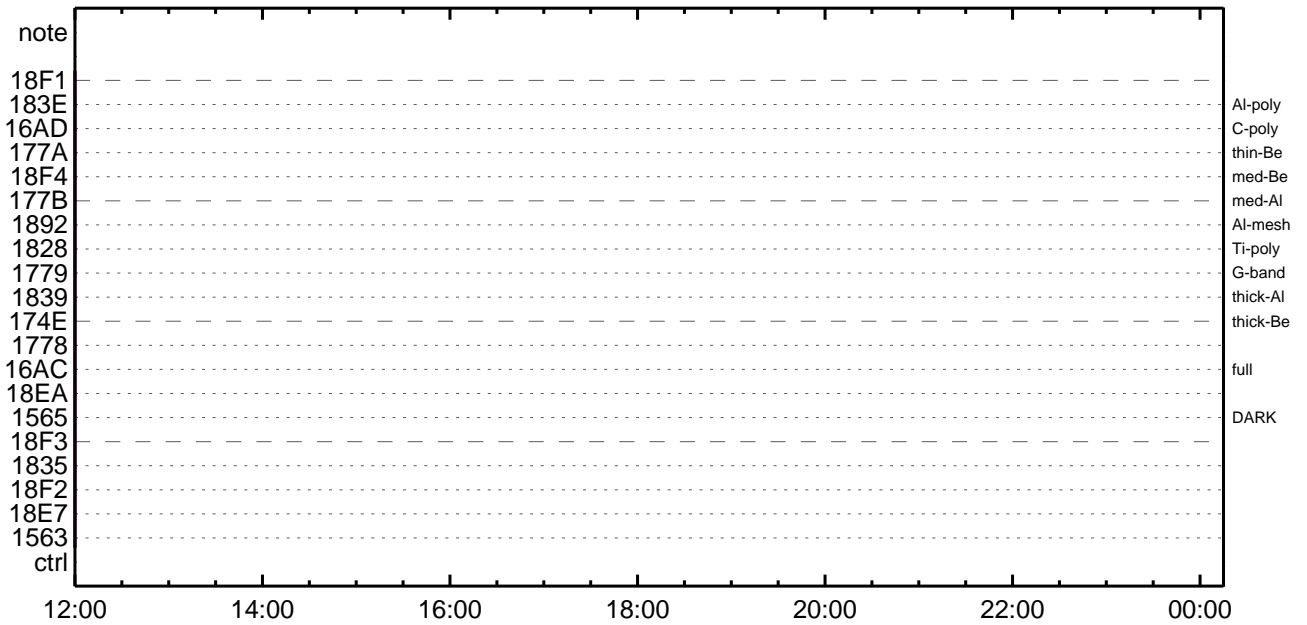
CMDI #0697 2012/06/24



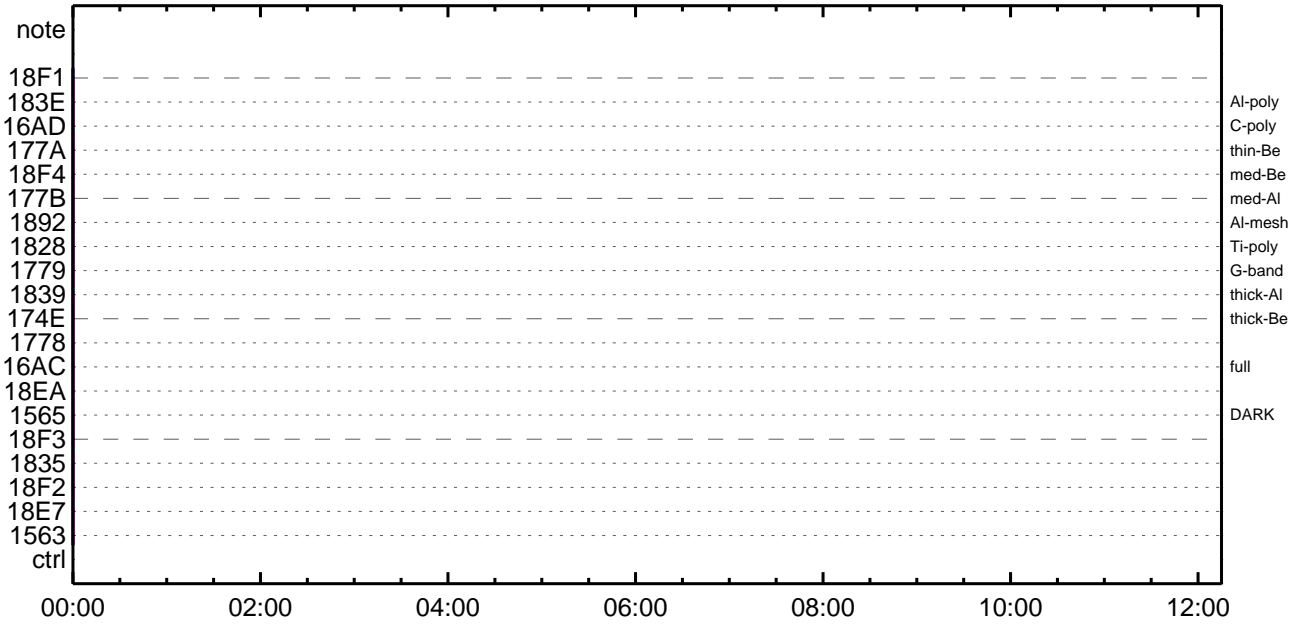
CMDI #0697 2012/06/25



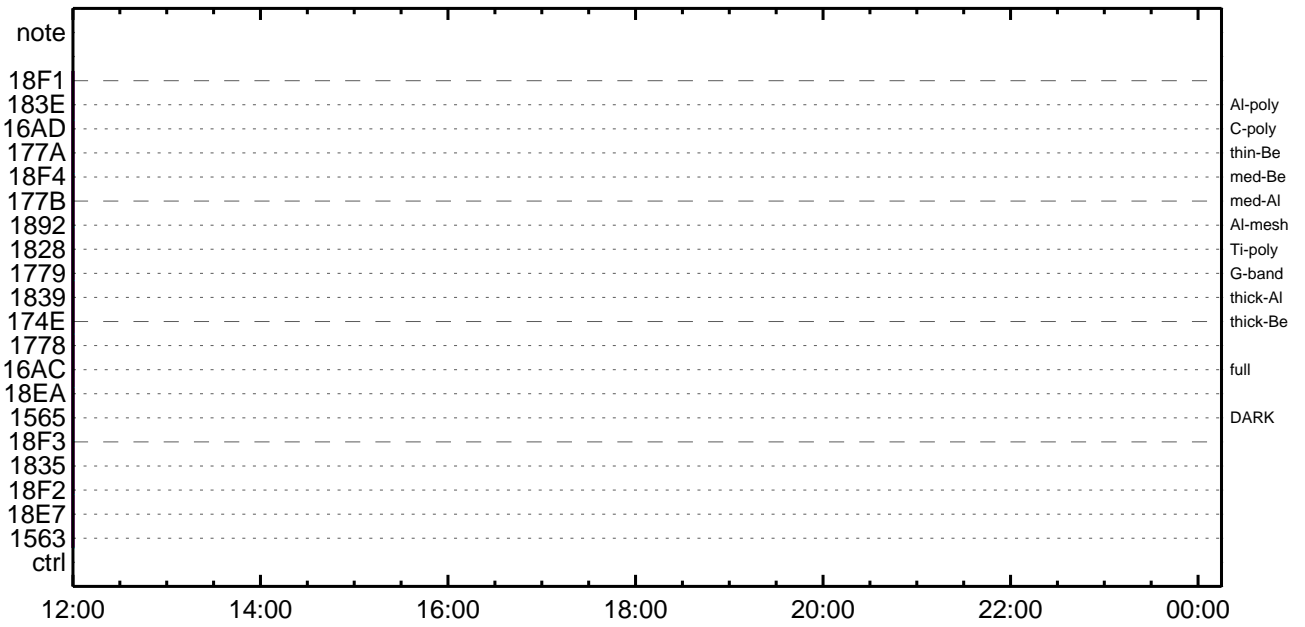
CMDI #0697 2012/06/25



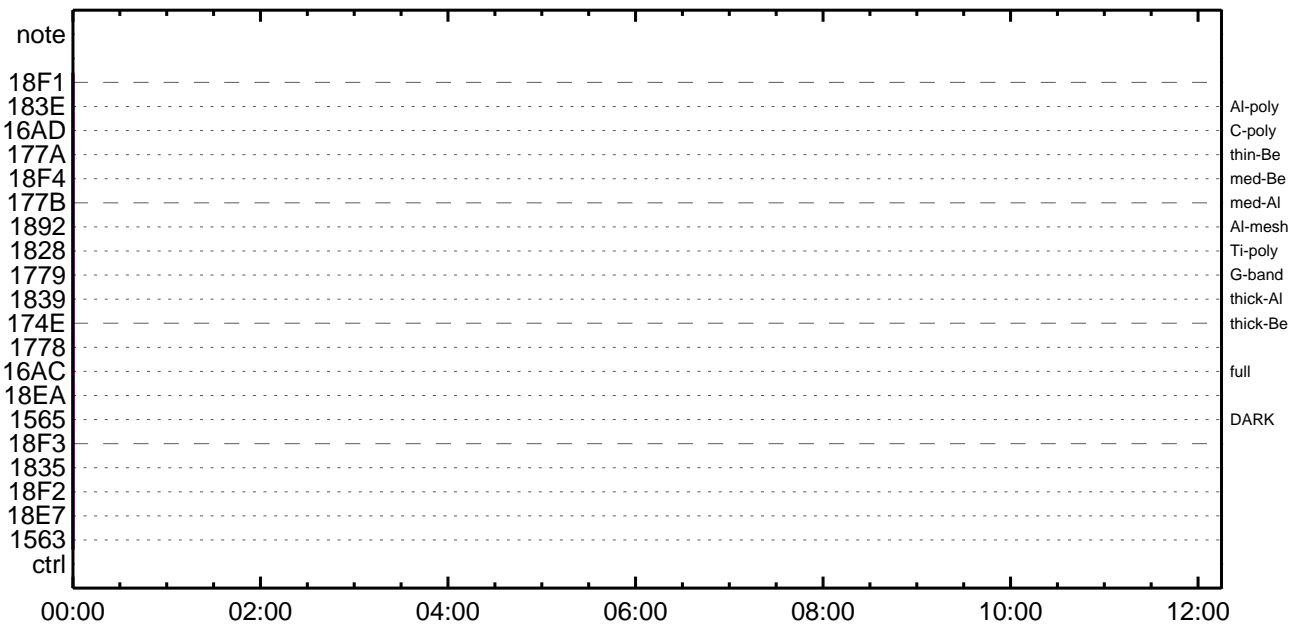
CMDI #0697 2012/06/26



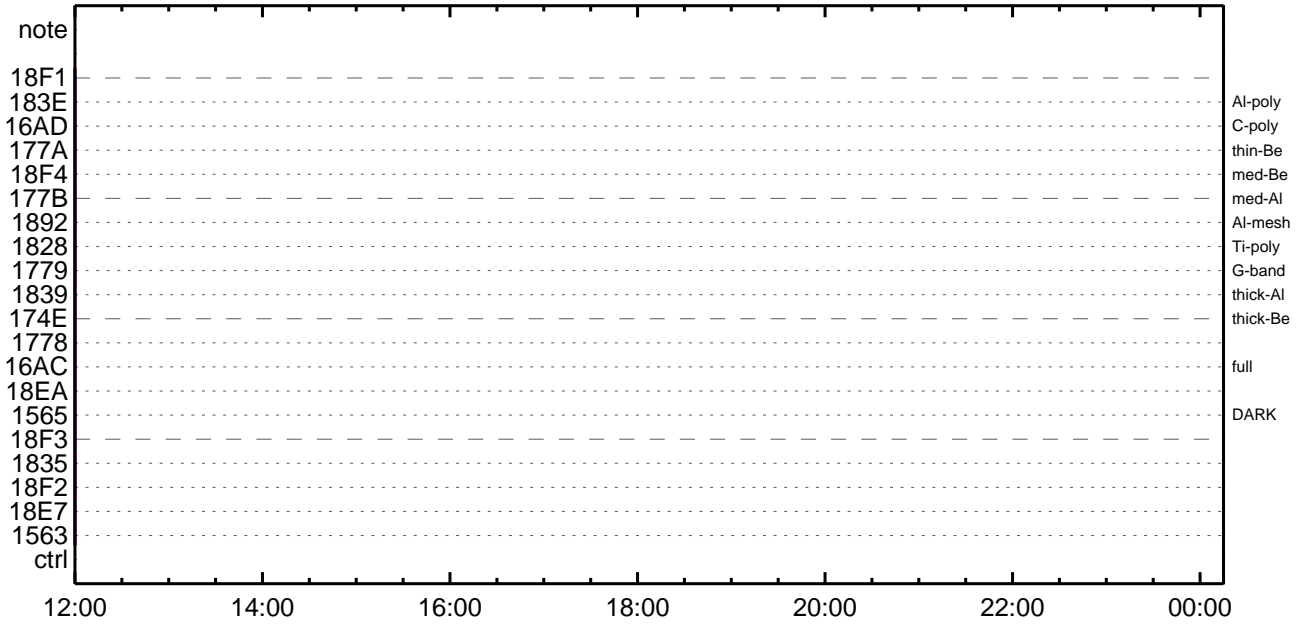
CMDI #0697 2012/06/26



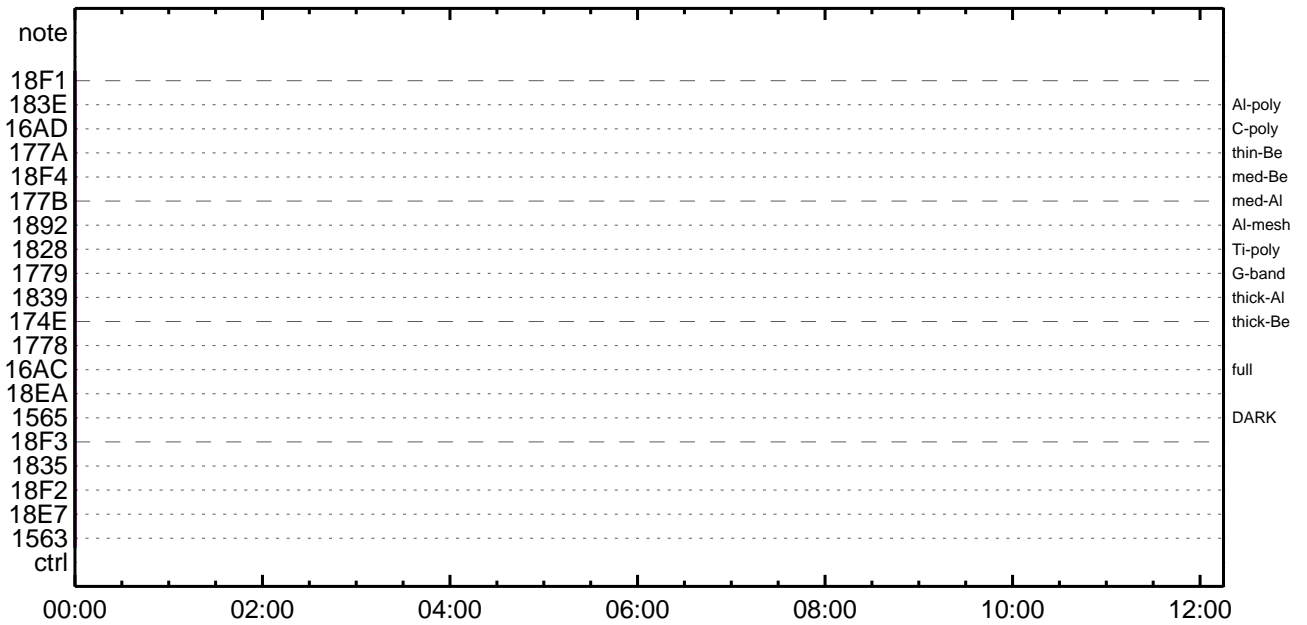
CMDI #0697 2012/06/27



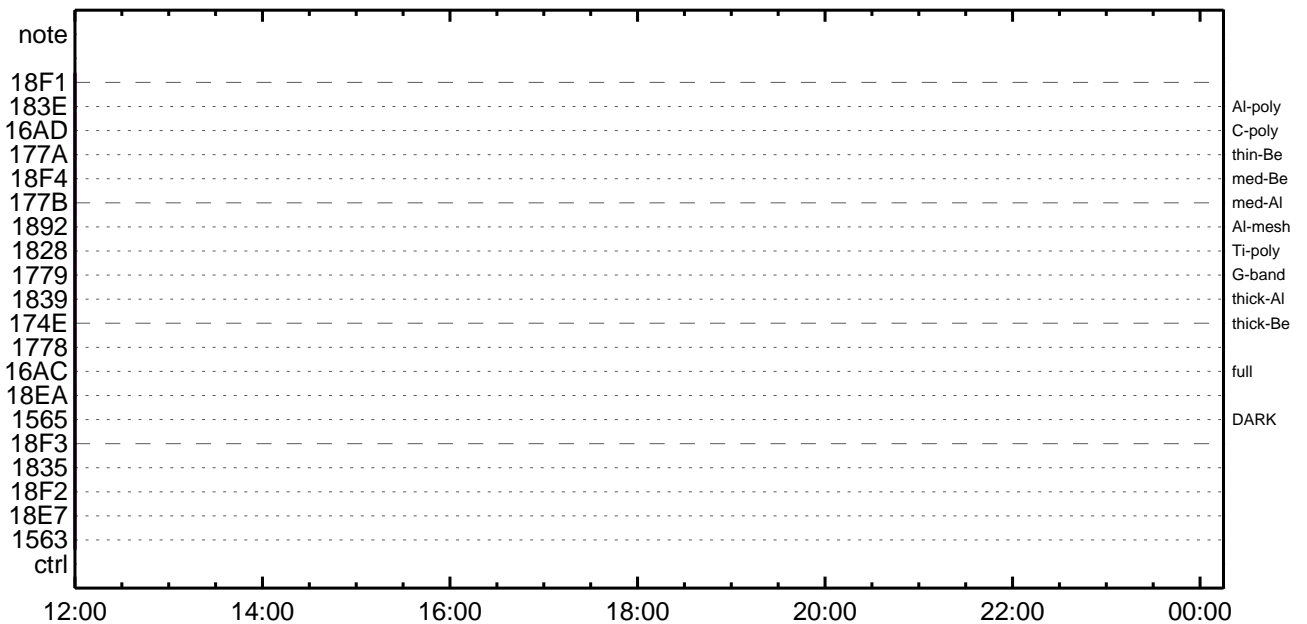
CMDI #0697 2012/06/27



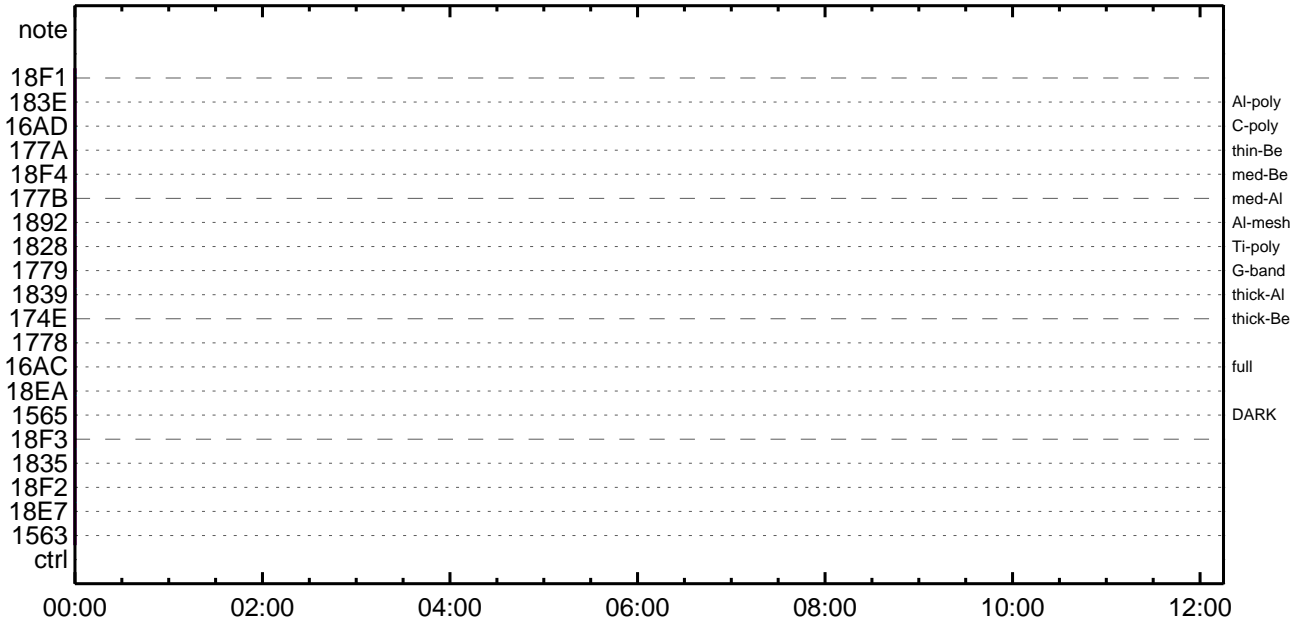
CMDI #0697 2012/06/28



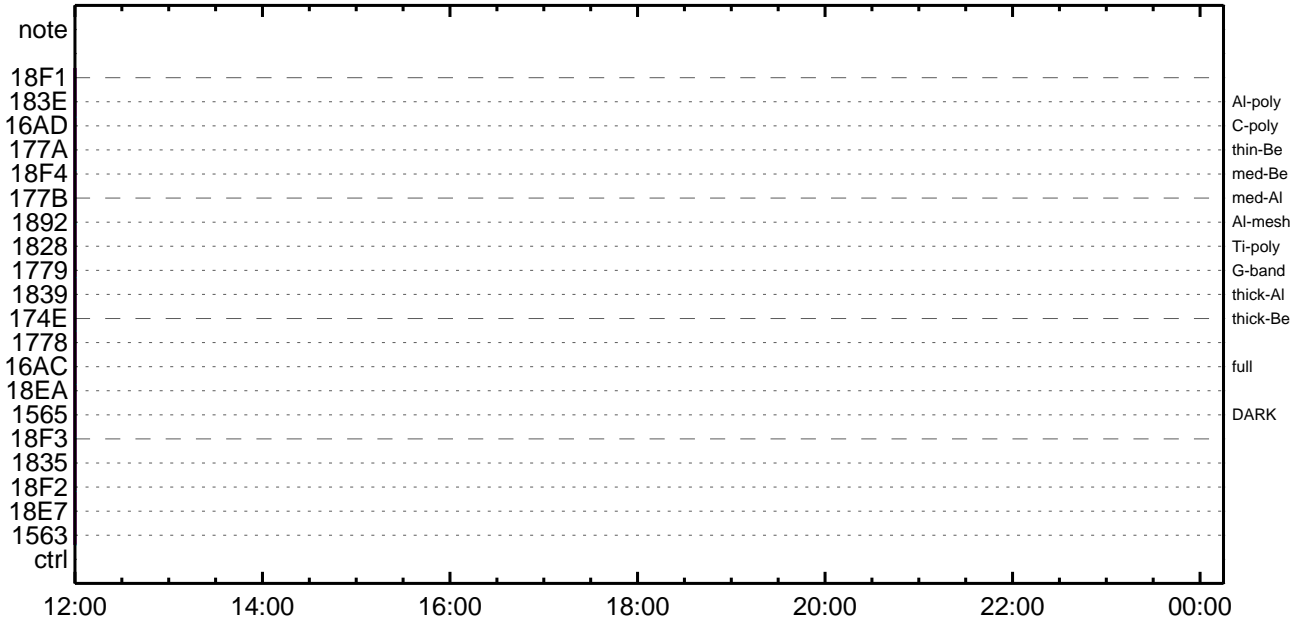
CMDI #0697 2012/06/28



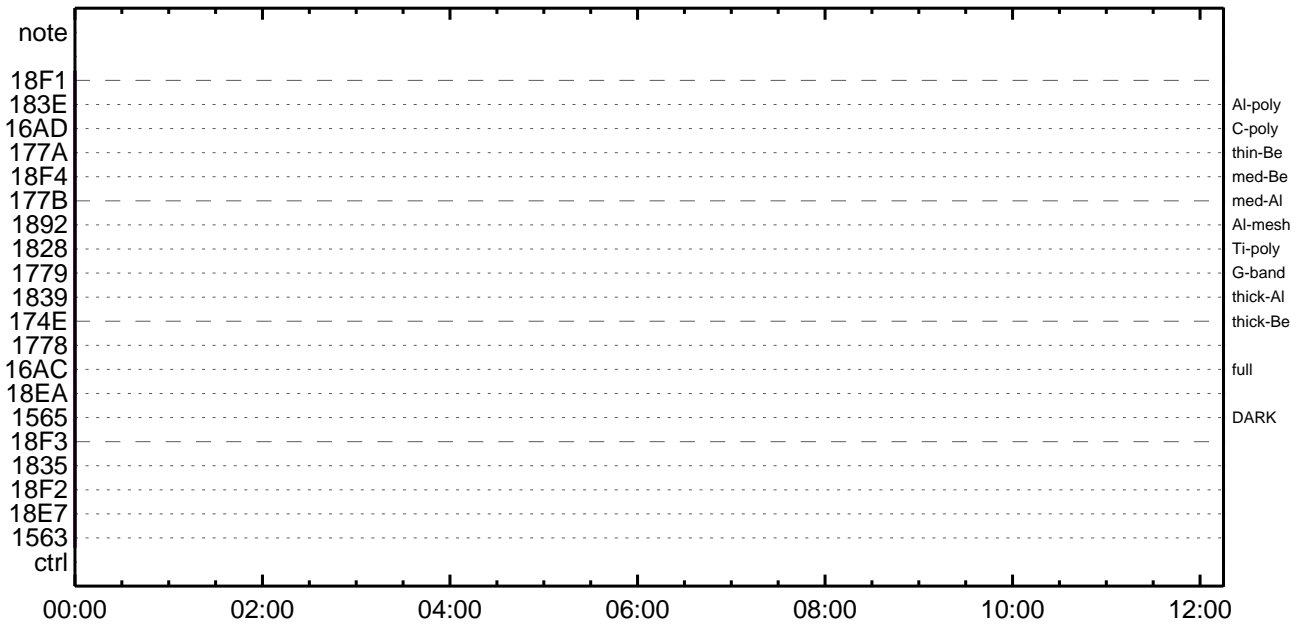
CMDI #0697 2012/06/29



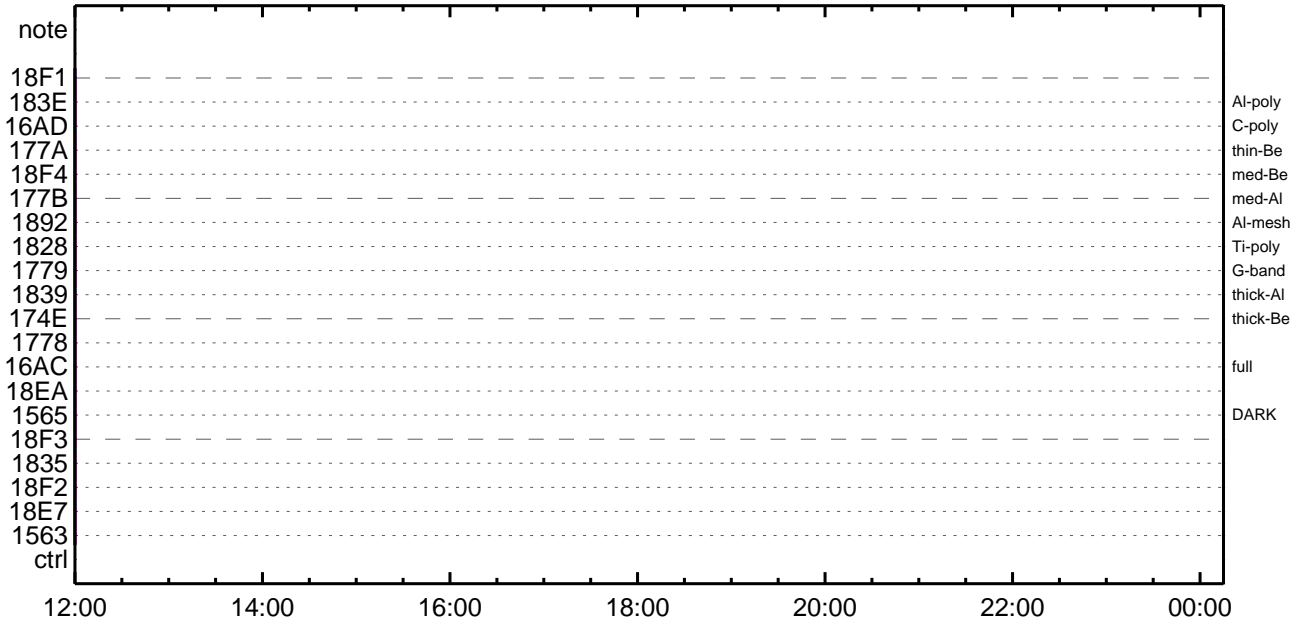
CMDI #0697 2012/06/29



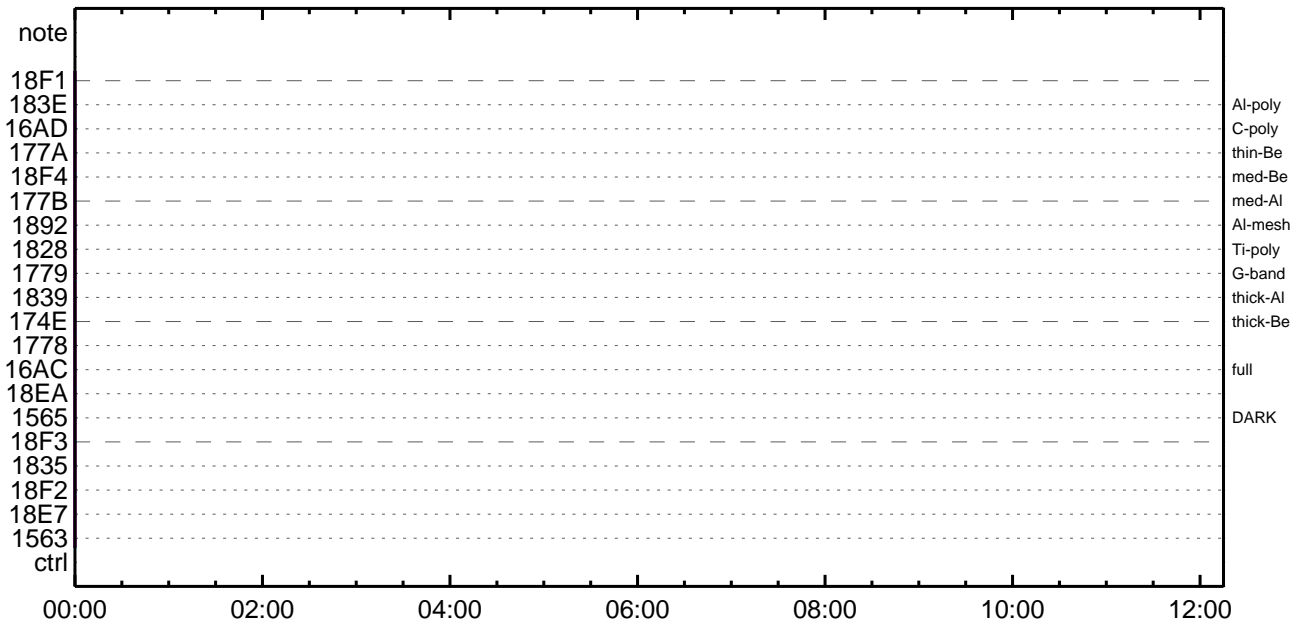
CMDI #0697 2012/06/30



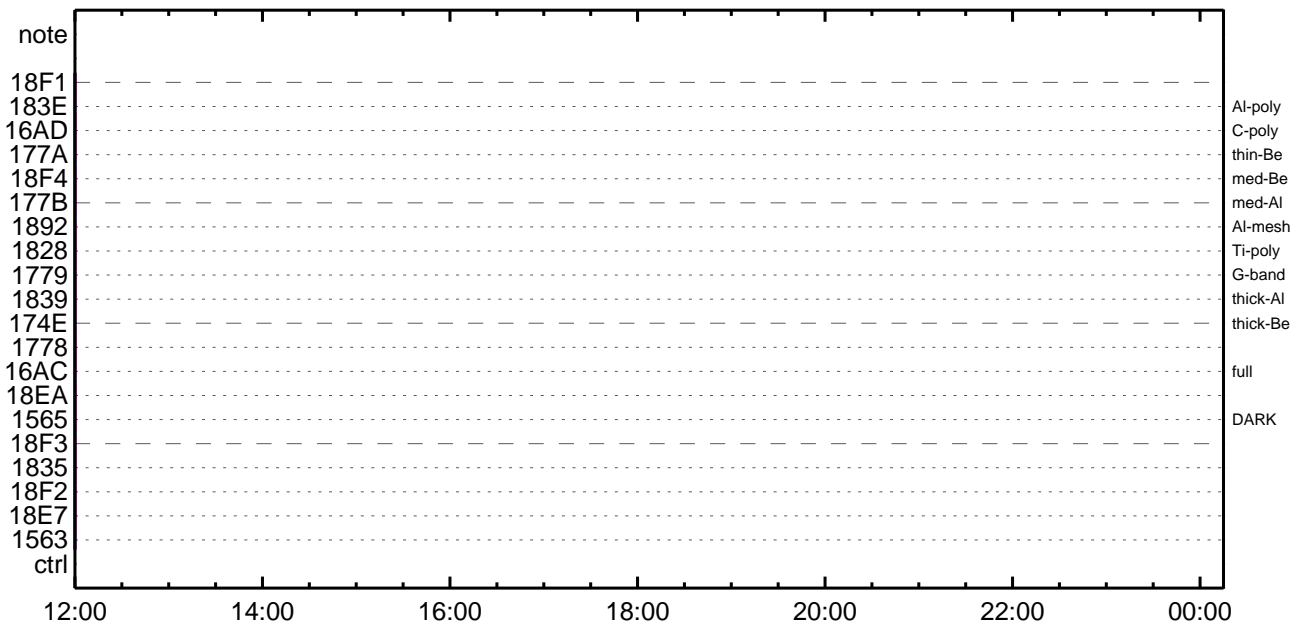
CMDI #0697 2012/06/30



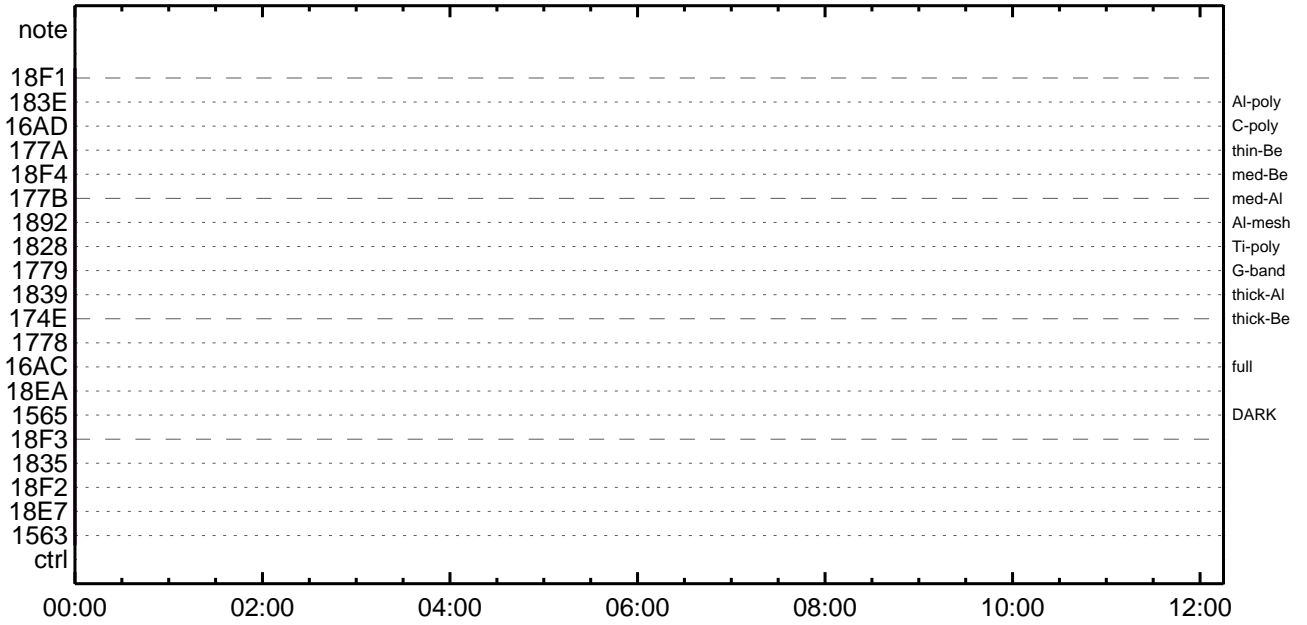
CMDI #0697 2012/07/01



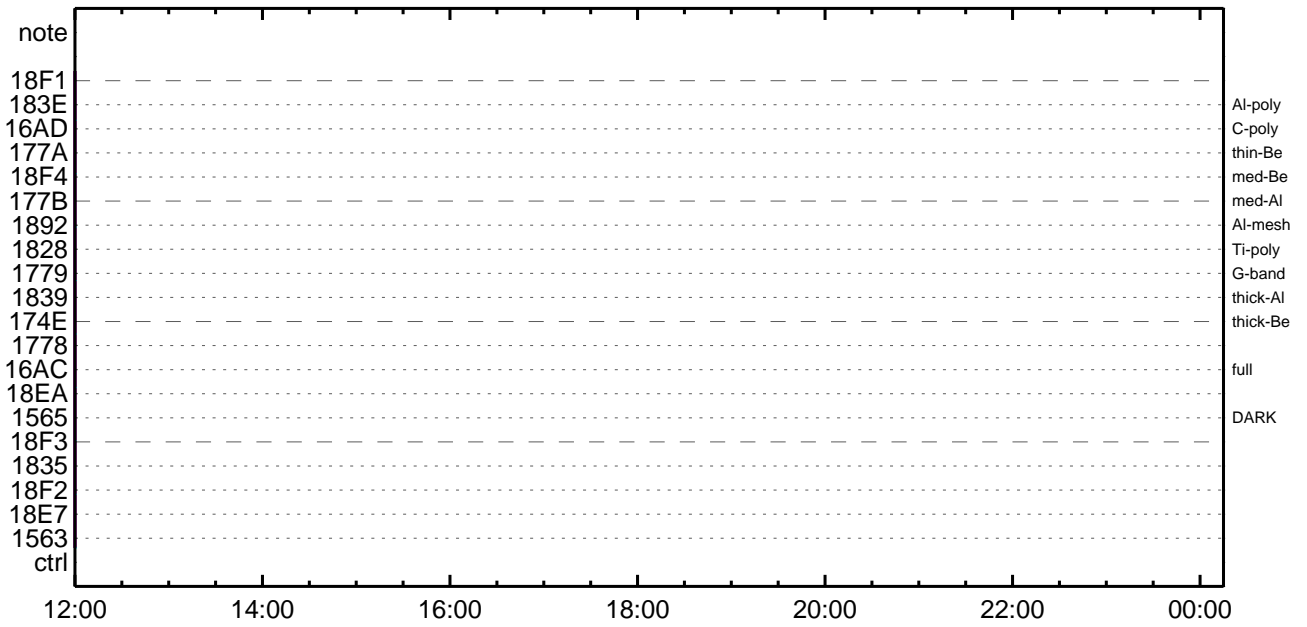
CMDI #0697 2012/07/01



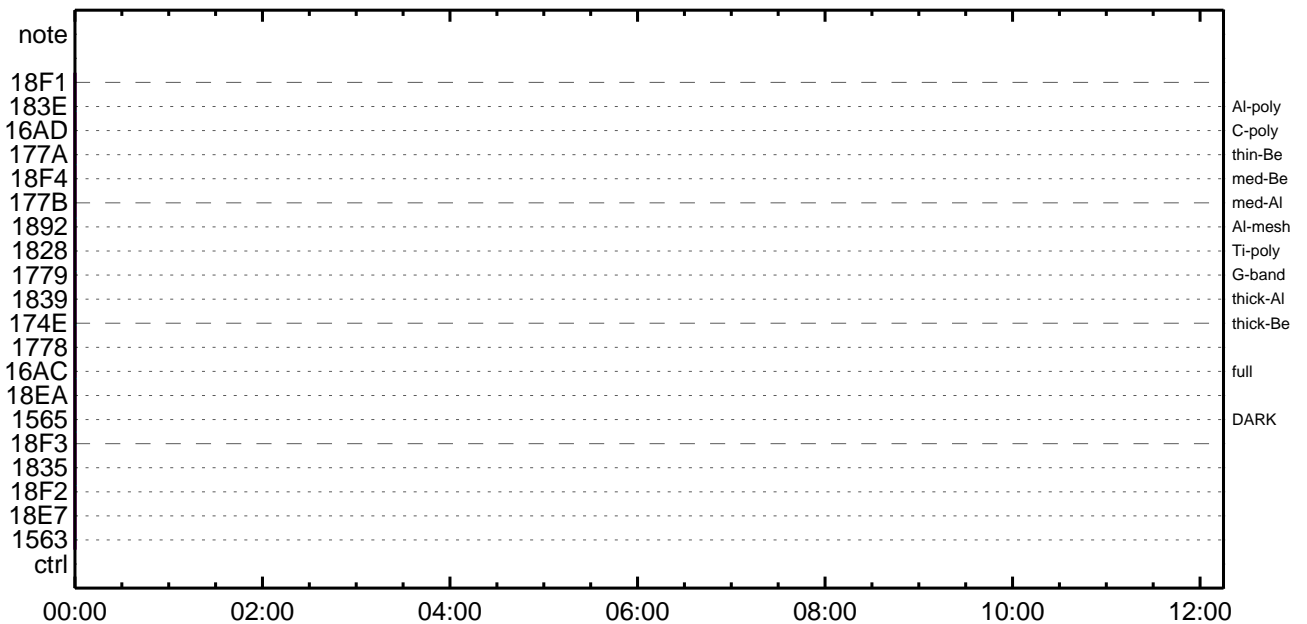
CMDI #0697 2012/07/02



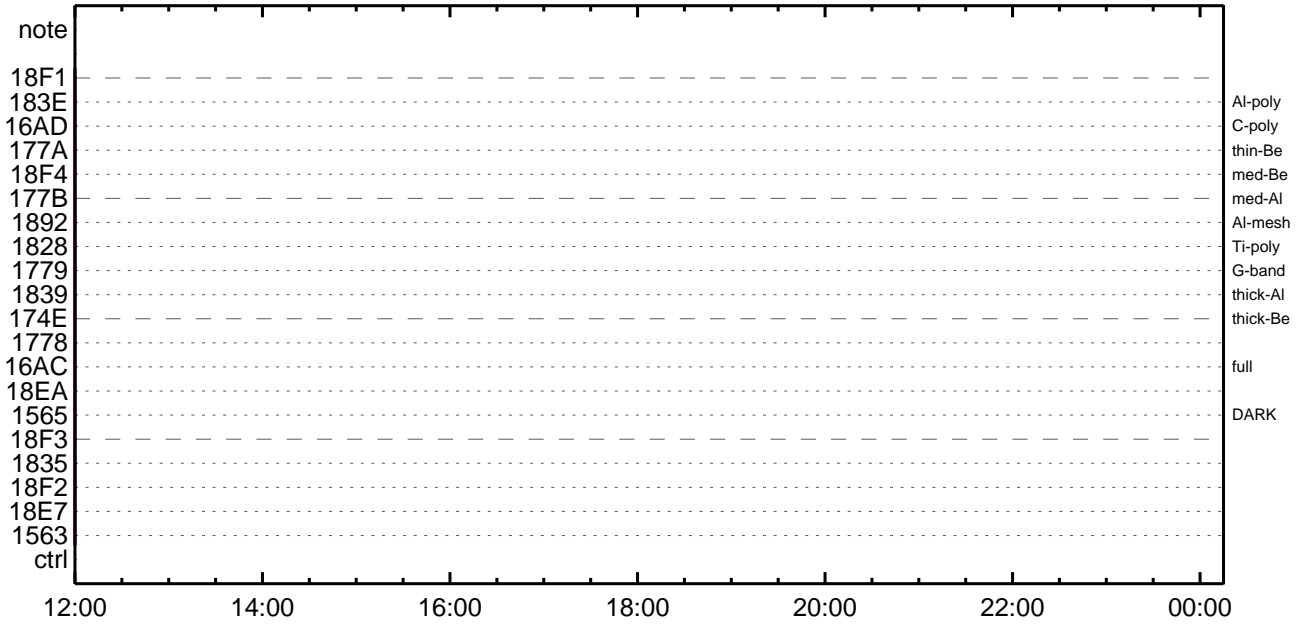
CMDI #0697 2012/07/02



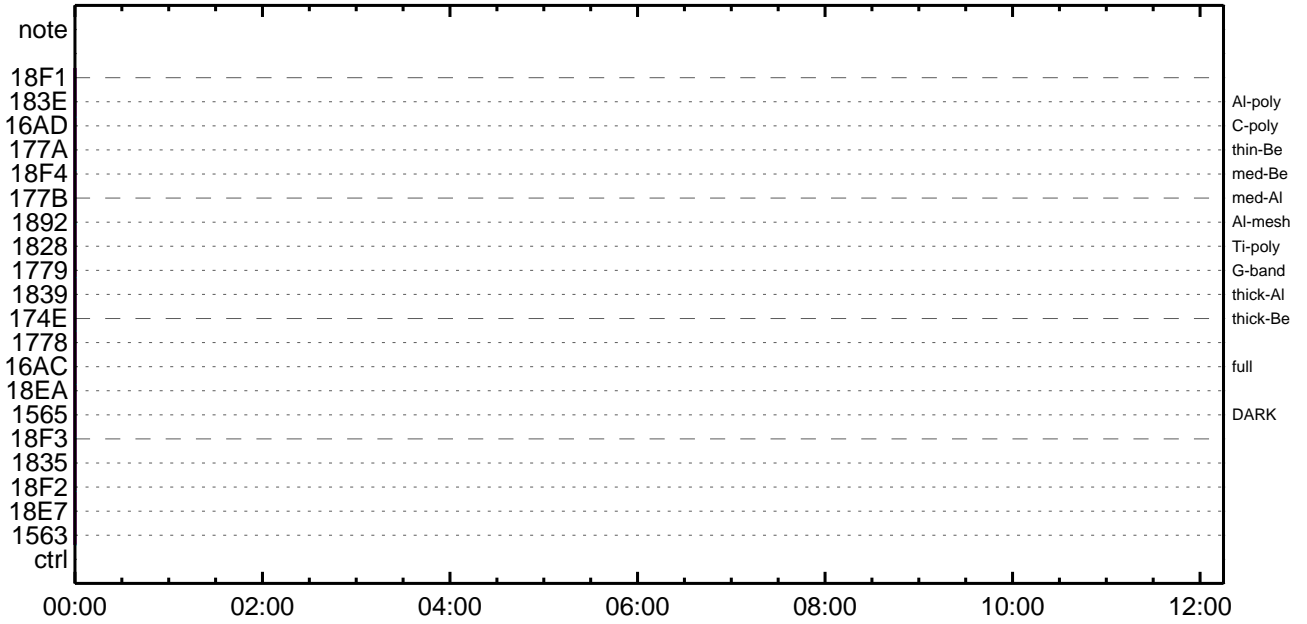
CMDI #0697 2012/07/03



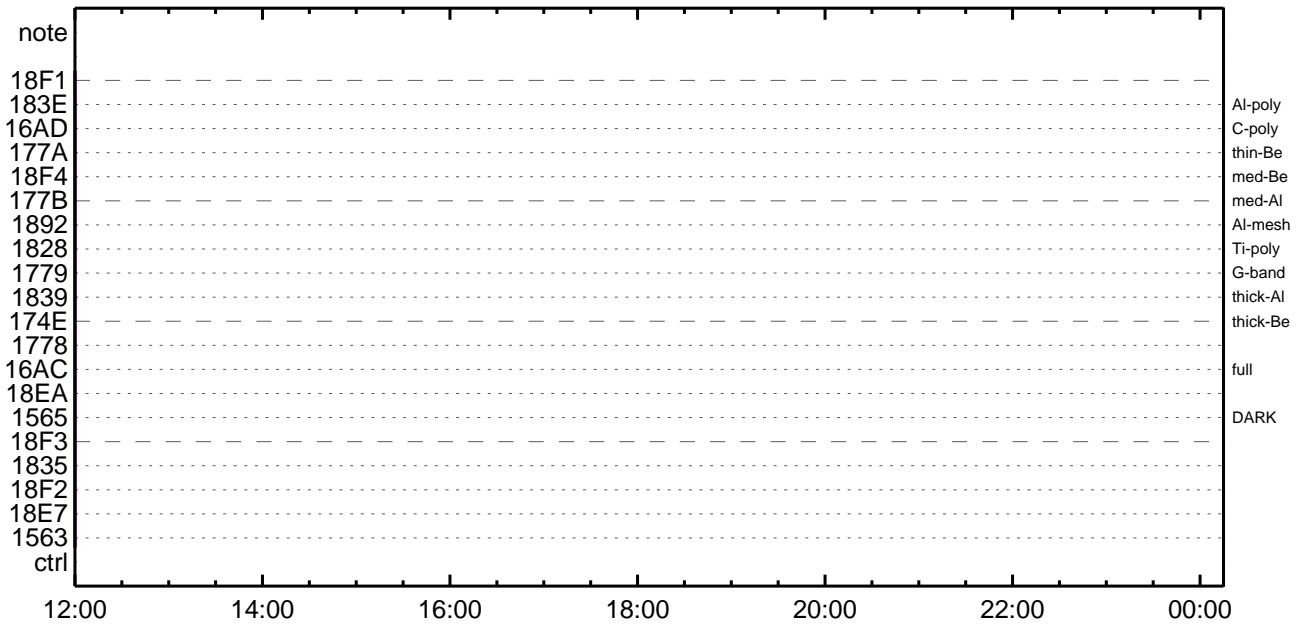
CMDI #0697 2012/07/03



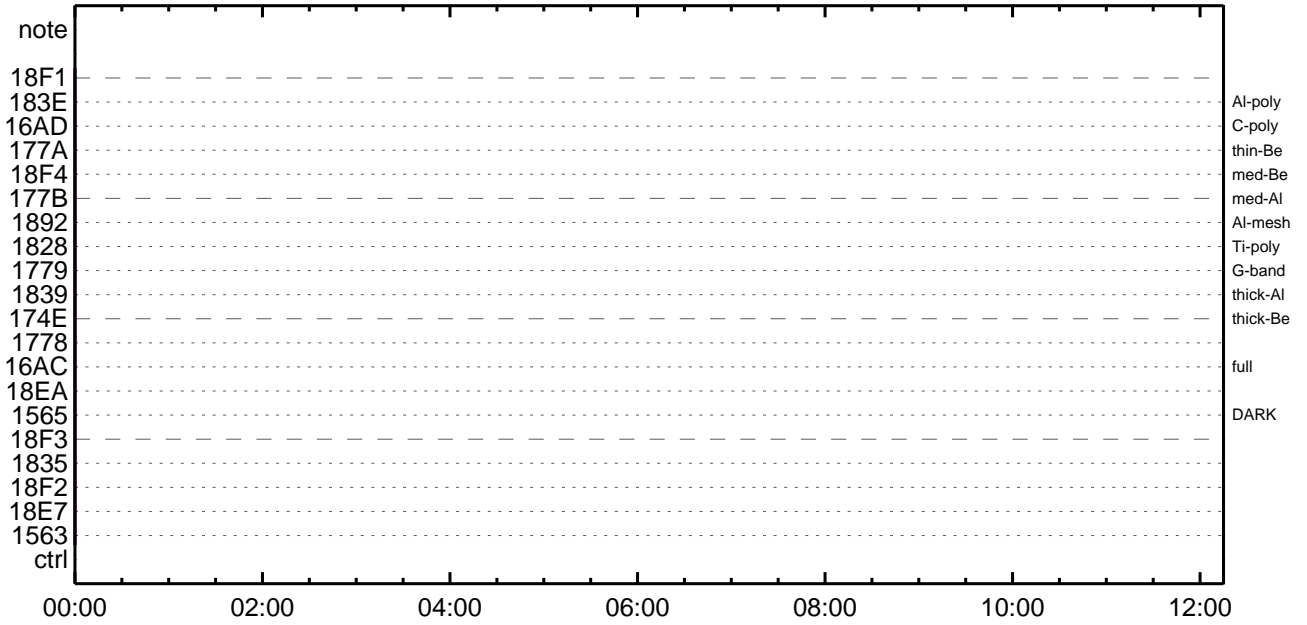
CMDI #0697 2012/07/04



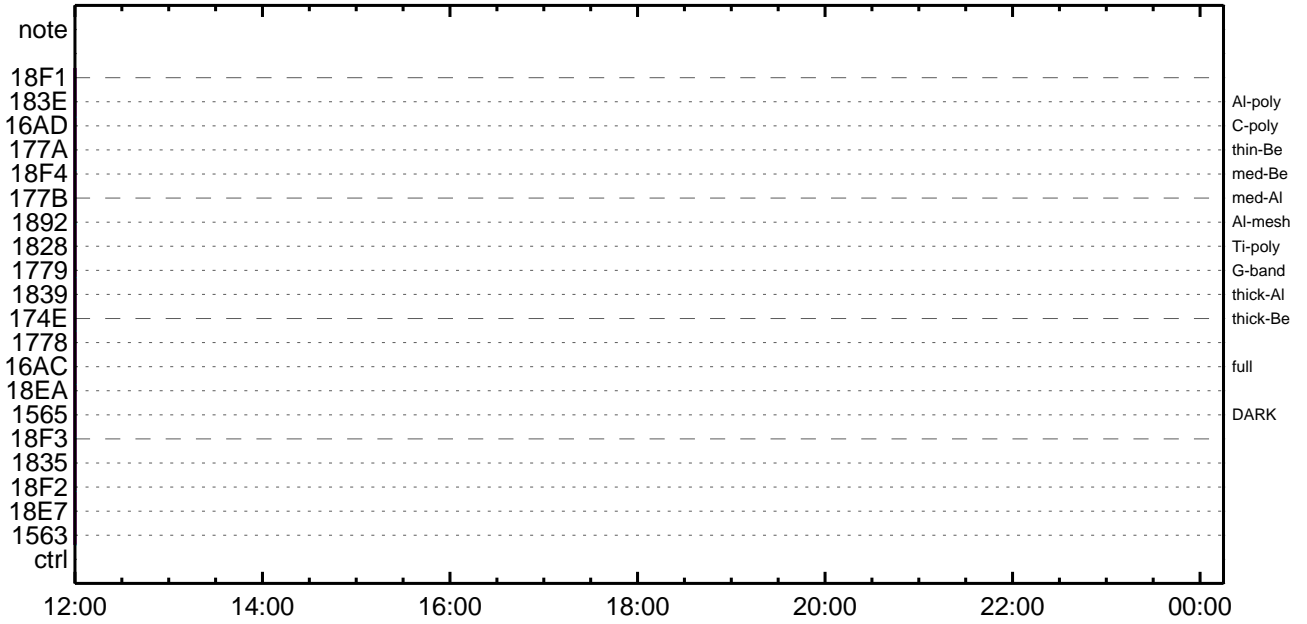
CMDI #0697 2012/07/04



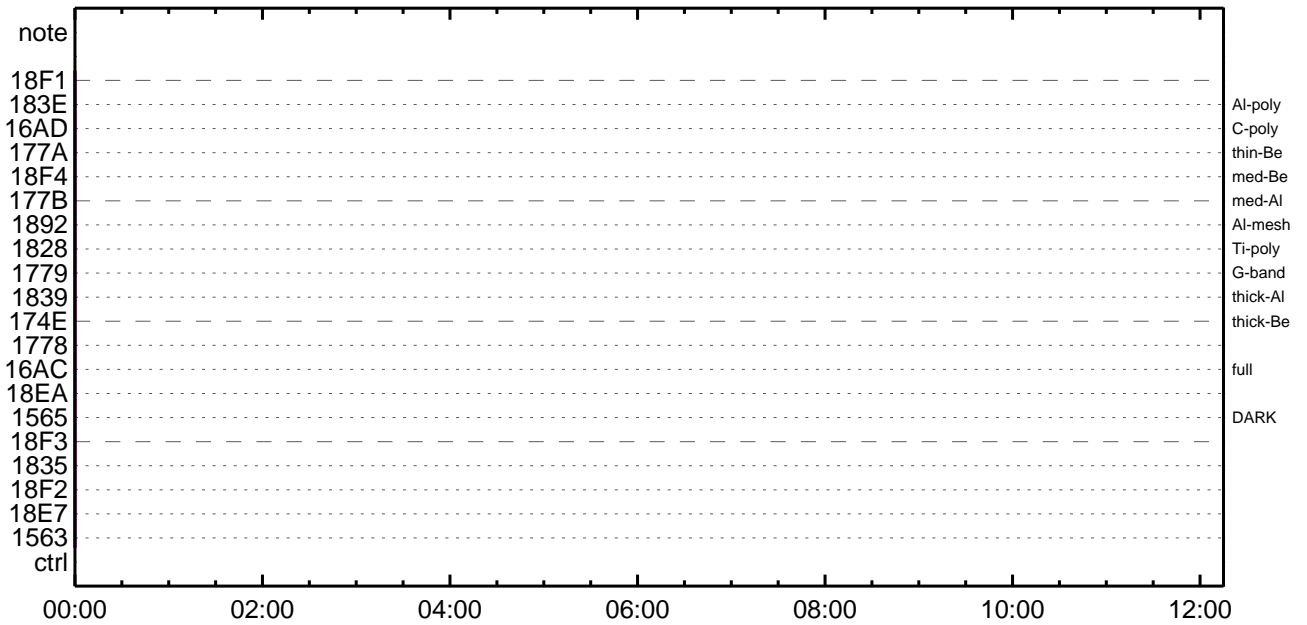
CMDI #0697 2012/07/05



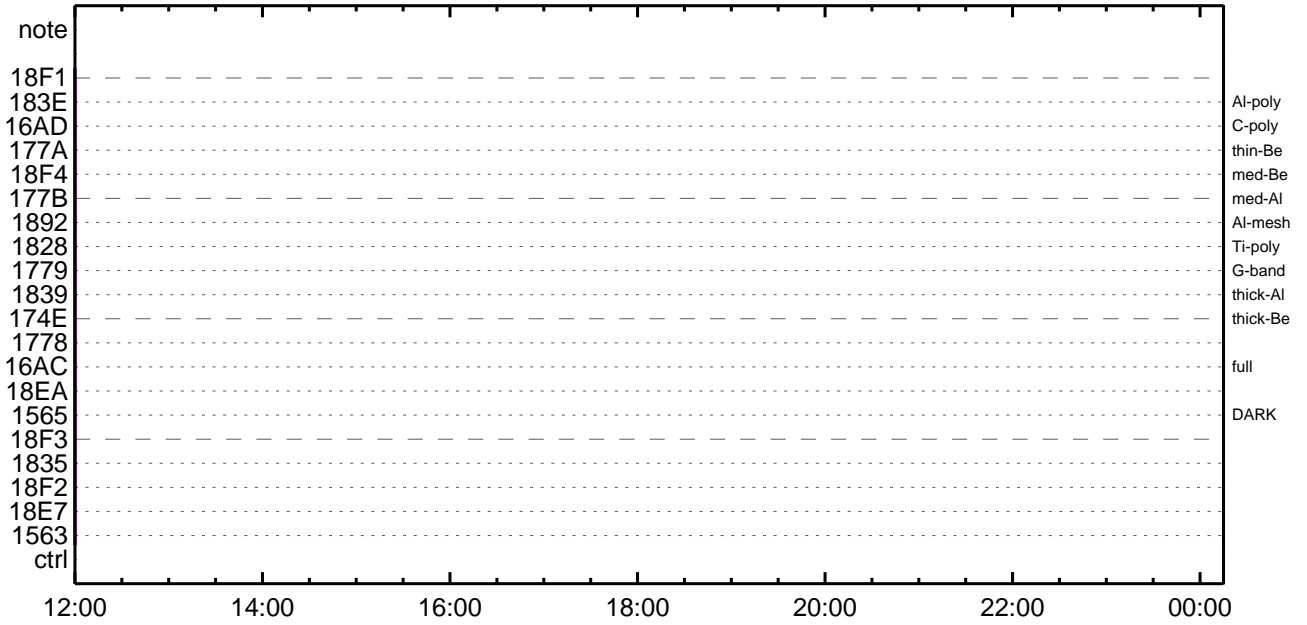
CMDI #0697 2012/07/05



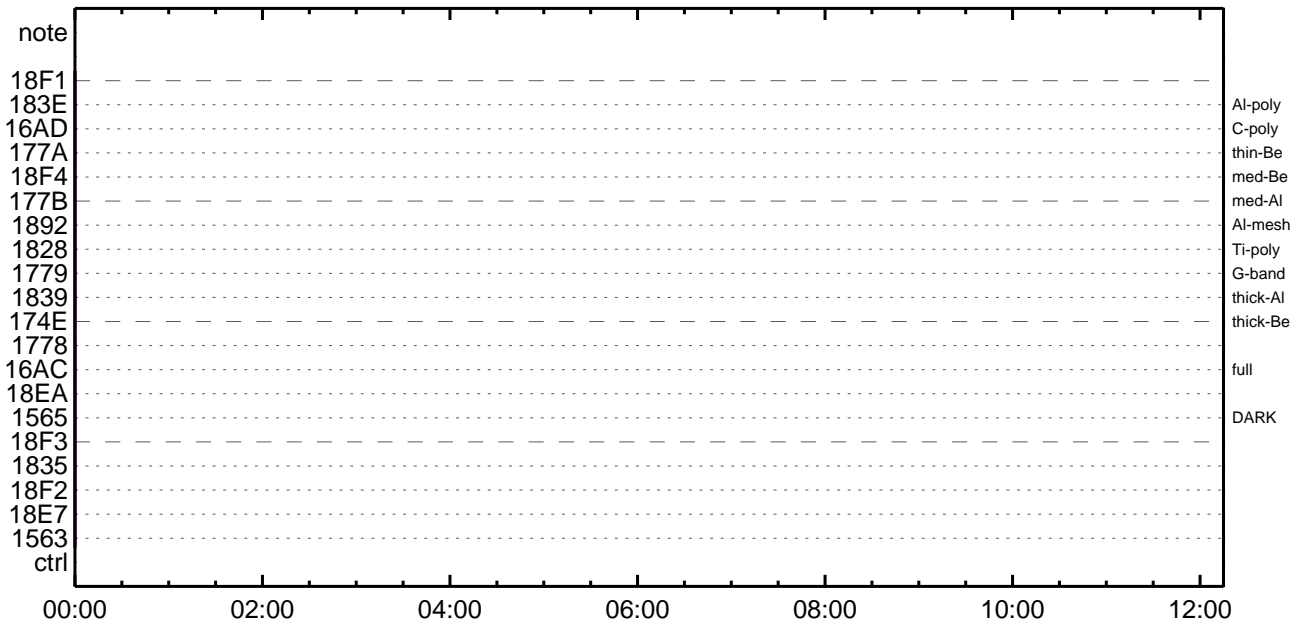
CMDI #0697 2012/07/06



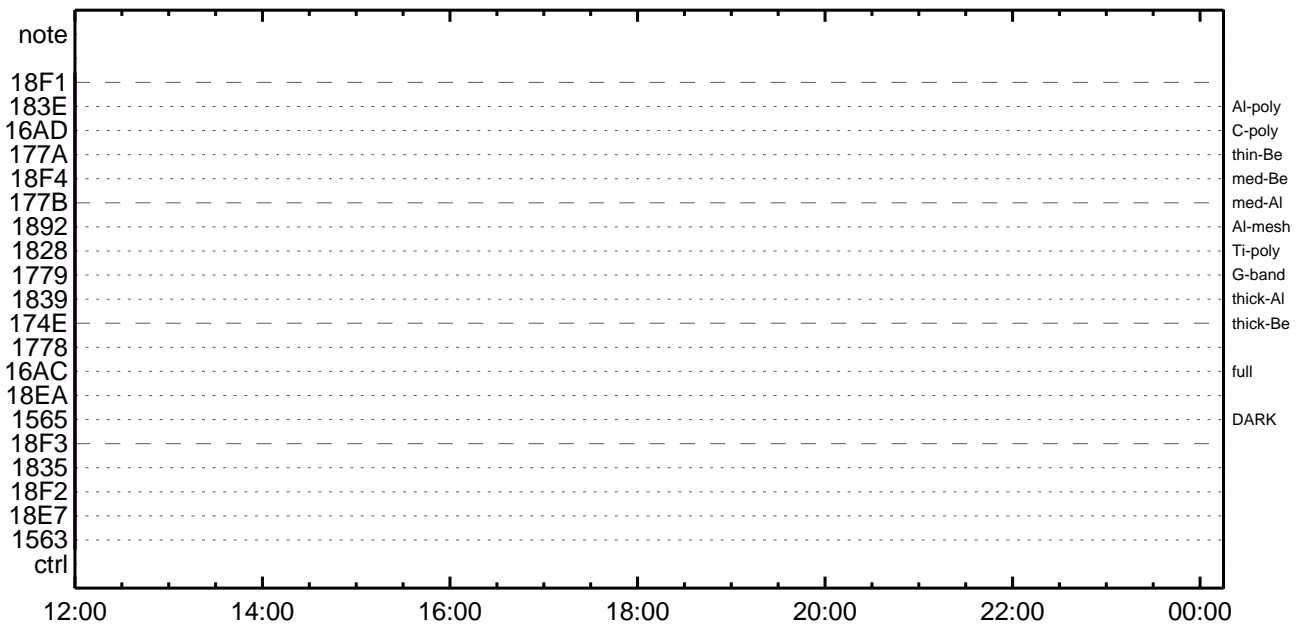
CMDI #0697 2012/07/06



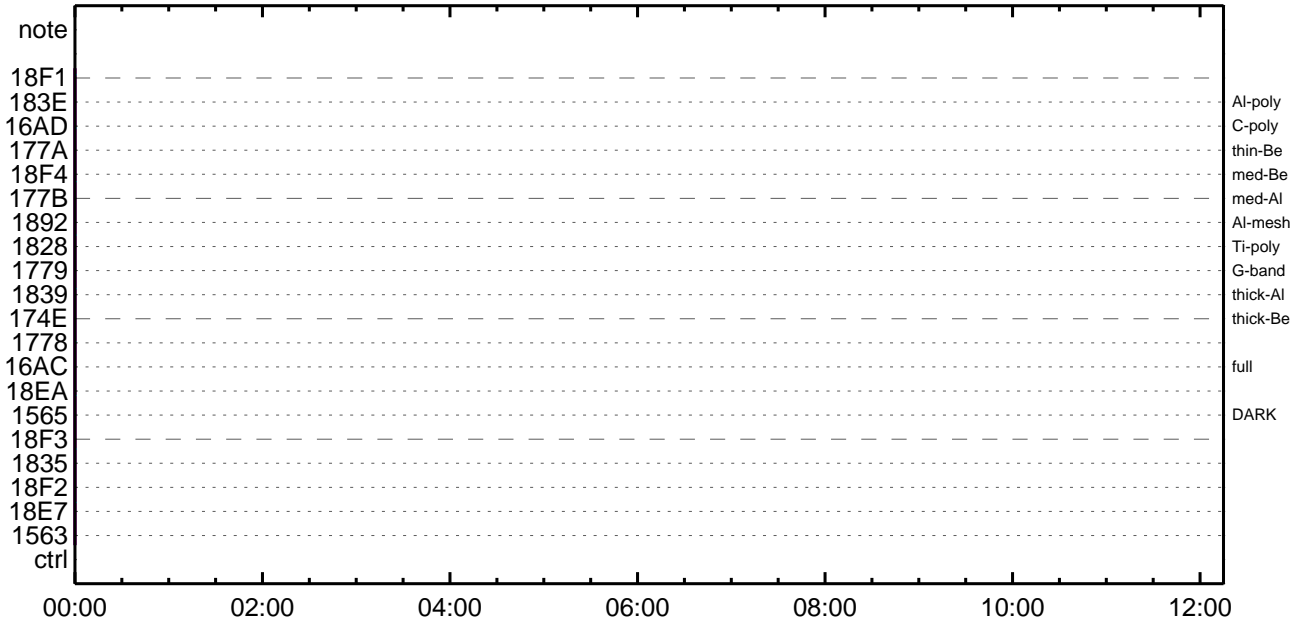
CMDI #0697 2012/07/07



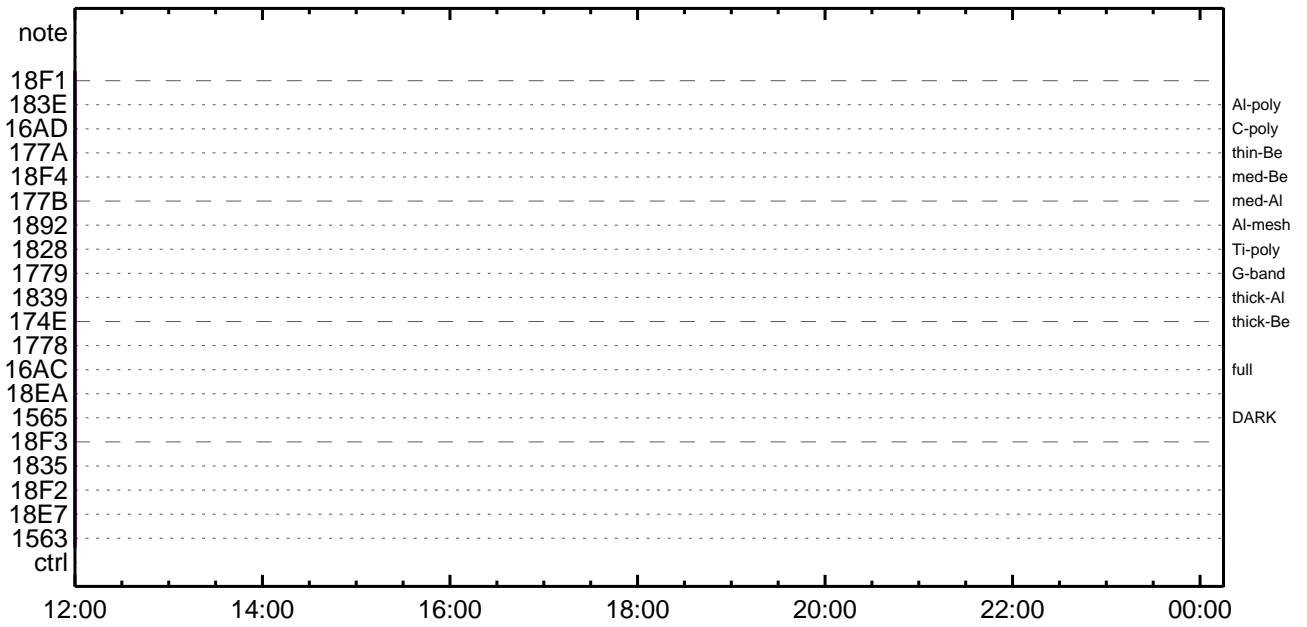
CMDI #0697 2012/07/07



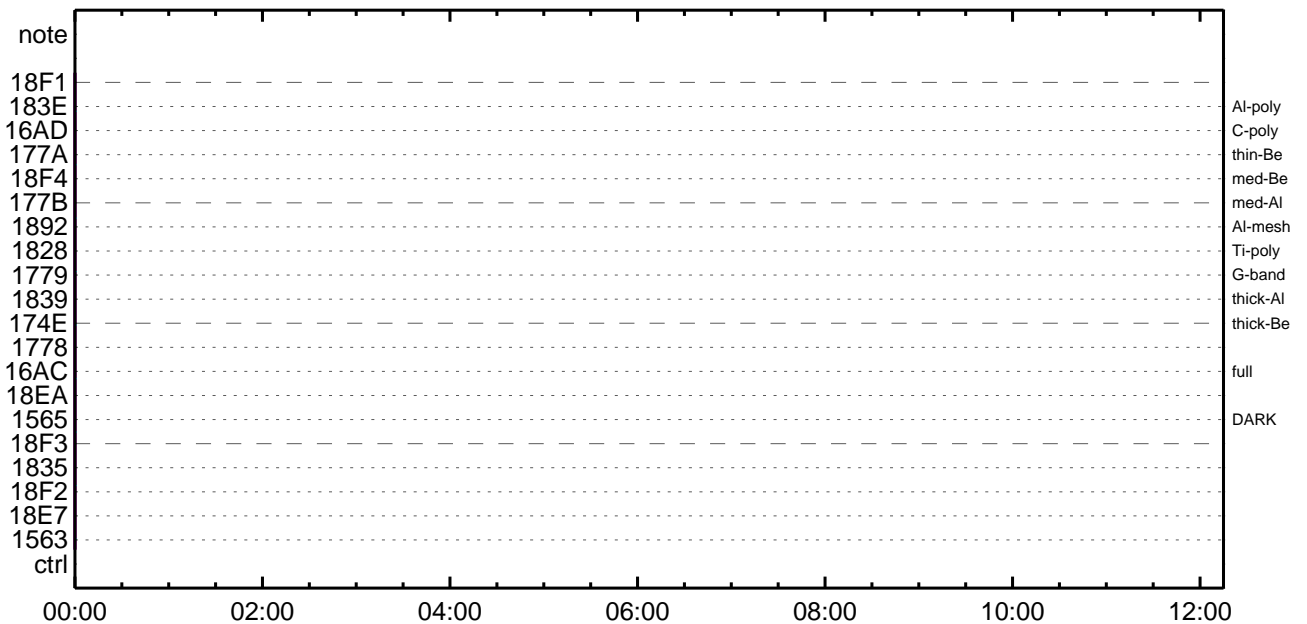
CMDI #0697 2012/07/08



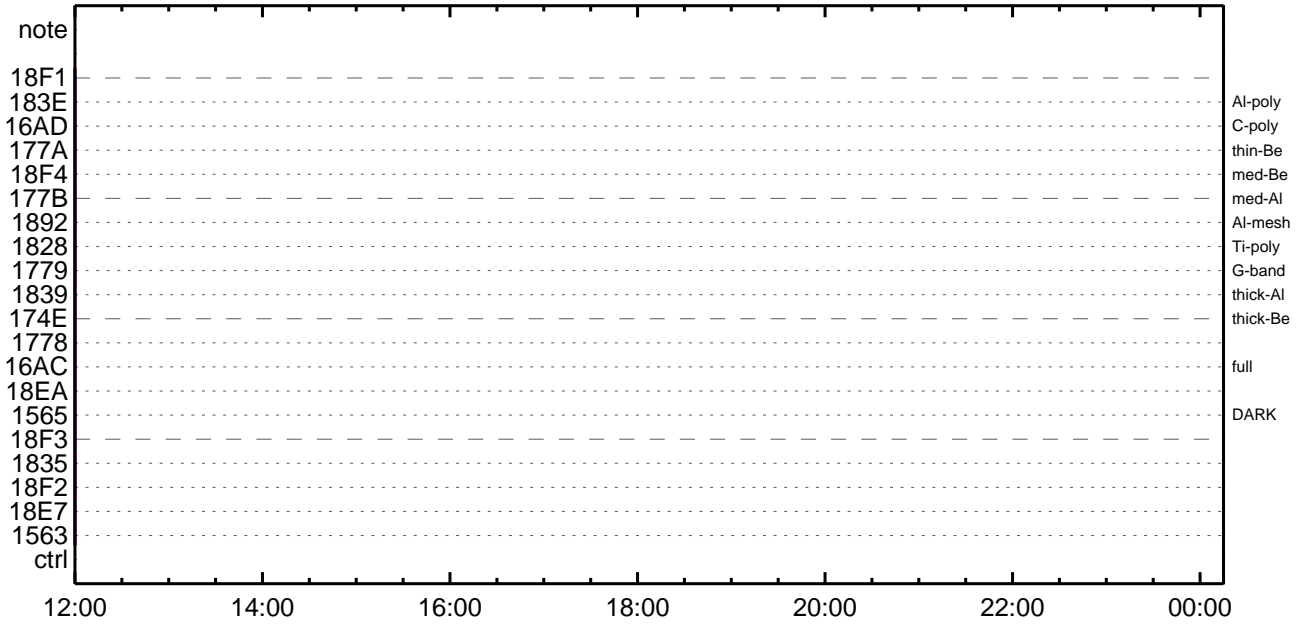
CMDI #0697 2012/07/08



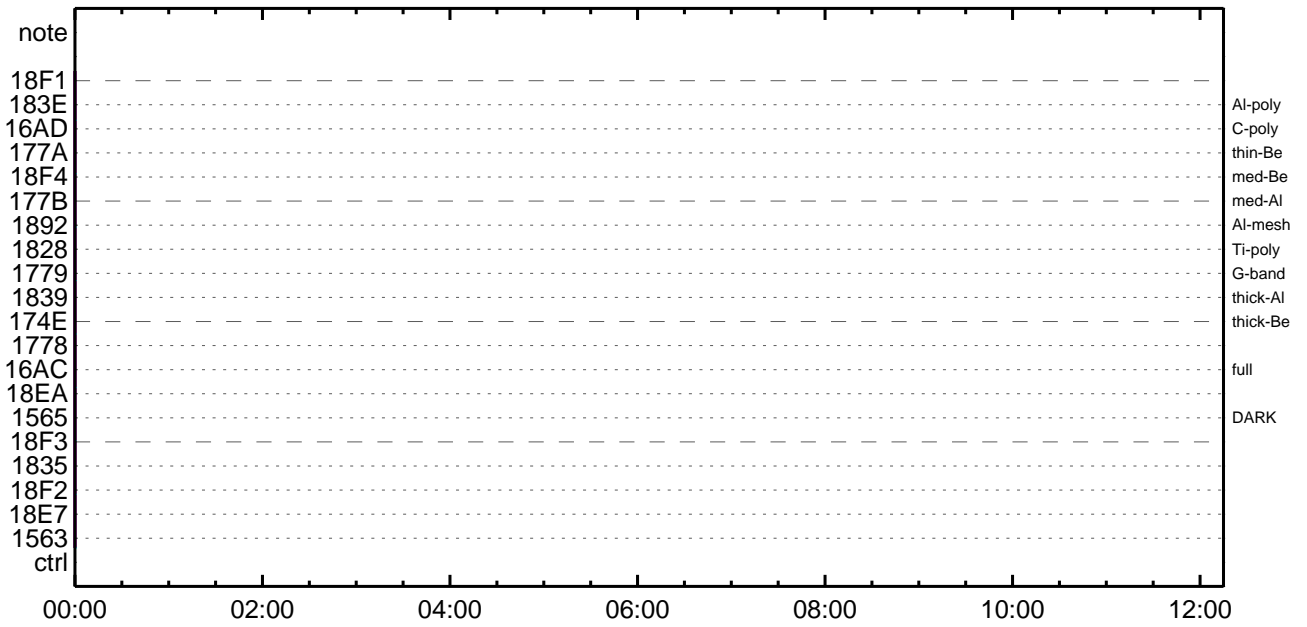
CMDI #0697 2012/07/09



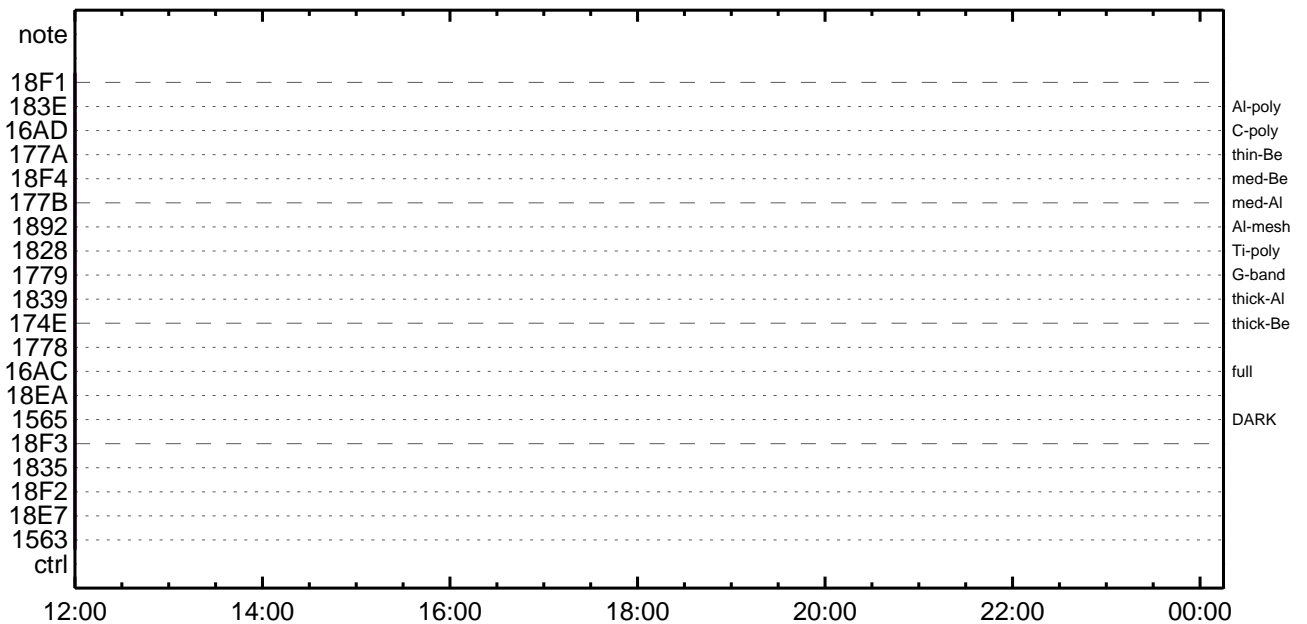
CMDI #0697 2012/07/09



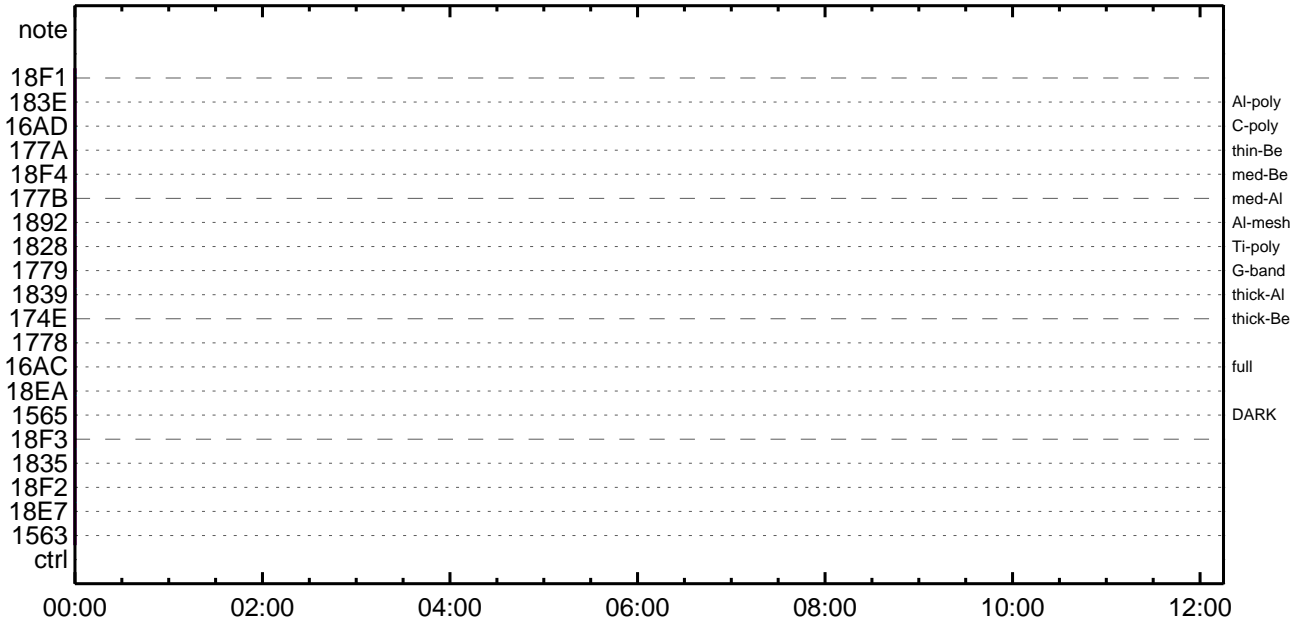
CMDI #0697 2012/07/10



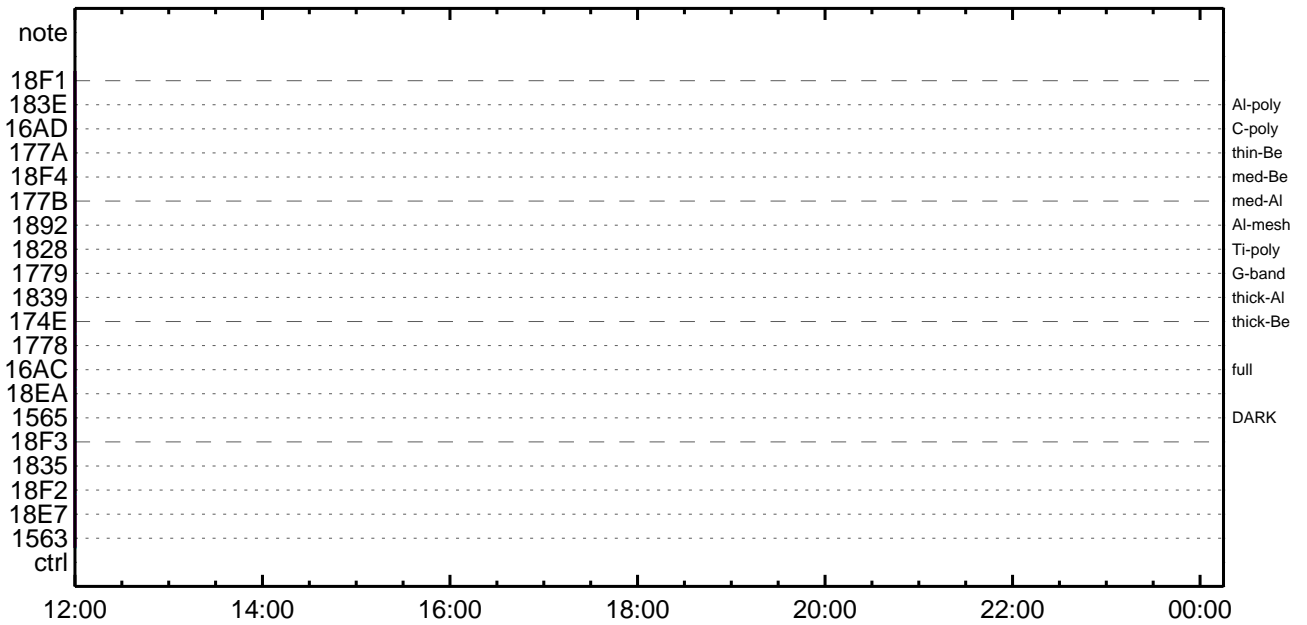
CMDI #0697 2012/07/10



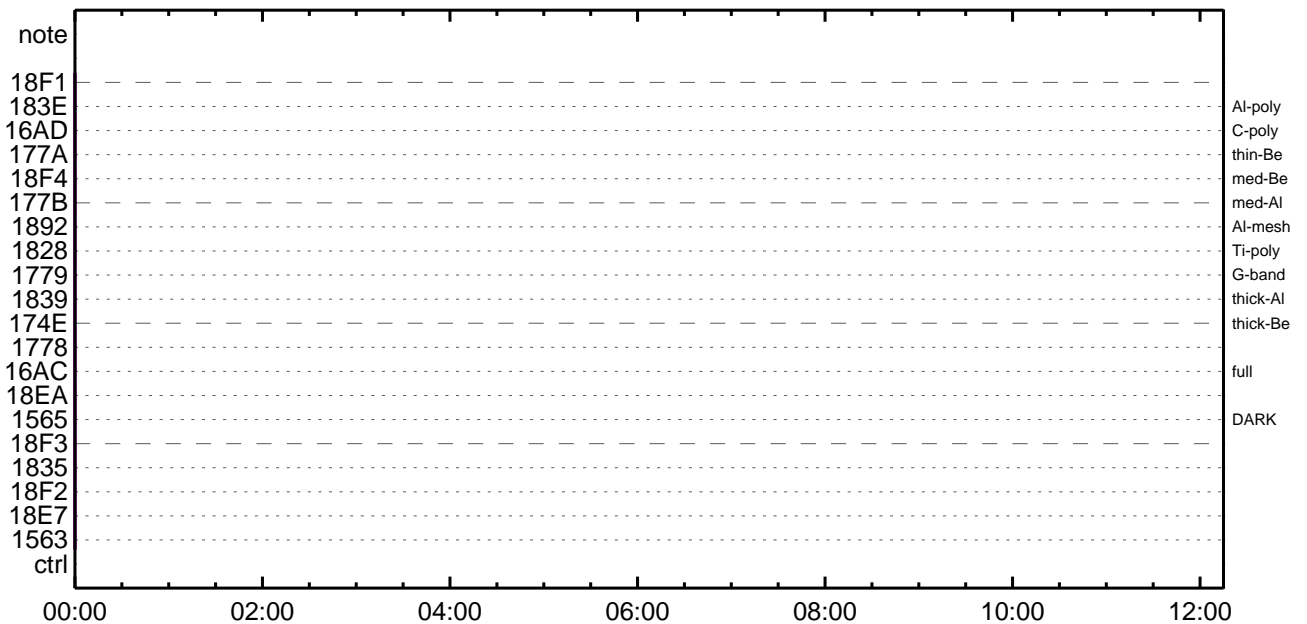
CMDI #0697 2012/07/11



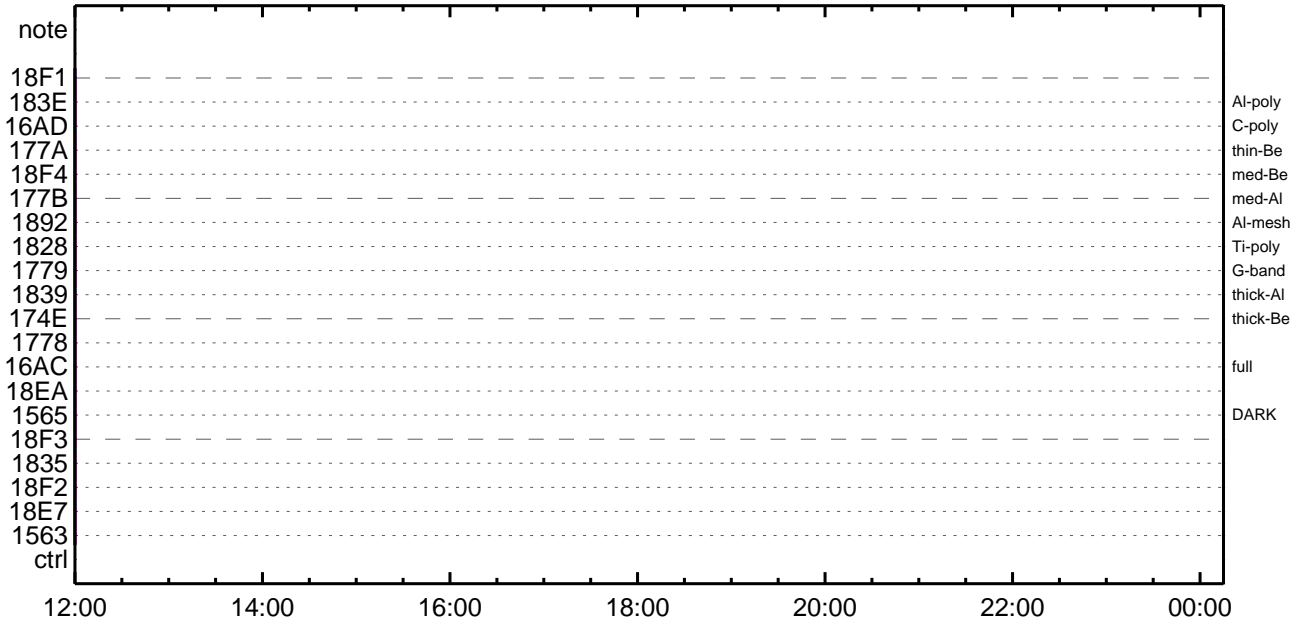
CMDI #0697 2012/07/11



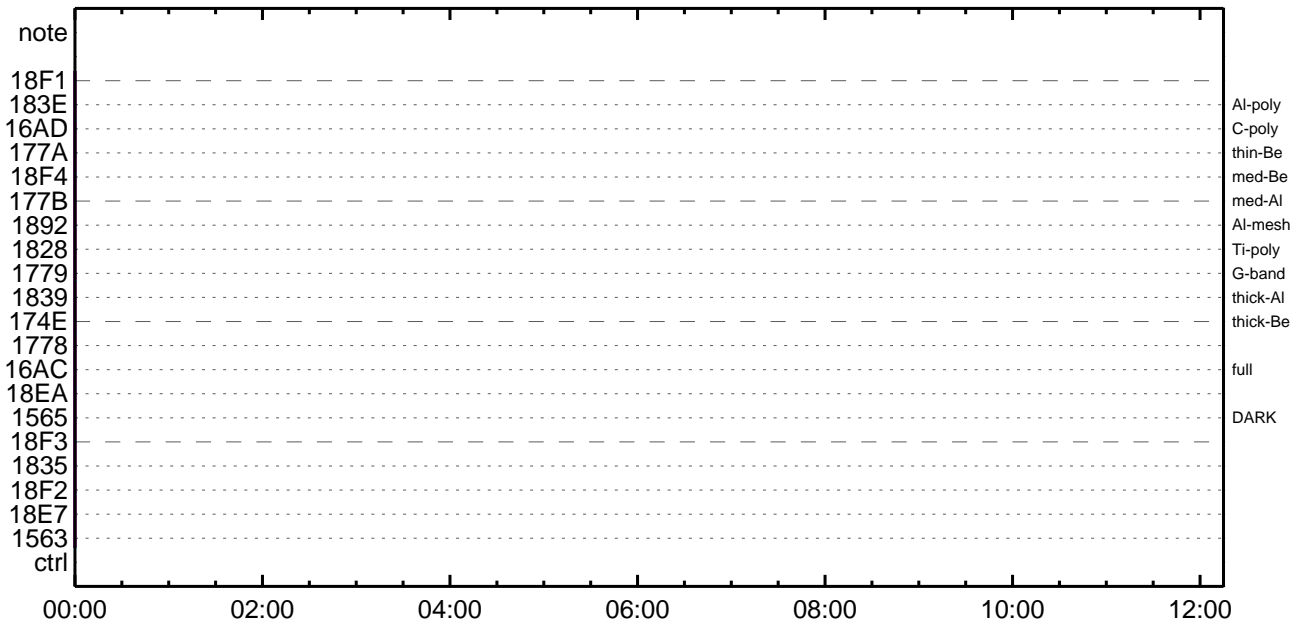
CMDI #0697 2012/07/12



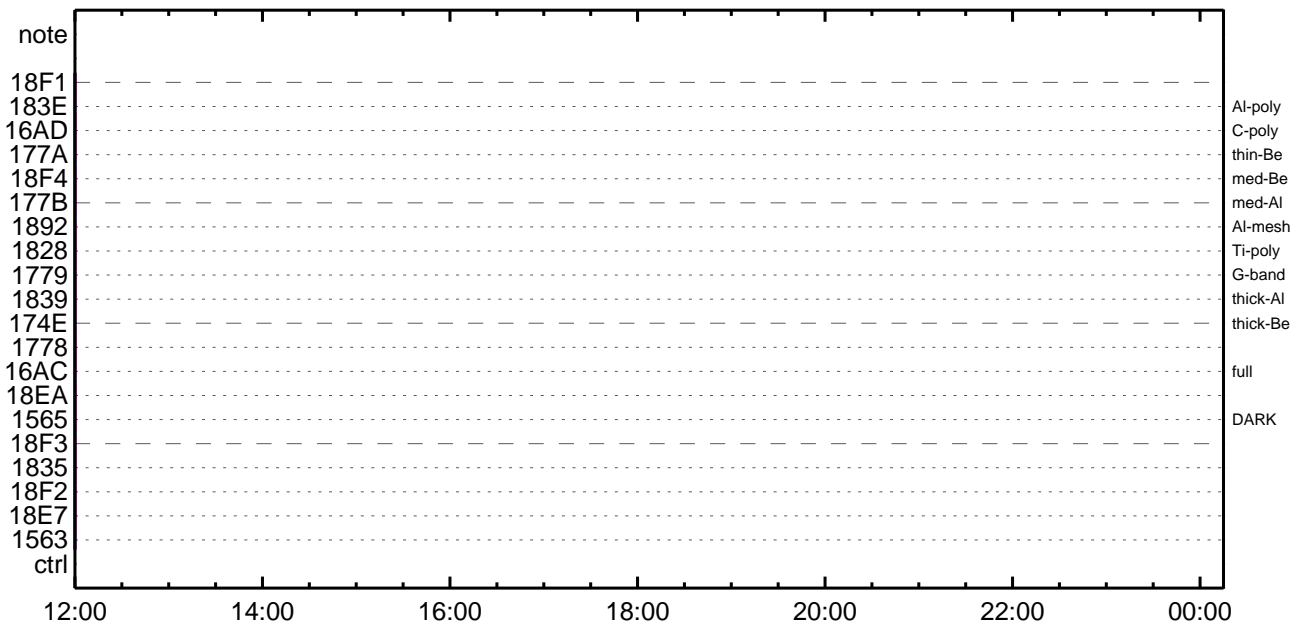
CMDI #0697 2012/07/12



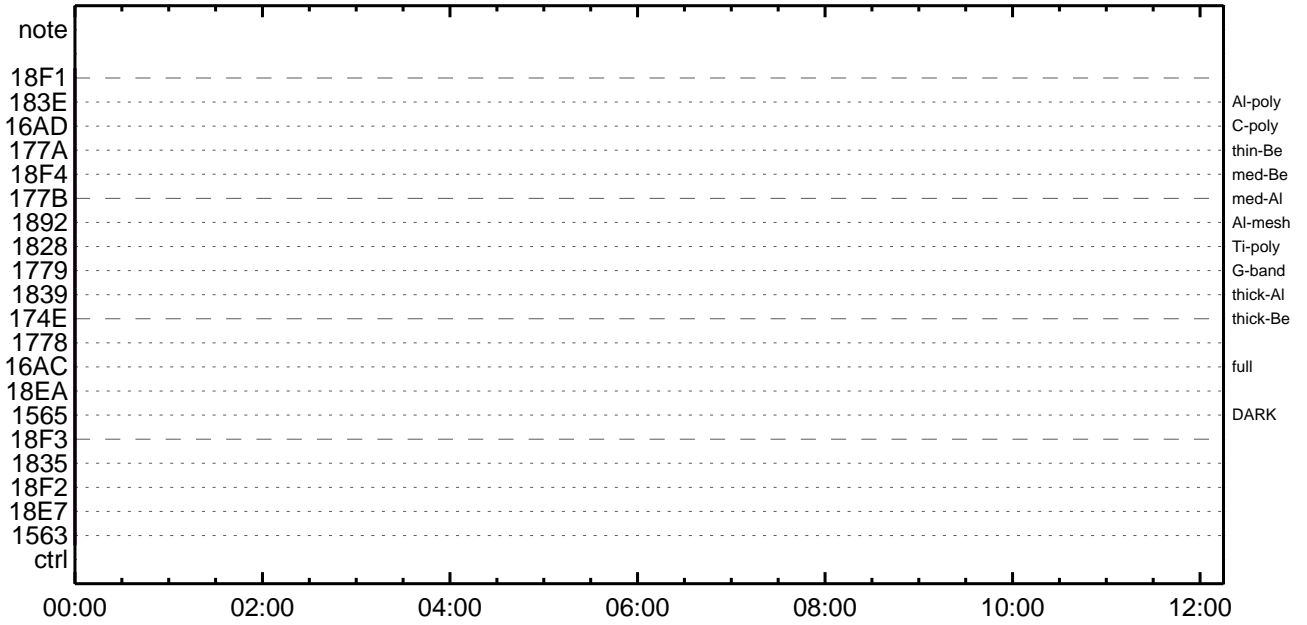
CMDI #0697 2012/07/13



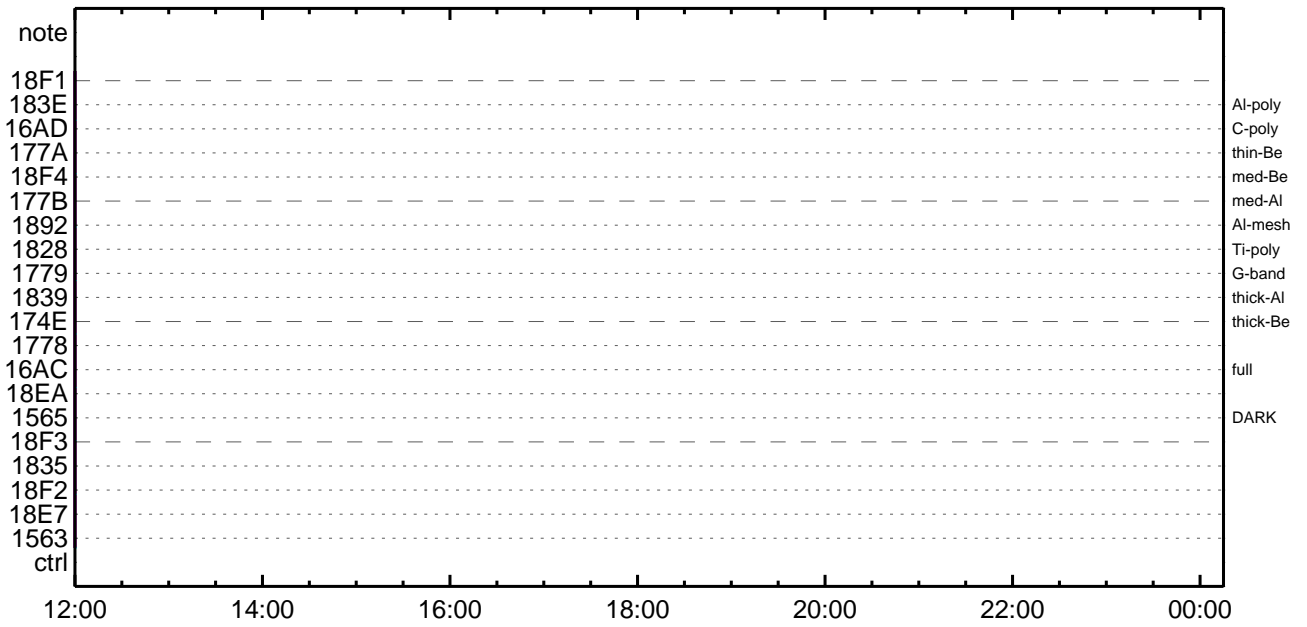
CMDI #0697 2012/07/13



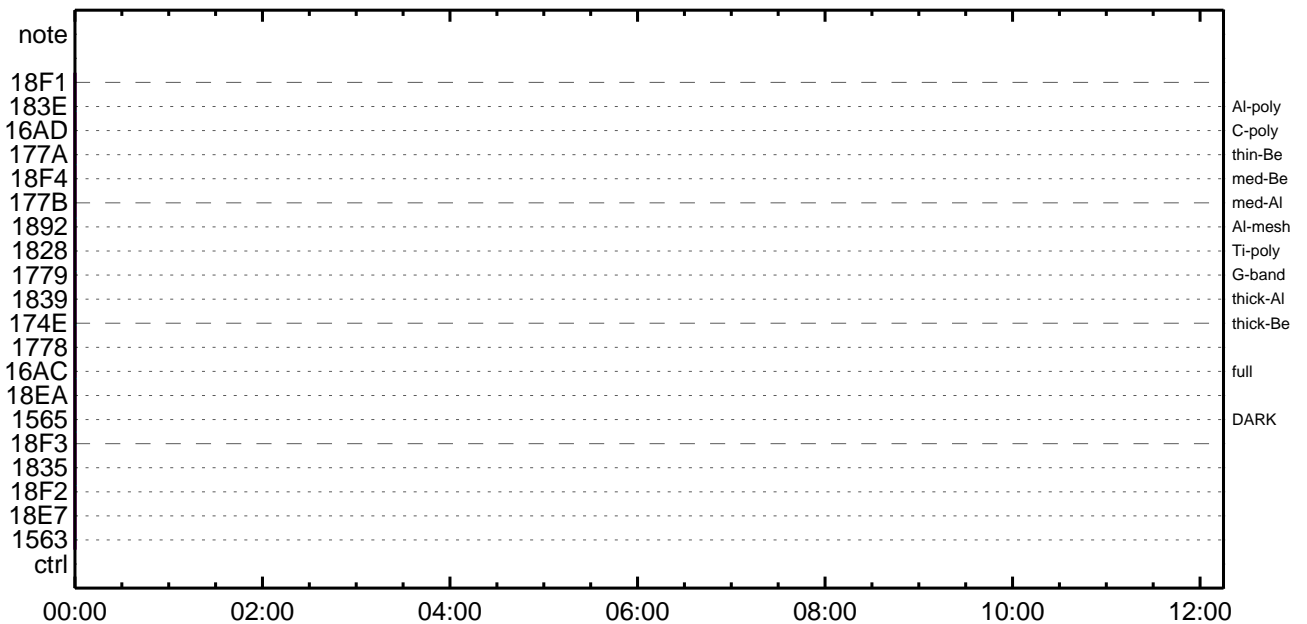
CMDI #0697 2012/07/14



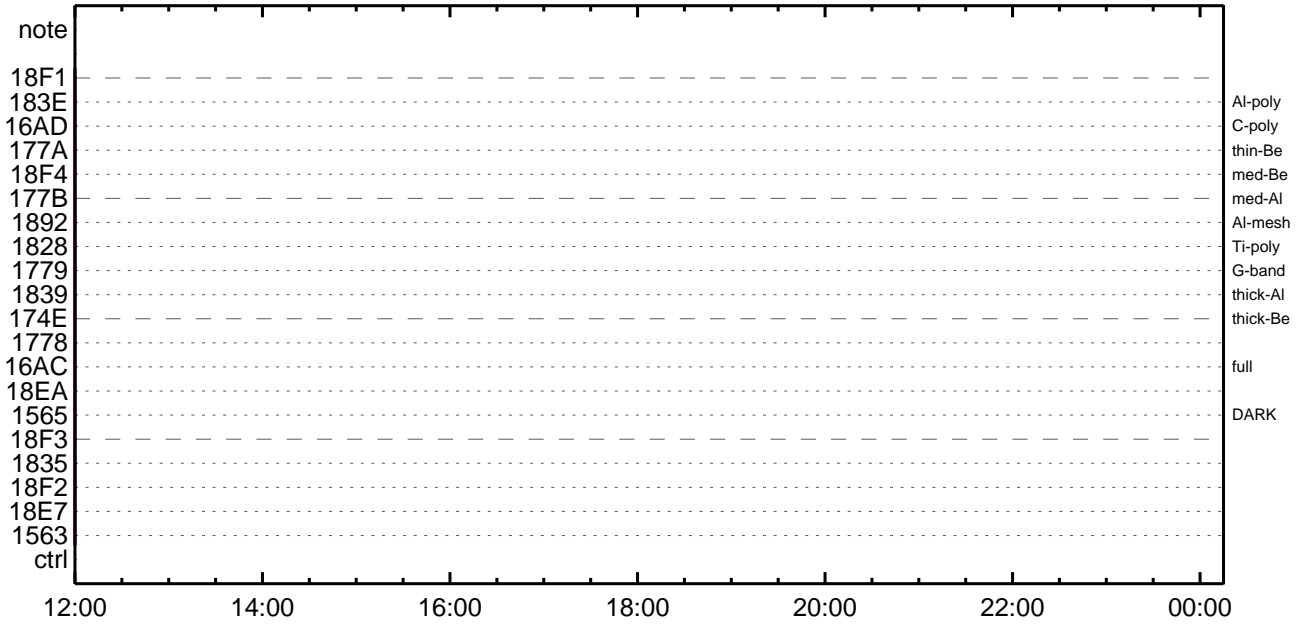
CMDI #0697 2012/07/14



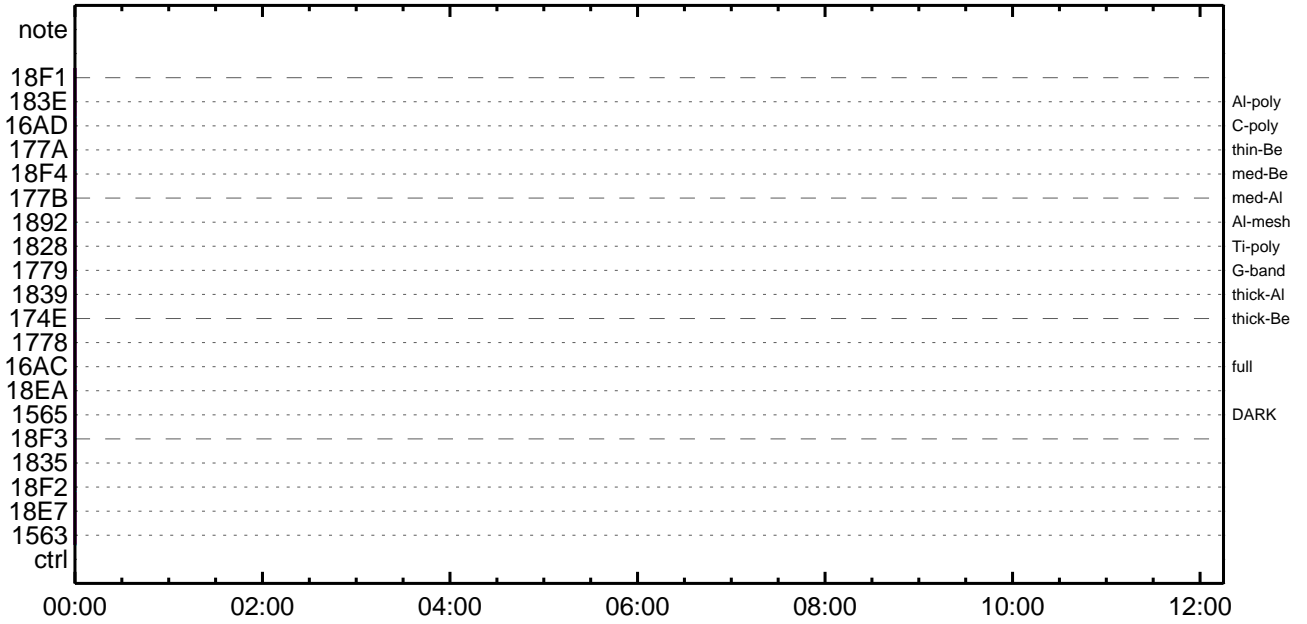
CMDI #0697 2012/07/15



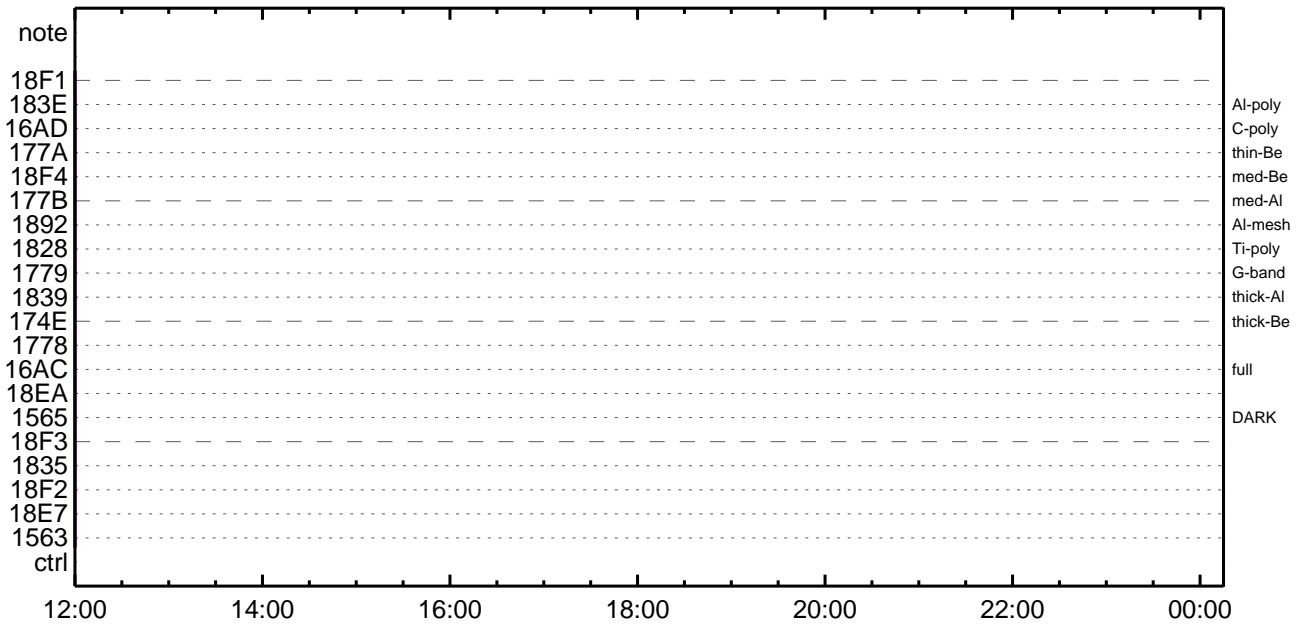
CMDI #0697 2012/07/15



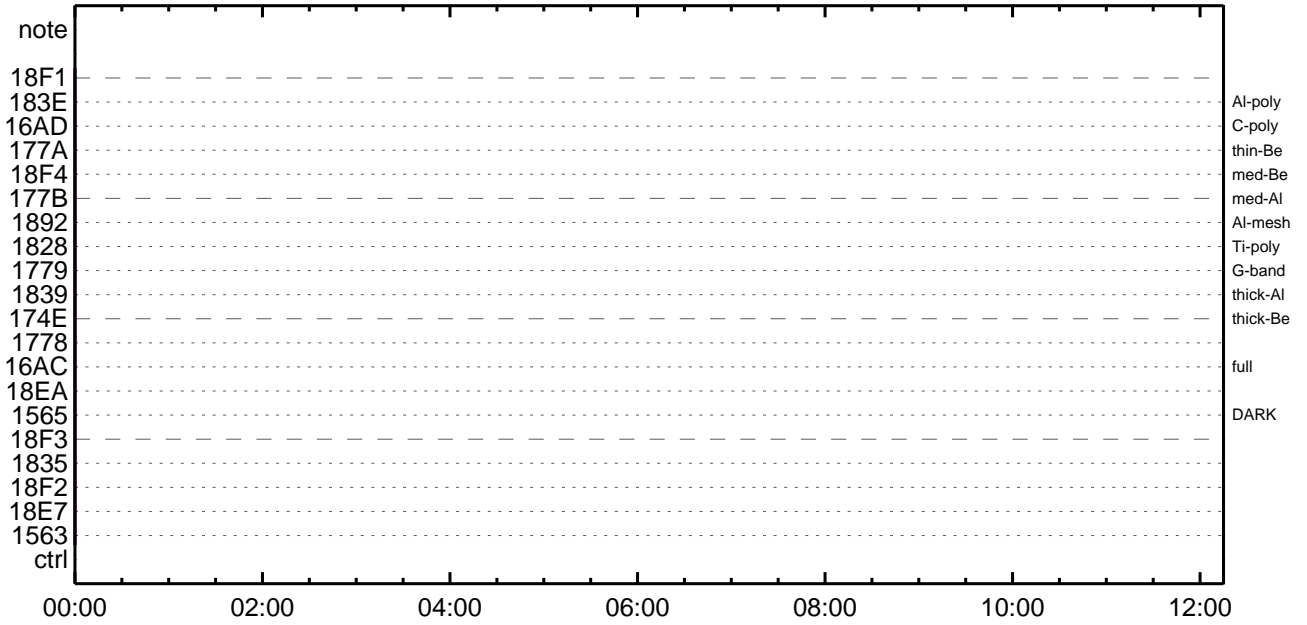
CMDI #0697 2012/07/16



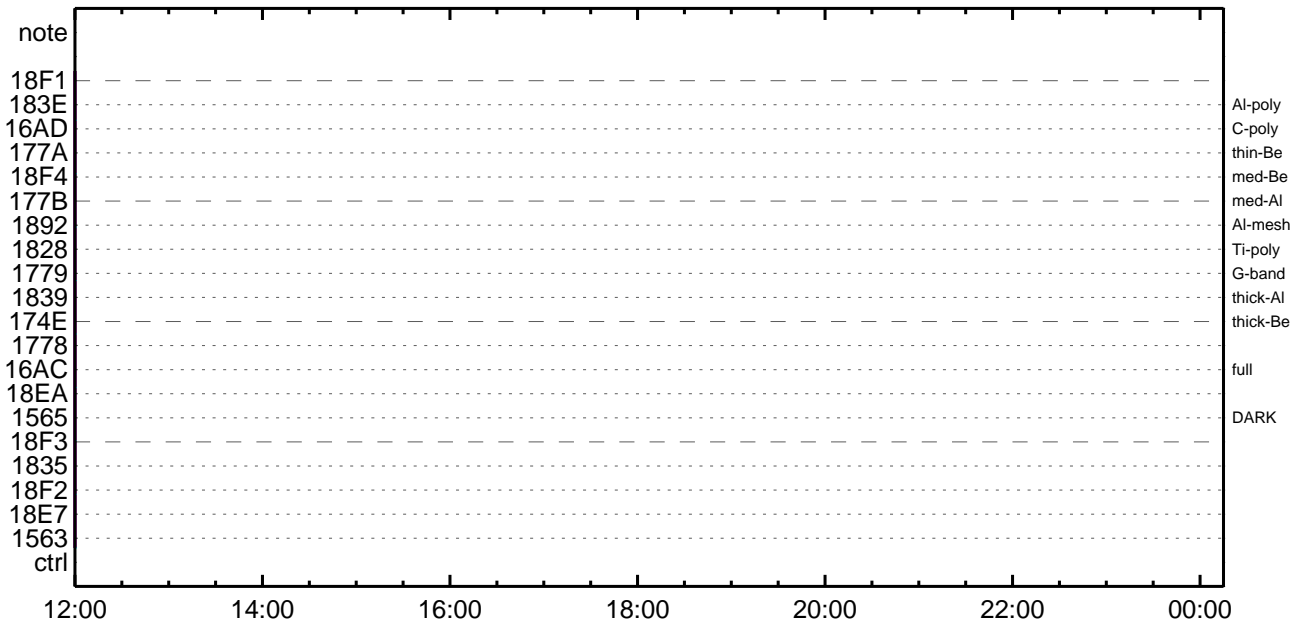
CMDI #0697 2012/07/16



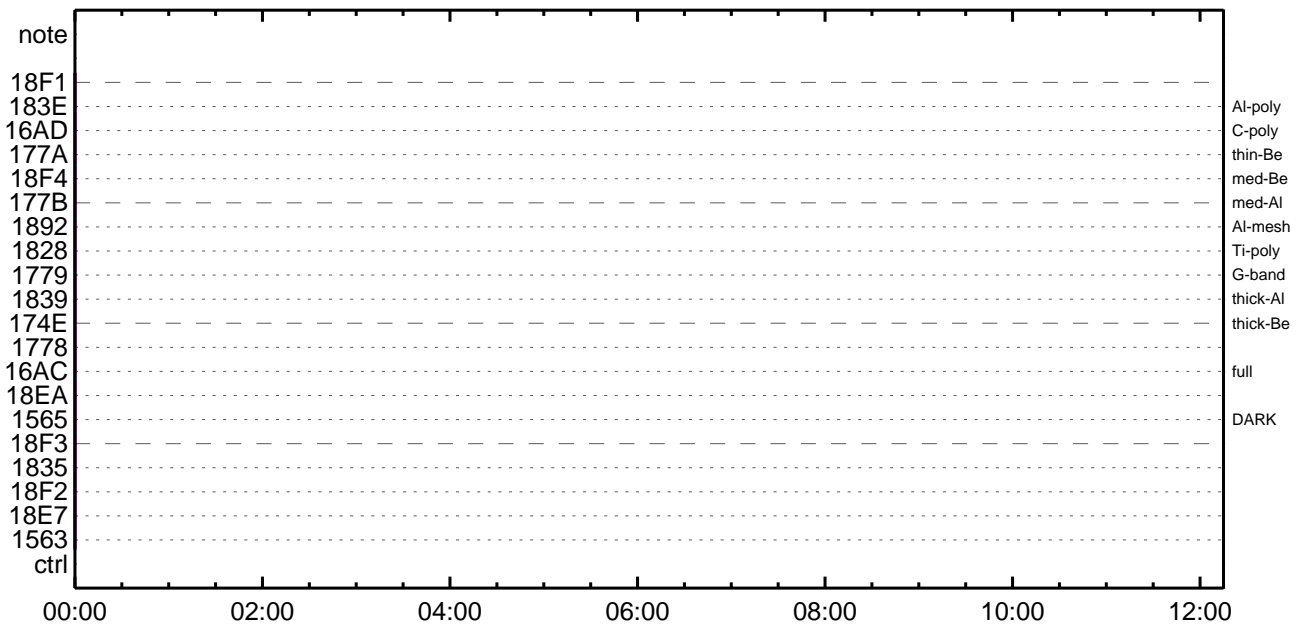
CMDI #0697 2012/07/17



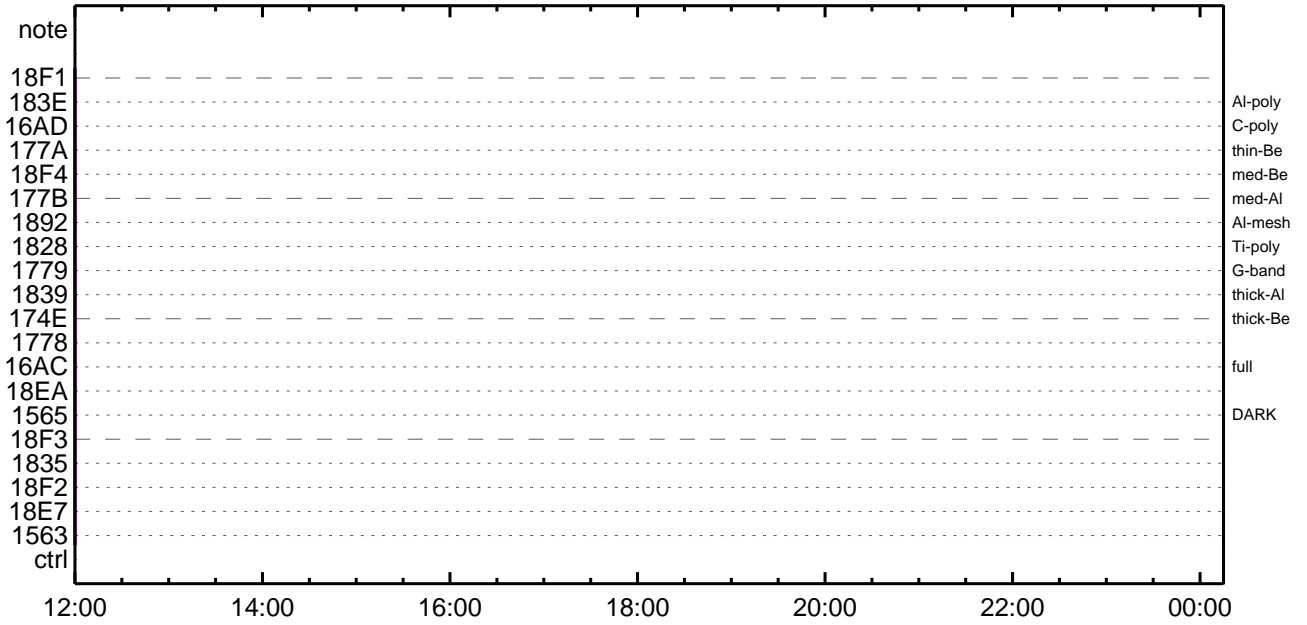
CMDI #0697 2012/07/17



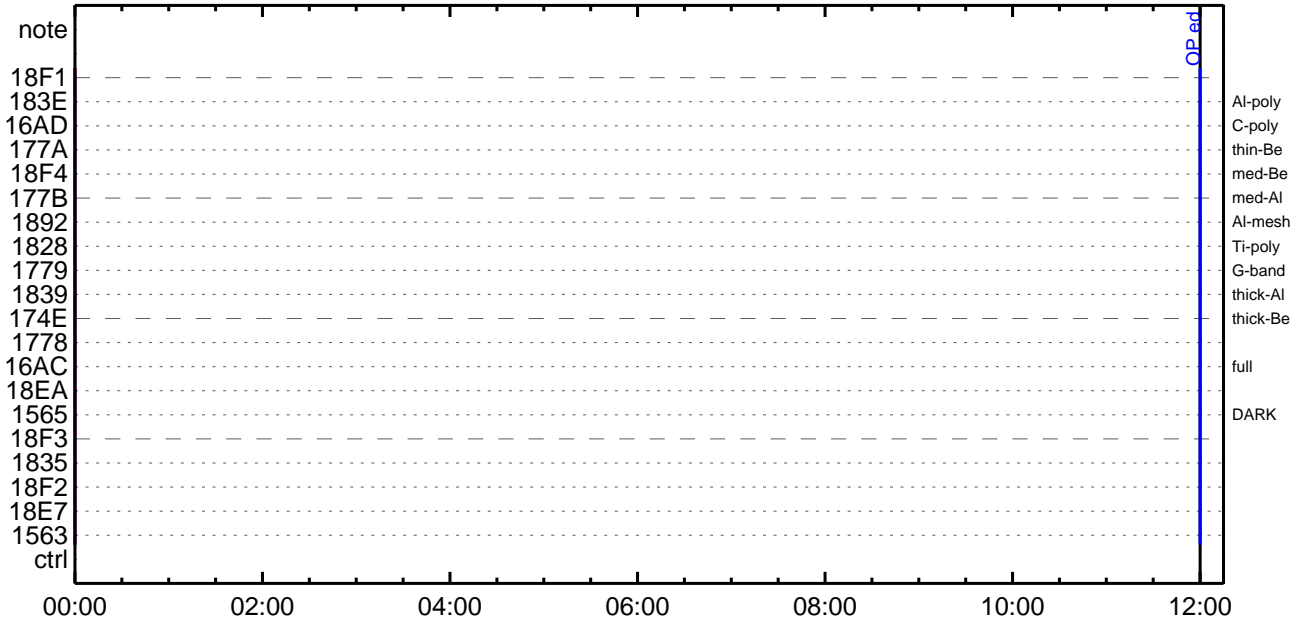
CMDI #0697 2012/07/18



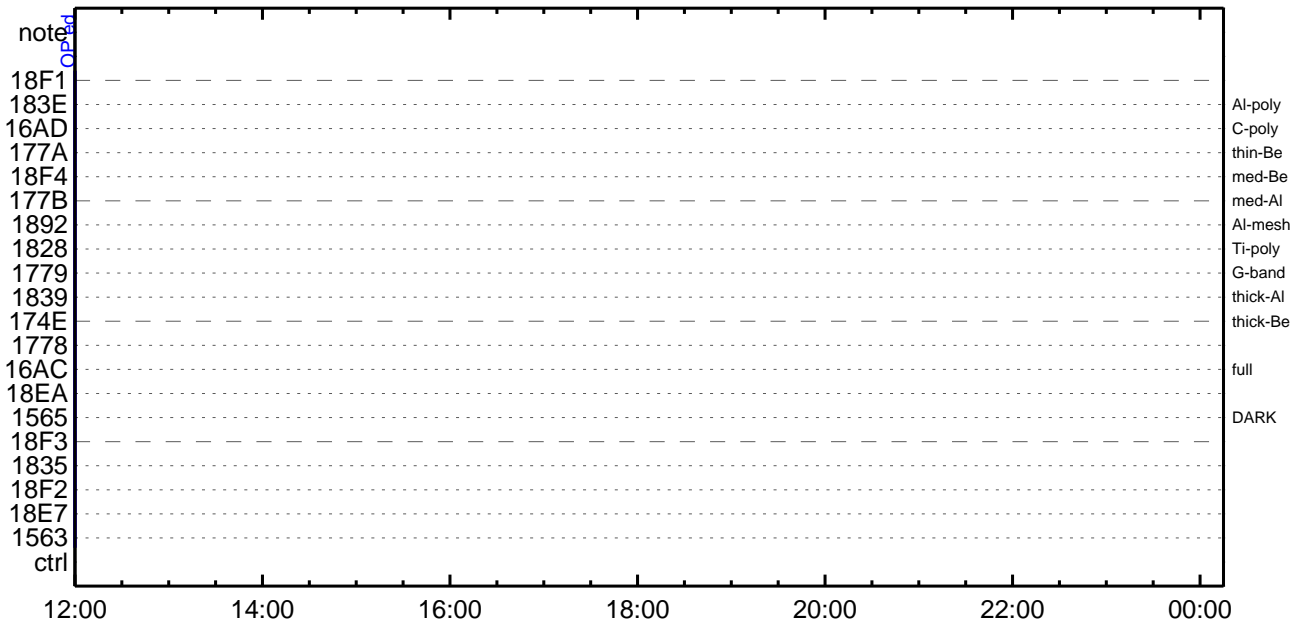
CMDI #0697 2012/07/18



CMDI #0697 2012/07/19



CMDI #0697 2012/07/19



(a) Spacecraft Operation Procedure (real-commands)

```
main-936 2012-06-14 12:11:13 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÃYÃY-¼Ã»Û;ã
0005 C.
0006 C. YÃYÃY;¼Y³YF¥ÓYÉÃ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿òÄº·μ°È>Í×ÃÇóÍYÇYÃY×YÍ;¼YÉ;ÈÈÈ%μ·ÍÉ;ÈÈÈ%ÇÓº·μ¿¼¿l¹Ç¿Í;ÇÃ®, ùº¹òÈòÈòÇÃ+¿®º·ÈÈÈ¿òÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÃ+¿μ;ON
0016 C. *****
0017 C. °ÈÀ, Í×ÈYòãLOSºÈÇóÍ»p´Öºò¹ÍÍ, μ·; ÇÉÓÍ×ºÈXÃÓONºÍ¹ÓºÈÈºÈÈºÈ¿òÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÃY-¾ÓÃÖº-ºÃÃÈº·μ¿;ÇÇ°È²¼ºÈ¹°ÈÀ, ¼È%Çºò¼Ã¹ÓºÈºÈ; f
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÍ¼í°ÈÀ,
0033 C. *****
0034 C. ° RESTART; ÈPT1; Èº·μ¿º¿¼¿l¹Ç¿Í; Ç°È²¼ºÈ¹°ÈÀ¹ÓºÈºº; ÇDCBC-150º¿¿Èºà; f
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ã¹Ò, ;¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ã¹Ò, ;¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ã¹Ò, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÈYÈÀÚÃØ; ÈÃ·ÃººòÈò; È, áºÈ¹°ÈÀ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ã¹Ò, ;¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ã¹Ò, ;¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ã¹Ò, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, º-¾«È°Ãã»ºº·μ¿, á; Ç°È²¼ºò¼Ã¹ÓºÈºÈ; f
0055 C. YÇYÓYÈYÈÀÚÃØºÈÃ·ÃººòÈò-¾áº¿¼¿l¹Ç¿Í´ººº¹ººººººÇÃºº; f
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÍ¼í°ÈÀ,
0059 C. *****
0060 C. ° RESTART; ÈPT2; Èº·μ¿º¿¼¿l¹Ç¿Í; Ç°È²¼ºÈ¹°ÈÀ¹ÓºÈºº; ÇDCBC-151º¿¿Èºà; f
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ã¹Ò, ;¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ã¹Ò, ;¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ã¹Ò, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÈYÈÀÚÃØ; ÈÃ·ÃººòÈò; È, áºÈ¹°ÈÀ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ã¹Ò, ;¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ã¹Ò, ;¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ã¹Ò, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÀ, Ãã»º; ÇXÃ+¿μ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÀ, Ãã»º;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 C. ;ãXÃ+¿μ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

```
0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-936:OP
0104 ( )
0105 S. OG og-936:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èÿÄÿóÿx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ÿÄÿóÿx½ªî»ò³îç§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî¼Ë¹ç•ë²îOKò³îç§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ÿÄÿóÿx½ªî»ò³îç§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî¼Ë¹ç•ë²îOKò³îç§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ÿÄÿóÿx½ªî»ò³îç§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OPãî¼Ë¹ç•ë²îOKò³îç§
0165 C.
0166 C. ***** òË²¼òî¼Ã´¶Á°òËË¬ò°Á÷¿@ (¼âµ-ÿÄÿóÿx½ªî»ò³îç§òÄÔÃæòç¼ª°¬òë¼î¹çòçòâ) *****
0167 C. DHUÿâ;¼ÿË;Ë¼ÿ½;ÿî;¼ÿË;Ëòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADò¬Á÷¿@NGòî¼î¹ç;ç°Ë²¼òî¼TI-CMDÁ÷¿@òî¼Á¹Ôò•òËòòò³òË;f
0180 C. òðò¿;çSETòÈDUMPòîÆ±°îÿNÿ¹òç¹Ôò|ò³òË;f
0181 C.
0182 C. TIÿ³ÿðÿóÿËòðòÄî¿¿(UT)
0183 +. TI 2012-06-14 09:16:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-06-14 09:16:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-06-14 09:16:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

```

0194 C.
0195 +. TI 2012-06-14 09:20:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄê%îíñαîî¥Á¥§¥Á¥-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°è¥Á¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ¥Á¥Ö¥×½ªî»αò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îÇ§
0229 C.
0230 C. DHU¥â;¼¥É;È¼¥¼. ¥î;¼¥È;Èαòîáα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-06-14 09:20:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-06-14 09:20:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2012-06-14 09:20:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-06-14 09:20:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´úÃîαî»ö¼ÝαÈÄα¹αèDCBC•x²è *****
0276 C. (¼á°îî¥Á¥È¥¥¥È¥á¥ç¥èè%¼αα¼Á»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•î Daily±çîñαè'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Á¥§¥Á¥-¼Á»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-262:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2012-06-14 09:20:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 80 80 20 20)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 08)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 08 20)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0f 80 80 06 06)
0168 + DC 07-F0 MDP_XRT_FLD_DIS
0169 BC (d9)
0170 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0171 BC (c9)
0172 + DC 07-F0 MDP_XRT_AEC_RESET
0173 BC (d0)
0174 + DC 07-F0 MDP_XRT_ARS_DIS
0175 BC (d5)
0176 + DC 07-F0 MDP_XRT_FLD_RESET
0177 BC (da)
0178 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0179 BC (c4 13)
0180 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0181 BC (c5 02)
0182 . C. ----- Success Verify ? OK / NG ____
0183 C.
0184 C.
0185 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0186 C.
0187 +. DC 07-F0 MDP_XRT_MODE_OBSV
0188 BC (c2)
0189 +. TI 2012-06-14 09:20:02.0
0190 DC 07-F0 MDP_XRT_MODE_OBSV
0191 BC (c2)
0192 . C. ----- Success Verify ? OK / NG ____
0193 C.

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0194 C. ***** XRT END *****
0195 C.
0196 . C. ***** MDP 'úÃîâî»ô¼ŸâÊâð¹âêDCBC•x²è *****
0197 C. (¼â°îŸŌŸÃŸÊŸpŸEŸáŸçŸèâE¼â¼â»Û¹âè)
0198 . S. DC-BC dcbc-402:DCBC
0199 (MDP_known_event)
0200 C.
0201 C.
0202 . C. ***** ŸÐŸ¹•î Daily±¿îÑâE´Ø¹âêDCBC•x²è *****
0203 . S. DC-BC dcbc-153:DCBC
0204 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0205 C.
0206 C.
0207 . C. ;ãLOSŸÁŸ§ŸÃŸ⁻¼â»Û;ã
0208 C.
0209 . C. ***** LOS *****
0210 C.

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*** OP Sequence for XRT ***

2012/06/14	09:31:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	57	ce	00	72
2012/06/14	10:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/14	11:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02	00	00	00	00
2012/06/14	11:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97		00
2012/06/14	11:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0					d8
2012/06/14	11:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2012/06/14	11:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0					d0
2012/06/14	11:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2012/06/14	11:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/06/14	11:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 04
2012/06/14	11:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0					c5 02
2012/06/14	11:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/06/14	11:12:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/14	11:12:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/06/14	11:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/06/14	11:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/06/14	11:42:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/14	11:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/06/14	12:51:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/14	12:51:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/06/14	12:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/06/14	12:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/06/14	13:21:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/14	13:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/06/14	14:29:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/14	14:29:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/06/14	14:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/06/14	14:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/06/14	15:07:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/14	15:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/06/14	16:08:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/14	16:08:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2012/06/14	16:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2012/06/14	16:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2012/06/14	16:54:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/14	16:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/06/14	17:31:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/14	17:31:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2012/06/14	17:31:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2012/06/14	17:31:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2012/06/14	17:31:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2012/06/14	17:31:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2012/06/14	17:34:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 07
2012/06/14	17:34:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2012/06/14	17:41:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/14	21:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2012/06/14	21:20:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02	00	00	00	00
2012/06/14	21:22:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							

2012/06/14	21:22:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		MDP_XRT_FLD_ENA		1	07-F0	d8			
2012/06/14	21:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/06/14	21:22:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/06/14	21:22:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/06/14	21:22:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/14	21:22:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2012/06/14	21:22:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2012/06/14	21:45:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/14	21:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/14	22:42:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/14	22:42:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/14	22:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/14	22:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/14	23:18:00.5	XRT_Custom_418_OG [0x1a2]							
2012/06/14	23:19:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/15	00:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/15	00:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/15	00:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/15	00:20:34.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/15	00:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/15	00:50:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/15	00:51:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/15	01:59:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/15	01:59:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/15	01:59:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/15	01:59:04.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/15	02:02:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/15	02:29:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/15	02:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/15	03:31:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/15	03:31:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/15	03:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/15	03:31:34.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/15	03:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/15	04:07:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/15	04:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/15	05:07:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/15	05:07:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/06/15	05:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/06/15	05:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/06/15	05:46:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/15	05:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/15	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/15	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2012/06/15	06:00:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00
2012/06/15	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/06/15	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/06/15	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/06/15	06:02:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07		
2012/06/15	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/06/15	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/06/15	06:10:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02	00	00	00

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2012/06/15	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/06/15	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/06/15	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/06/15	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/06/15	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/15	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/15	06:12:56.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2012/06/15	06:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2012/06/15	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/15	06:47:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/15	06:47:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/15	06:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/15	06:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/15	07:24:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/15	07:25:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/15	07:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/15	08:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	57	ce	00	72
2012/06/15	10:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/15	11:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01	00	00	00	00
2012/06/15	11:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/06/15	11:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/06/15	11:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/06/15	11:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/06/15	11:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/15	11:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/15	11:02:56.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2012/06/15	11:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2012/06/15	11:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/15	11:49:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/15	11:49:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/15	11:49:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/15	11:52:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/15	12:20:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/15	12:21:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/15	13:28:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/15	13:28:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/15	13:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/15	13:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/15	13:58:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/15	13:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/15	15:06:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/15	15:06:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/15	15:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/15	15:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/15	15:54:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/15	15:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/15	16:45:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/15	16:45:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/15	16:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							

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2012/06/15	16:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/15	17:31:00.0	XRT_Custom_418_OG [0x1a2]						
2012/06/15	17:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/15	18:02:24.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/15	18:02:26.0	XRT_FOCUS_POSITION_401_OG [0x191]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/06/15	18:02:30.0	AOCS_ORe-point_Start_3_OG [0x099]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2012/06/15	18:02:46.0	XRT_FLD_DIS_402_OG [0x192]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/06/15	18:02:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/06/15	18:02:50.0	XRT_ARS_DIS_438_OG [0x1b6]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/06/15	18:05:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2012/06/15	18:05:30.0	XRT_CTRL_AUTO_406_OG [0x196]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/15	18:12:24.0	XRT_CTRL_MANU_439_OG [0x1b7]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/15	18:12:30.0	AOCS_ORe-point_Start_4_OG [0x09a]						
			AOCU_NM	5	02-76	01 00 00 00 00		
2012/06/15	18:14:56.0	XRT_FOCUS_POSITION_409_OG [0x199]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/06/15	18:15:16.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/06/15	18:15:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/06/15	18:15:20.0	XRT_AEC_RESET_443_OG [0x1bb]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/06/15	18:15:22.0	XRT_ARS_DIS_431_OG [0x1af]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/06/15	18:15:24.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/15	18:15:26.0	XRT_QT_PROG_SET_415_OG [0x19f]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2012/06/15	18:15:28.0	XRT_FL_PROG_SET_414_OG [0x19e]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2012/06/15	18:15:30.0	XRT_CTRL_AUTO_406_OG [0x196]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/15	18:23:30.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/15	18:23:32.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/15	18:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/06/15	18:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/15	19:07:30.0	XRT_Custom_418_OG [0x1a2]						
2012/06/15	19:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/15	20:02:00.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/15	20:02:02.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/15	20:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/06/15	20:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/15	20:45:00.0	XRT_Custom_418_OG [0x1a2]						
2012/06/15	20:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/15	21:40:30.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/15	21:40:32.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/15	21:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/06/15	21:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/15	22:21:00.0	XRT_Custom_418_OG [0x1a2]						
2012/06/15	22:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/15	23:19:00.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/15	23:19:02.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/15	23:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/06/15	23:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/06/15	23:49:30.0	XRT_Custom_418_OG [0x1a2]						
2012/06/15	23:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/06/16	00:57:30.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/06/16	00:57:32.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/06/16	00:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						

2012/06/16	01:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/16	01:28:00.0	XRT_Custom_418_OG [0x1a2]					
2012/06/16	01:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/16	02:35:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/16	02:35:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/16	02:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/16	02:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/16	03:06:30.0	XRT_Custom_418_OG [0x1a2]					
2012/06/16	03:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/16	04:04:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/16	04:04:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/16	04:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/16	04:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/16	04:45:00.0	XRT_Custom_418_OG [0x1a2]					
2012/06/16	04:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/16	05:45:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/16	05:45:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/16	05:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/16	05:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/16	06:23:30.0	XRT_Custom_418_OG [0x1a2]					
2012/06/16	06:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/16	06:29:24.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/16	06:29:26.0	XRT_FOCUS_POSITION_401_OG [0x191]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/06/16	06:29:30.0	AOCS_ORe-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2012/06/16	06:29:46.0	XRT_FLD_DIS_402_OG [0x192]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/06/16	06:29:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/06/16	06:29:50.0	XRT_ARS_DIS_438_OG [0x1b6]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/06/16	06:32:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2012/06/16	06:32:30.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/16	06:39:24.0	XRT_CTRL_MANU_439_OG [0x1b7]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/16	06:39:30.0	AOCS_ORe-point_Start_4_OG [0x09a]					
			AOCU_NM	5	02-76	01 00 00 00 00	
2012/06/16	06:41:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/06/16	06:42:16.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/06/16	06:42:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/06/16	06:42:20.0	XRT_AEC_RESET_443_OG [0x1bb]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/06/16	06:42:22.0	XRT_ARS_DIS_431_OG [0x1af]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/06/16	06:42:24.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/16	06:42:26.0	XRT_QT_PROG_SET_415_OG [0x19f]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2012/06/16	06:42:28.0	XRT_FL_PROG_SET_414_OG [0x19e]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2012/06/16	06:42:30.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/16	07:25:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/16	07:25:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/16	07:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/16	07:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/16	07:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/16	08:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	00 57 ce 00 72	
2012/06/16	09:41:00.0	AOCS_ORe-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	00 00 00 00 00	