

XRT Timeline to be uploaded on 2012/06/19

Period: 2012/06/19 10:16:00 - 2012/07/23 12:00:00

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Normal mode

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XOB #183D: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 40s cad												
Term	Pointing (x, y)	Comment										
06/19 12:03:00 - 06/19 15:57:30	Track (742.9, -329.4) ^{Ⓜ 06/19 12:00:00}	AR 11504										
06/20 03:58:30 - 06/20 05:52:54	Track (806.8, -326.6) ^{Ⓜ 06/20 03:30:00}	AR 11504										
06/20 06:06:00 - 06/20 07:59:54	Track (815.7, -326.0) ^{Ⓜ 06/20 06:03:00}	cont.										
06/20 11:03:00 - 06/20 17:58:54	Track (831.5, -325.0) ^{Ⓜ 06/20 11:00:00}	AR 11504										
06/20 20:36:30 - 06/21 06:19:24	Track (855.8, -322.8) ^{Ⓜ 06/20 20:09:00}	cont.										
06/21 06:32:30 - 06/21 07:59:54	Track (875.4, -320.2) ^{Ⓜ 06/21 06:29:30}	cont.										
PROG= 20 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec			
└─ Open/G-band	Open/G-band open	Safe Norm	63ms	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec			
└─ Seqn= 1 4-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
└─ Open/thick-Al	Open/thick-Be close	Safe Norm	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 49 18-time(s) 2.0sec												
└─ Open/Al-mesh	Open/thick-Al close	Safe Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
└─ Open/Ti-poly	Open/thick-Be close	Safe Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	8.0sec			
└─ Open/Al-mesh	Open/thick-Al close	Safe Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec			
└─ Open/Ti-poly	Open/thick-Be close	Safe Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	1	8.0sec			
└─ Open/Al-mesh	Open/thick-Al close	Safe Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec			
└─ Open/Ti-poly	Open/thick-Be close	Safe Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	2	8.0sec			
└─ Open/Al-mesh	Open/thick-Al close	Safe Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec			
└─ Open/Ti-poly	Open/thick-Be close	Safe Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	3	8.0sec			
Default Filter	Thicker Filter	VLS	mode image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

XOB #18EA: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-band(16)

Term	Pointing (x, y)	Comment										
06/19 18:24:00 - 06/19 19:14:30	Fixed (0.0, 0.0)	synoptic, shifted 21.0 min										
06/20 05:56:00 - 06/20 06:02:54	Fixed (0.0, 0.0)	synoptic, shifted -7.0 min										
06/20 18:02:00 - 06/20 18:13:30	Fixed (0.0, 0.0)	synoptic, shifted -1.0 min, and SOT flat field for 2hrs.										
06/21 06:22:30 - 06/21 06:29:24	Fixed (0.0, 0.0)	synoptic, shifted 19.5 min										
PROG= 07 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 46 1-time(s) 4.0sec												
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm	12ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm	707ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec			
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec			
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec			
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec			
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec			
└─ Seqn= 69 1-time(s) 4.0sec												
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm	24ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm	1.41s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
└─ Seqn= 4 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band open	Safe Norm	16ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Default Filter	Thicker Filter	VLS	mode image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

XOB #18F2: HOP130-Synoptic 3 Filter- 1x1 Q98 Short exp 2 loop

Term	Pointing (x, y)	Comment										
06/19 20:07:00 - 06/19 20:53:00	Fixed (0.0, 0.0)	3/15										
PROG= 03 2-time(s)												
└─ Subr= 1 1-time(s) 180.0sec												
└─ Seqn= 55 1-time(s) 25.0sec												
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm	44ms	Obs 1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec			
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm	2.83s	Obs 1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec			
└─ Seqn= 97 1-time(s) 25.0sec												
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm	86ms	Obs 1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec			
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm	5.66s	Obs 1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec			
└─ Seqn= 12 1-time(s) 2.0sec												
└─ Open/thick-Al	Open/thick-Al close	Safe Norm	64.0s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec			
Default Filter	Thicker Filter	VLS	mode image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment									
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06/19 12:03:00 - 06/19 15:57:30	Track (742.9, -329.4)	@ 06/19 12:00:00	AR 11504
06/20 03:58:30 - 06/20 05:52:54	Track (806.8, -326.6)	@ 06/20 03:30:00	AR 11504
06/20 06:06:00 - 06/20 07:59:54	Track (815.7, -326.0)	@ 06/20 06:03:00	cont.
06/20 11:03:00 - 06/20 17:58:54	Track (831.5, -325.0)	@ 06/20 11:00:00	AR 11504
06/20 20:36:30 - 06/21 06:19:24	Track (855.8, -322.8)	@ 06/20 20:09:00	cont.
06/21 06:32:30 - 06/21 07:59:54	Track (875.4, -320.2)	@ 06/21 06:29:30	cont.

PROG= 13 1-time(s)

Subr= 1 30-time(s) 20.0sec												
Seqn= 88 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 100 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 88 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 88 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 88 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec												
Seqn= 89 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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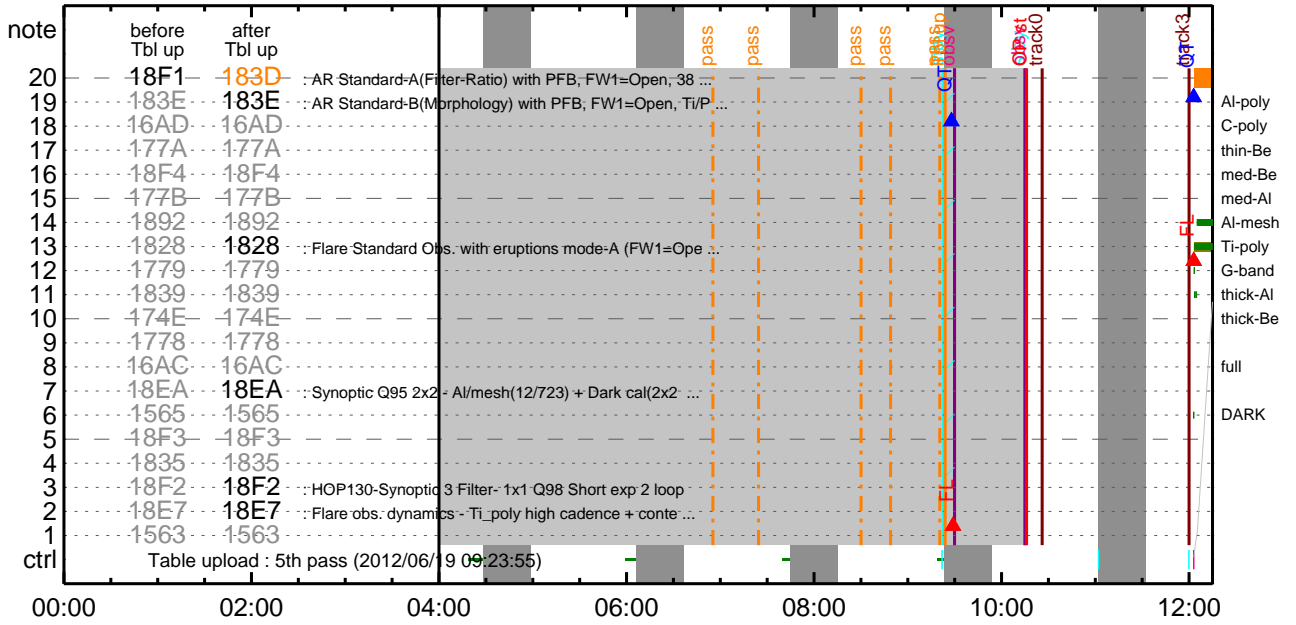
Flare Detection

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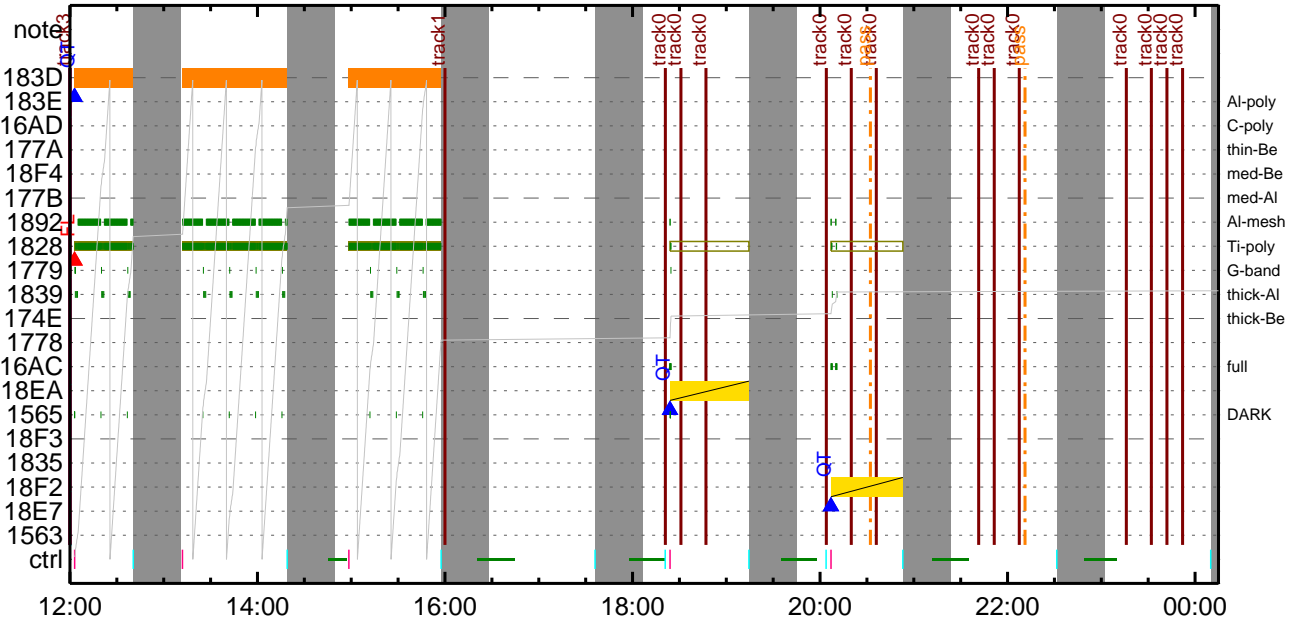
FLD Patrol

Term	Pointing (x, y)	Comment
06/20 03:32:46 - 06/20 05:53:16	Track (806.8, -326.6) @ 06/20 03:30:00	AR 11504
06/20 06:05:46 - 06/20 17:59:16	Track (815.7, -326.0) @ 06/20 06:03:00	cont.
06/20 20:11:46 - 06/21 06:19:46	Track (855.8, -322.8) @ 06/20 20:09:00	cont.
06/21 06:32:16 - 07/23 12:00:00	Track (875.4, -320.2) @ 06/21 06:29:30	cont.
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

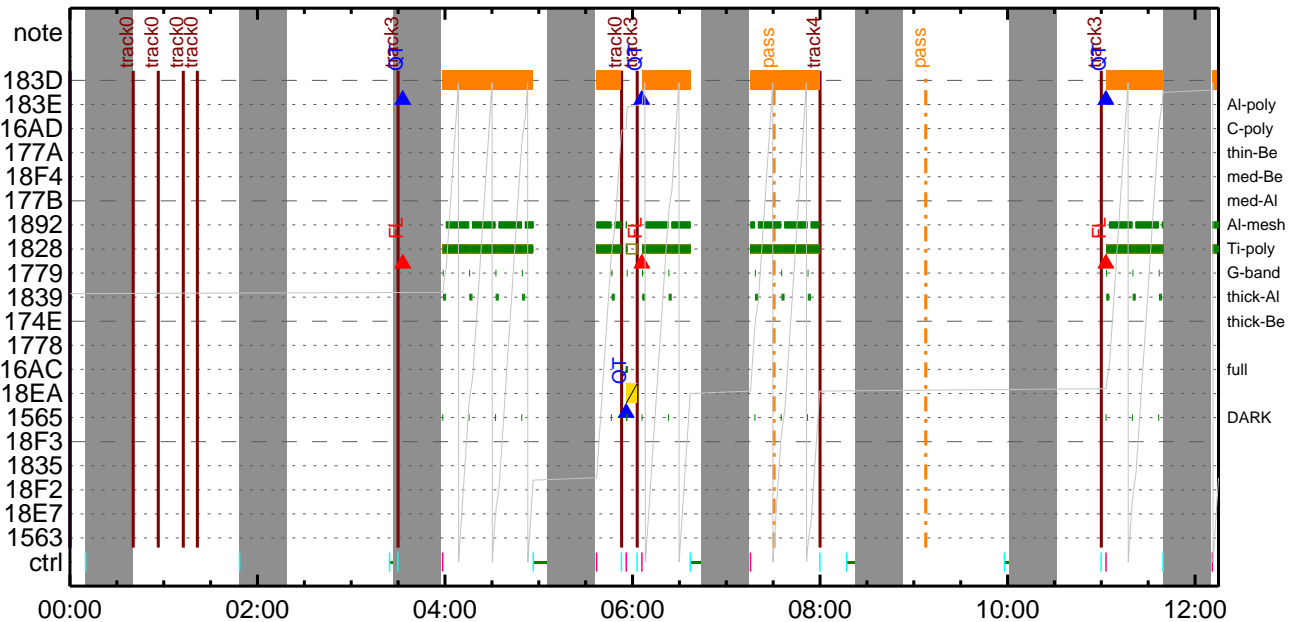
CMDI #0713 2012/06/19



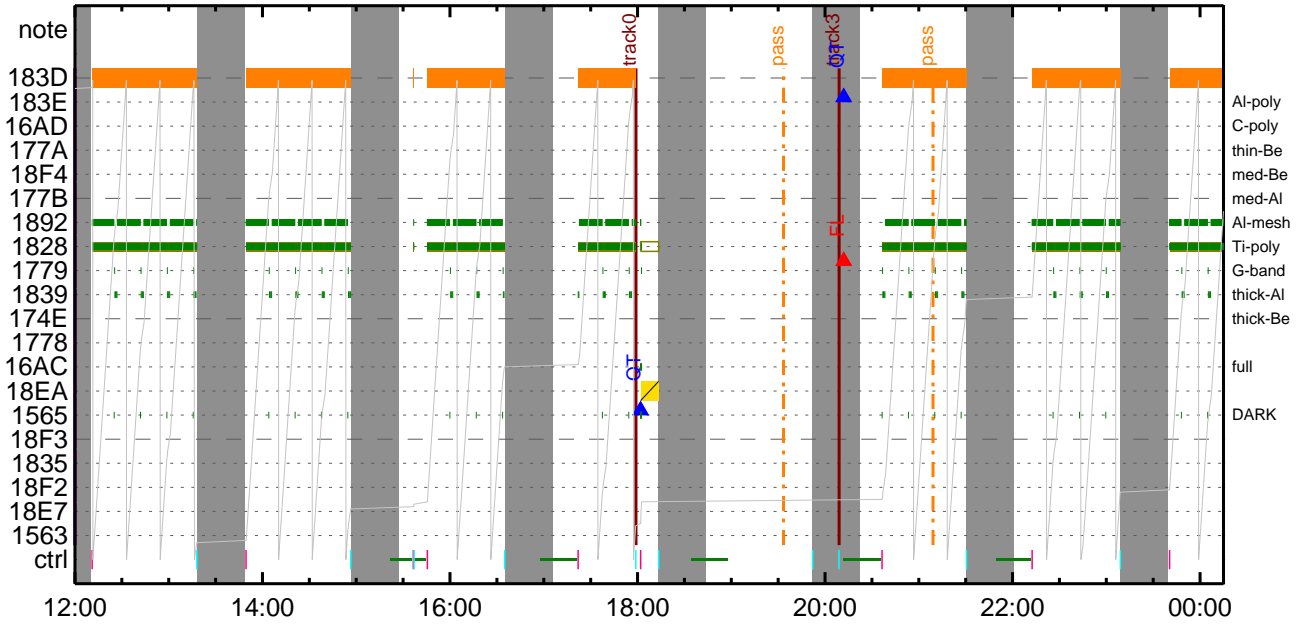
CMDI #0713 2012/06/19



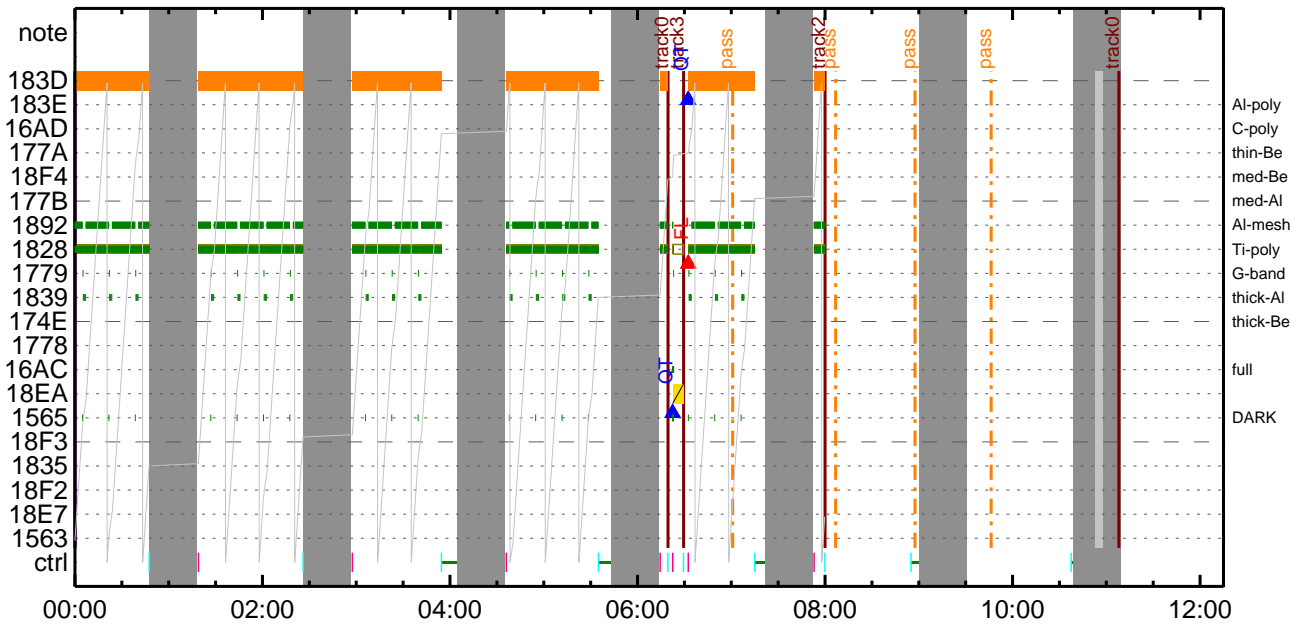
CMDI #0713 2012/06/20



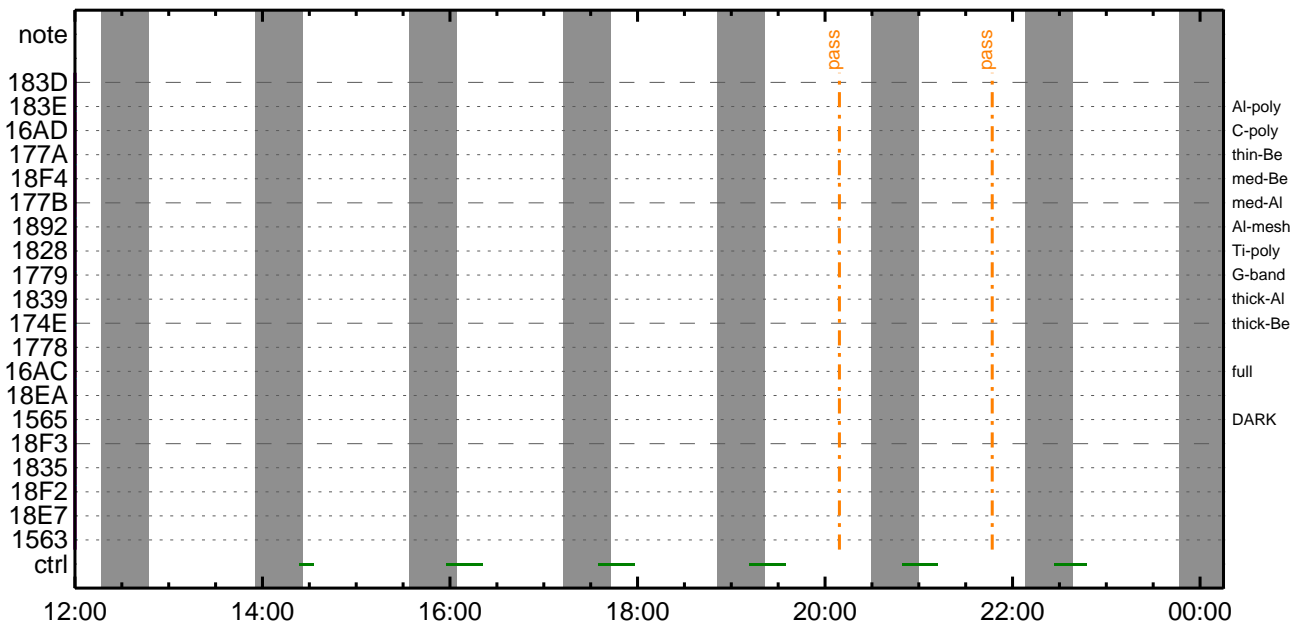
CMDI #0713 2012/06/20



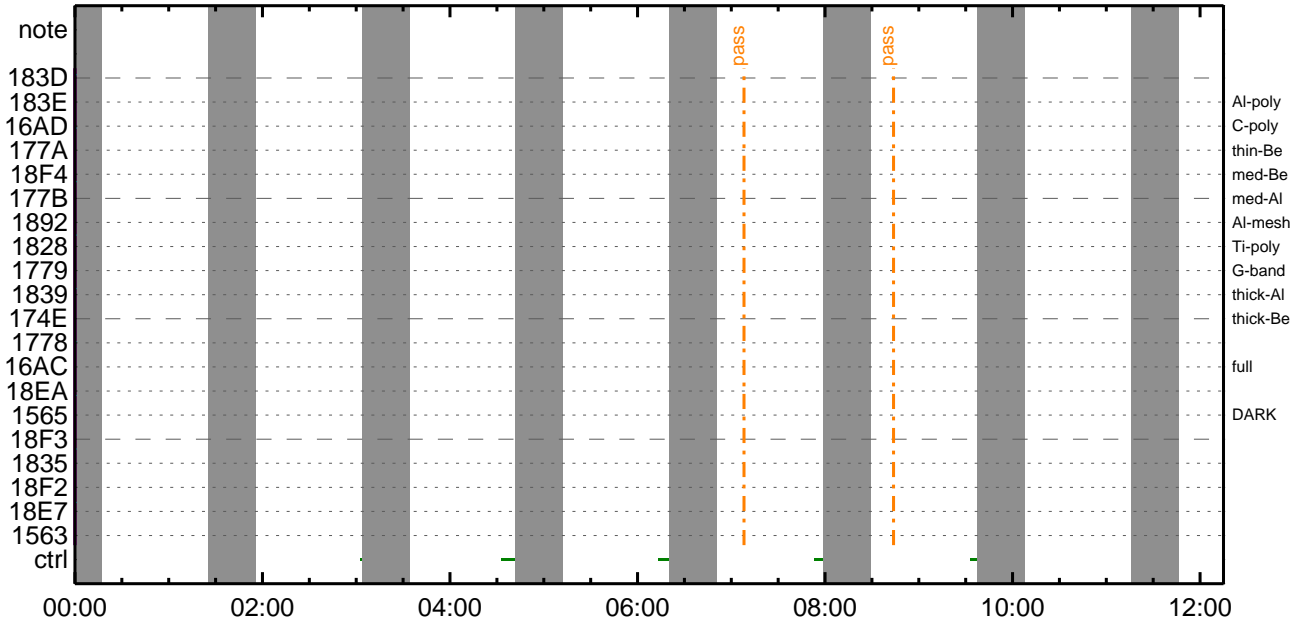
CMDI #0713 2012/06/21



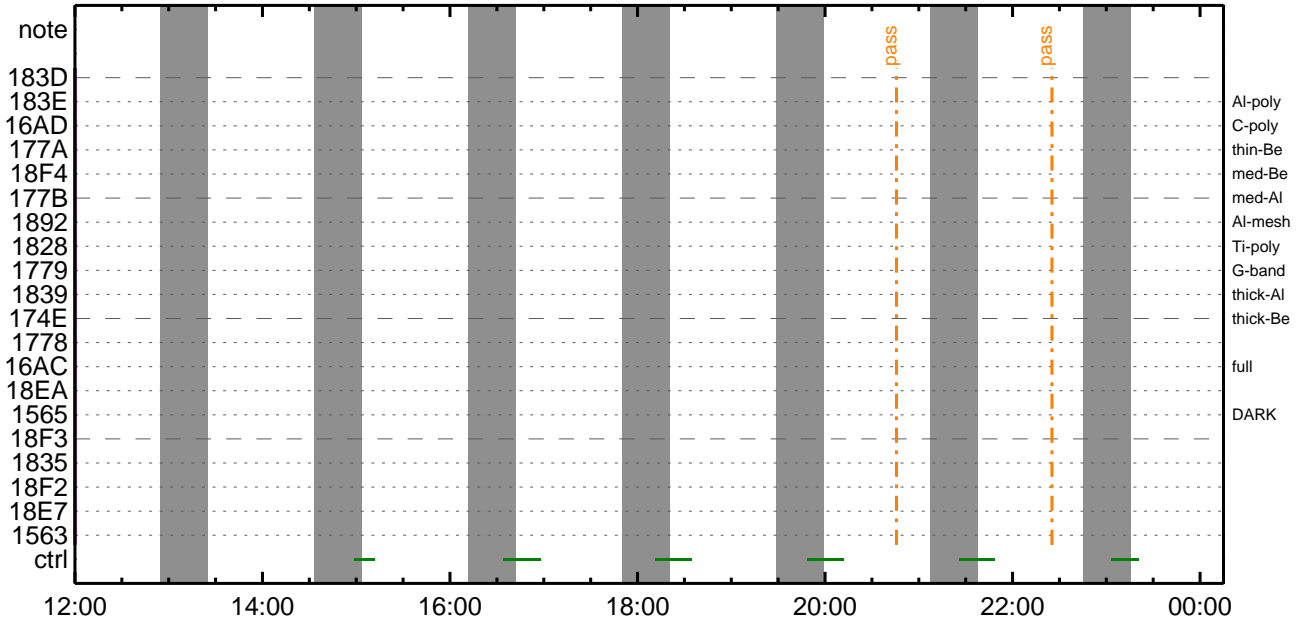
CMDI #0713 2012/06/21



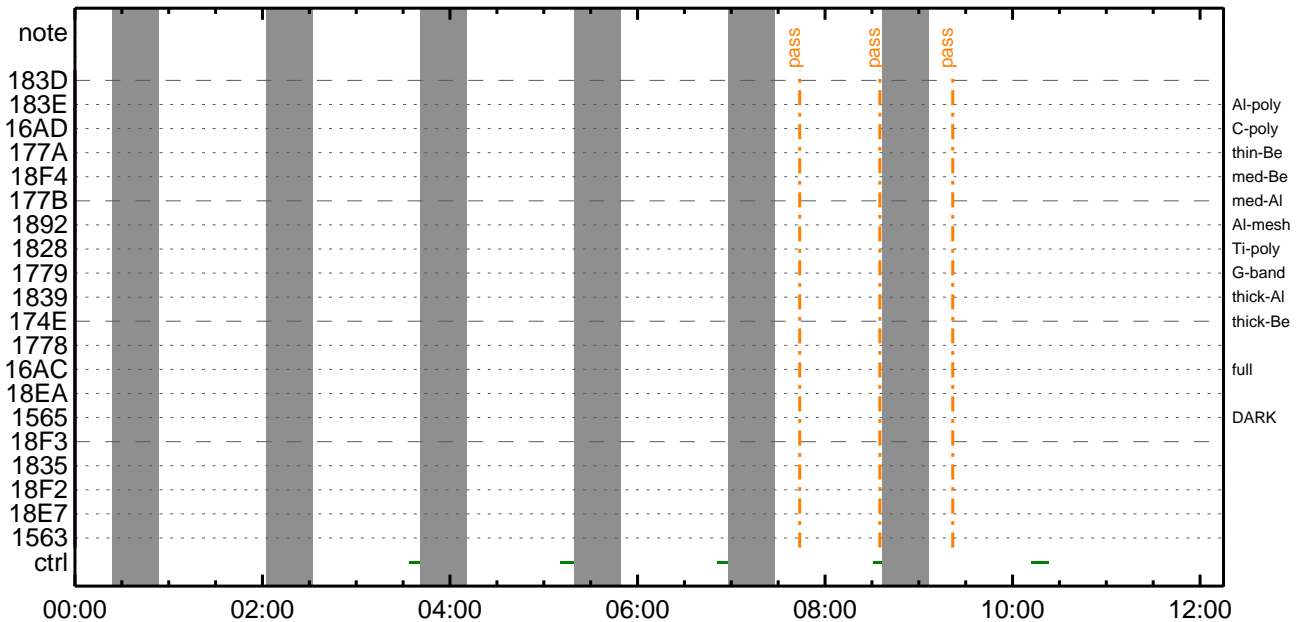
CMDI #0713 2012/06/22



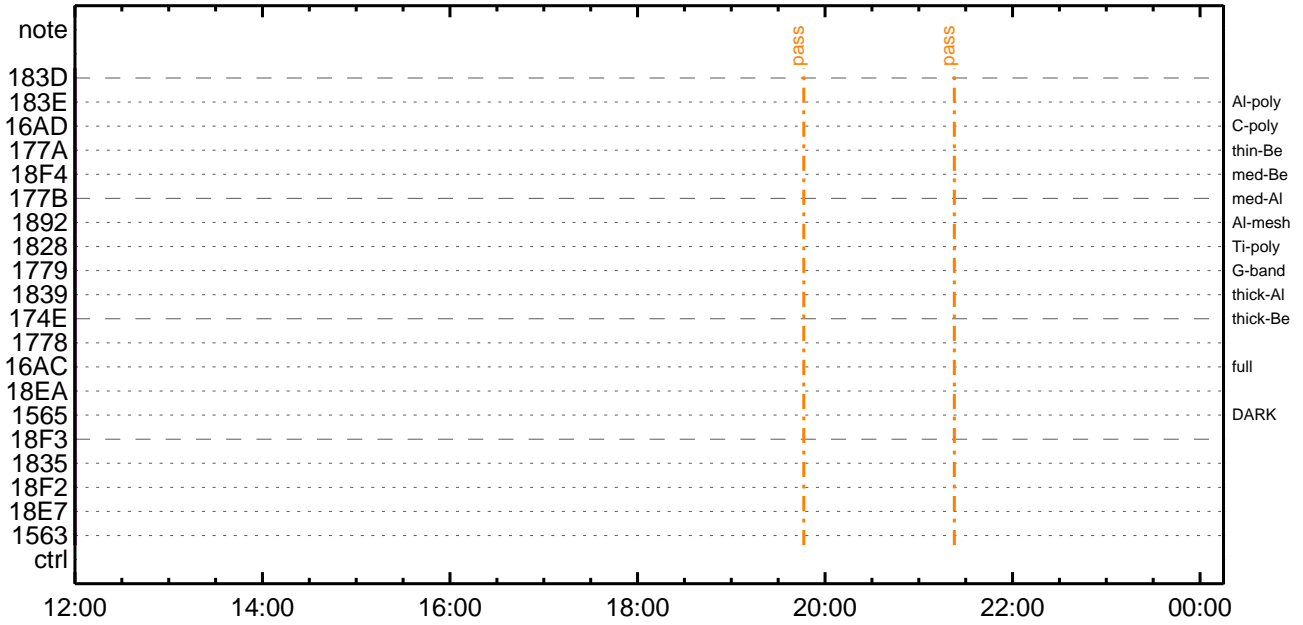
CMDI #0713 2012/06/22



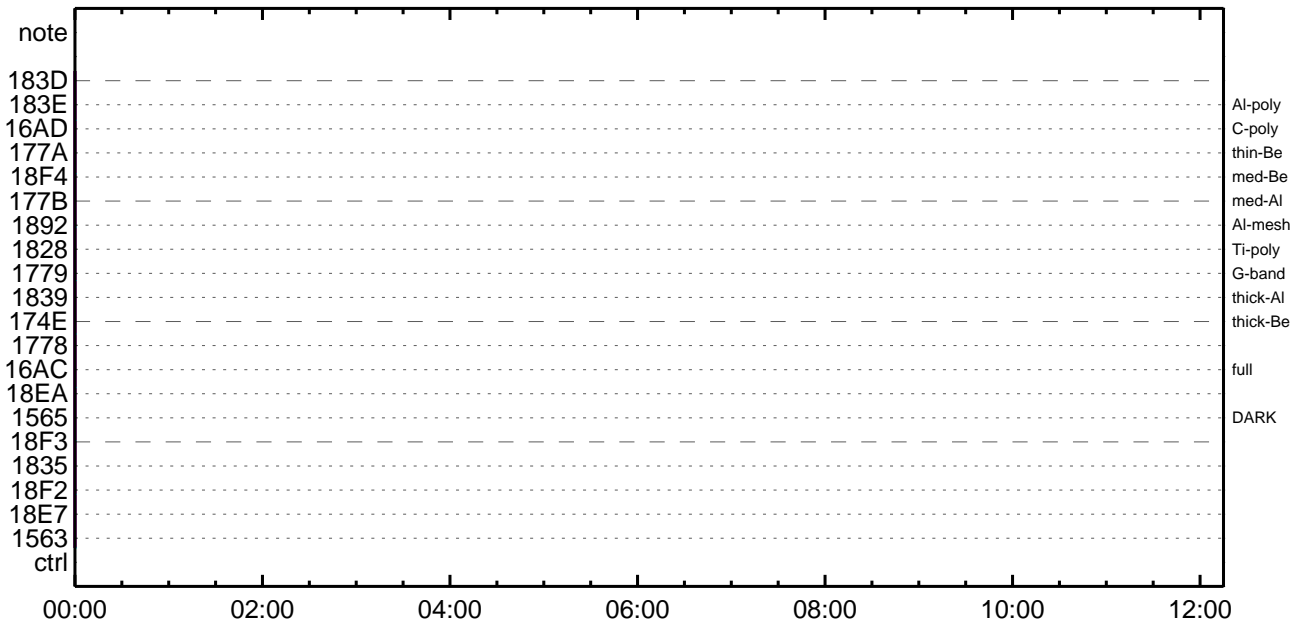
CMDI #0713 2012/06/23



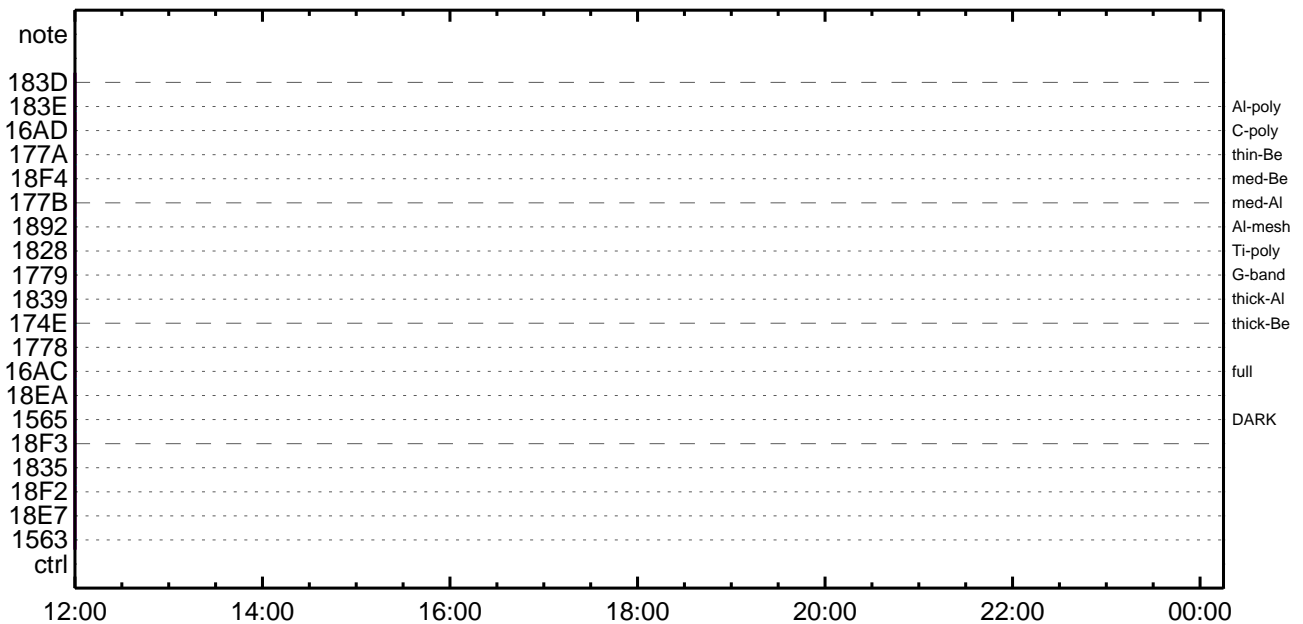
CMDI #0713 2012/06/23



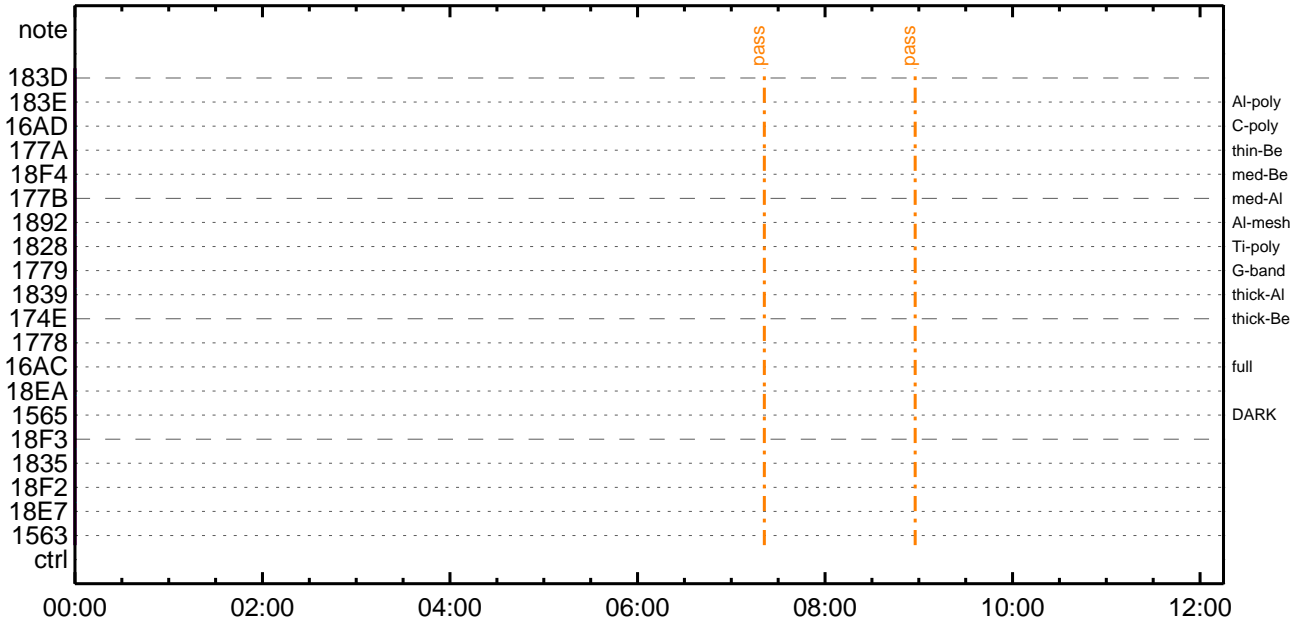
CMDI #0713 2012/06/24



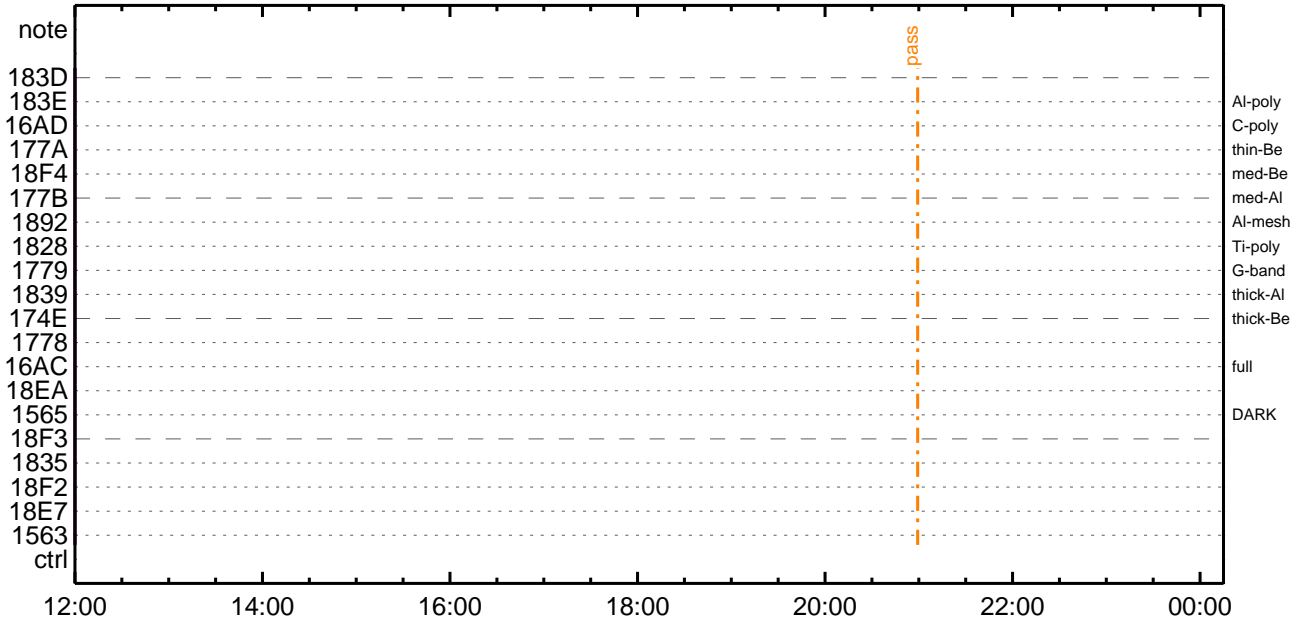
CMDI #0713 2012/06/24



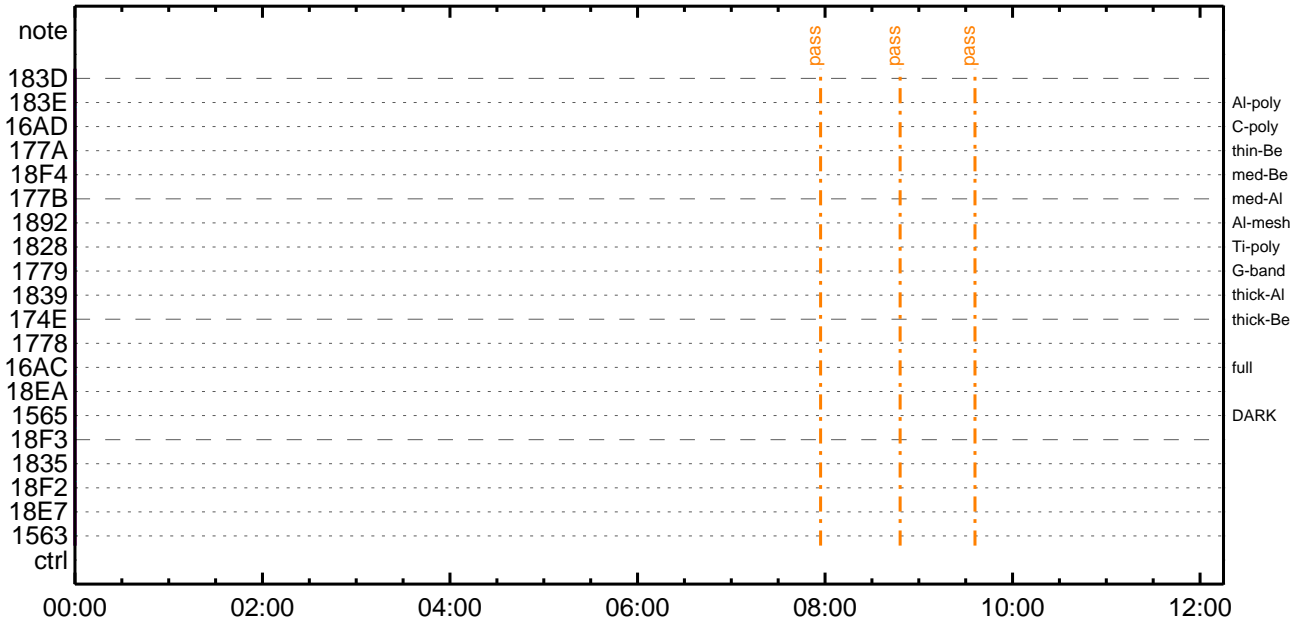
CMDI #0713 2012/06/25



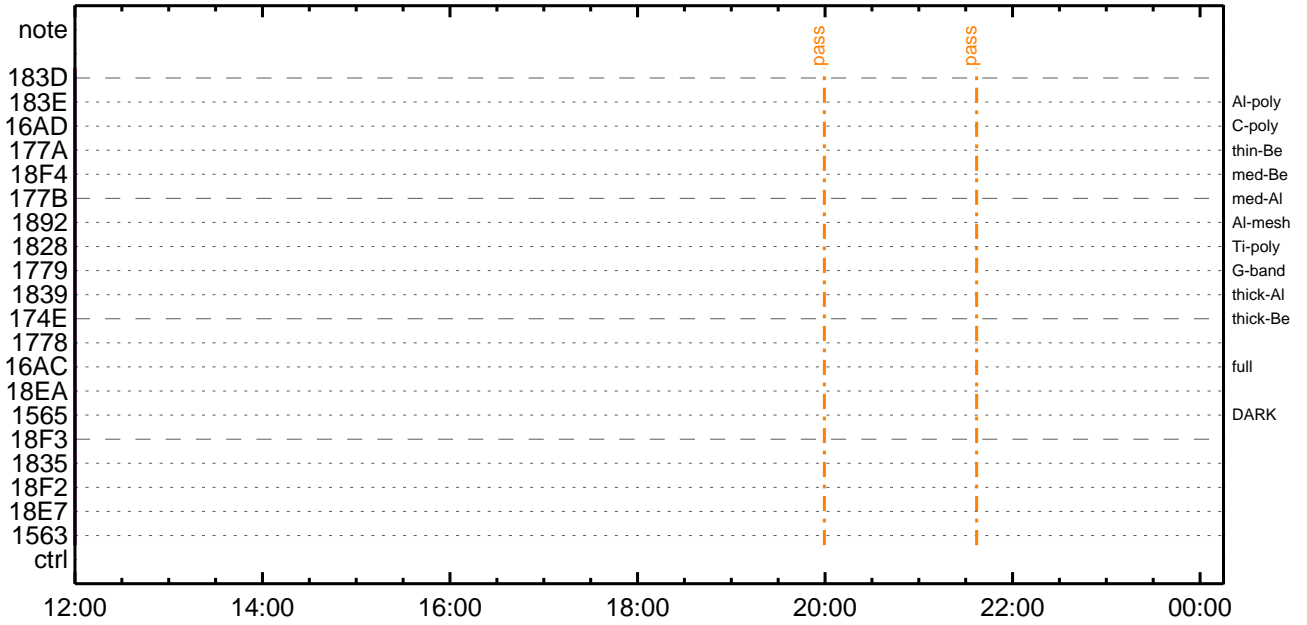
CMDI #0713 2012/06/25



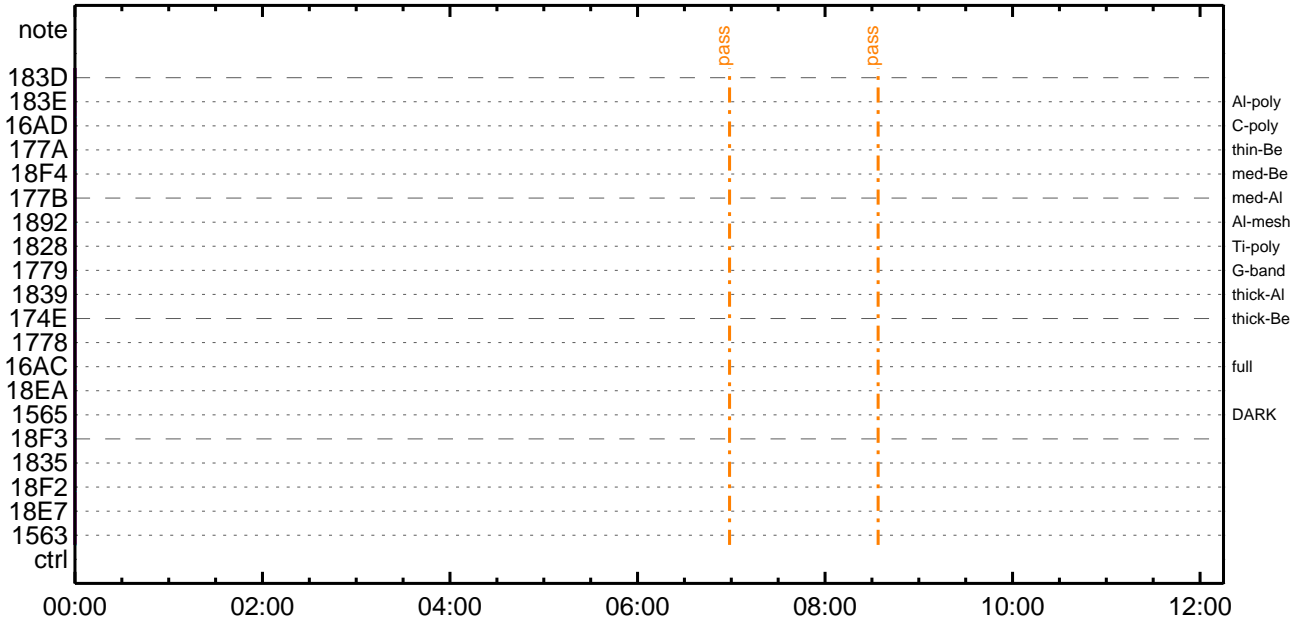
CMDI #0713 2012/06/26



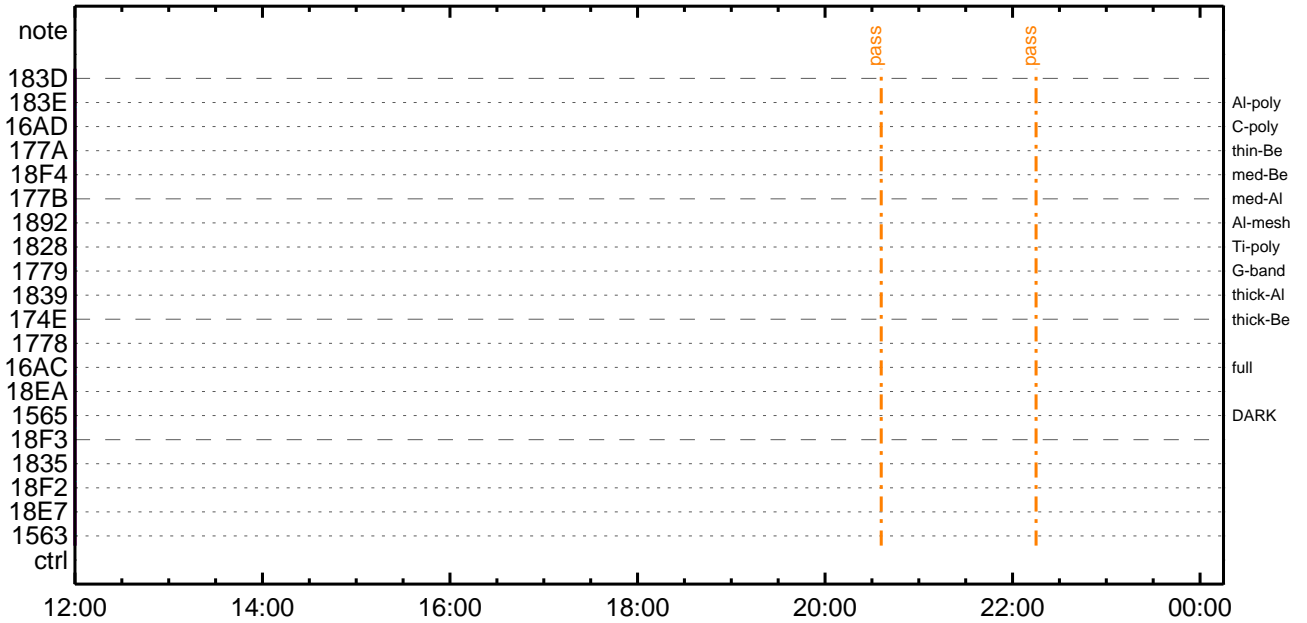
CMDI #0713 2012/06/26



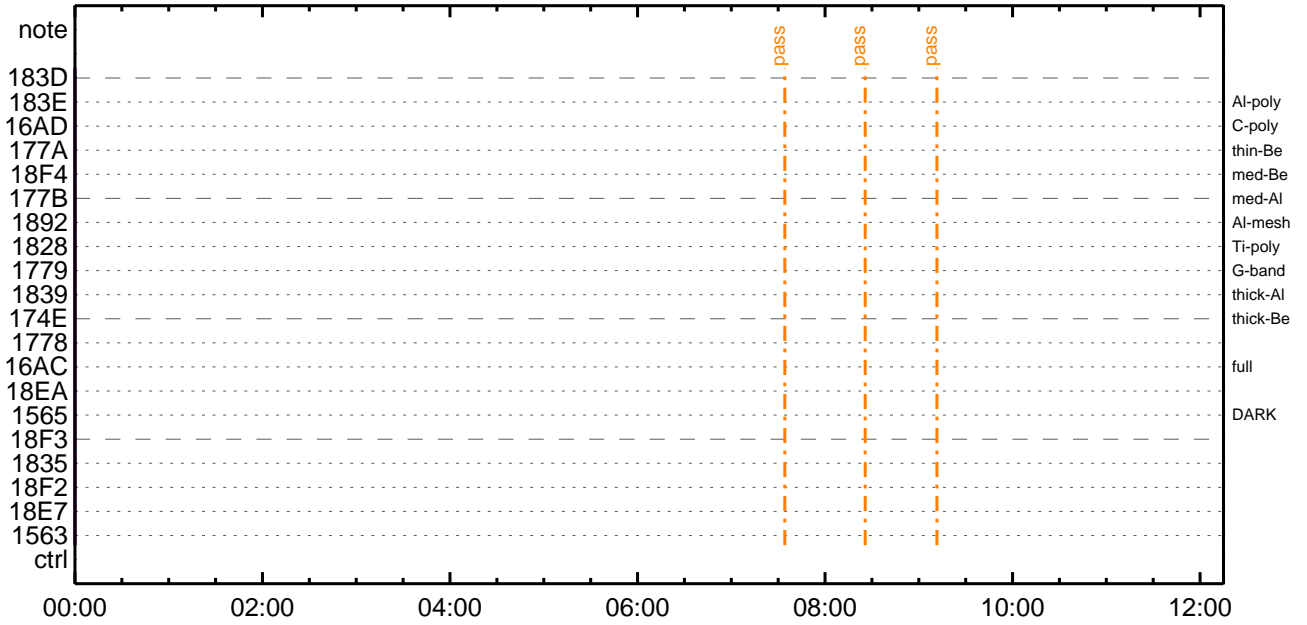
CMDI #0713 2012/06/27



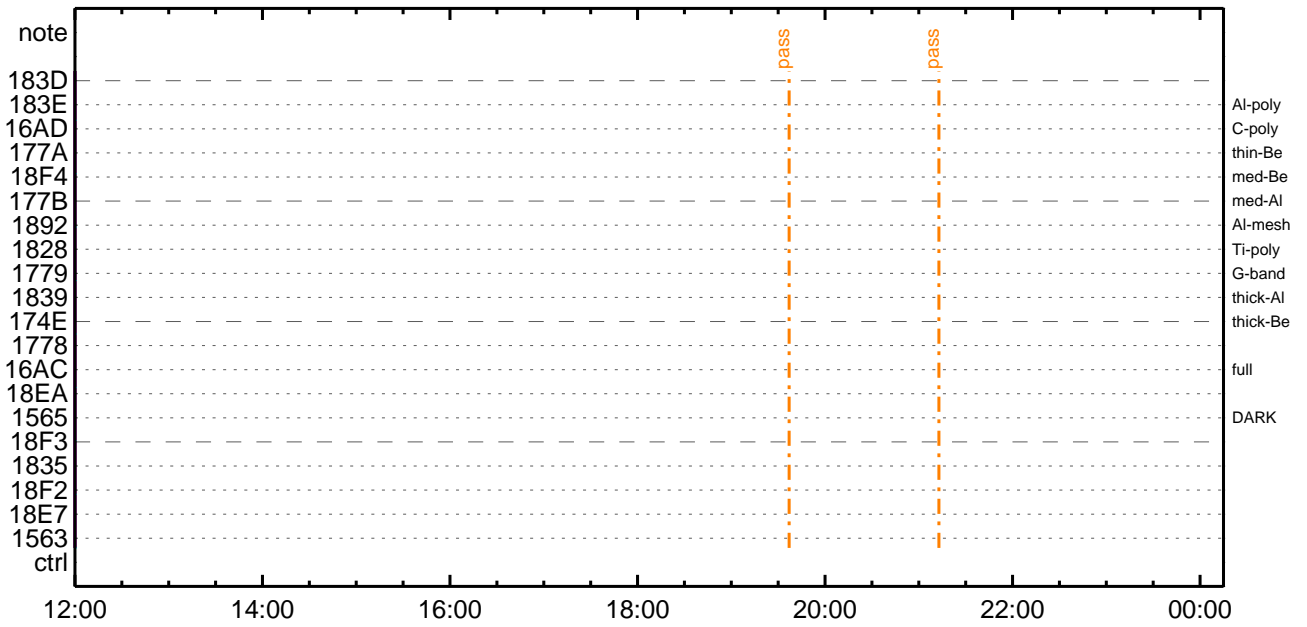
CMDI #0713 2012/06/27



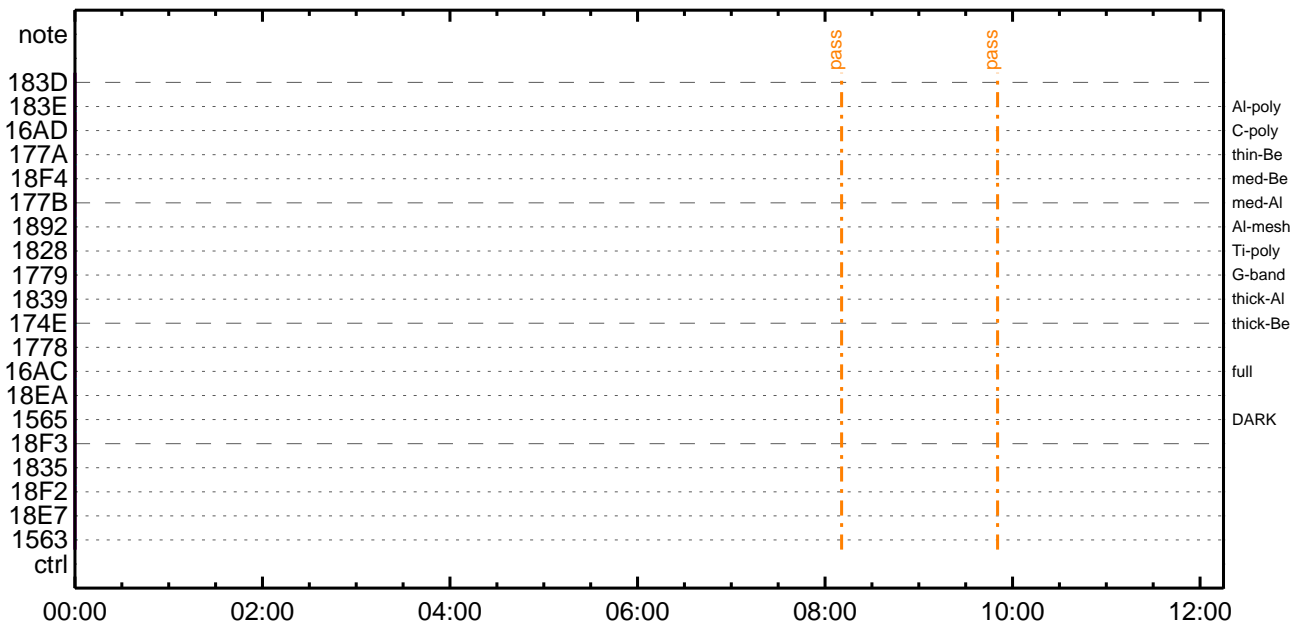
CMDI #0713 2012/06/28



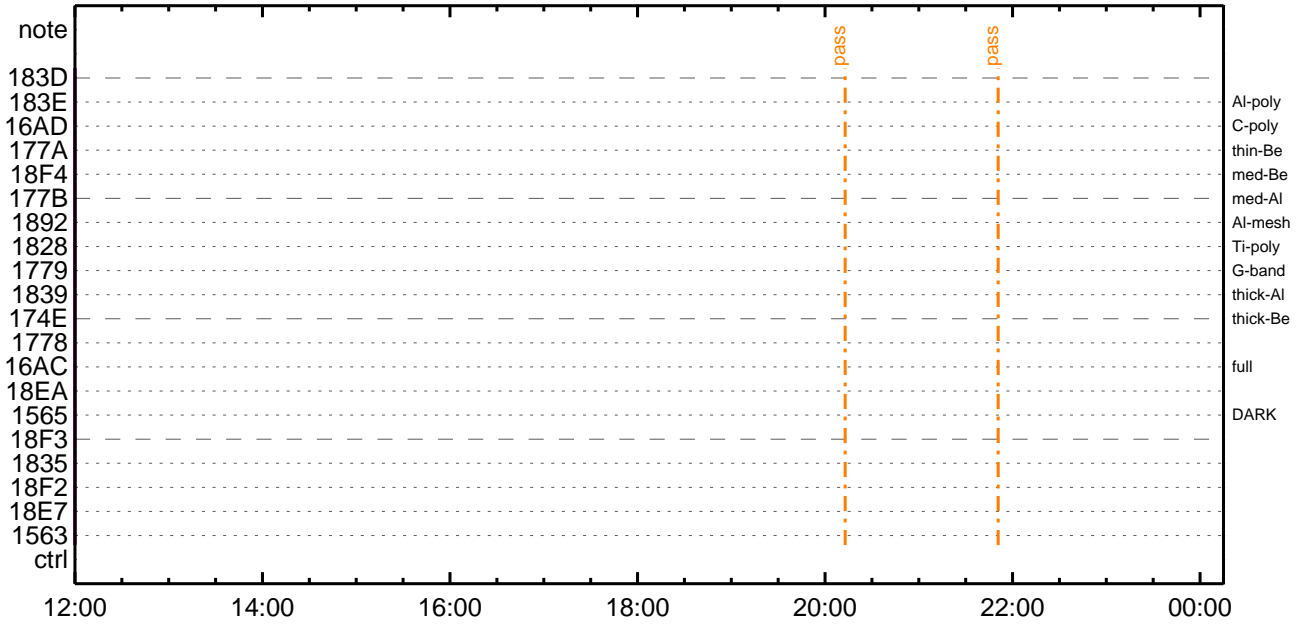
CMDI #0713 2012/06/28



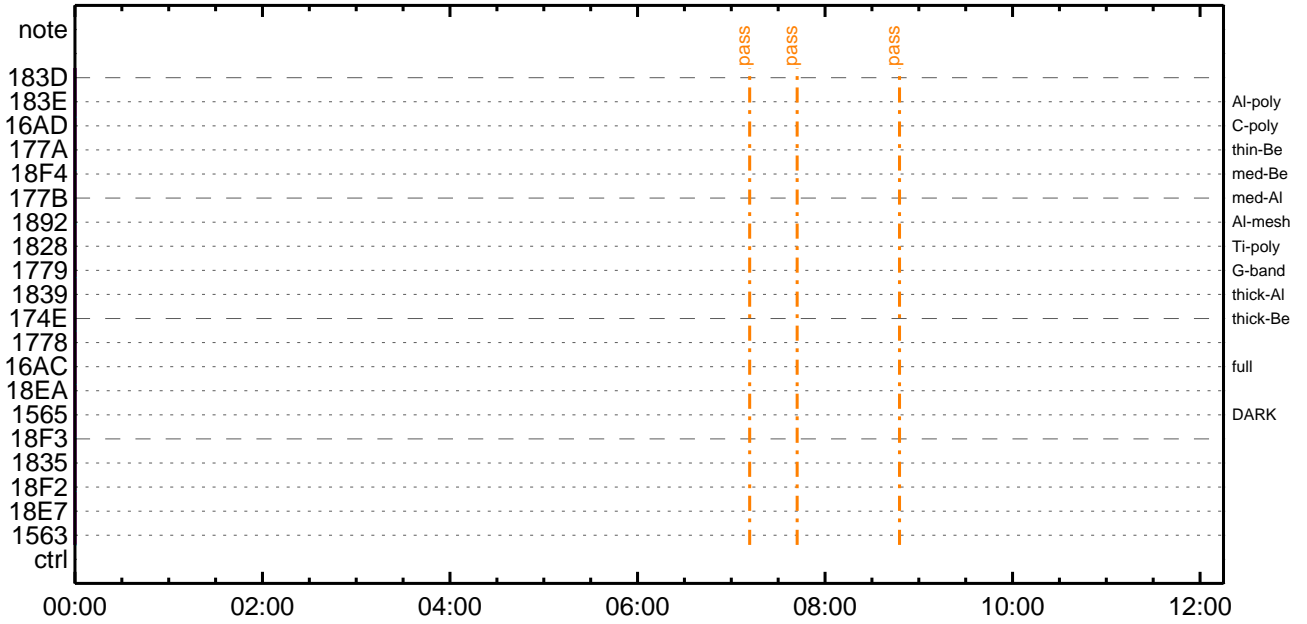
CMDI #0713 2012/06/29



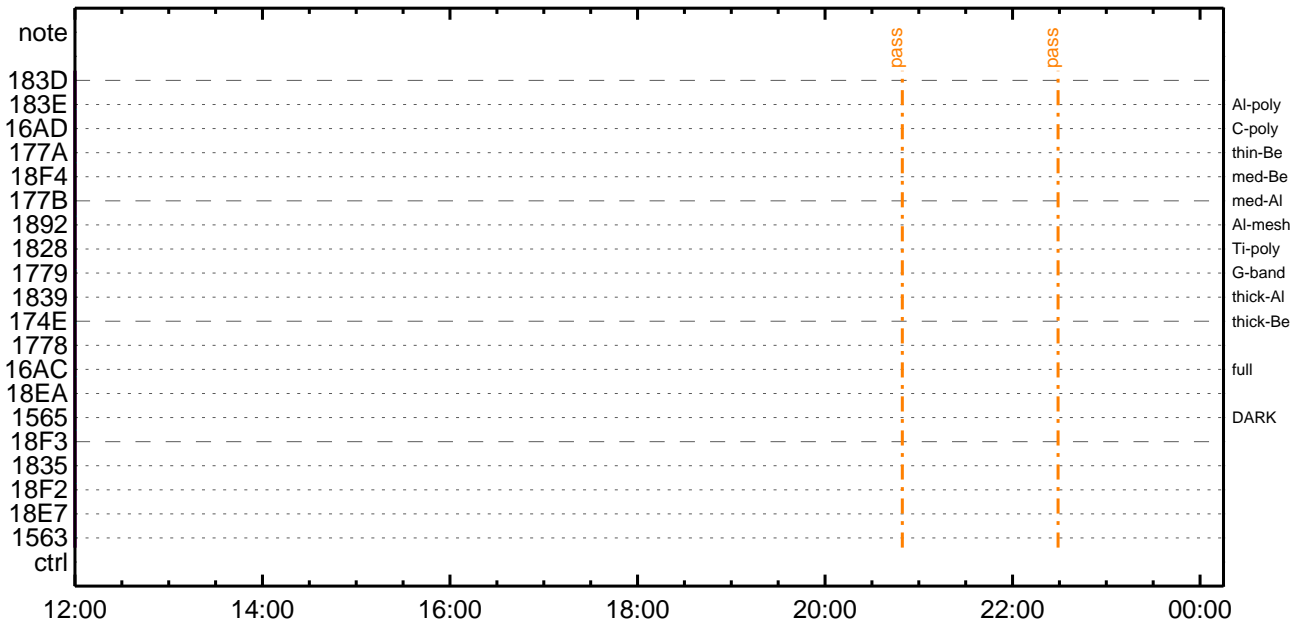
CMDI #0713 2012/06/29



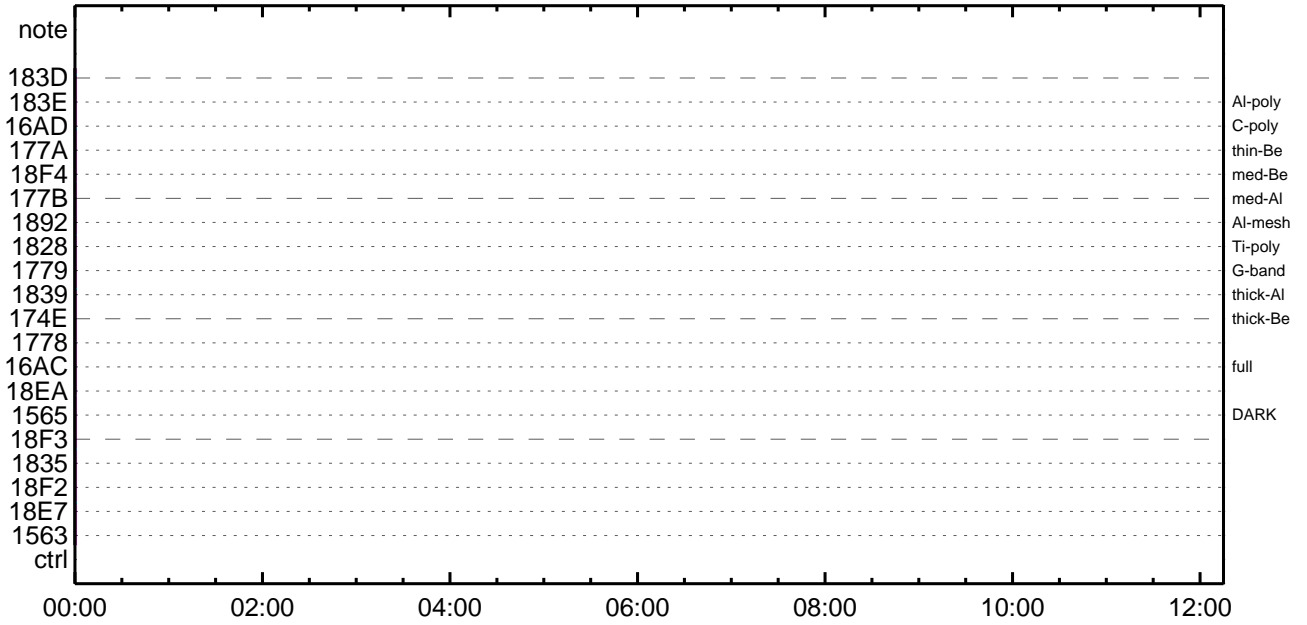
CMDI #0713 2012/06/30



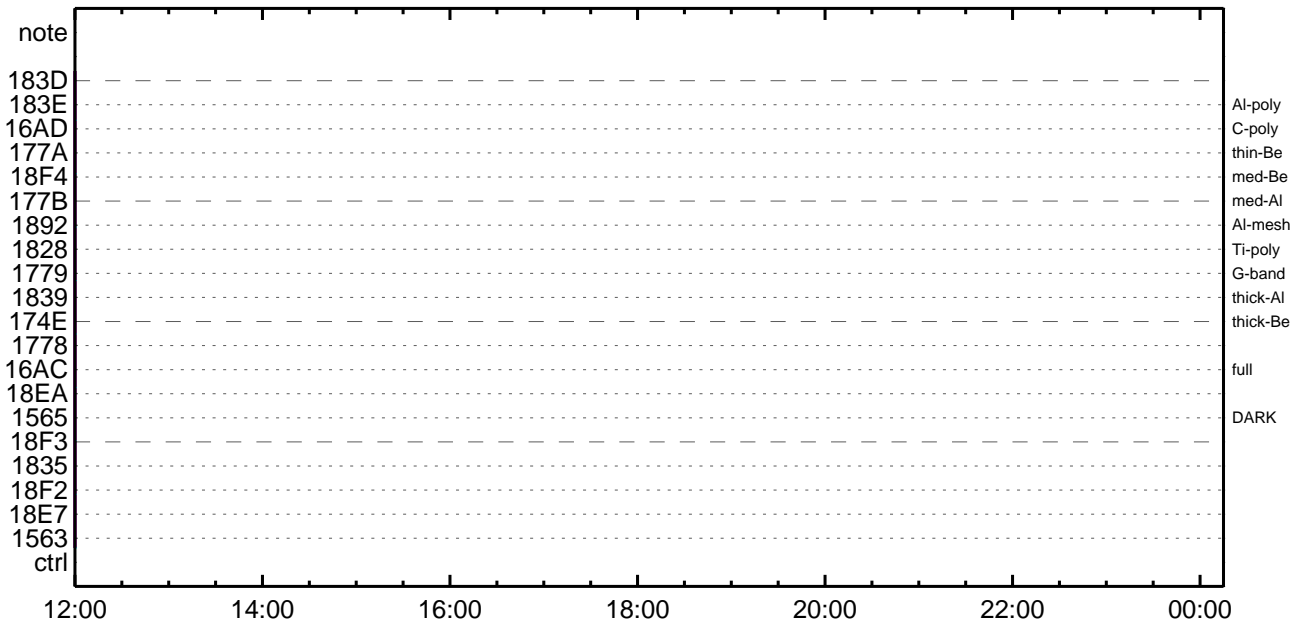
CMDI #0713 2012/06/30



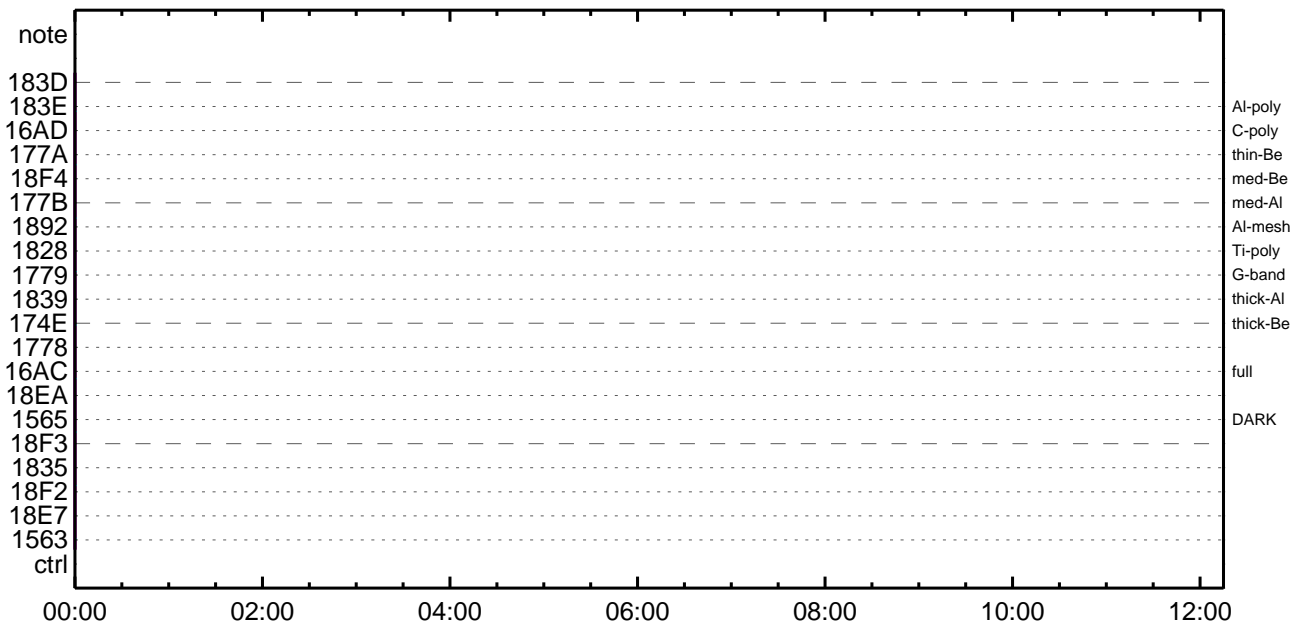
CMDI #0713 2012/07/01



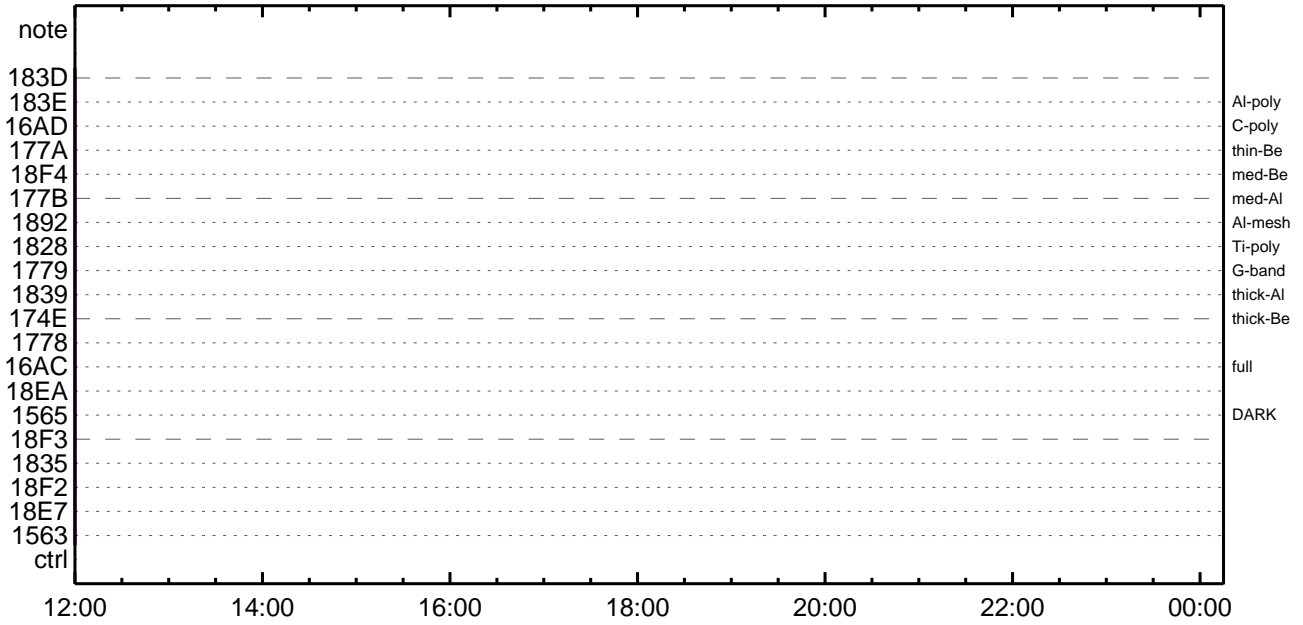
CMDI #0713 2012/07/01



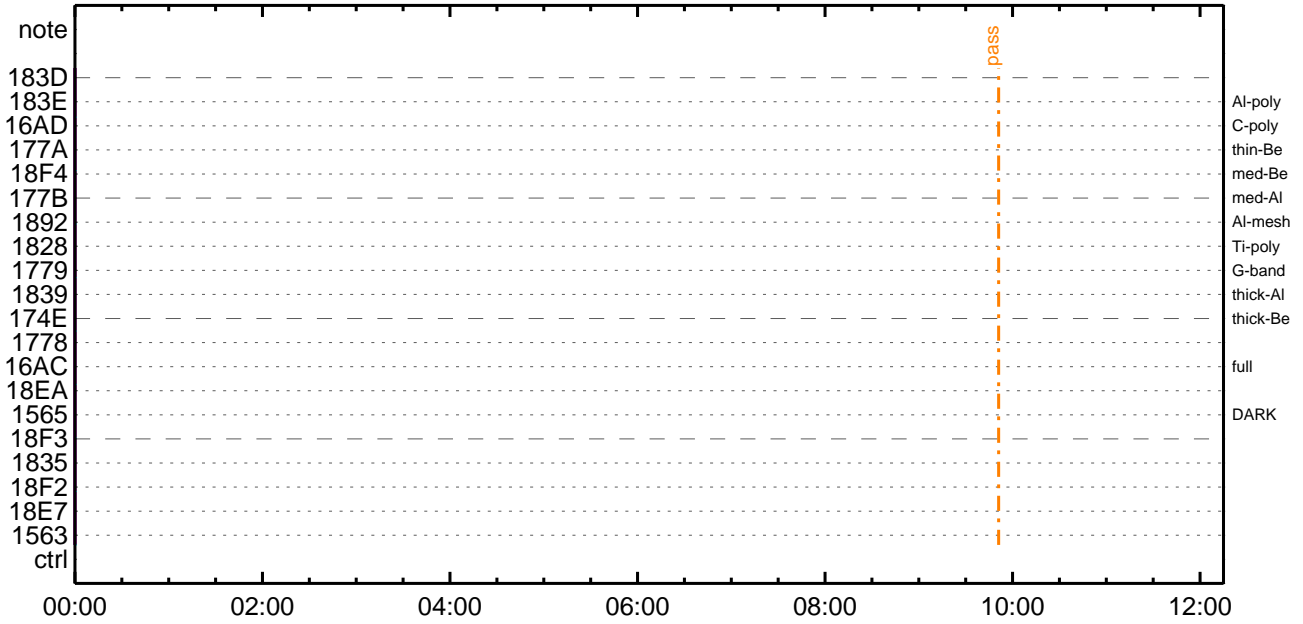
CMDI #0713 2012/07/02



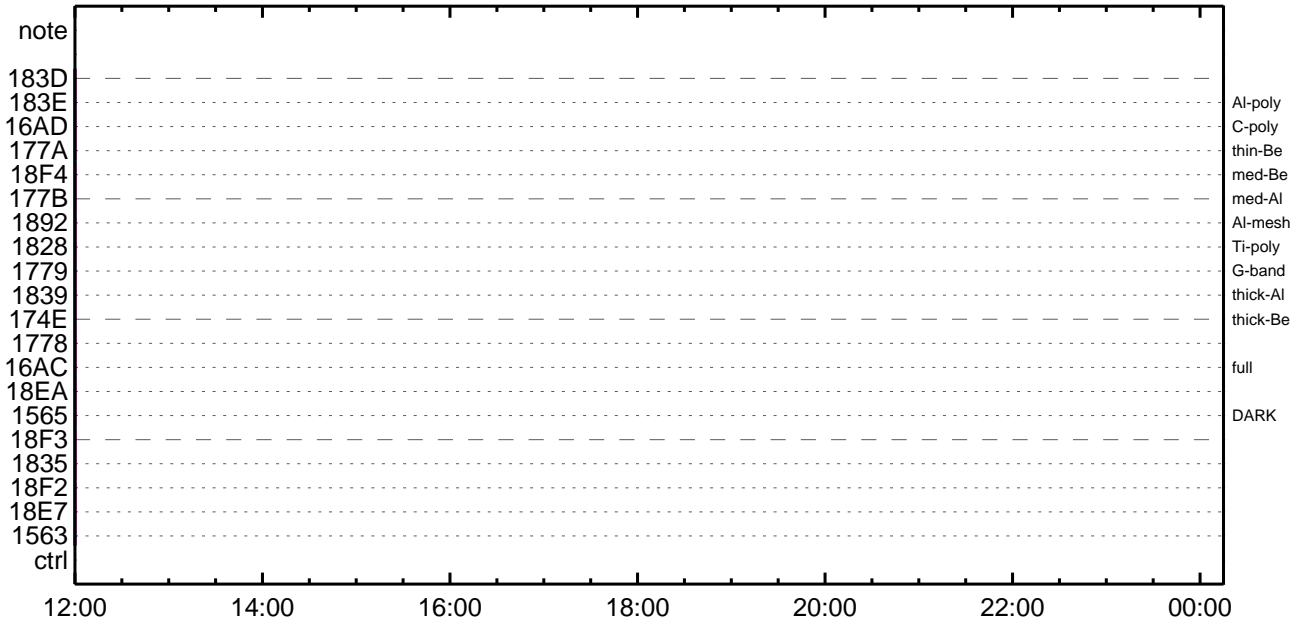
CMDI #0713 2012/07/02



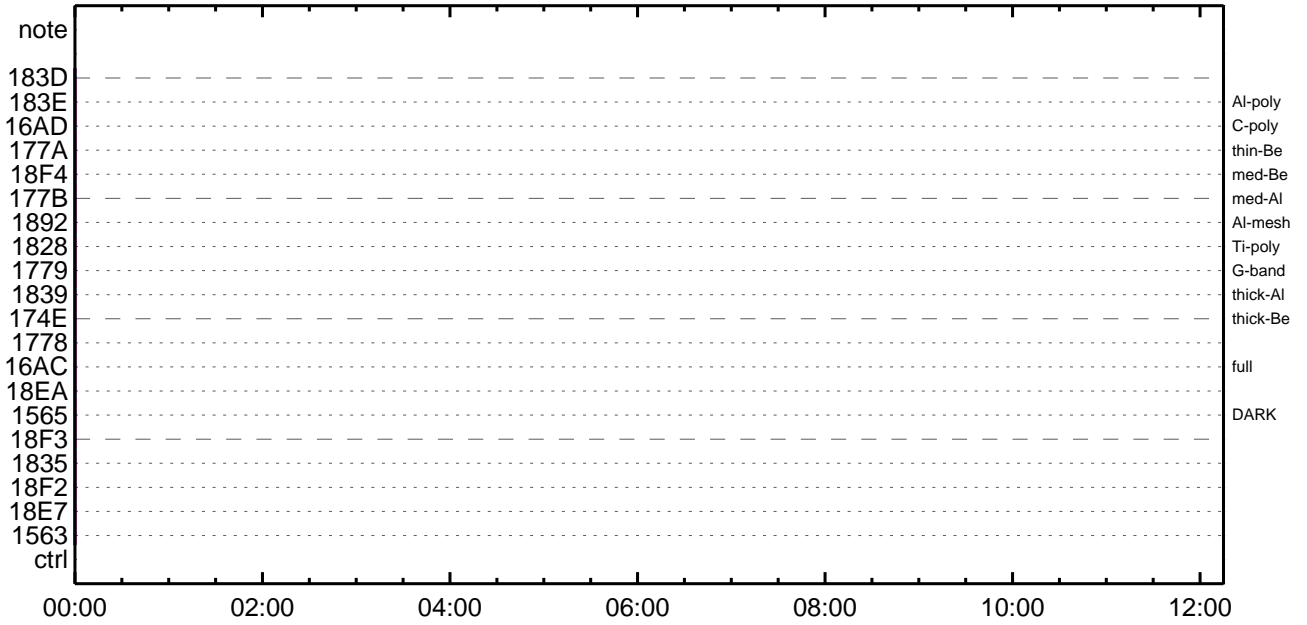
CMDI #0713 2012/07/03



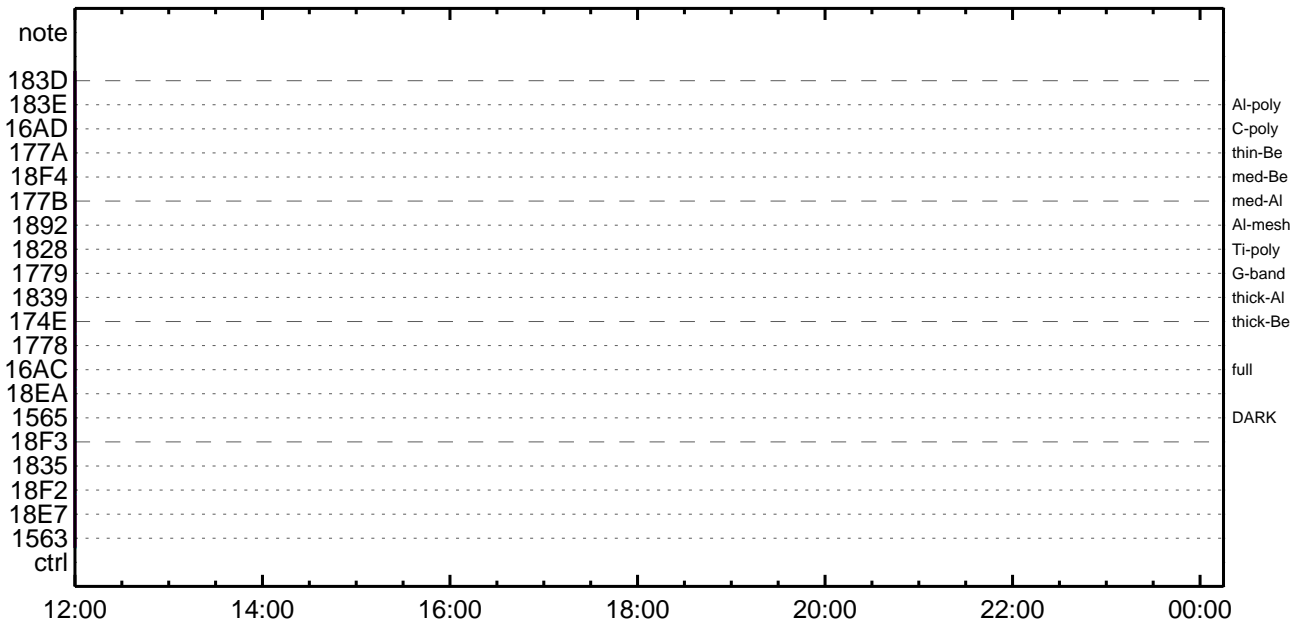
CMDI #0713 2012/07/03



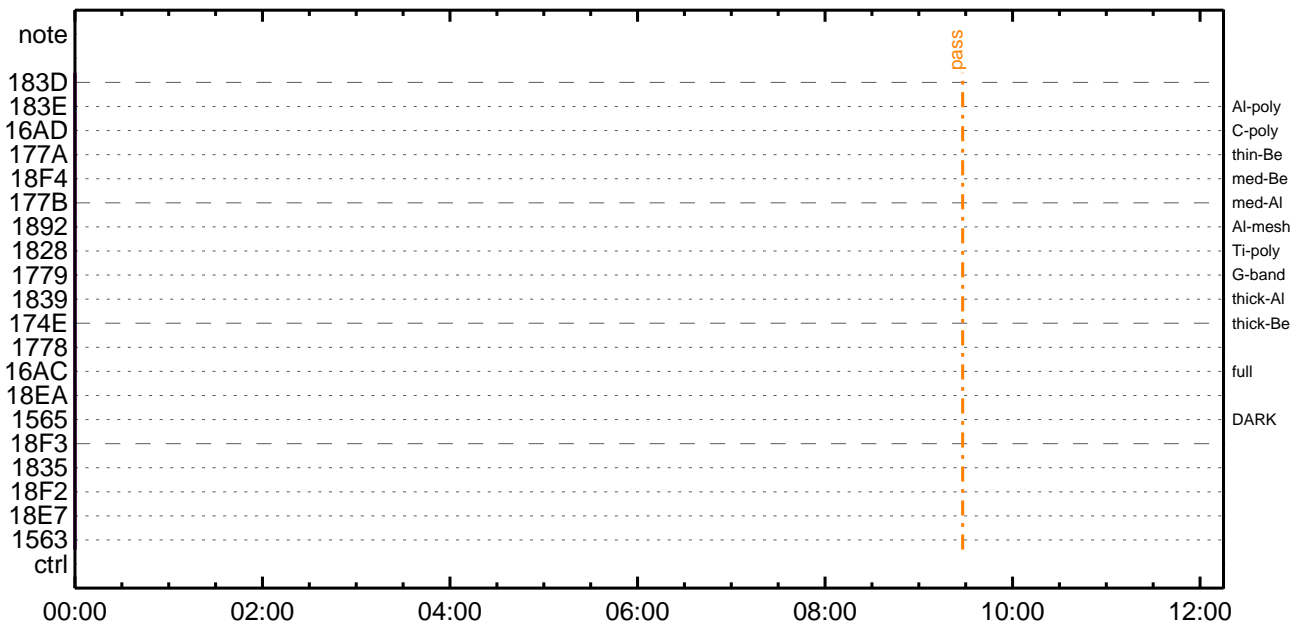
CMDI #0713 2012/07/04



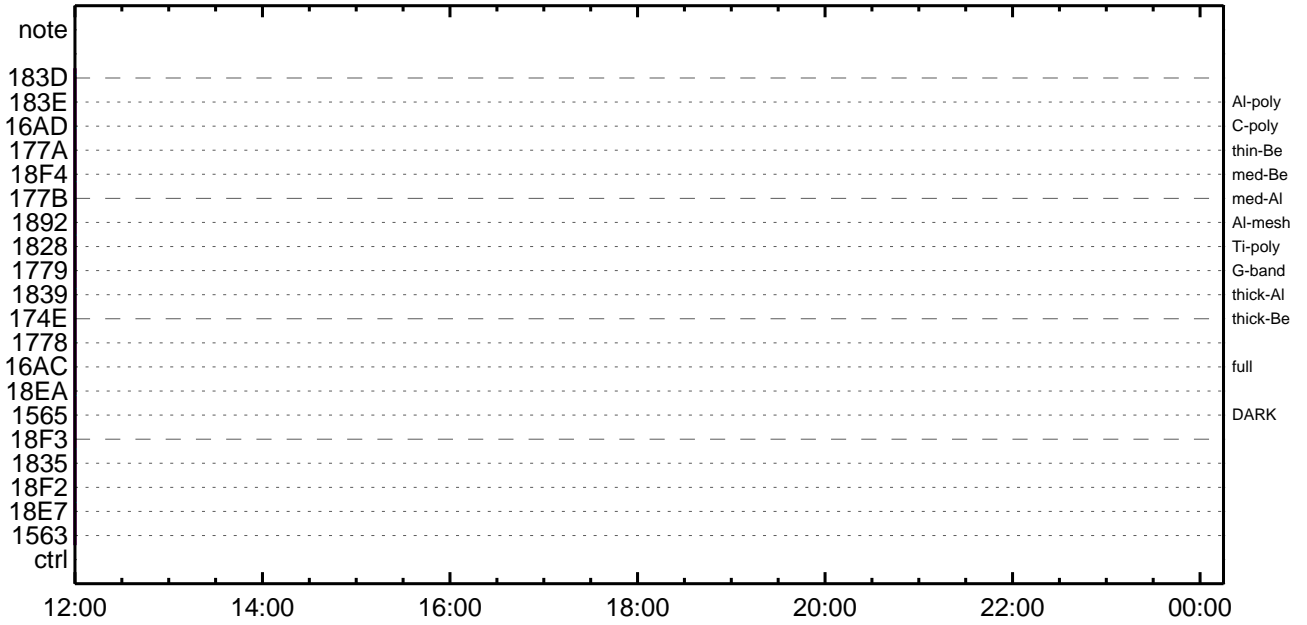
CMDI #0713 2012/07/04



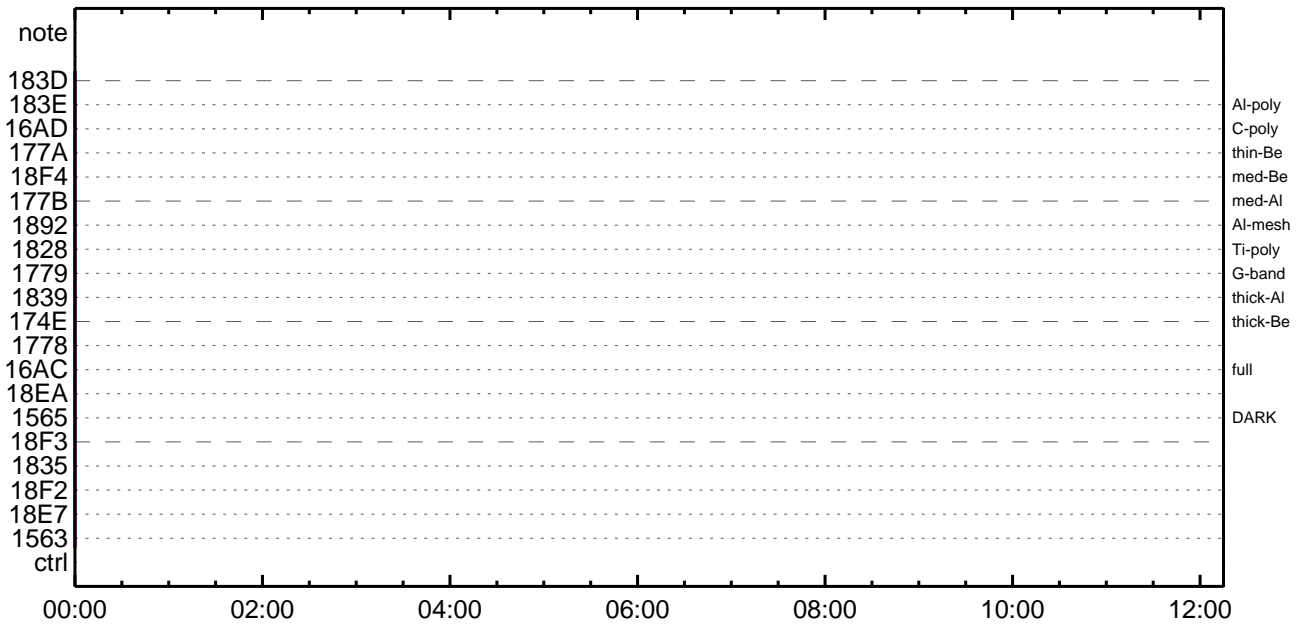
CMDI #0713 2012/07/05



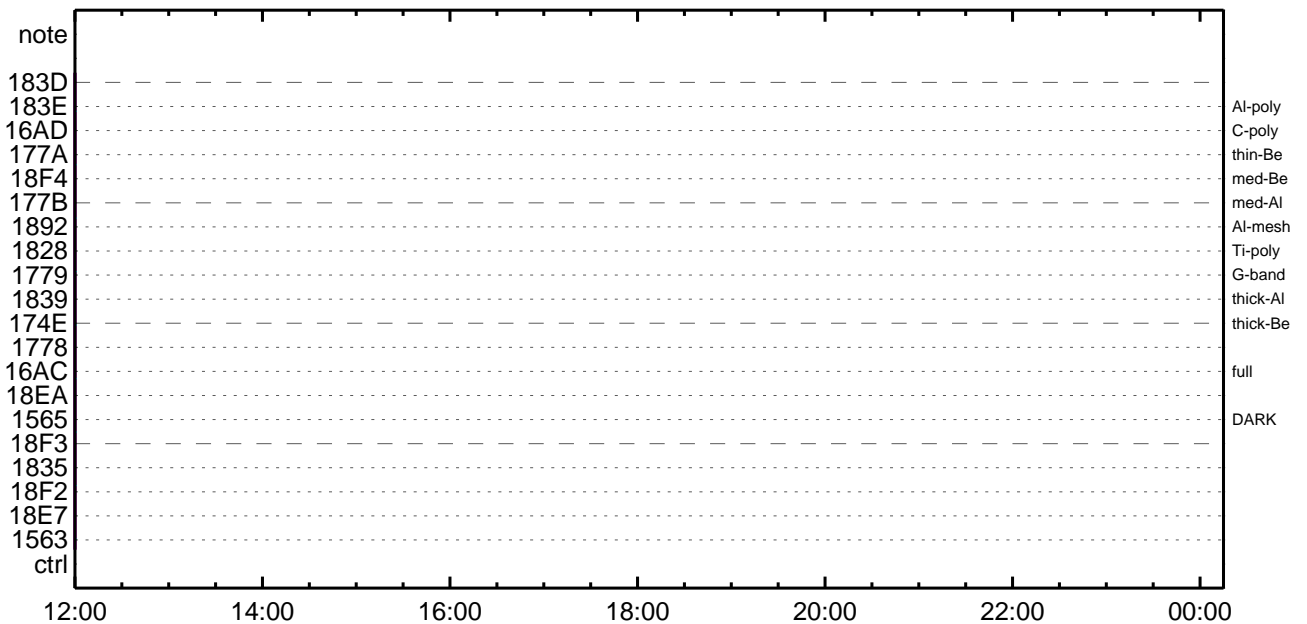
CMDI #0713 2012/07/05



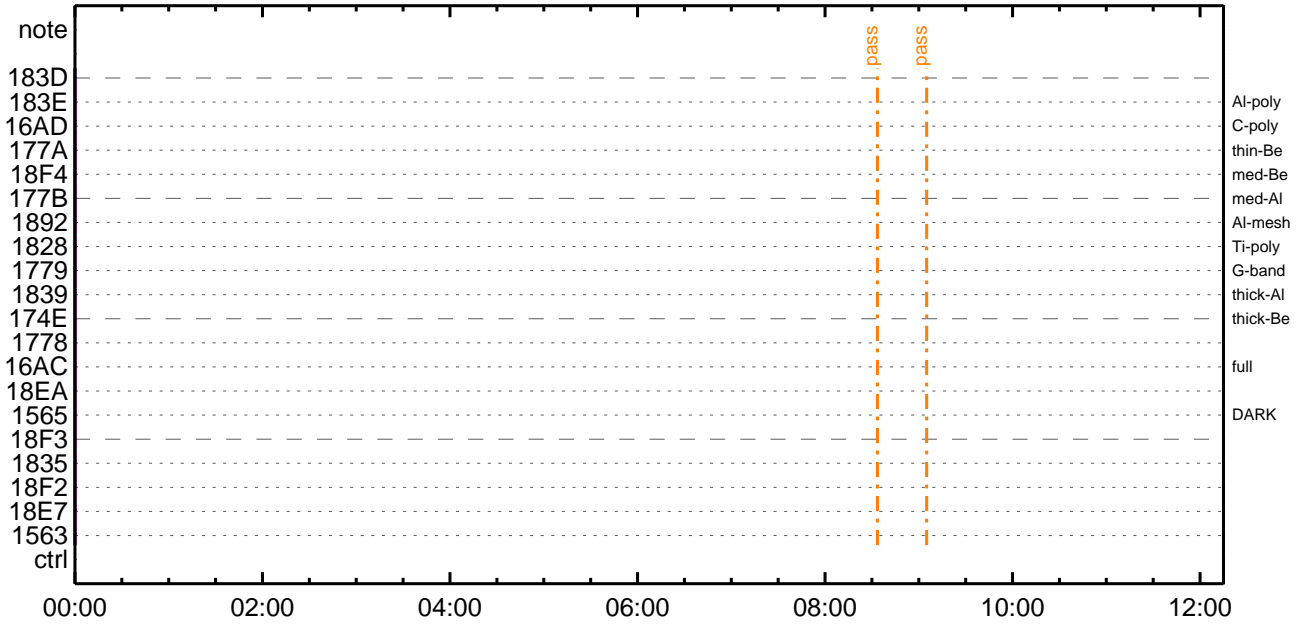
CMDI #0713 2012/07/06



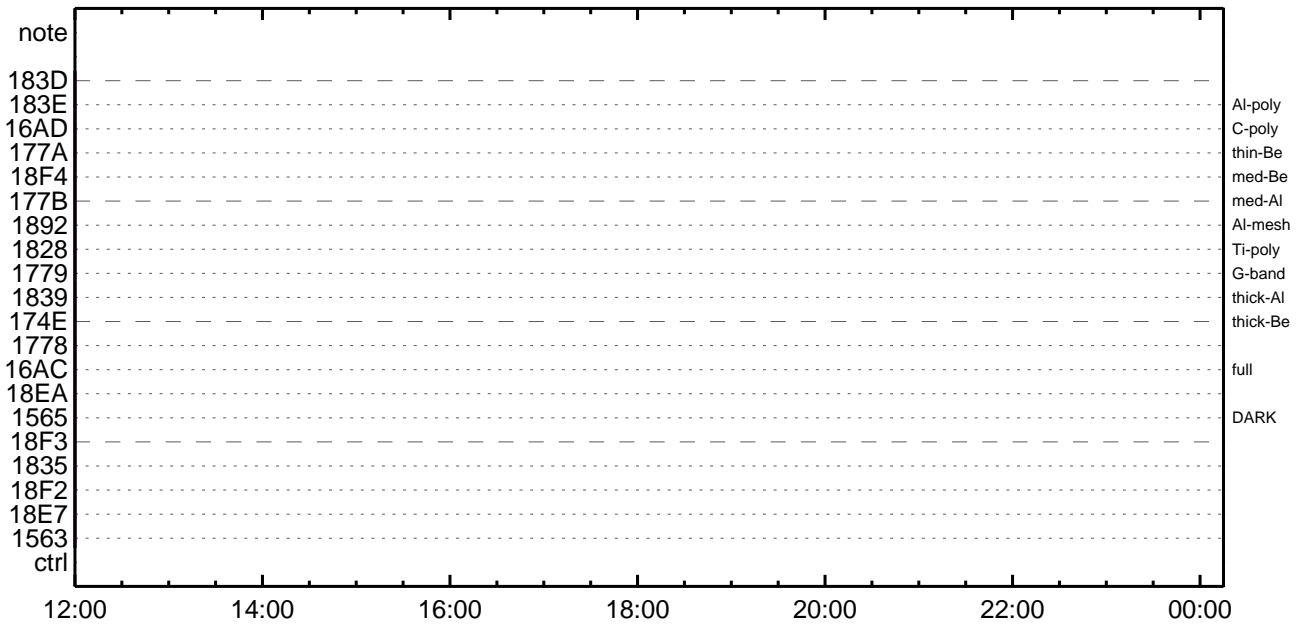
CMDI #0713 2012/07/06



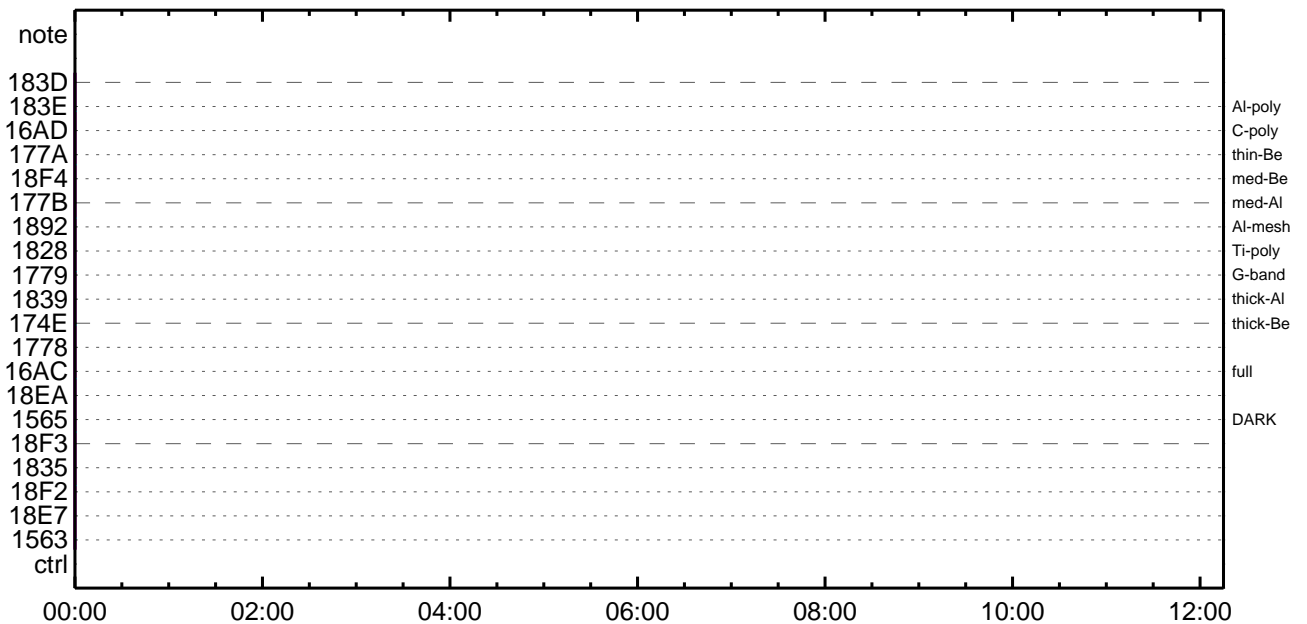
CMDI #0713 2012/07/07



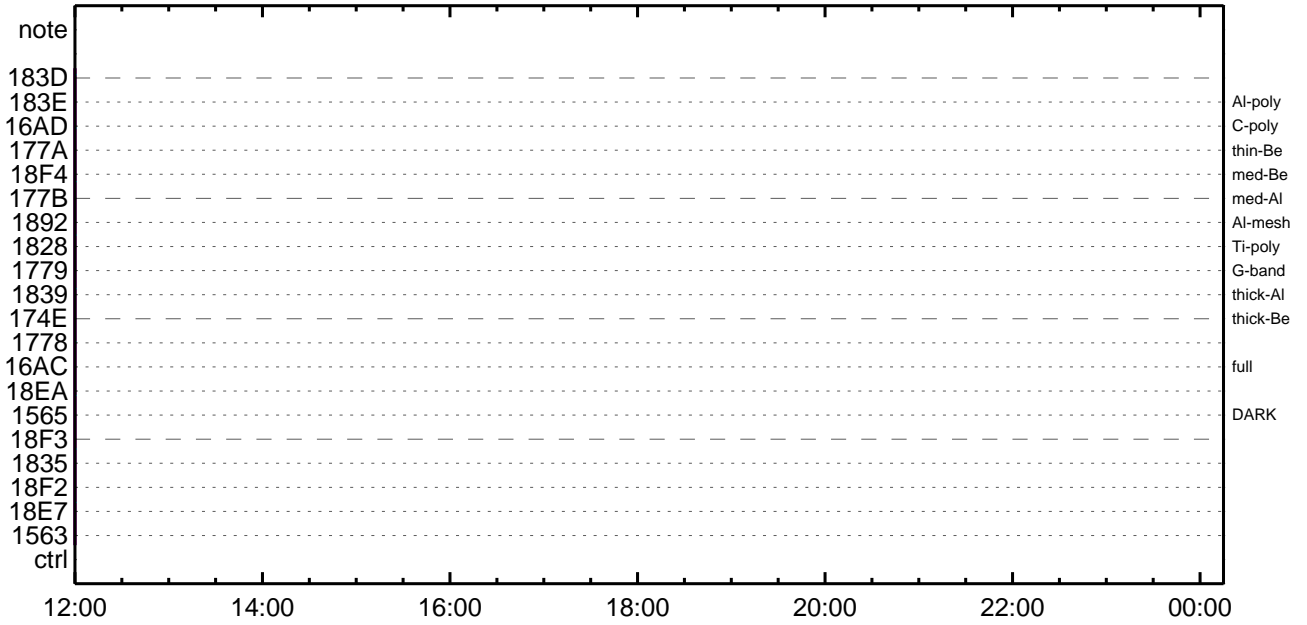
CMDI #0713 2012/07/07



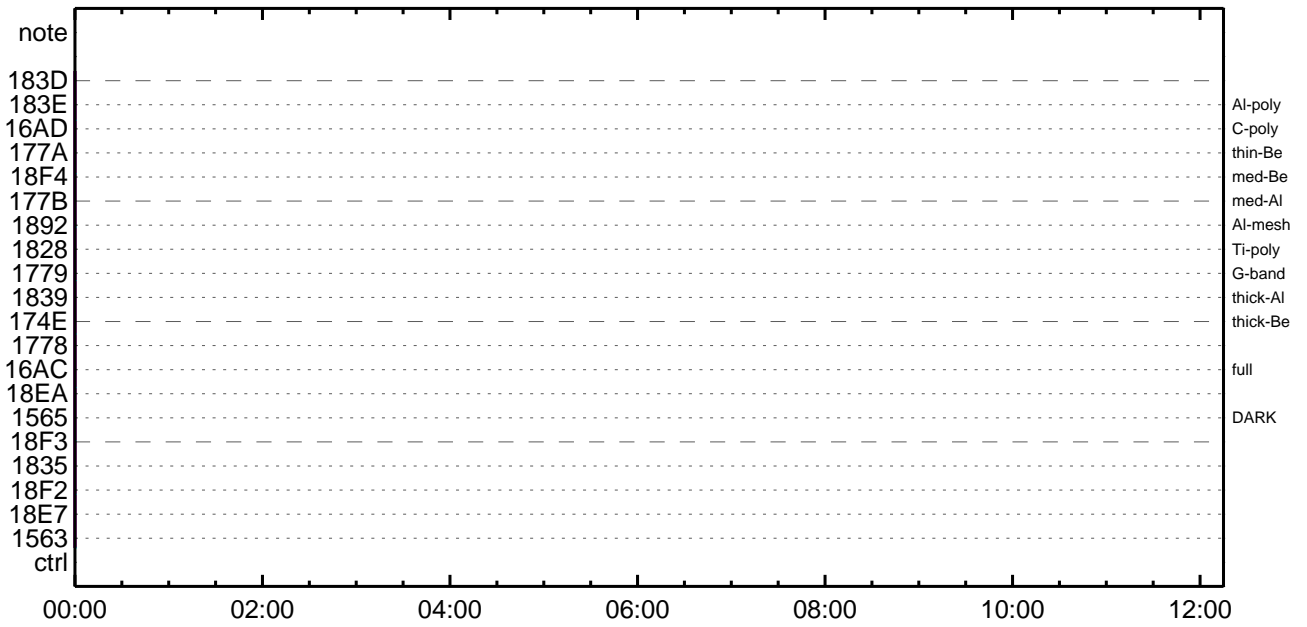
CMDI #0713 2012/07/08



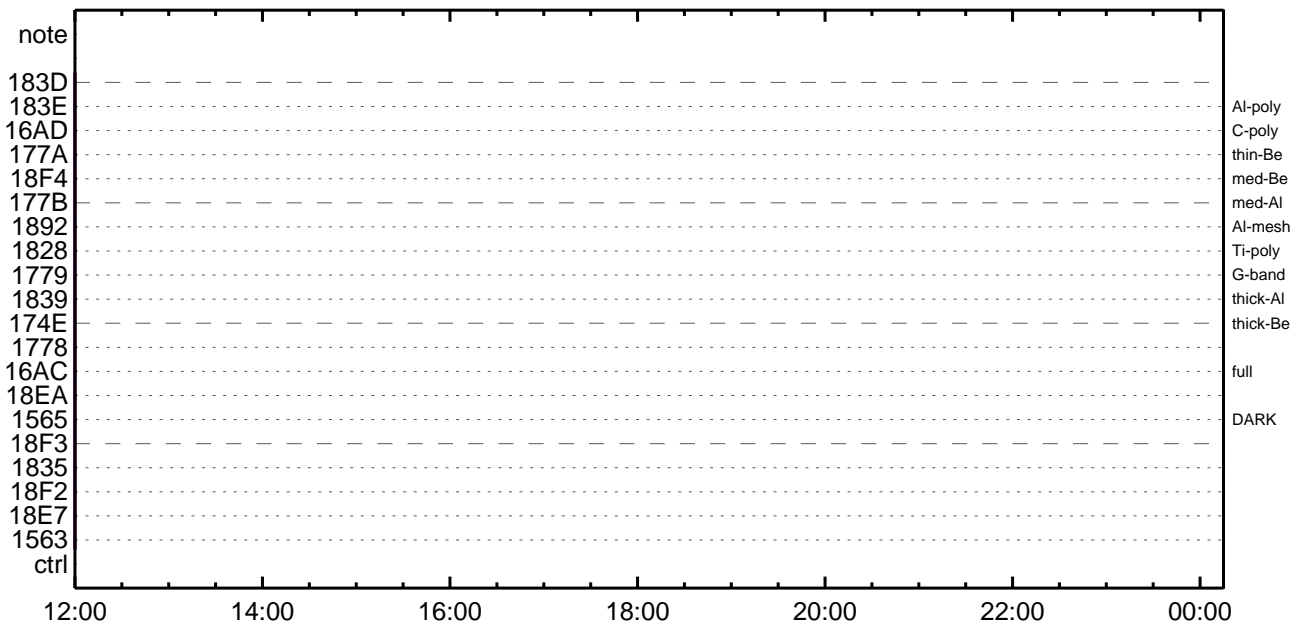
CMDI #0713 2012/07/08



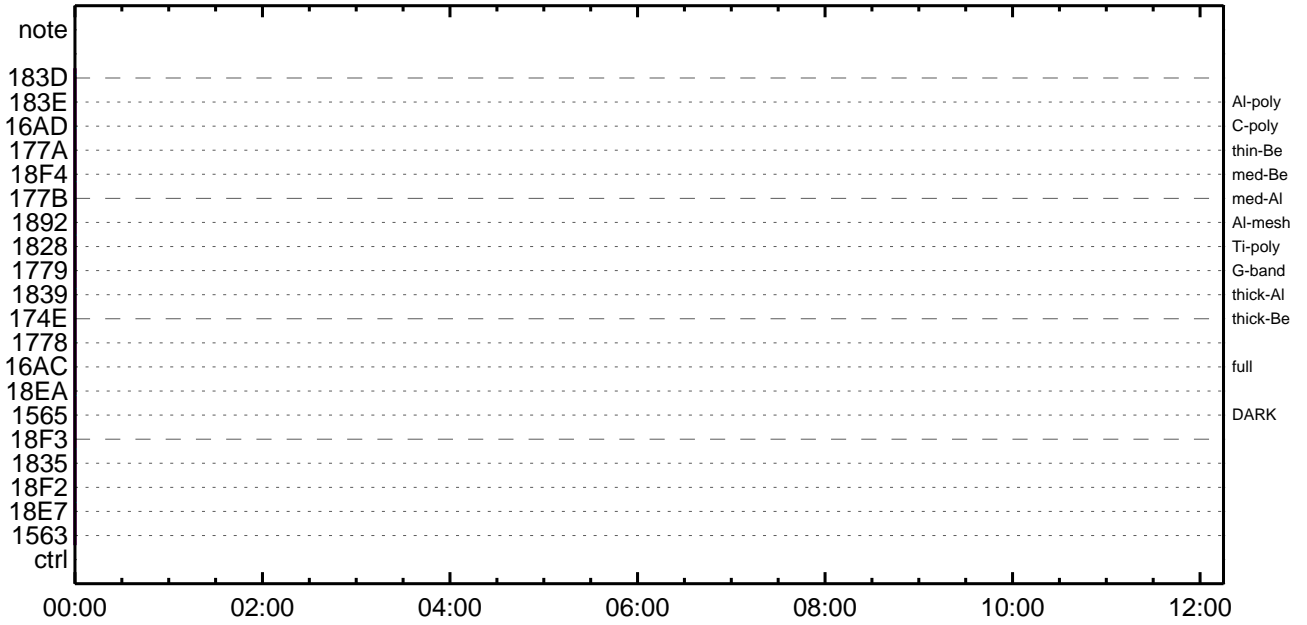
CMDI #0713 2012/07/09



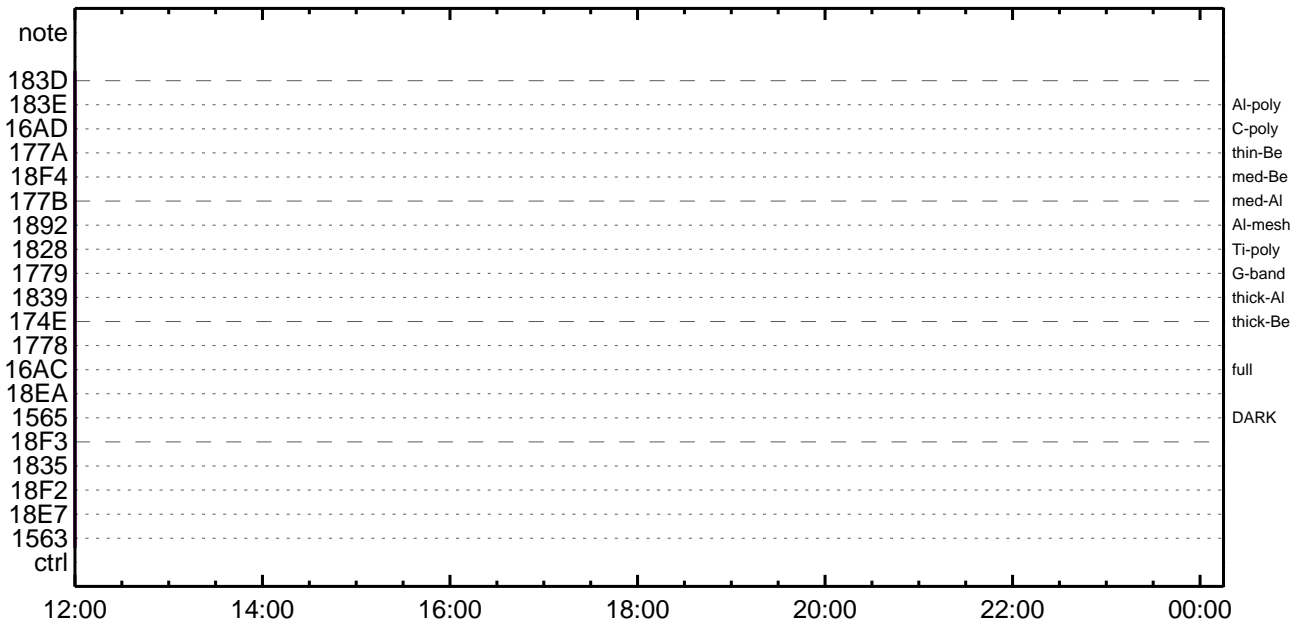
CMDI #0713 2012/07/09



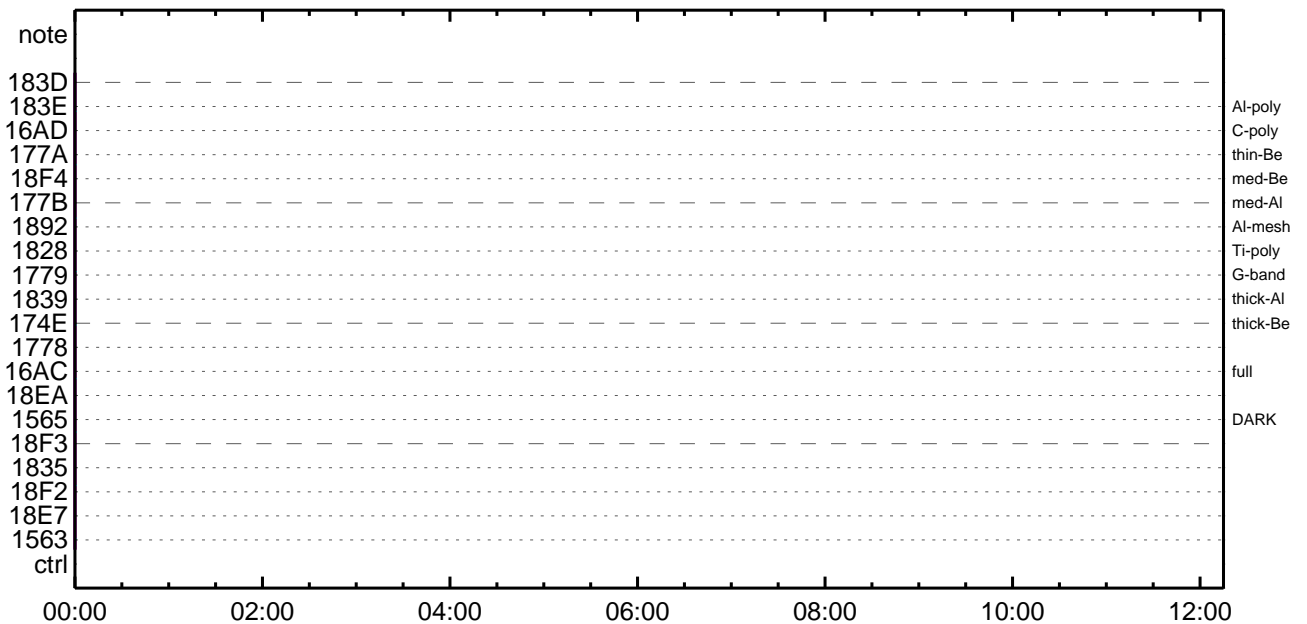
CMDI #0713 2012/07/10



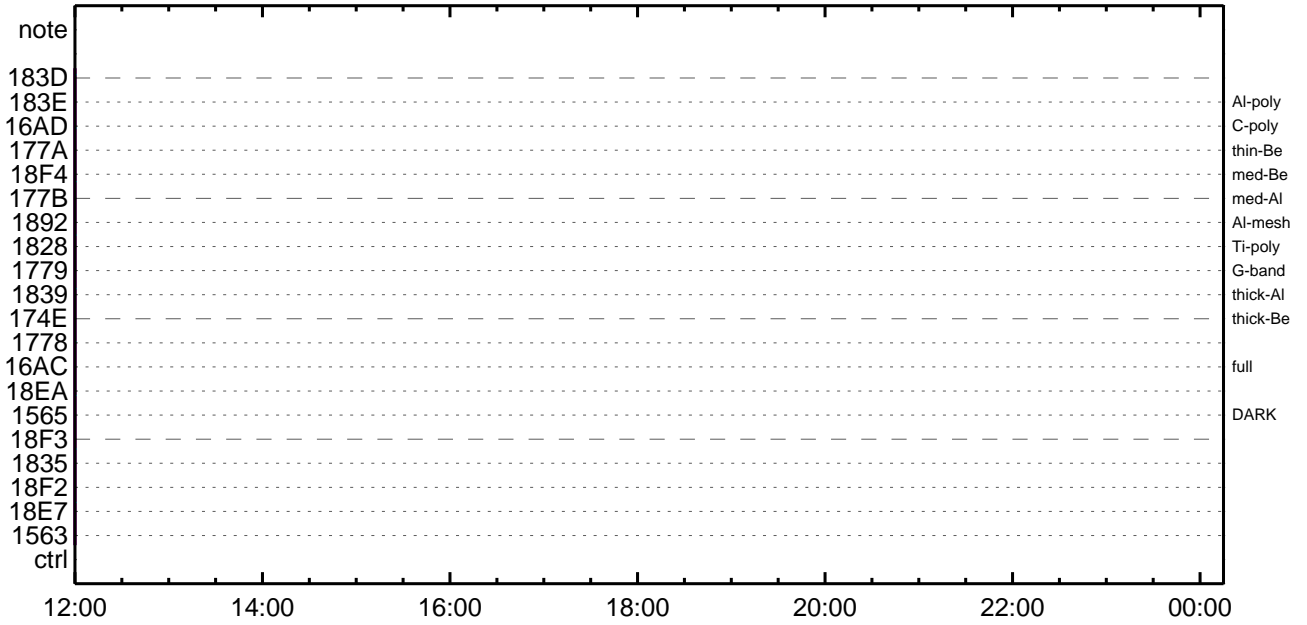
CMDI #0713 2012/07/10



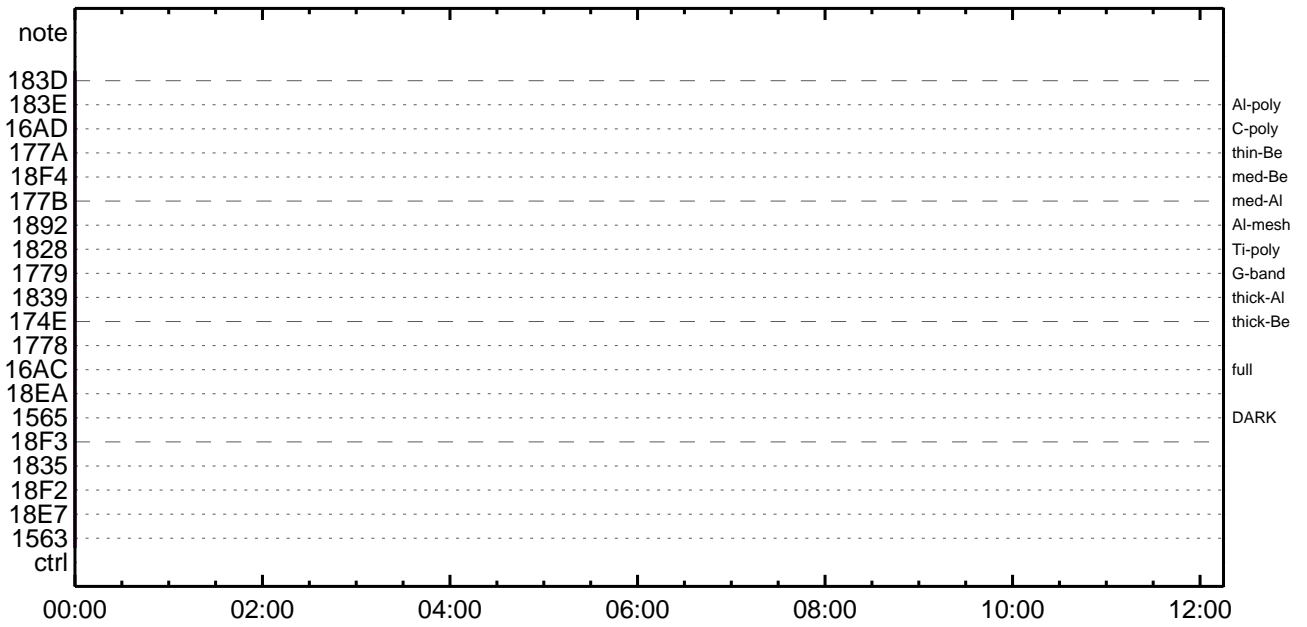
CMDI #0713 2012/07/11



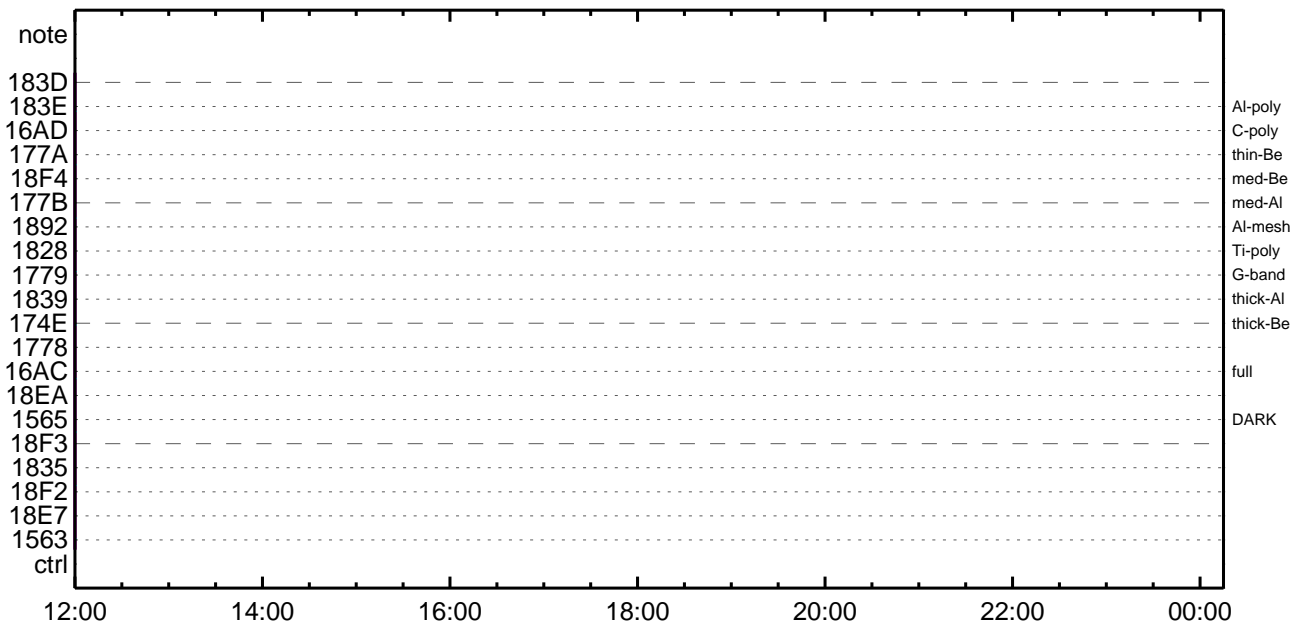
CMDI #0713 2012/07/11



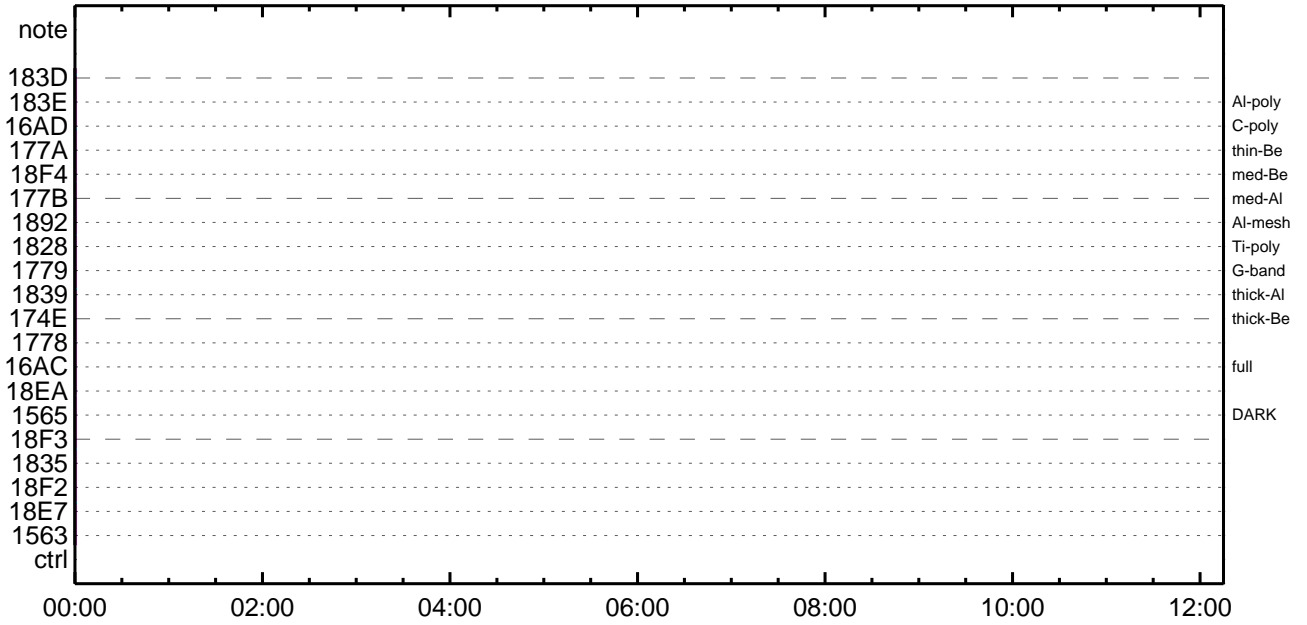
CMDI #0713 2012/07/12



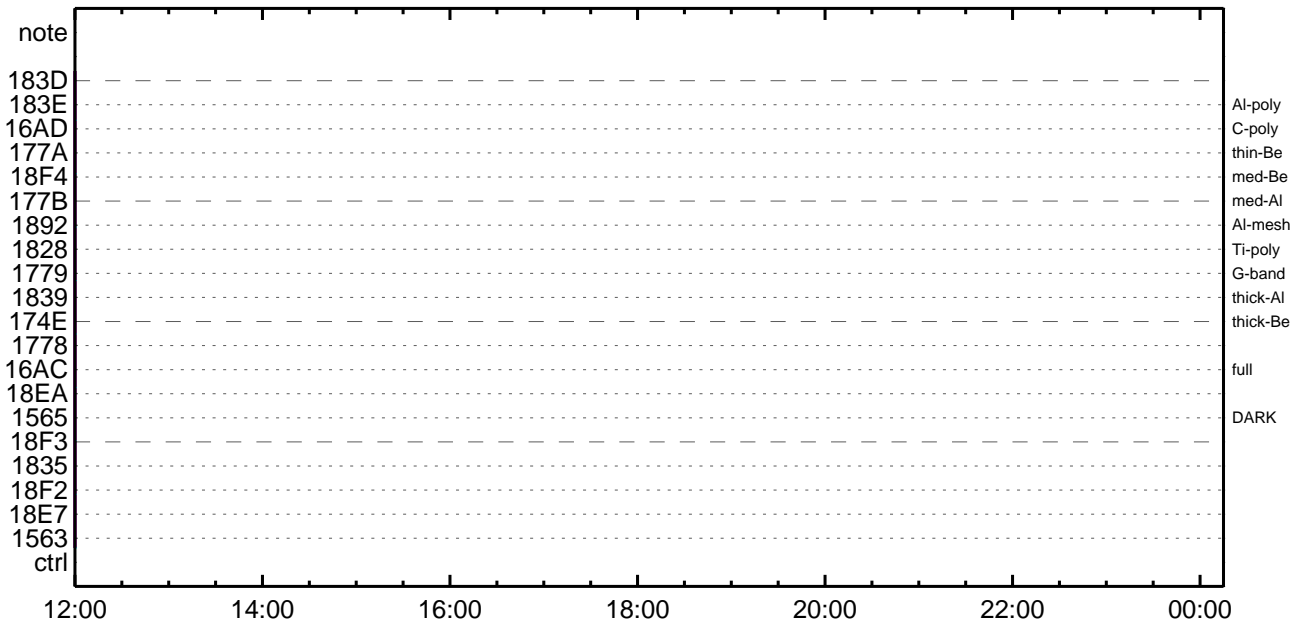
CMDI #0713 2012/07/12



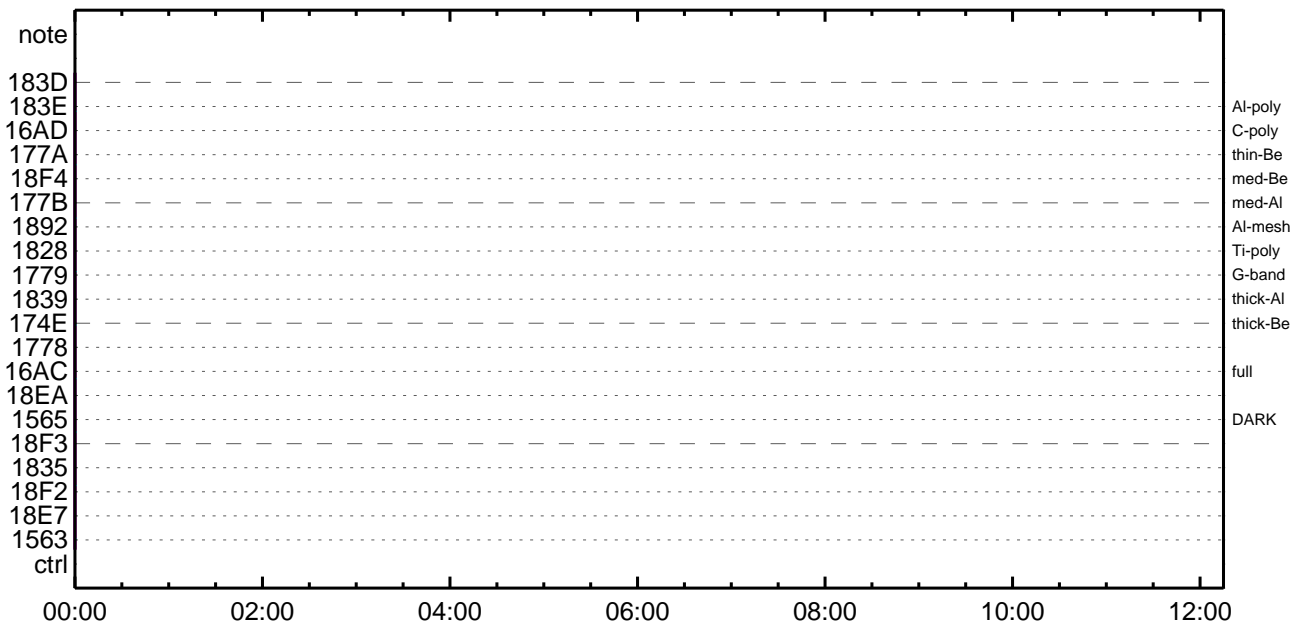
CMDI #0713 2012/07/13



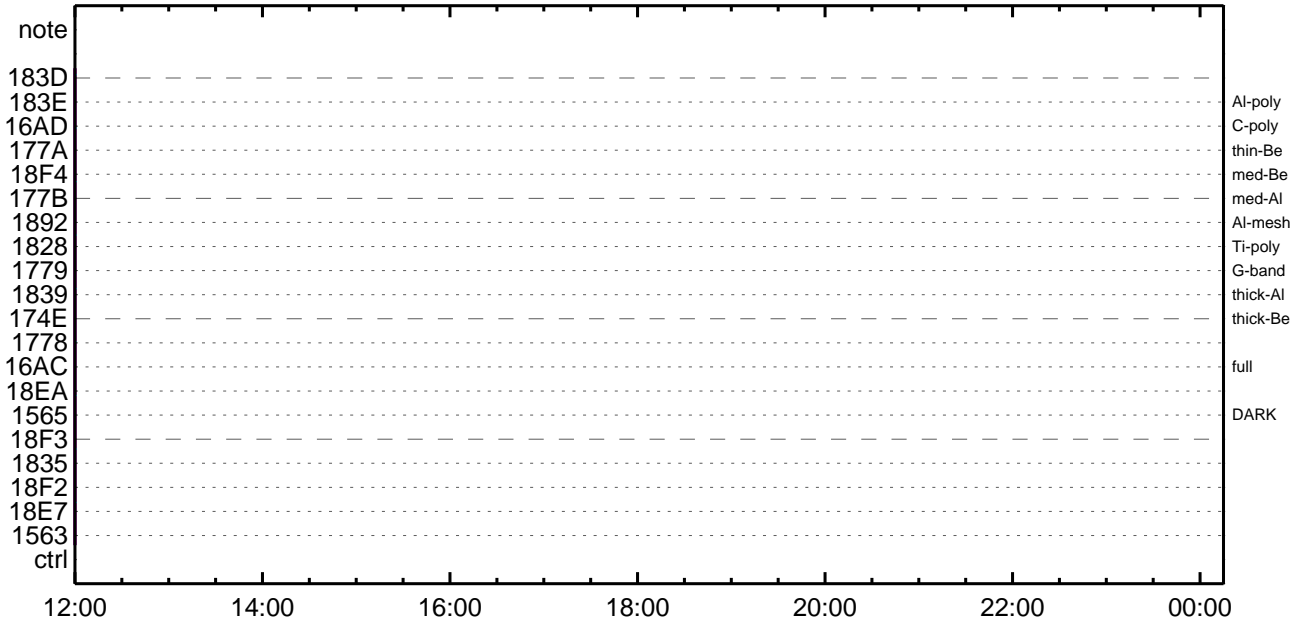
CMDI #0713 2012/07/13



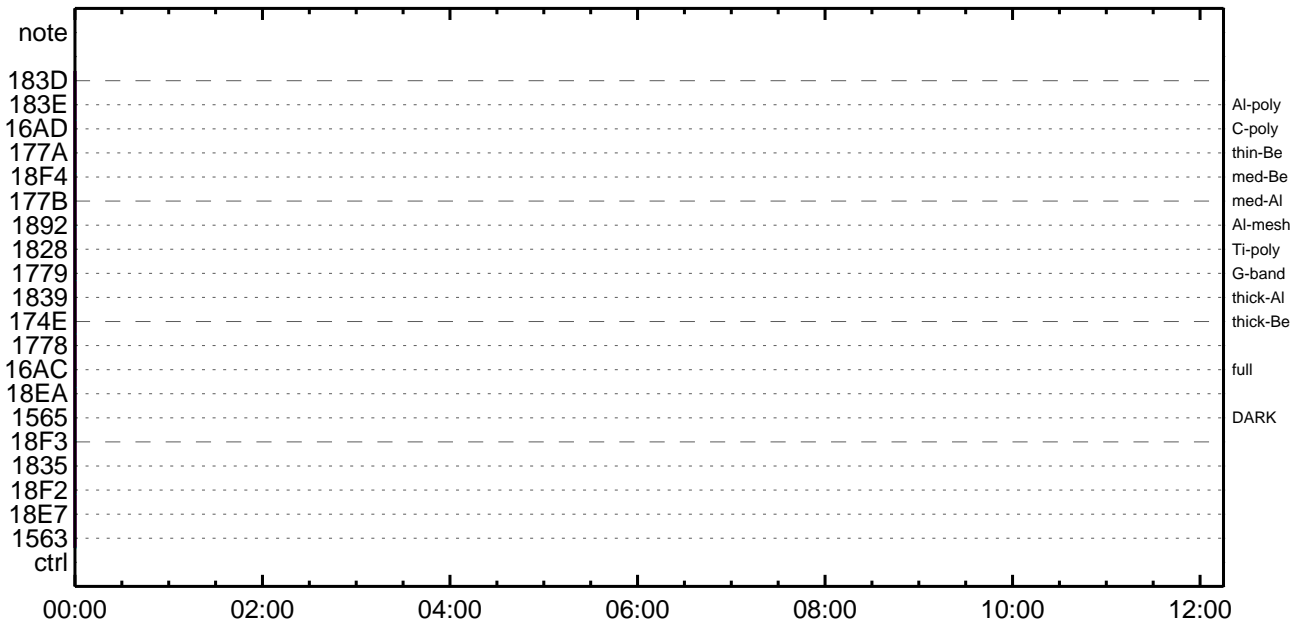
CMDI #0713 2012/07/14



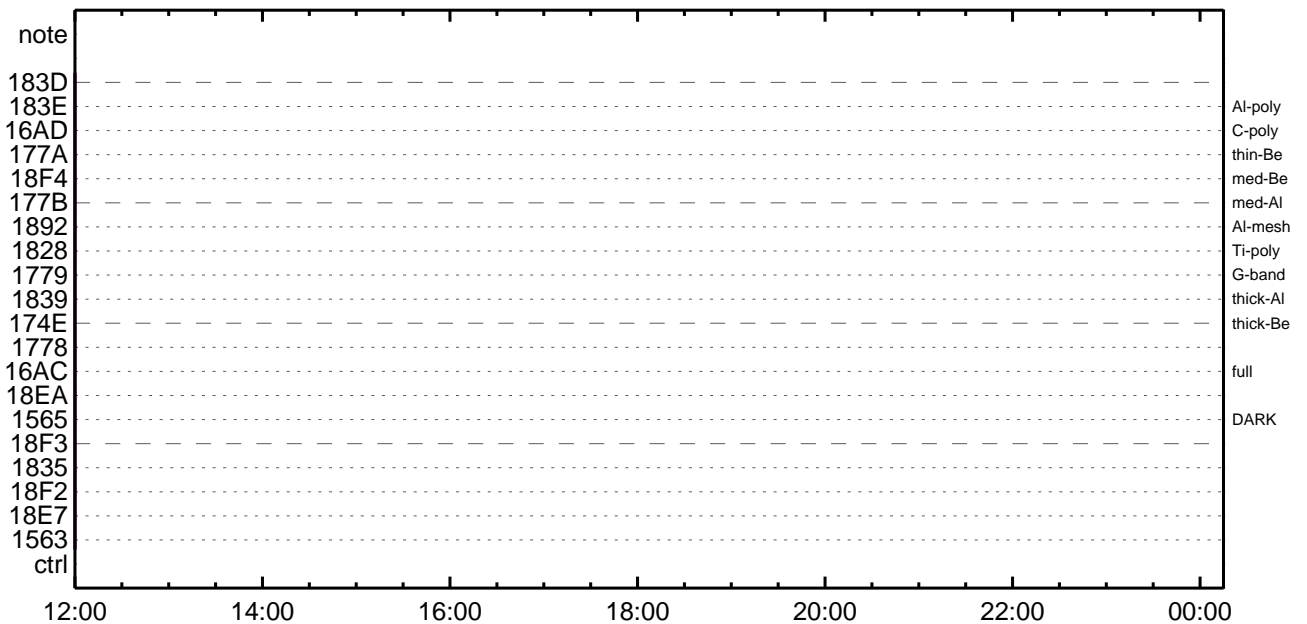
CMDI #0713 2012/07/14



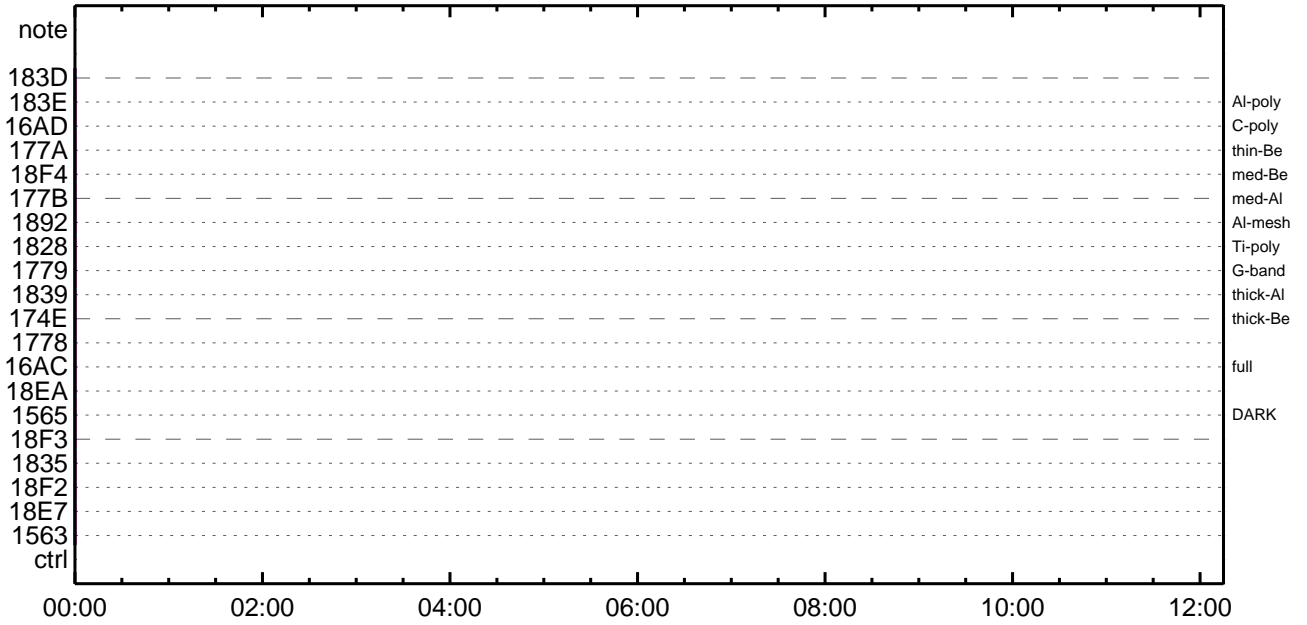
CMDI #0713 2012/07/15



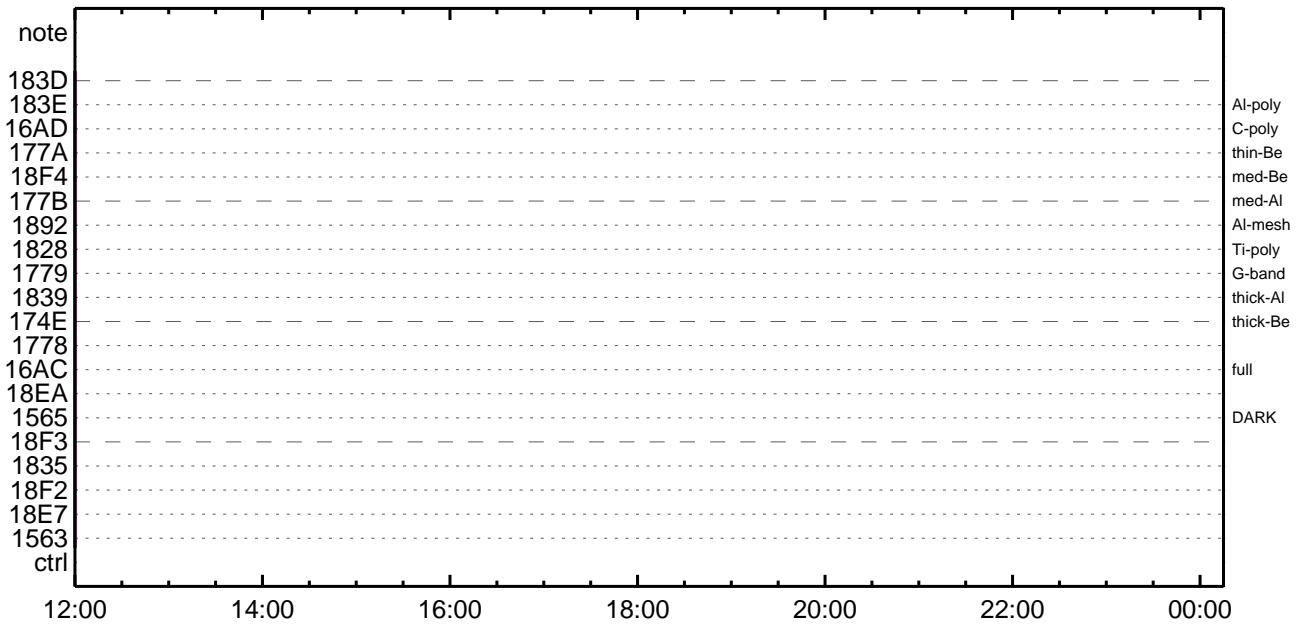
CMDI #0713 2012/07/15



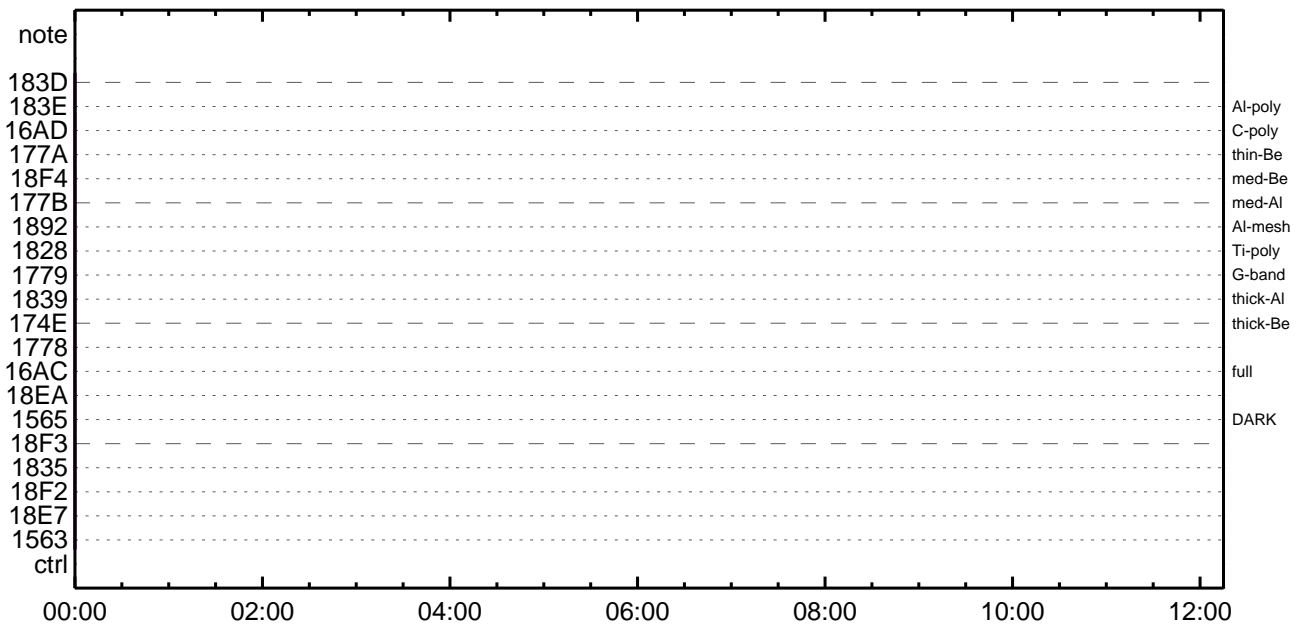
CMDI #0713 2012/07/16



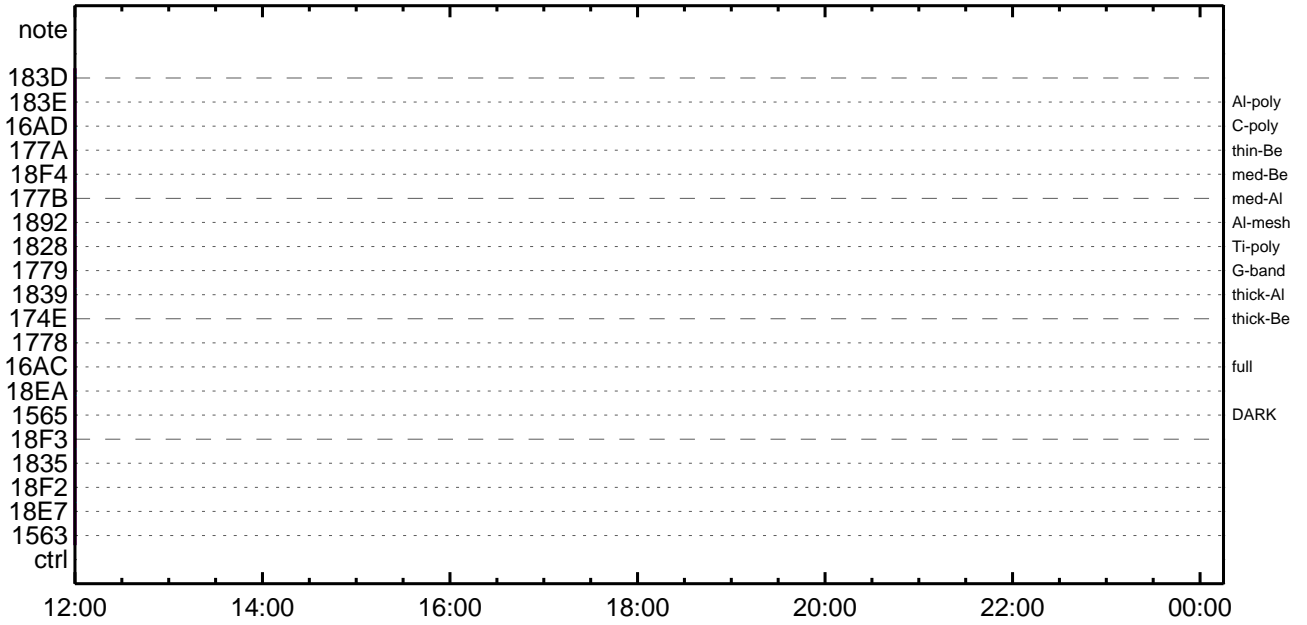
CMDI #0713 2012/07/16



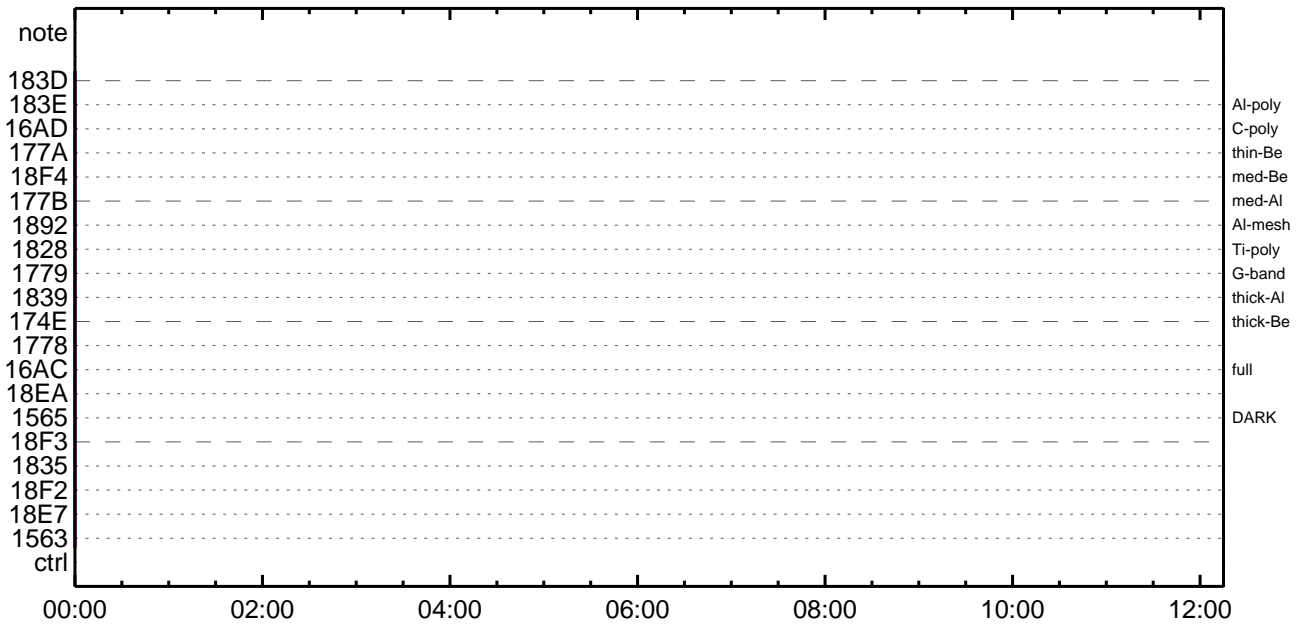
CMDI #0713 2012/07/17



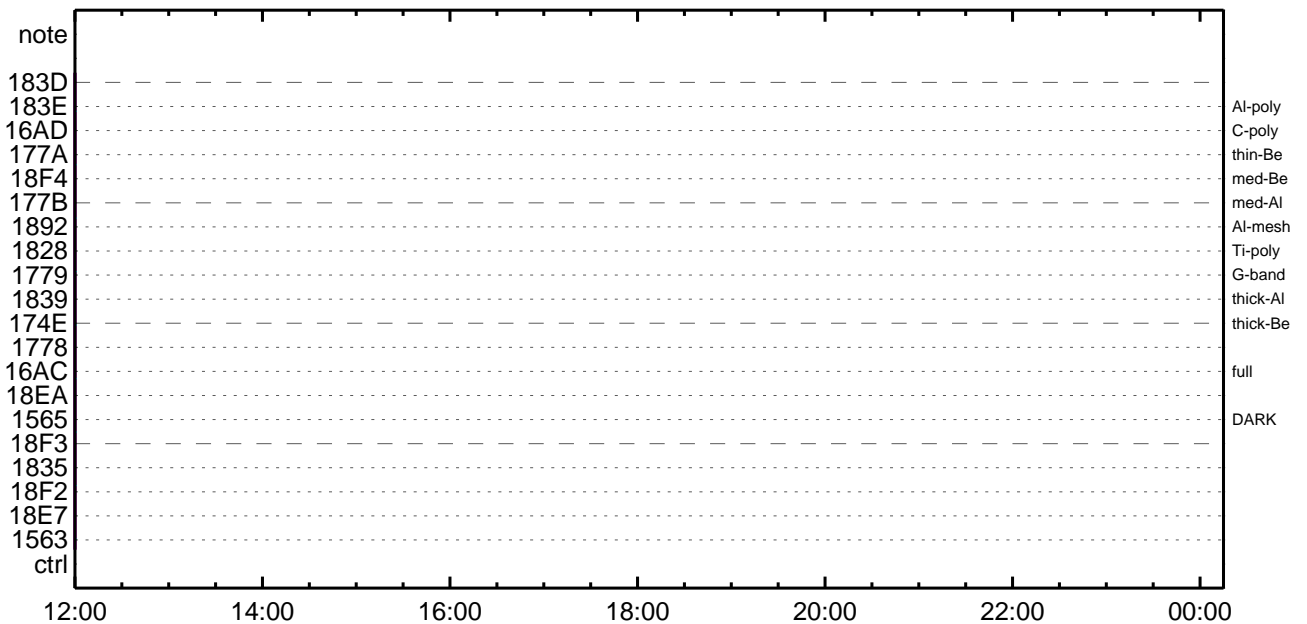
CMDI #0713 2012/07/17



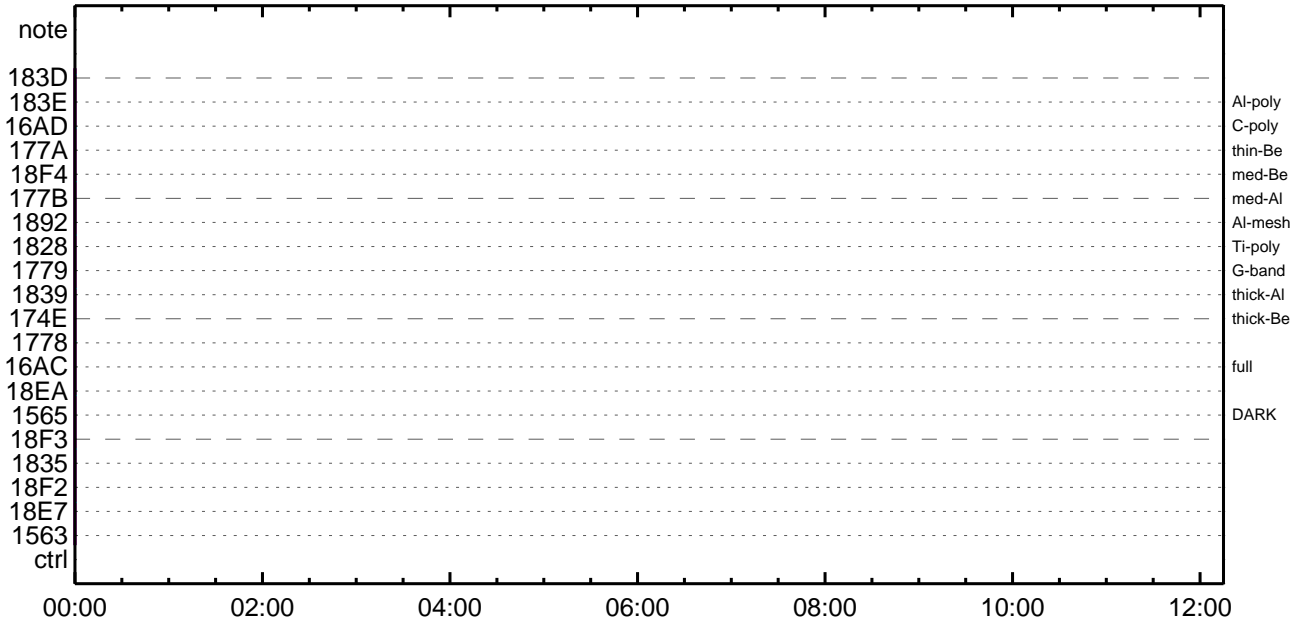
CMDI #0713 2012/07/18



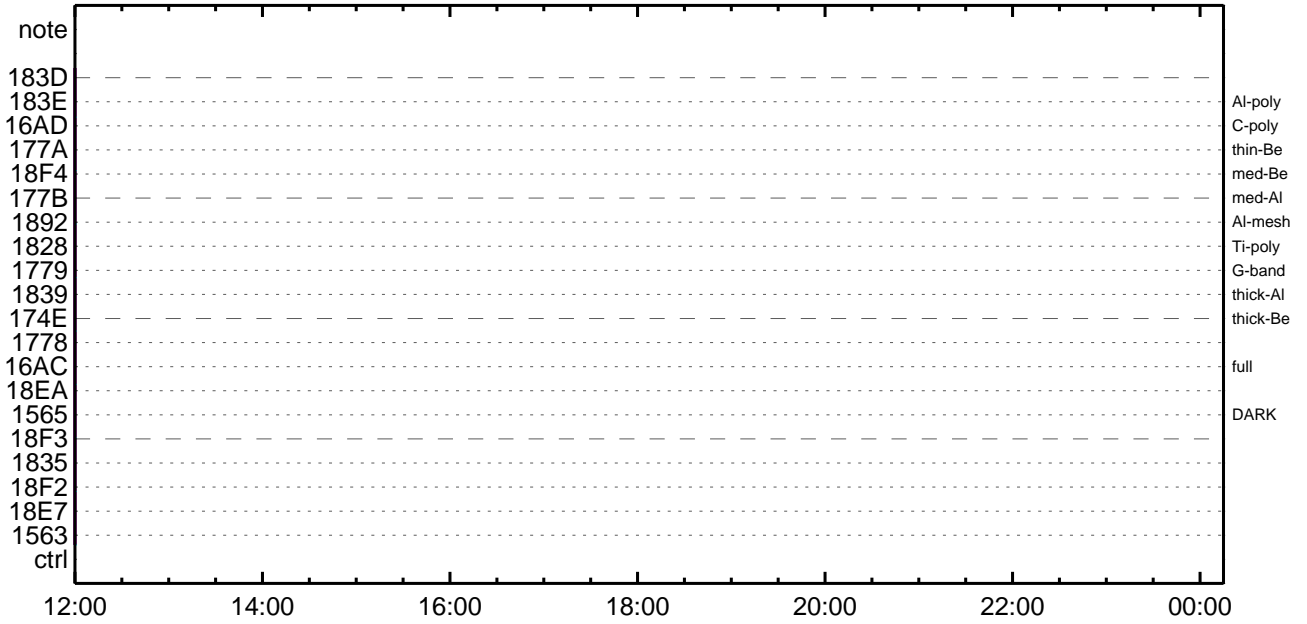
CMDI #0713 2012/07/18



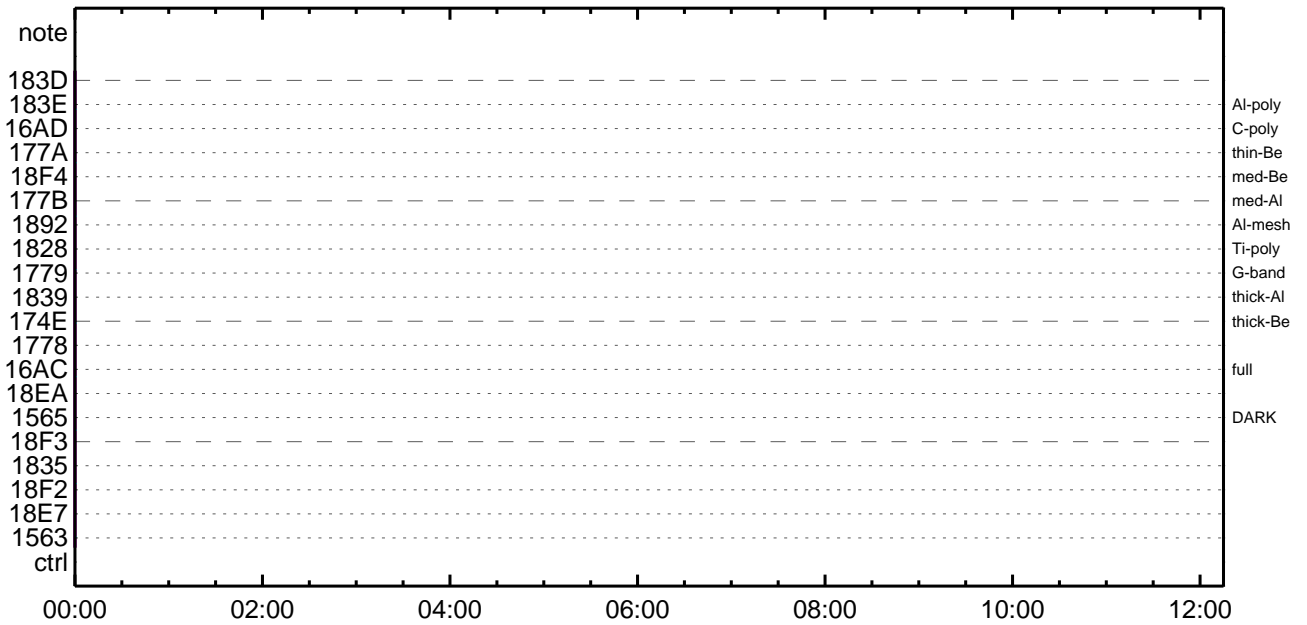
CMDI #0713 2012/07/19



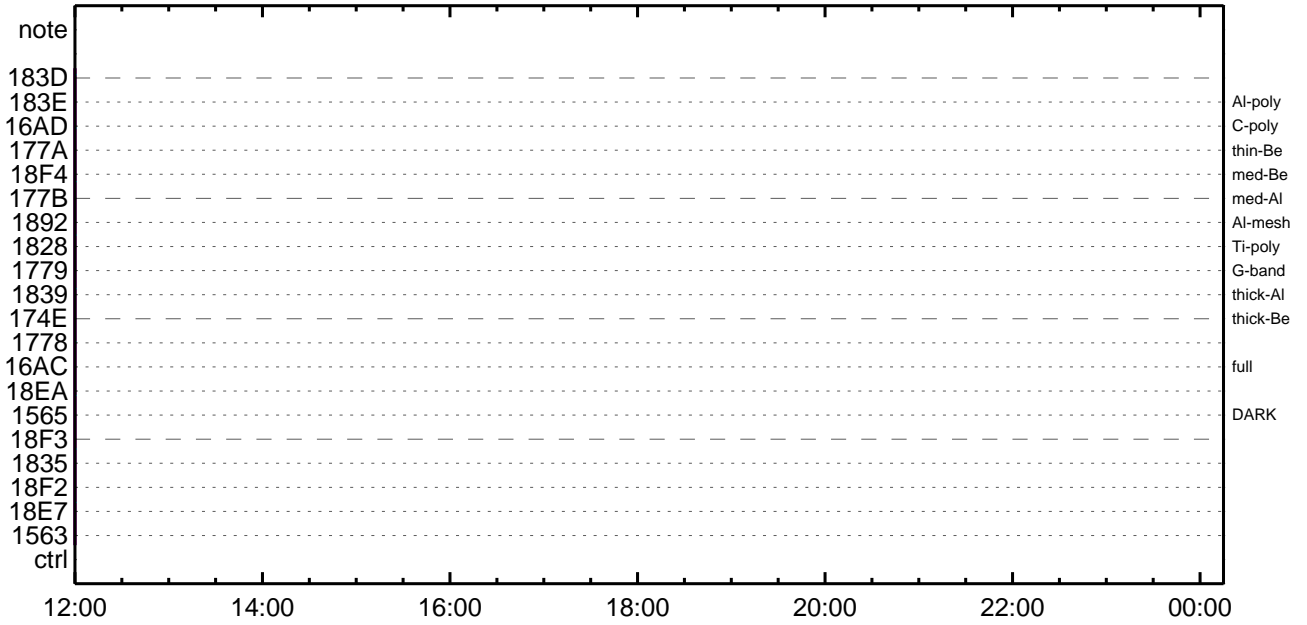
CMDI #0713 2012/07/19



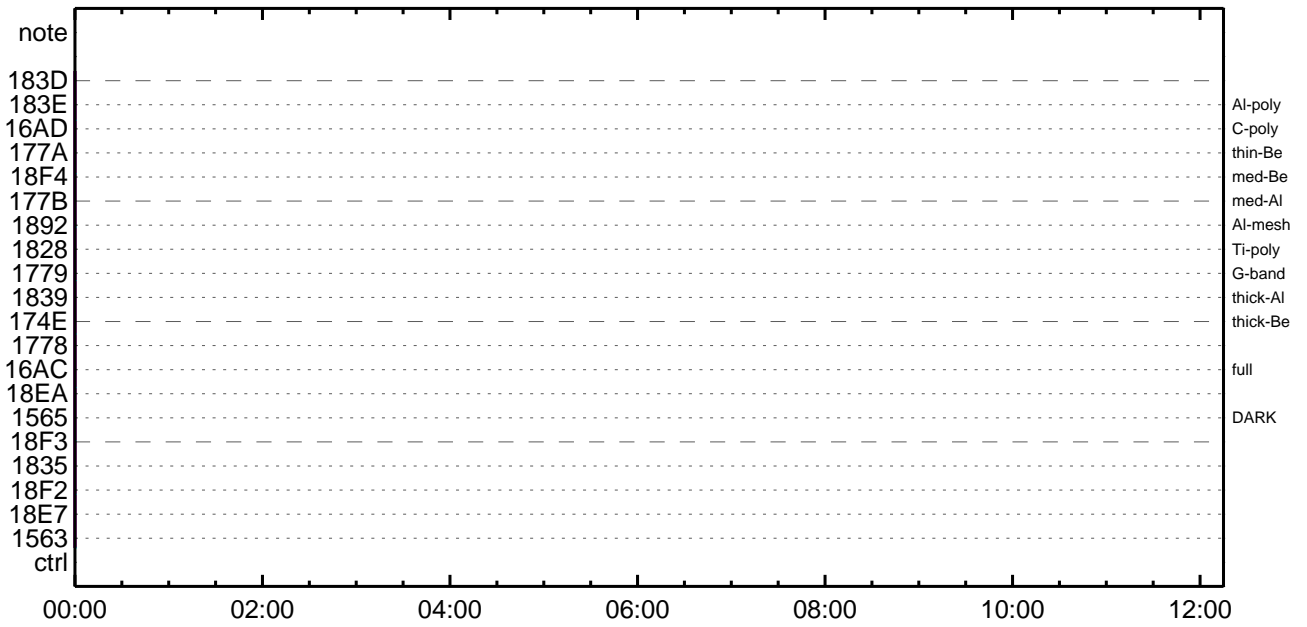
CMDI #0713 2012/07/20



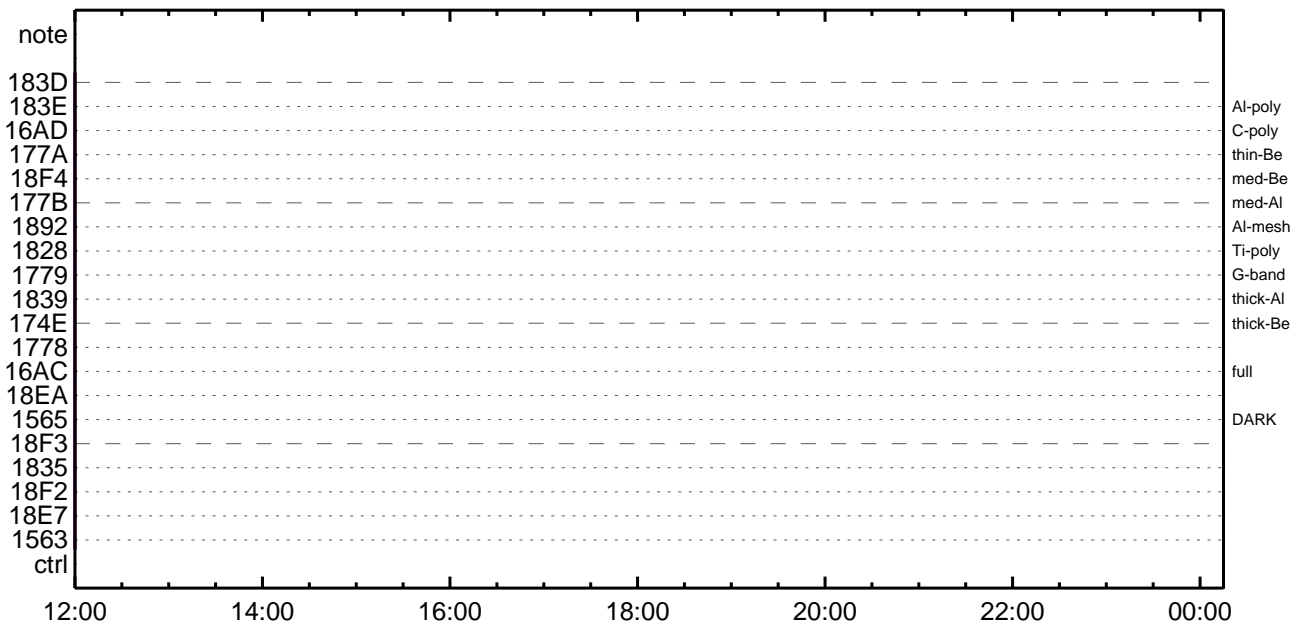
CMDI #0713 2012/07/20



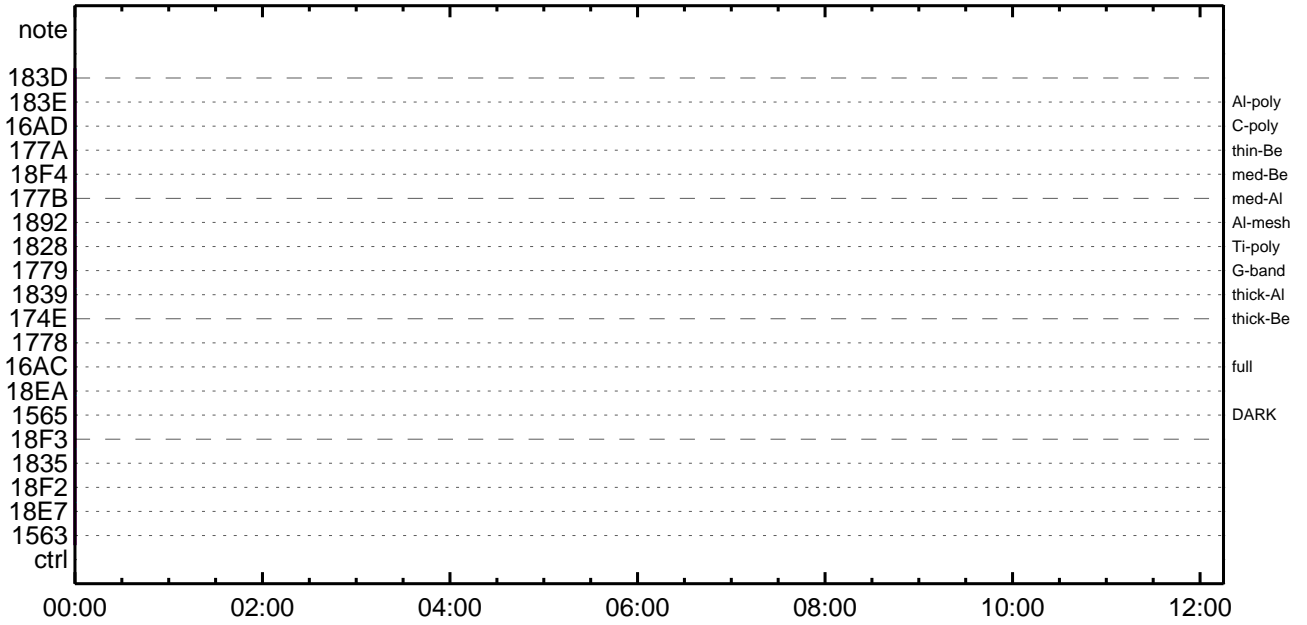
CMDI #0713 2012/07/21



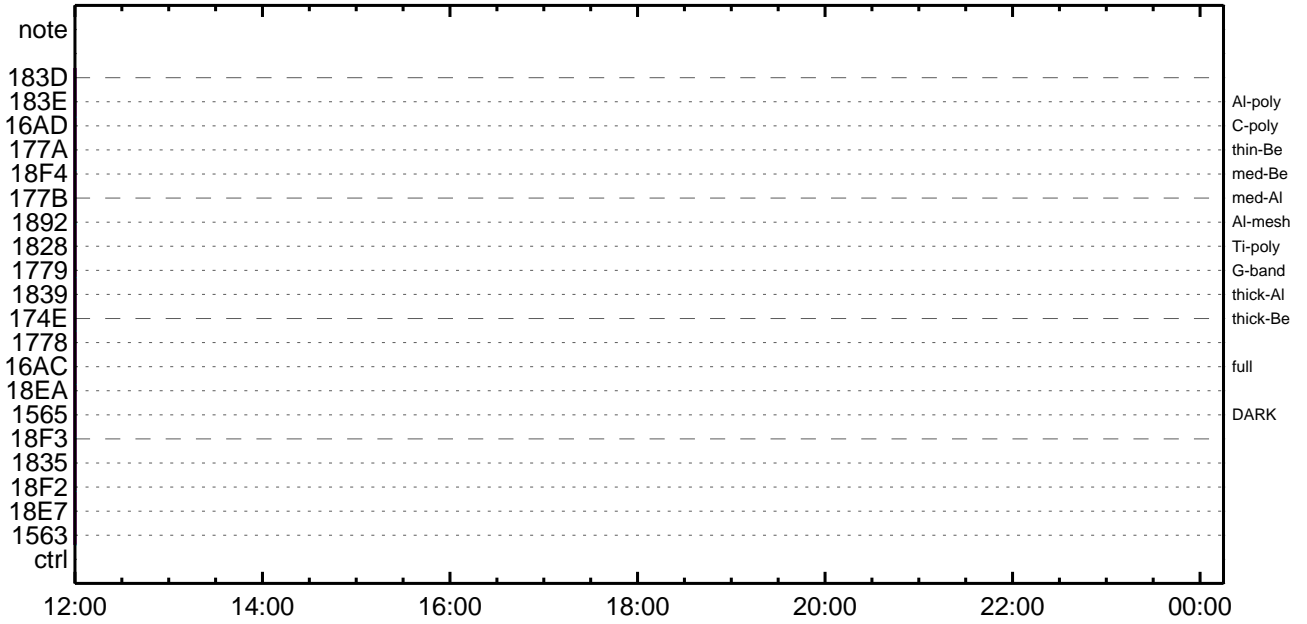
CMDI #0713 2012/07/21



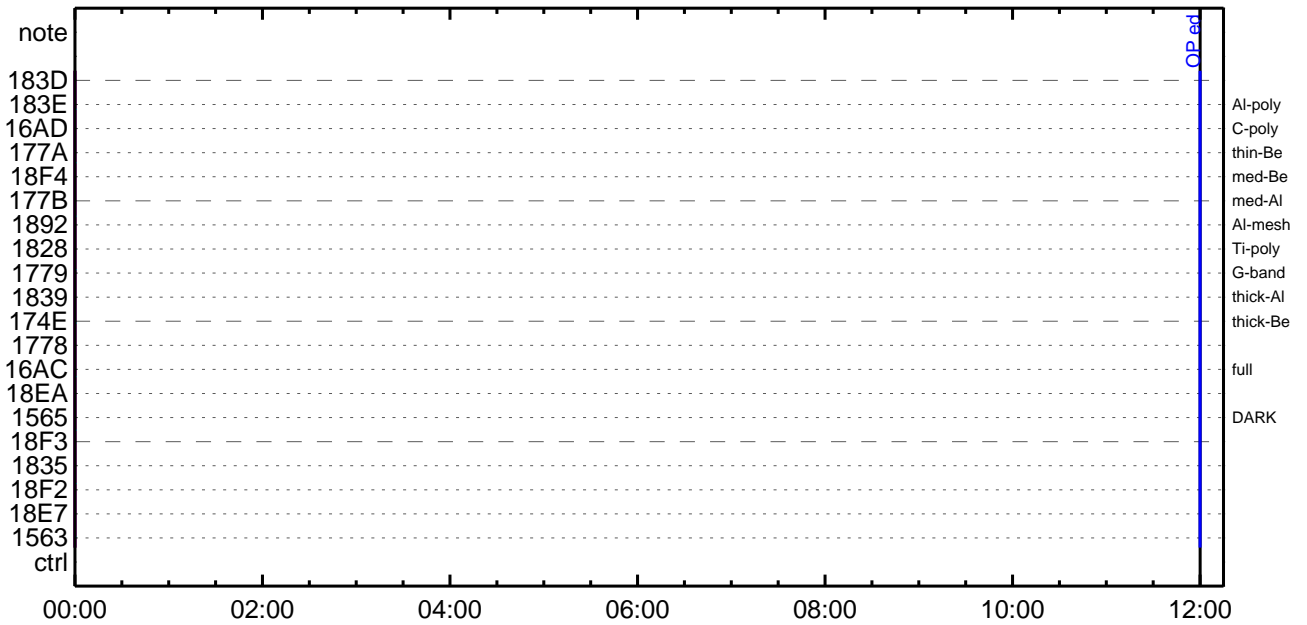
CMDI #0713 2012/07/22

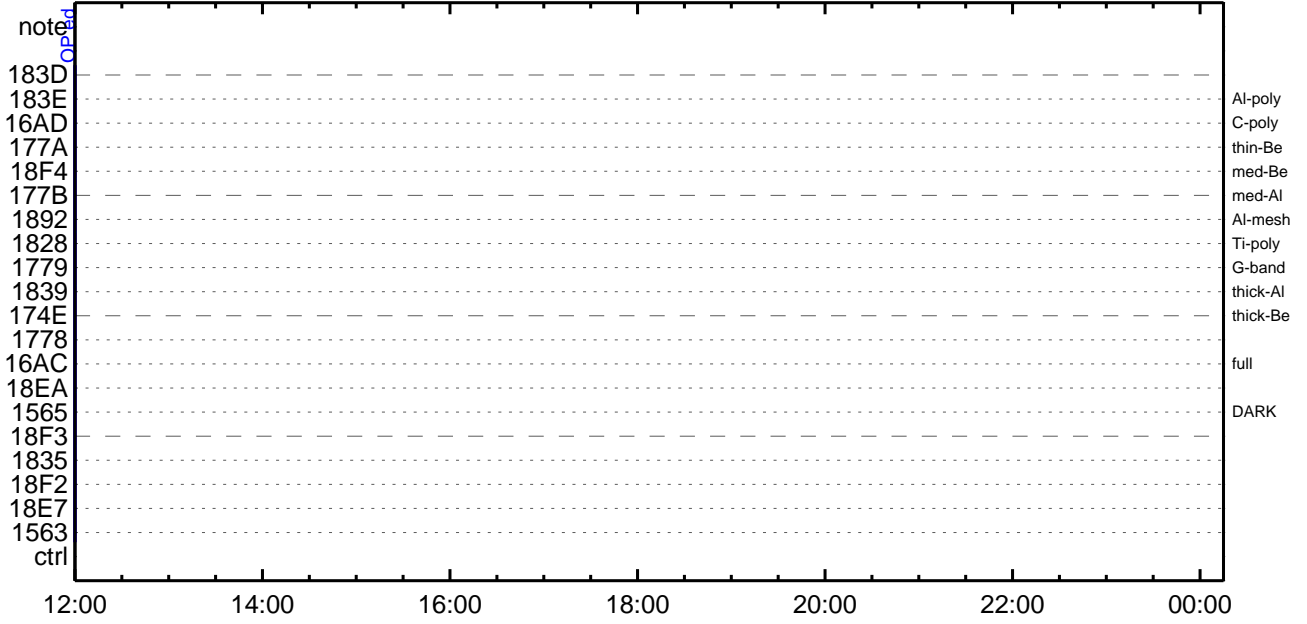


CMDI #0713 2012/07/22



CMDI #0713 2012/07/23






```

0096 C.          0300; SET EDUMP 0100 0100 0100 0100 0100 0100
0097 C.
0098 C. TI 2012-06-19 10:11:00.0
0099 +. TI 2012-06-19 10:11:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          0100 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2012-06-19 10:11:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          0100 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2012-06-19 10:11:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          0100 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2012-06-19 10:15:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          0100 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C.          0100 [HK1_TI_CMD_ENA/DIS] EQ ENA
0116 C.          0100 [HK1_TI_CMD_NUM] EQ 4
0117 C.          0100 [HK1_NEXT_EXEC_PIM] EQ DHU
0118 C.          0100 [HK1_NEXT_EXEC_DC] EQ 0xB3
0119 C.
0120 C.
0121 C. *****
0122 C. TI 2012-06-19 10:15:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.          0100 [HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.          0100 [HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.          0100 [HK1_DMP_BLOCK_NUM] EQ 3
0131 C.          0100 [HK1_DMP_REPEAT_NUM] EQ 0
0132 C.          0100 [HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.          0100 [HK1_PKT_FORM_NO] EQ 7
0136 C.          0100 [HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.          0100 [HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.          0100 [HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.          0100 [HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 C.          0100 [HK1_DMP_CHK_FLG] EQ NON
0142 C.
0143 C.
0144 C. RAM ID=TI_TBL 0100 0100 0100 0100 0100 0100
0145 C.
0146 C. DHU 0100 0100 0100 0100 0100 0100
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.          0100 [HK1_PKT_FORM_NO] EQ 2
0150 C.          0100 [HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.          0100 [HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.          0100 [HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2012-06-19 10:15:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C.
0162 C.          [ ] [HK1_TI_CMD_NUM] EQ 1 CNTUP [ ]
0163 C.
0164 C. ***** SOT END *****
0165 C.
0166 C. ***** XRT START *****
0167 C. Execute, after the success of OP upload.
0168 +. TI 2012-06-19 10:15:00.0
0169 DC 07-F0 MDP_XRT_MODE_STBY
0170 BC (c3)
0171 C.          [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0172 C.
0173 C. ***** XRT END *****
0174 C. Stop EIS observation and temporarily disable EIS mode changes
0175 C.
0176 C.
0177 C. ***** Start EIS operation (TI set) *****
0178 C. Execute, after the success of OP upload.
0179 C. Set EIS TI-commands
0180 +. TI 2012-06-19 10:15:30.0
0181 DC 07-FC EIS_MODE_MANU
0182 BC (21 02)
0183 +. TI 2012-06-19 10:15:40.0
0184 DC 07-FC EIS_MODE_CHG_DIS
0185 BC (22)
0186 C.          [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0187 C. ***** End EIS operation (TI set) *****
0188 C.
0189 C.
0190 C.
0191 C. ***** MDP 0100 0100 0100 0100 0100 0100 *****
0192 C. (0100 0100 0100 0100 0100 0100)
0193 S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-952 2012-06-19 12:54:51 113 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYŞ;¼Y³YÞYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëççãâ•µ°Æ»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ËÈèµ•íÉ;ËÈÈ¼°ÇÖã•çç¼í¹ççí;çÀ®, ùã¹ãèããçÁ+ç®ã•ãèããçãÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç" °ÆÀ, Í×ÈYããLOSããççã»p´Öãð¹íí, ç. ; çÉÖÍ×ãÈXÁÖONãí¹ÖãÈçããçãÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY-¾ÖÁÖã-°ÁÀèã•ççç; ç°È²¼ççí°ÆÀ, ¼èççççç¼Á¹Öã¹çç;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. ç" RESTART;ËPT1;Ëç•çççç¼í¹ççí; ç°È²¼ççí°ÆÀ¹Öã»ç°; çDCBC-150çççÈçã;ç
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYËÁÚÁØ;ËÁ•Á°²óÈð;Ë, áãí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ç-¼«Æ°Áã»ßç•ççç, á; ç°È²¼ççç¼Á¹Öã¹çç;ç
0055 C. YçYÓYÉYËÁÚÁØãÁ•Á°²óÈðç-¼áçç¼í¹ççí°ÆÀ¹Öã»ç°; çDCBC-150çççÈçã;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. ç" RESTART;ËPT2;Ëç•çççç¼í¹ççí; ç°È²¼ççí°ÆÀ¹Öã»ç°; çDCBC-151çççÈçã;ç
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYËÁÚÁØ;ËÁ•Á°²óÈð;Ë, áãí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áã»ß;çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áã»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```



```
0096 C.
0097 C.
0098 C.
0099 . C. ***** MDP 'ûÃîñî»ò¼ÿñÊÃĐñ¹ñèDCBC•x²è *****
0100 C. (¼á°îÿÓÿÃÿÈÿËÿËÿáÿçÿèñË¼ññ¼Ã»Ûñ¹ñè)
0101 . S. DC-BC dcbc-402:DCBC
0102 (MDP_known_event)
0103 C.
0104 C.
0105 . C. ***** ÿĐÿ¹•Ï Daily±;îÑñÊ´Øñ¹ñèDCBC•x²è *****
0106 . S. DC-BC dcbc-153:DCBC
0107 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0108 C.
0109 C.
0110 . C. ;ãLOSÿÁÿ§ÿÃÿ¼Ã»Û;ã
0111 C.
0112 . C. ***** LOS *****
0113 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-954 2012-06-19 12:54:51 152 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁã•µ°È»Í×ÁÇãíYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÈ;ÈãÈ¼°ÇÔã•ã¿¼í¹çãí;çÀ®, ùã¹ãÈãÈãÇÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-263:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. YçYÁY×Yí;¼YÉãíÁ°ãÈSTS_CHKãðOFFãÈã¹ãè
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKãðONãÈã¹ãè
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700 [ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 C.
0059 C. *****
0060 C. SOT TI command set
0061 C. *****
0062 C. Execute, after the success of TBL upload.
0063 +. TI 2012-06-19 10:15:18.0
0064 DC 07-F0 MDP_SOT_MODE_OBSV
0065 BC (40)
0066 . C. -----
0067 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0068 C. -----
0069 C.
0070 C.
0071 C. ***** XRT START *****
0072 C.
0073 +. DC 07-F0 MDP_XRT_CTRL_MANU
0074 BC (c1)
0075 +. DC 07-F0 MDP_XRT_MODE_STBY
0076 BC (c3)
0077 . C. ----- Success Verify ? OK / NG_____
0078 C.
0079 C. XRT Obs. Table Upload
0080 . S. RAM ram-291:MDP_OBS_X
0081 ( )
0082 C.
0083 +. DC 07-F0 MDP_DUMP_XRTTBL
0084 BC (84 07 00 00 00 3a d4)
0085 . C. ----- Comparison Check ? OK / ERR _____
0086 C.
0087 C.
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 01 b1 b1 04 04)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 02 b1 b1 08 08)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 03 b1 b1 08 08)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 04 b1 b1 06 06)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 85 83 06 06)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 80 80 20 20)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 80 80 20 08)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 08 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0f 80 80 06 06)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 10 80 80 08 08)
0110 + DC 07-F0 MDP_XRT_FLD_ENA
0111 BC (d8)
0112 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0113 BC (c8)
0114 + DC 07-F0 MDP_XRT_AEC_RESET
0115 BC (d0)
0116 + DC 07-F0 MDP_XRT_ARS_DIS
0117 BC (d5)
0118 + DC 07-F0 MDP_XRT_FLD_RESET
0119 BC (da)
0120 + DC 07-F0 MDP_XRT_QT_PROG_SET
0121 BC (c4 13)
0122 + DC 07-F0 MDP_XRT_FL_PROG_SET
0123 BC (c5 02)
0124 . C. ----- Success Verify ? OK / NG ____
0125 C.
0126 C.
0127 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0128 C.
0129 + DC 07-F0 MDP_XRT_MODE_OBSV
0130 BC (c2)
0131 + TI 2012-06-19 10:15:02.0
0132 DC 07-F0 MDP_XRT_MODE_OBSV
0133 BC (c2)
0134 . C. ----- Success Verify ? OK / NG ____
0135 C.
0136 C. ***** XRT END *****
0137 C.
0138 . C. ***** MDP `uãîñï»ö¼ýñëãðñ¹ñèDCBC•x²è *****
0139 C. (¼áºîÿÓÿÃÿÈÿÞÿËÿÿáÿçÿèñÈ¾¼ññ¼Ã»Ûñ¹ñè)
0140 . S. DC-BC dcbc-402:DCBC
0141 (MDP_known_event)
0142 C.
0143 C.
0144 . C. ***** ÿDÿ¹•Ï Daily±¿ÎññÈ´Øñ¹ñèDCBC•x²è *****
0145 . S. DC-BC dcbc-153:DCBC
0146 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0147 C.
0148 C.
0149 . C. ;ãLOSÿÁÿ§ÿÿÃÿ~¼Ã»Û;ä
0150 C.
0151 . C. ***** LOS *****
0152 C.
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*** OP Sequence for XRT ***

```

2012/06/19 10:26:00.0 AOCs_Ore-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 00 57 ce 00 72
2012/06/19 11:02:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2012/06/19 11:02:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2012/06/19 11:02:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT       1 07-F0 e8
2012/06/19 11:05:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP       1 07-F0 e9
2012/06/19 11:59:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2012/06/19 12:00:00.0 AOCs_Ore-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 03 00 00 00 00
2012/06/19 12:02:26.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION        4 07-F8 22 fe 97 00
2012/06/19 12:02:46.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA          1 07-F0 d8
2012/06/19 12:02:48.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA       1 07-F0 c8
2012/06/19 12:02:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET         1 07-F0 d0
2012/06/19 12:02:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2012/06/19 12:02:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2012/06/19 12:02:56.0 XRT_QT_PROG_SET_445_OG [0x1bd]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 14
2012/06/19 12:02:58.0 XRT_FL_PROG_SET_449_OG [0x1c1]
                        MDP_XRT_FL_PROG_SET       2 07-F0 c5 0d
2012/06/19 12:03:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2012/06/19 12:40:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2012/06/19 12:40:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2012/06/19 12:40:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT       1 07-F0 e8
2012/06/19 12:43:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP       1 07-F0 e9
2012/06/19 13:11:00.0 XRT_Custom_418_OG [0x1a2]
2012/06/19 13:12:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2012/06/19 14:19:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2012/06/19 14:19:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2012/06/19 14:19:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT       1 07-F0 e8
2012/06/19 14:22:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP       1 07-F0 e9
2012/06/19 14:57:30.0 XRT_Custom_418_OG [0x1a2]
2012/06/19 14:58:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2012/06/19 15:57:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2012/06/19 15:57:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2012/06/19 15:57:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT       1 07-F0 e8
2012/06/19 16:00:00.0 AOCs_Ore-point_Start_3_OG [0x099]
                        AOCU_NM                    5 02-76 01 00 00 00 00
2012/06/19 16:00:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP       1 07-F0 e9
2012/06/19 17:36:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2012/06/19 17:36:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2012/06/19 17:36:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT       1 07-F0 e8
2012/06/19 17:39:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP       1 07-F0 e9
2012/06/19 18:20:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2012/06/19 18:20:56.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2012/06/19 18:21:00.0 AOCs_Ore-point_Start_4_OG [0x09a]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2012/06/19 18:21:16.0 XRT_FLD_DIS_402_OG [0x192]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2012/06/19 18:21:18.0 XRT_FLRCTRL_DIS_433_OG [0x1b1]
                        MDP_XRT_FLRCTRL_DIS       1 07-F0 c9
2012/06/19 18:21:20.0 XRT_ARS_DIS_438_OG [0x1b6]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2012/06/19 18:23:58.0 XRT_QT_PROG_SET_423_OG [0x1a7]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 07
2012/06/19 18:24:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2012/06/19 18:31:00.0 AOCs_Ore-point_Start_5_OG [0x09b]
                        AOCU_NM                    5 02-76 00 00 00 00 ac cd
2012/06/19 18:47:00.0 AOCs_Ore-point_Start_6_OG [0x09c]

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2012/06/19	19:14:30.0	XRT_CTRL_MANU_408_OG [0x198]	AOCU_NM	5	02-76	00	00	00	d6	67
2012/06/19	19:14:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/19	19:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/19	19:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/19	20:03:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/19	20:03:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/19	20:04:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/06/19	20:04:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00	00	00	00	00
2012/06/19	20:04:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/06/19	20:04:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/06/19	20:06:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/19	20:07:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2012/06/19	20:20:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/19	20:36:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	00	00	29	99
2012/06/19	20:53:00.0	XRT_CTRL_MANU_408_OG [0x198]	AOCU_NM	5	02-76	00	00	00	53	33
2012/06/19	20:53:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/19	20:53:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/19	20:56:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/19	21:41:30.0	AOCS_ORe-point_Start_9_OG [0x09f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/19	21:51:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00	d6	36	b7	8e
2012/06/19	22:07:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00	b4	b5	db	75
2012/06/19	22:31:30.0	XRT_CTRL_MANU_408_OG [0x198]	AOCU_NM	5	02-76	00	ac	5b	00	00
2012/06/19	22:31:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/19	22:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/19	22:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/19	23:16:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/19	23:32:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	b4	b5	24	8b
2012/06/19	23:42:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	d6	36	48	72
2012/06/19	23:52:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00	29	ca	b7	8e
2012/06/20	00:10:00.0	XRT_CTRL_MANU_408_OG [0x198]	AOCU_NM	5	02-76	00	4b	4b	db	75
2012/06/20	00:10:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/20	00:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/20	00:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/20	00:40:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/20	00:56:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00	53	a5	00	00
2012/06/20	01:12:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00	4b	4b	24	8b
2012/06/20	01:21:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	29	db	48	72
2012/06/20	01:48:30.0	XRT_CTRL_MANU_408_OG [0x198]	AOCU_NM	5	02-76	00	00	00	00	00
2012/06/20	01:48:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/20	01:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/20	01:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/20	03:24:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/20	03:24:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/20	03:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/20	03:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/20	03:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2012/06/20	03:30:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	03	00	00	00	00
2012/06/20	03:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/06/20	03:32:46.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/06/20	03:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/06/20	03:32:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/06/20	03:32:52.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/20	03:32:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/20	03:32:56.0	XRT_QT_PROG_SET_445_OG [0x1bd] MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2012/06/20	03:32:58.0	XRT_FL_PROG_SET_449_OG [0x1c1] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2012/06/20	03:57:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/20	03:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/20	04:56:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/20	04:56:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/20	04:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/20	04:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/20	05:36:00.0	XRT_Custom_418_OG [0x1a2]							
2012/06/20	05:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/20	05:52:54.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/20	05:52:56.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/06/20	05:53:00.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	00	00	00	00	00
2012/06/20	05:53:16.0	XRT_FLD_DIS_402_OG [0x192] MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/06/20	05:53:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/06/20	05:53:20.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/20	05:55:58.0	XRT_QT_PROG_SET_423_OG [0x1a7] MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2012/06/20	05:56:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/20	06:02:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/20	06:03:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	03	00	00	00	00
2012/06/20	06:05:26.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/06/20	06:05:46.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/06/20	06:05:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/06/20	06:05:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/06/20	06:05:52.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/20	06:05:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/20	06:05:56.0	XRT_QT_PROG_SET_445_OG [0x1bd] MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2012/06/20	06:05:58.0	XRT_FL_PROG_SET_449_OG [0x1c1] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2012/06/20	06:06:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/20	06:37:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/20	06:37:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/20	06:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/20	06:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/20	07:14:30.0	XRT_Custom_418_OG [0x1a2]							
2012/06/20	07:15:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/20	07:59:54.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/20	08:00:00.0	AOCS_ORe-point_Start_19_OG [0x0a9] AOCU_NM	5	02-76	04	00	00	00	00
2012/06/20	08:17:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/20	08:17:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/20	08:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/20	08:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2012/06/20	09:58:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/20	09:58:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/20	09:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/20	10:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/20	10:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/20	11:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00	
2012/06/20	11:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/06/20	11:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/06/20	11:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/06/20	11:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/06/20	11:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/06/20	11:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/20	11:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2012/06/20	11:02:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2012/06/20	11:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/20	11:39:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/20	11:39:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/20	11:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/20	11:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/20	12:10:00.0	XRT_Custom_418_OG [0x1a2]					
2012/06/20	12:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/20	13:18:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/20	13:18:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/20	13:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/20	13:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/20	13:48:30.0	XRT_Custom_418_OG [0x1a2]					
2012/06/20	13:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/20	14:56:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/20	14:56:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/20	14:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/20	14:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/20	15:35:30.0	XRT_Custom_418_OG [0x1a2]					
2012/06/20	15:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/20	15:37:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/20	15:37:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/20	15:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/20	15:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/20	15:44:30.0	XRT_Custom_418_OG [0x1a2]					
2012/06/20	15:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/20	16:35:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/20	16:35:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/20	16:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/20	16:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/20	17:21:00.0	XRT_Custom_418_OG [0x1a2]					
2012/06/20	17:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/20	17:58:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/20	17:58:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/06/20	17:59:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00	
2012/06/20	17:59:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	

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2012/06/20	17:59:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/06/20	17:59:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/06/20	18:01:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2012/06/20	18:02:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/20	18:13:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/20	18:13:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/20	18:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/20	18:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/20	19:52:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/20	19:52:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/20	19:52:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/20	19:55:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/20	20:08:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/20	20:09:00.0	AOCs_Or-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03	00 00 00 00
2012/06/20	20:11:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2012/06/20	20:11:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/06/20	20:11:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/06/20	20:11:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/06/20	20:11:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/06/20	20:11:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/20	20:11:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14
2012/06/20	20:11:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2012/06/20	20:35:30.0	XRT_Custom_418_OG [0x1a2]					
2012/06/20	20:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/20	21:30:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/20	21:30:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/20	21:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/20	21:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/20	22:11:31.0	XRT_Custom_418_OG [0x1a2]					
2012/06/20	22:12:31.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/20	23:09:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/20	23:09:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/20	23:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/20	23:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/20	23:39:30.0	XRT_Custom_418_OG [0x1a2]					
2012/06/20	23:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/21	00:47:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/21	00:47:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/21	00:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/21	00:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/21	01:18:00.0	XRT_Custom_418_OG [0x1a2]					
2012/06/21	01:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/21	02:26:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/06/21	02:26:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/06/21	02:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/06/21	02:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/06/21	02:56:30.0	XRT_Custom_418_OG [0x1a2]					
2012/06/21	02:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/06/21	03:54:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

