

# XRT Timeline to be uploaded on 2012/06/30

Period: 2012/06/30 09:28:00 - 2012/08/05 12:00:00

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Normal mode

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## XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)

Term	Pointing (x, y)	Comment
06/30 17:41:30 - 06/30 17:48:24	Fixed ( 0.0, 0.0)	synoptic, shifted -21.5 min
07/01 06:03:00 - 07/01 06:09:54	Fixed ( 0.0, 0.0)	synoptic
07/01 17:59:00 - 07/01 18:05:54	Fixed ( 0.0, 0.0)	synoptic, shifted -4.0 min
07/02 05:28:30 - 07/02 05:35:24	Fixed ( 0.0, 0.0)	synoptic, shifted -34.5 min
07/02 18:18:30 - 07/02 18:24:24	Fixed ( 0.0, 0.0)	synoptic, shifted 15.5 min
07/03 06:04:00 - 07/03 06:10:54	Fixed ( 0.0, 0.0)	synoptic, shifted 1.0 min

**PROG= 14 1-time(s)**

Subr= 1 1-time(s) 12.0sec	Seqn= 7 1-time(s) 4.0sec	Seqn= 5 1-time(s) 2.0sec	Seqn= 8 1-time(s) 4.0sec	Seqn= 4 1-time(s) 2.0sec
Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec	Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #18F9: Coronal cavity (FW1 open)-HOP114 with ARs-DPCM 2x2-768FOV-AEC0-5min cad

Term	Pointing (x, y)	Comment
06/30 18:39:30 - 06/30 20:48:24	Fixed ( -531.0, -810.0)	# ToO HOP 114, coronal cavity observations.

**PROG= 04 Inf.-time(s)**

Subr= 1 5-time(s) 300.0sec	Subr= 2 1-time(s) 2.0sec
Seqn= 99 1-time(s) 4.0sec	Seqn= 14 1-time(s) 4.0sec
Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec	Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/Ti-poly Open/thick-Al close Safe Norm 4.00s Obs 2x2 768x768 (1064, 1048) DPCM 0 0 2.0sec	

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #18FA: Filament with ARs FW1=open Al/mesh and Ti/poly fixed exp 2m cadence

Term	Pointing (x, y)	Comment
06/30 20:51:30 - 06/30 22:18:24	Track ( 832.0, 296.2) <sup>06/30 20:48:30</sup> * Filament observations for EIS tests.	

**PROG= 19 1-time(s)**

Subr= 1 1-time(s) 120.0sec	Subr= 2 1-time(s) 2.0sec
Seqn= 72 1-time(s) 2.0sec	Seqn= 13 1-time(s) 2.0sec
Open/Al-mesh Open/Ti-poly close Safe Norm 4.00s Obs 1x1 512x512 (1064, 1048) Q=92 0 0 2.0sec	Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) Q=90 0 0 2.0sec
Open/Ti-poly Open/thick-Al close Safe Norm 8.00s Obs 1x1 512x512 (1064, 1048) Q=92 0 0 2.0sec	Seqn= 25 2-time(s) 2.0sec
	Seqn= 47 1-time(s) 25.0sec
	Open/Ti-poly Open/thick-Al close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #18FC: High cadence Ti/Poly (25s) Al/mesh and Gband context 512FOV offset ROI

Term	Pointing (x, y)	Comment
06/30 22:21:30 - 07/01 05:14:30	Track ( 397.2, -337.3) <sup>06/30 22:18:30</sup> * AR 11512	

**PROG= 10 Inf.-time(s)**

Subr= 1 2-time(s) 2.0sec	Subr= 2 48-time(s) 2.0sec
Seqn= 13 1-time(s) 2.0sec	Seqn= 47 1-time(s) 25.0sec
Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) Q=90 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 25 2-time(s) 2.0sec	

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #18F8: AR Standard-A(Filter-Ratio) with 384PFB, FW1=Open, 512x512 at 1064 1048, 100s cad

Term	Pointing (x, y)	Comment
07/01 06:13:00 - 07/01 07:59:54	Track ( 456.4, -336.1) <sup>07/01 06:10:00</sup> # Cont.	
07/01 17:39:30 - 07/01 17:55:54	Track ( 533.7, -334.0) <sup>07/01 17:00:00</sup> * AR 11512.	

07/02 05:38:30 - 07/02 07:33:00 Track ( 616.3, -331.0) <sup>Ⓢ 07/02 05:35:30</sup> # Cont.  
 07/02 17:03:00 - 07/02 17:30:00 Track ( 683.4, -327.6) <sup>Ⓢ 07/02 17:00:00</sup> \* AR 11512  
 07/03 06:15:30 - 07/03 07:59:54 Track ( 750.7, -323.1) <sup>Ⓢ 07/03 06:11:00</sup> # Cont.

**PROG= 01 Inf.-time(s)**

<b>Subr= 1</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
<b>Seqn= 17</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec		
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec		
<b>Seqn= 15</b>		<b>4-time(s)</b>		<b>2.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec		
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec		
<b>Subr= 2</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
<b>Seqn= 23</b>		<b>18-time(s)</b>		<b>2.0sec</b>											
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec		
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec		
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec		
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec		
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec		
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec		
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec		
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

**XOB #183D: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 100s cad**

Term	Pointing (x, y)	Comment
07/01 18:09:00 - 07/02 05:25:24	Track ( 541.2, -333.8) <sup>Ⓢ 07/01 18:06:00</sup>	# Cont.
07/02 18:27:30 - 07/03 06:00:54	Track ( 691.2, -327.2) <sup>Ⓢ 07/02 18:25:30</sup>	# Cont.

**PROG= 20 Inf.-time(s)**

<b>Subr= 1</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
<b>Seqn= 19</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec		
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec		
<b>Seqn= 1</b>		<b>4-time(s)</b>		<b>2.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
<b>Subr= 2</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
<b>Seqn= 49</b>		<b>18-time(s)</b>		<b>2.0sec</b>											
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec		
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec		
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec		
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec		
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec		
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

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**Flare mode**

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**XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)**

Term	Pointing (x, y)	Comment
06/30 18:39:30 - 06/30 20:48:24	Fixed ( -531.0, -810.0)	# ToO HOP 114, coronal cavity observations.
06/30 20:51:30 - 06/30 22:18:24	Track ( 832.0, 296.2) <sup>Ⓢ 06/30 20:48:30</sup>	* Filament observations for EIS tests.
06/30 22:21:30 - 07/01 05:14:30	Track ( 397.2, -337.3) <sup>Ⓢ 06/30 22:18:30</sup>	* AR 11512
07/01 06:13:00 - 07/01 07:59:54	Track ( 456.4, -336.1) <sup>Ⓢ 07/01 06:10:00</sup>	# Cont.
07/01 17:39:30 - 07/01 17:55:54	Track ( 533.7, -334.0) <sup>Ⓢ 07/01 17:00:00</sup>	* AR 11512.
07/01 18:09:00 - 07/02 05:25:24	Track ( 541.2, -333.8) <sup>Ⓢ 07/01 18:06:00</sup>	# Cont.
07/02 05:38:30 - 07/02 07:33:00	Track ( 616.3, -331.0) <sup>Ⓢ 07/02 05:35:30</sup>	# Cont.
07/02 17:03:00 - 07/02 17:30:00	Track ( 683.4, -327.6) <sup>Ⓢ 07/02 17:00:00</sup>	* AR 11512
07/02 18:27:30 - 07/03 06:00:54	Track ( 691.2, -327.2) <sup>Ⓢ 07/02 18:25:30</sup>	# Cont.
07/03 06:15:30 - 07/03 07:59:54	Track ( 750.7, -323.1) <sup>Ⓢ 07/03 06:11:00</sup>	# Cont.

**PROG= 13 1-time(s)**

<b>Subr= 1</b>		<b>30-time(s)</b>		<b>20.0sec</b>											
<b>Seqn= 88</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
<b>Seqn=100</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
<b>Subr= 2</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
<b>Seqn= 90</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
<b>Subr= 3</b>		<b>30-time(s)</b>		<b>60.0sec</b>											
<b>Seqn= 88</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		

Seqn= 60		1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Subr= 2		1-time(s)		2.0sec											
Seqn= 90		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec			
Subr= 3		30-time(s)		60.0sec											
Seqn= 88		1-time(s)		2.0sec											
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Seqn= 60		1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Subr= 2		1-time(s)		2.0sec											
Seqn= 90		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec			
Subr= 3		30-time(s)		60.0sec											
Seqn= 88		1-time(s)		2.0sec											
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Seqn= 60		1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Subr= 4		24-time(s)		600.0sec											
Seqn= 89		1-time(s)		2.0sec											
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec			
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec			
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

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### Active Region Search

\* \* \* \* \*

NOT USED

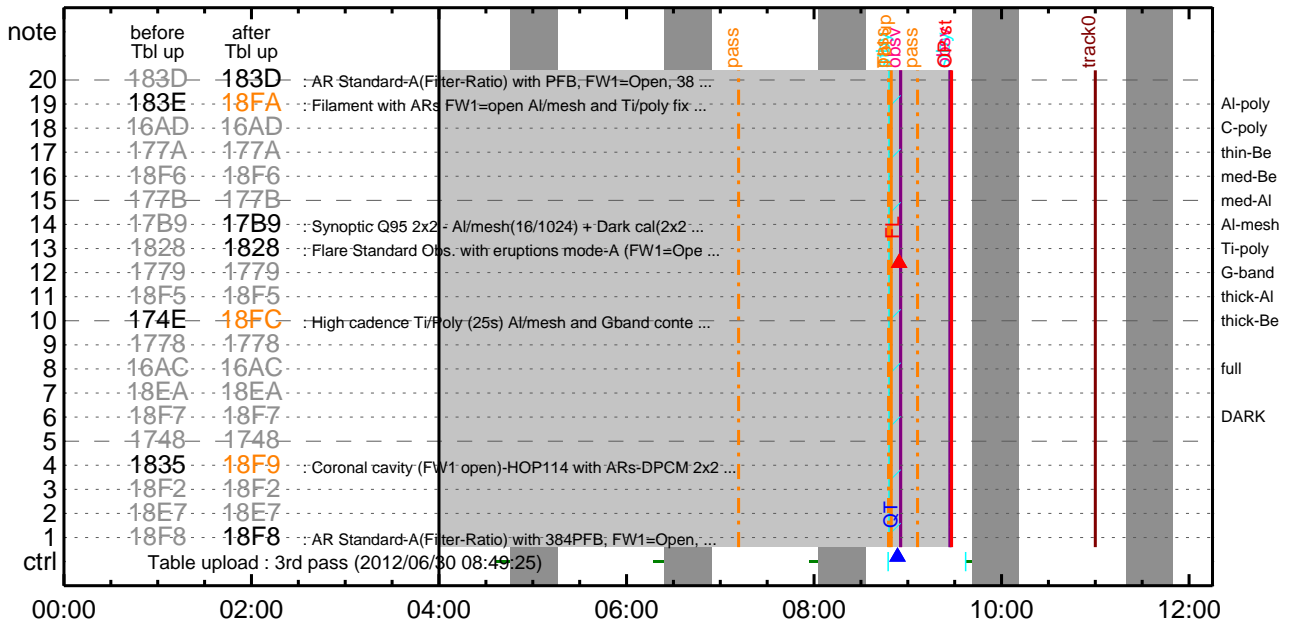
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### Flare Detection

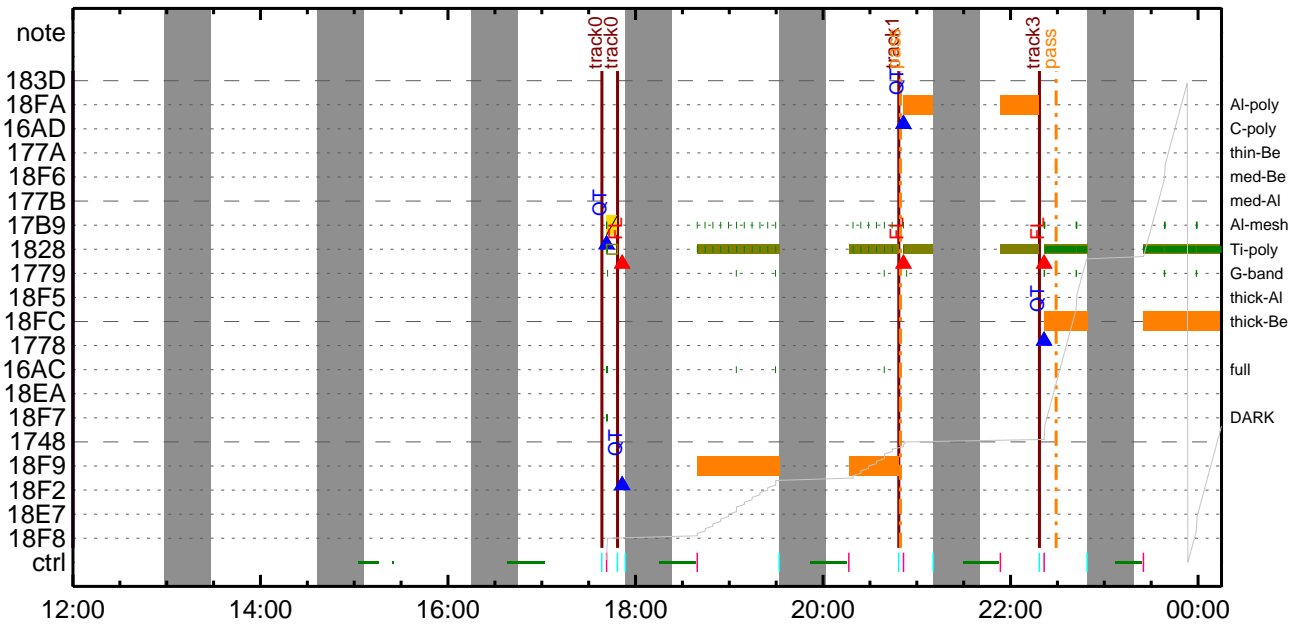
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FLD Patrol												
Term	Pointing (x, y)				Comment							
06/30 17:51:16 - 07/01 06:00:16	Fixed ( -531.0, -810.0)				# ToO HOP 114, coronal cavity observations.							
07/01 06:12:46 - 07/01 17:56:16	Track ( 456.4, -336.1) <sup>Ⓢ 07/01 06:10:00</sup>				# Cont.							
07/01 18:08:46 - 07/02 05:25:46	Track ( 541.2, -333.8) <sup>Ⓢ 07/01 18:06:00</sup>				# Cont.							
07/02 05:38:16 - 07/02 18:15:46	Track ( 616.3, -331.0) <sup>Ⓢ 07/02 05:35:30</sup>				# Cont.							
07/02 18:27:16 - 07/03 06:01:16	Track ( 691.2, -327.2) <sup>Ⓢ 07/02 18:25:30</sup>				# Cont.							
07/03 06:15:16 - 08/05 12:00:00	Track ( 750.7, -323.1) <sup>Ⓢ 07/03 06:11:00</sup>				# Cont.							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

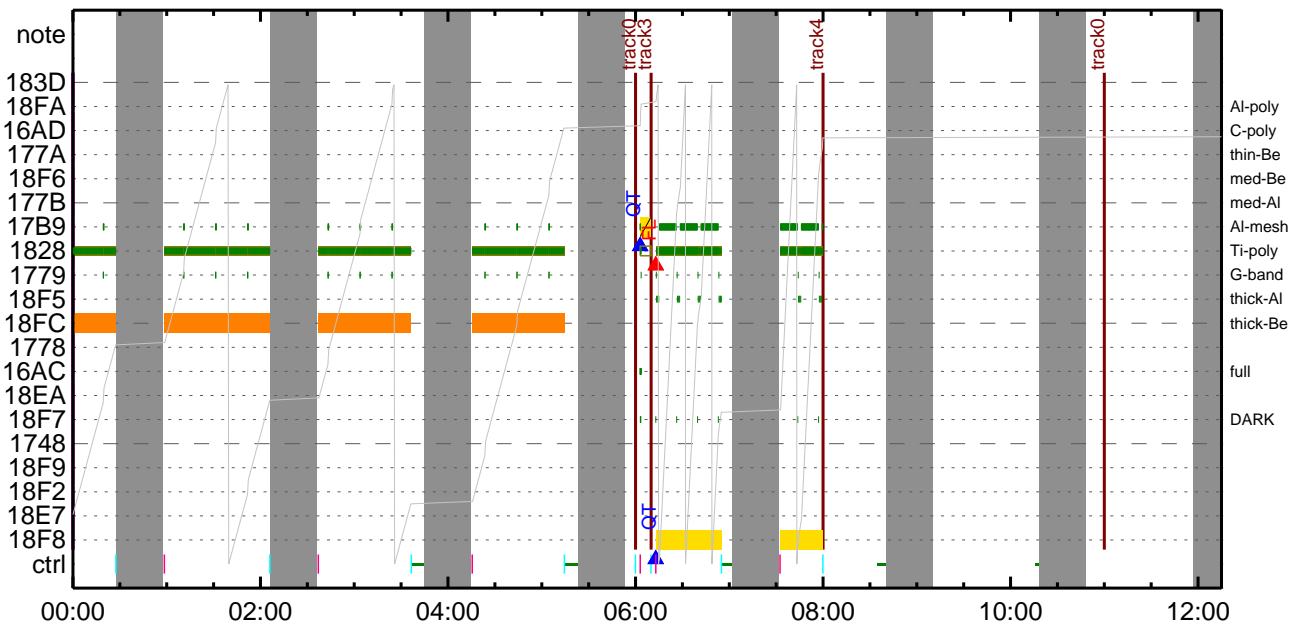
### CMDI #0740 2012/06/30



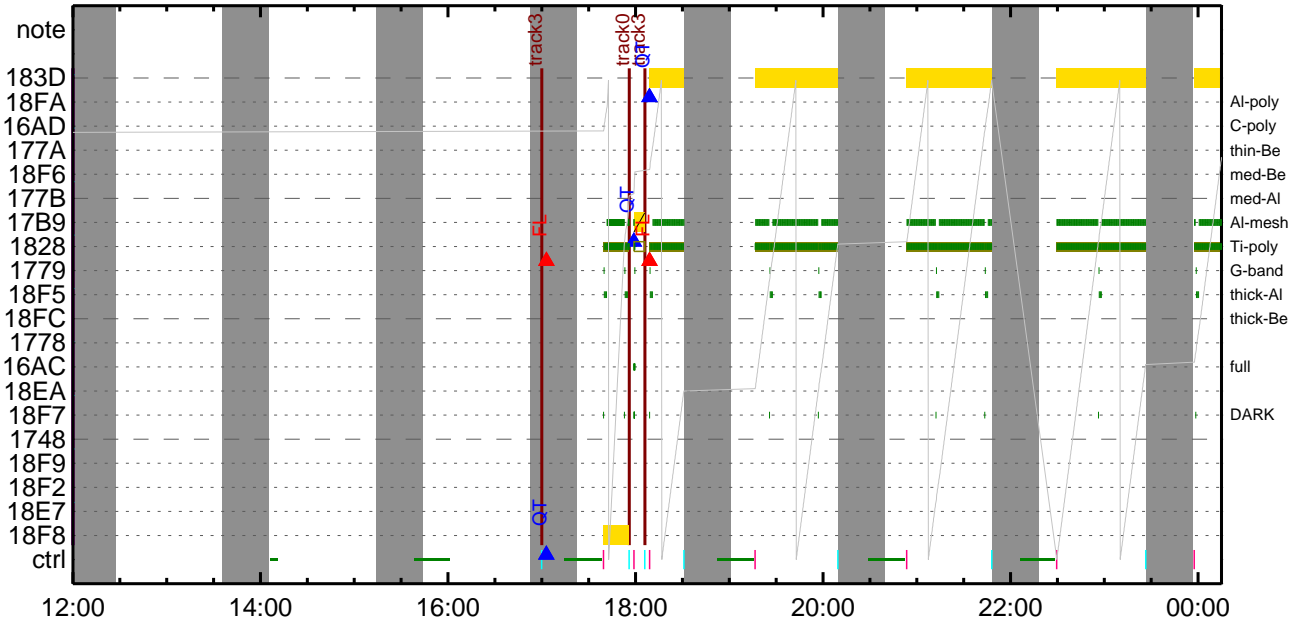
### CMDI #0740 2012/06/30



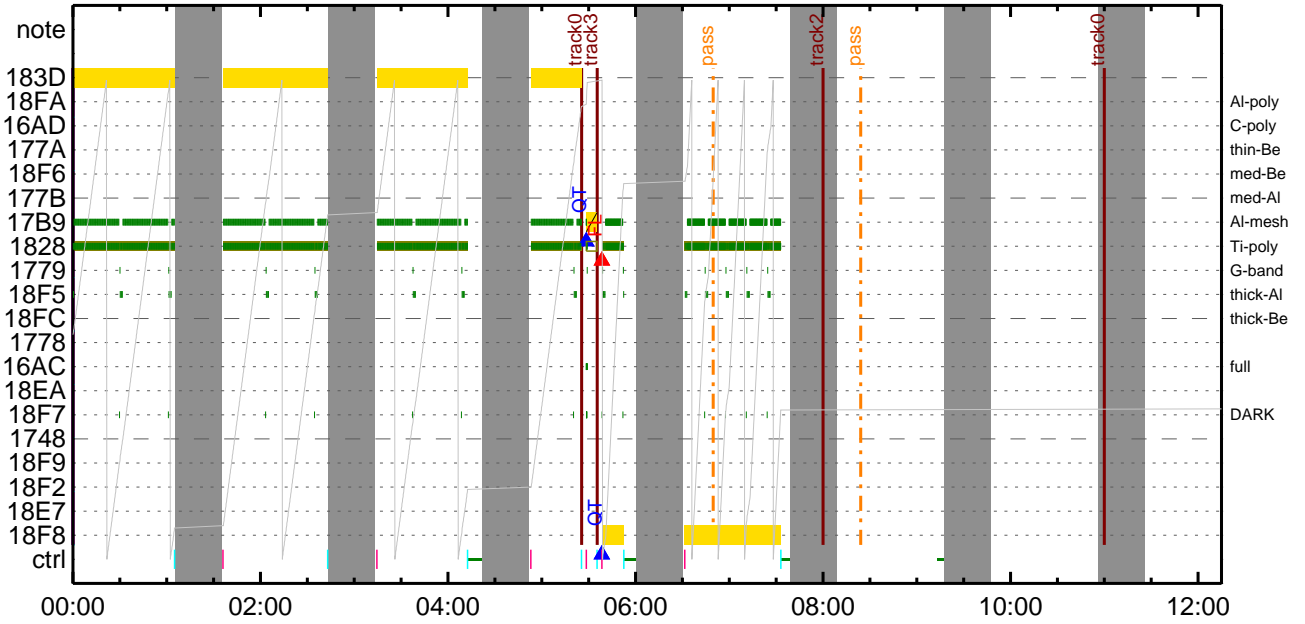
### CMDI #0740 2012/07/01



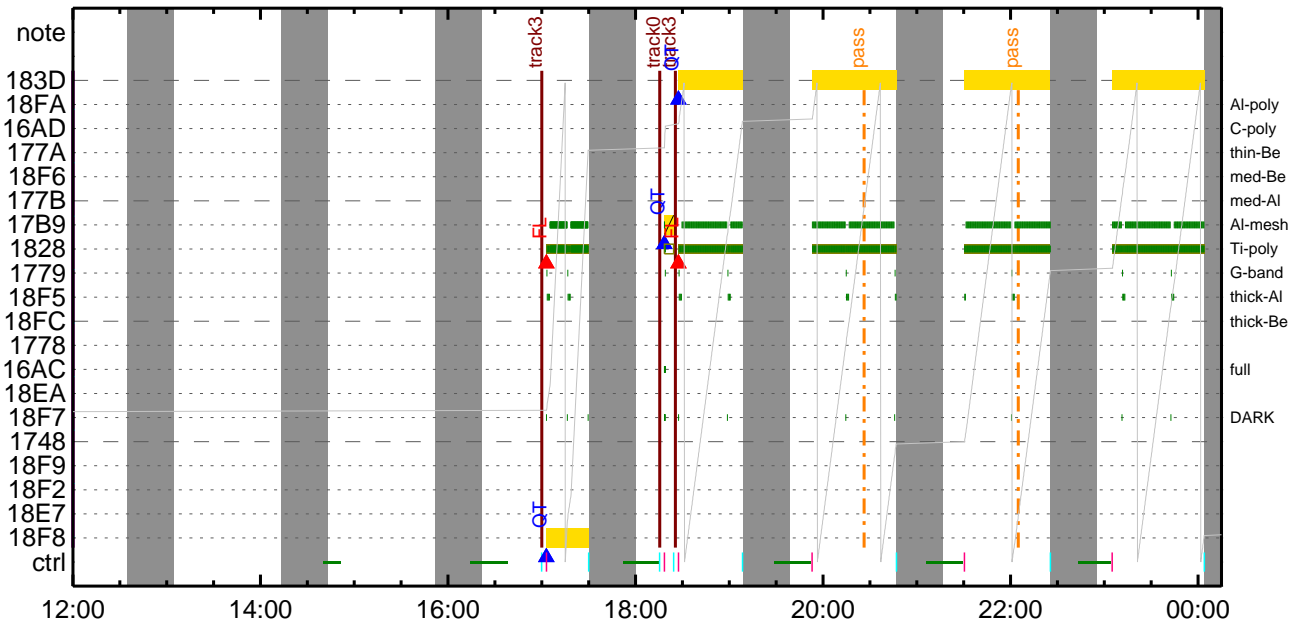
### CMDI #0740 2012/07/01



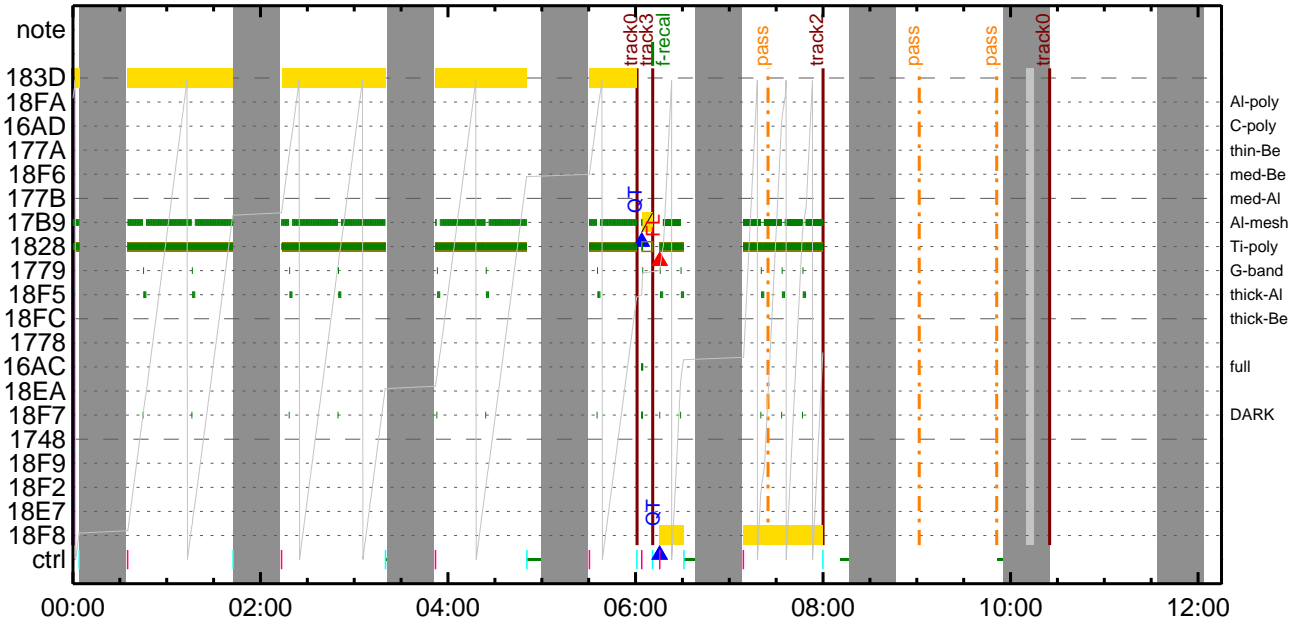
### CMDI #0740 2012/07/02



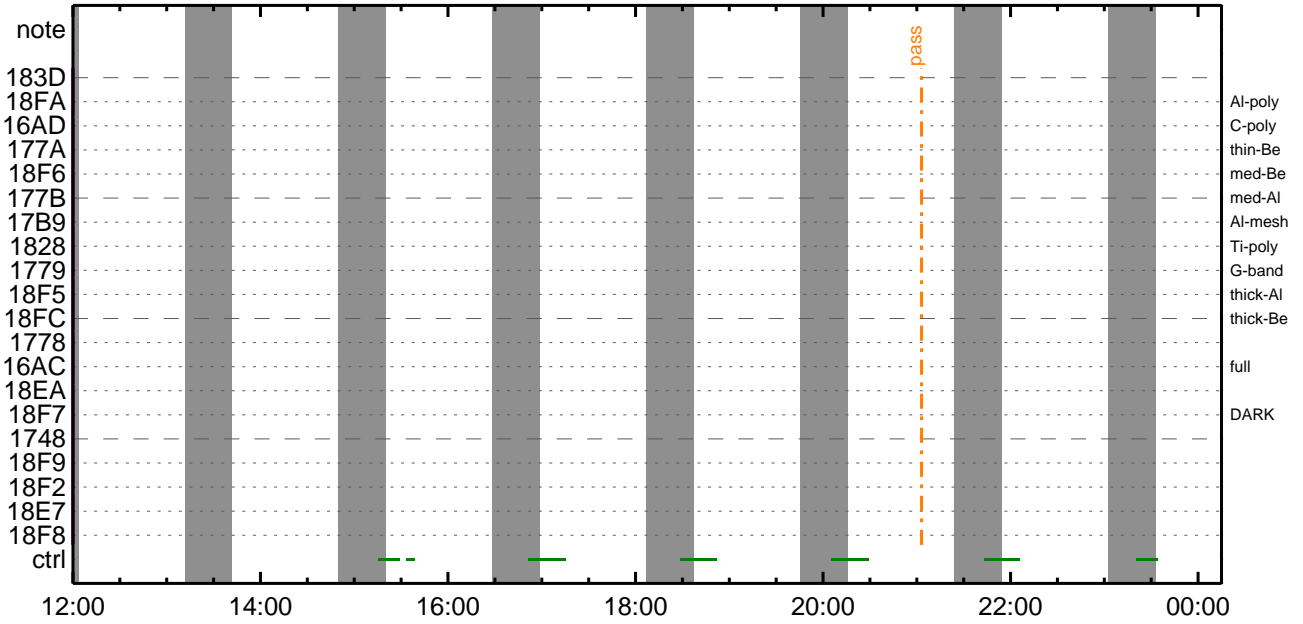
### CMDI #0740 2012/07/02



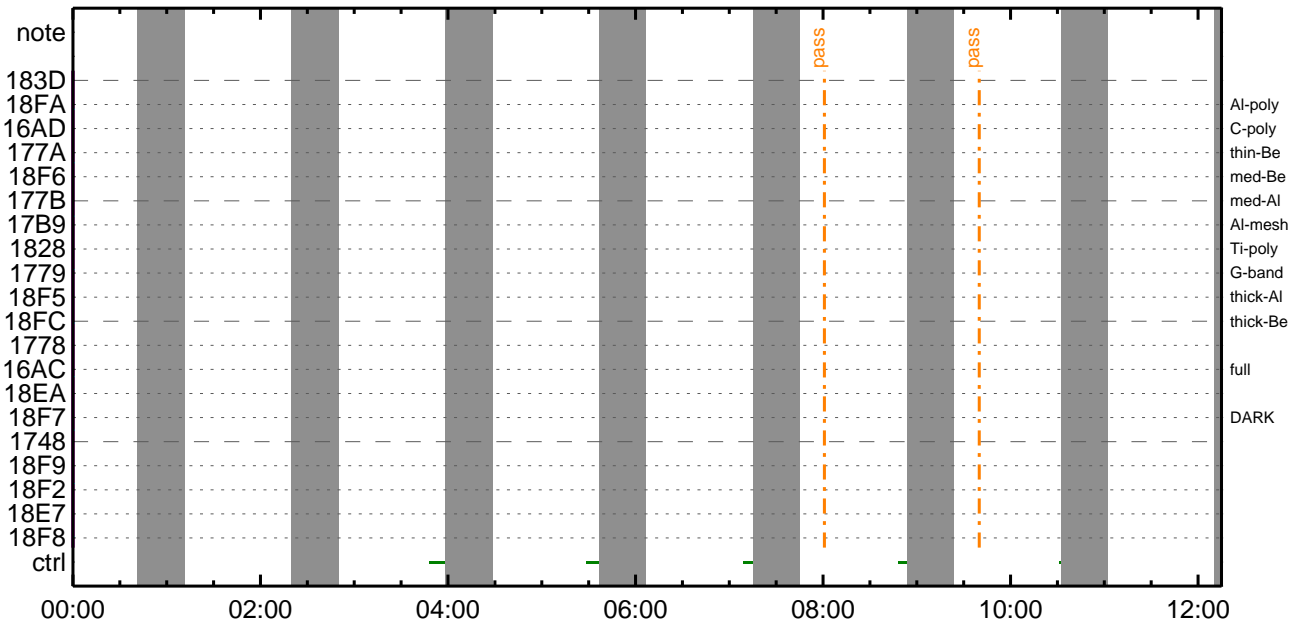
CMDI #0740 2012/07/03



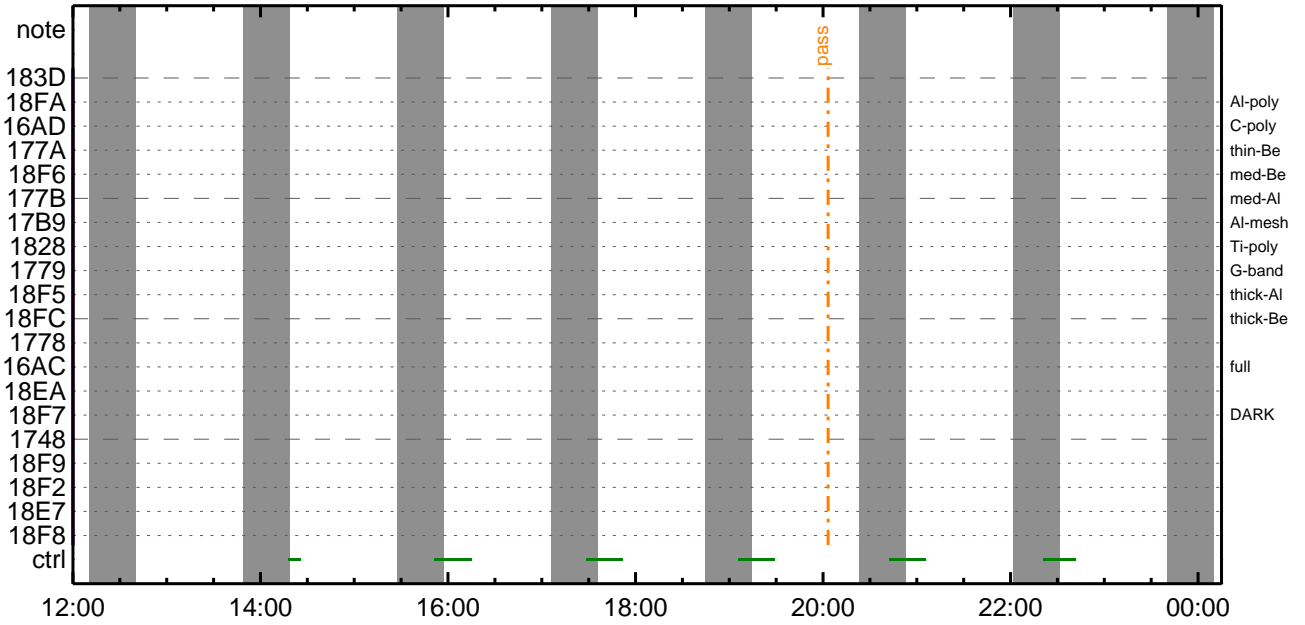
CMDI #0740 2012/07/03



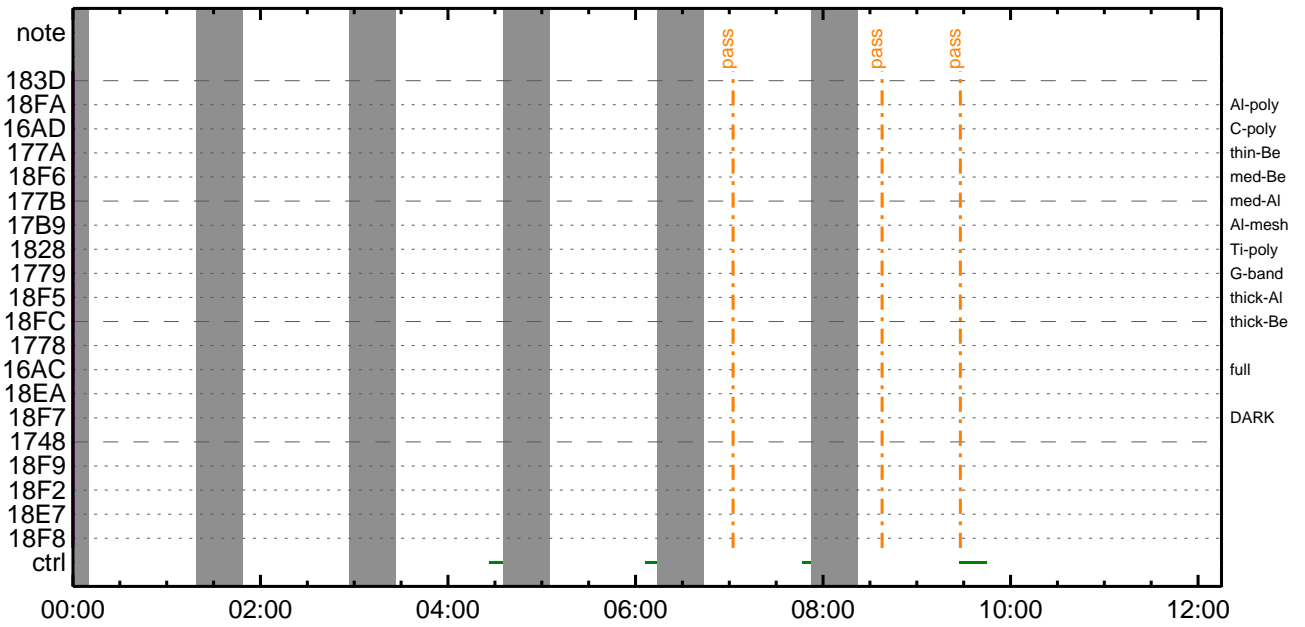
CMDI #0740 2012/07/04



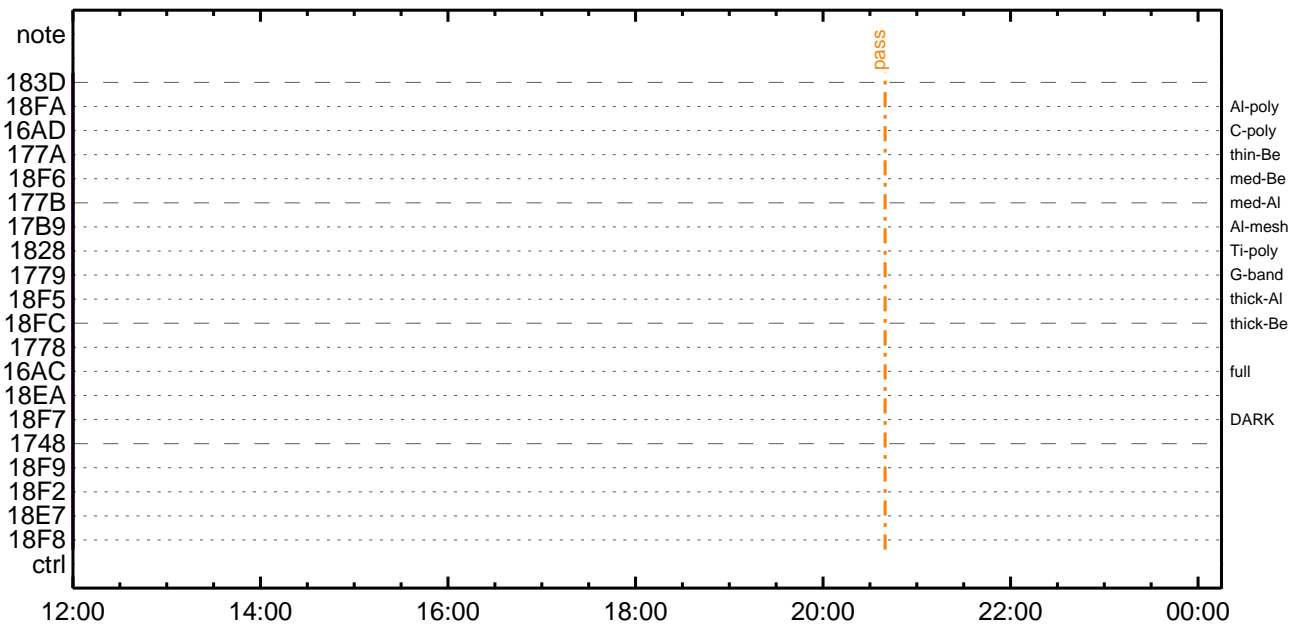
CMDI #0740 2012/07/04



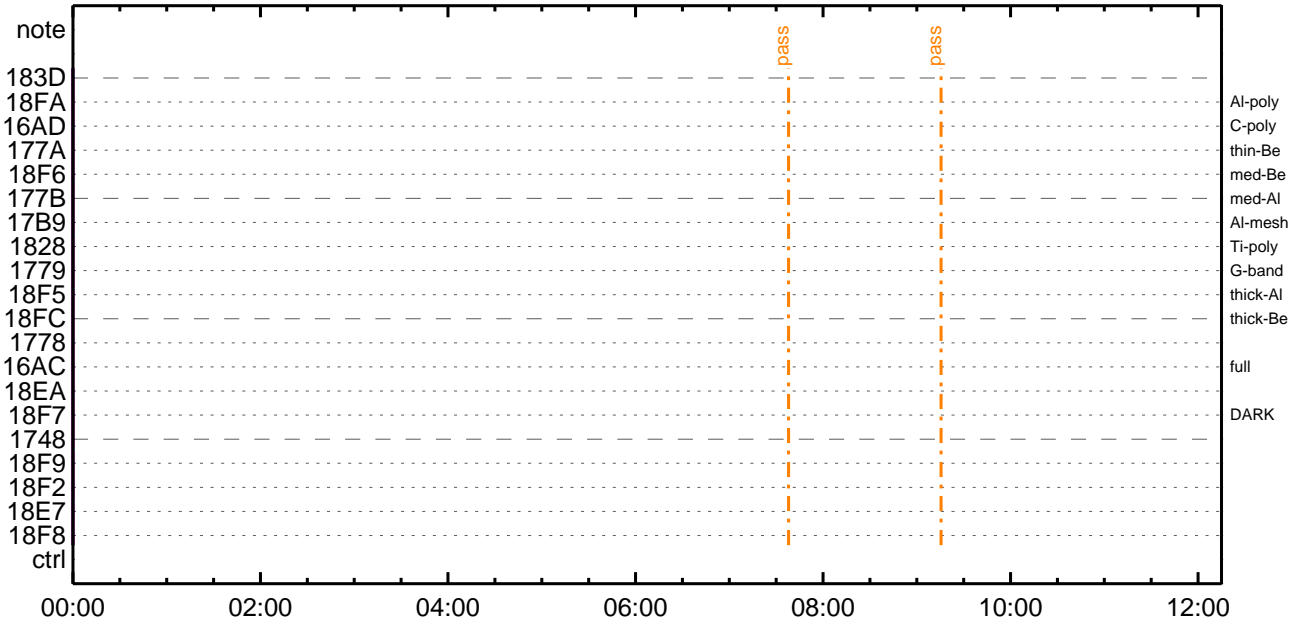
CMDI #0740 2012/07/05



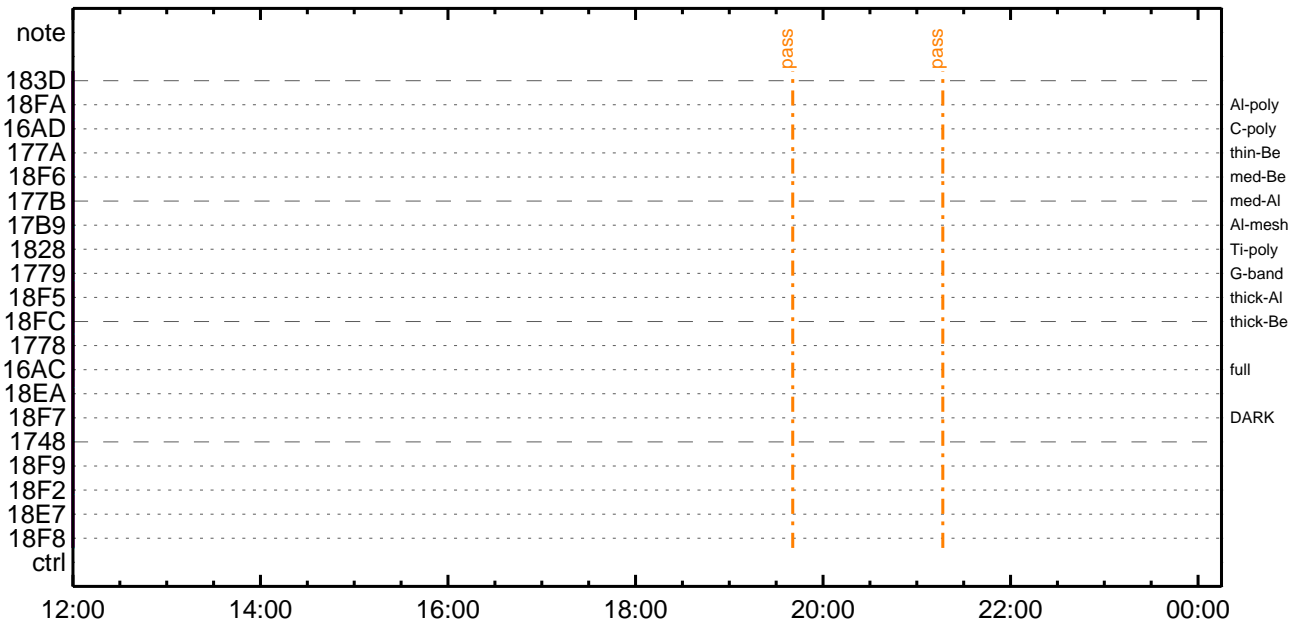
CMDI #0740 2012/07/05



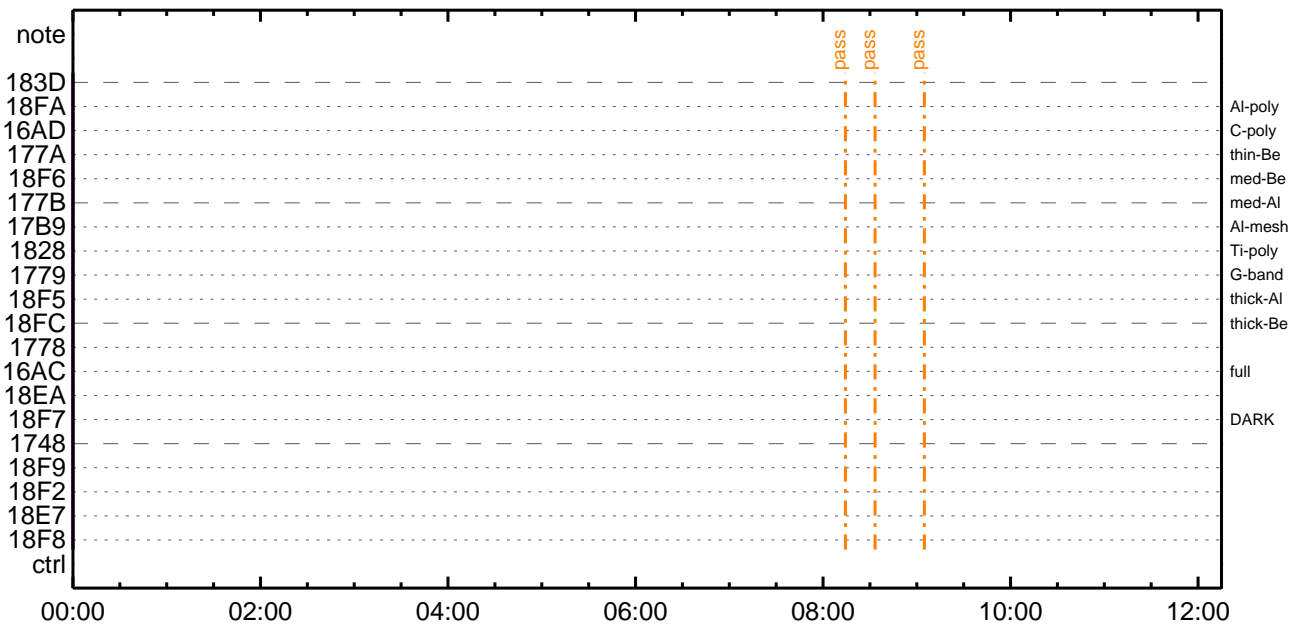
CMDI #0740 2012/07/06



CMDI #0740 2012/07/06

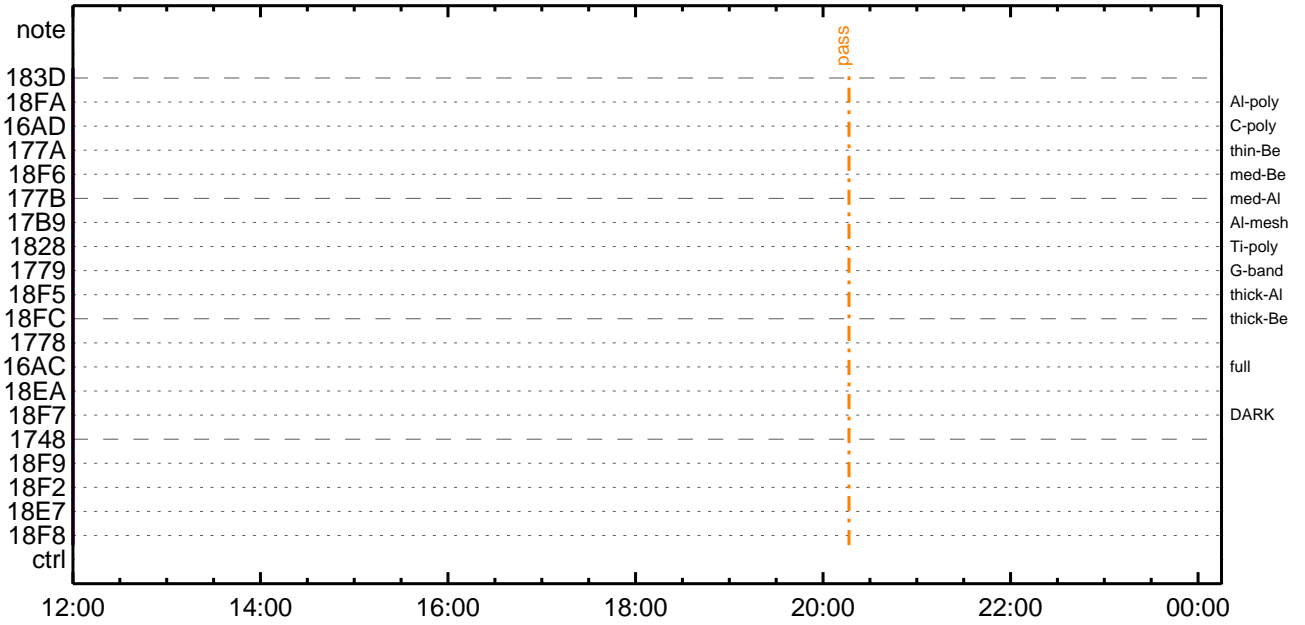


CMDI #0740 2012/07/07

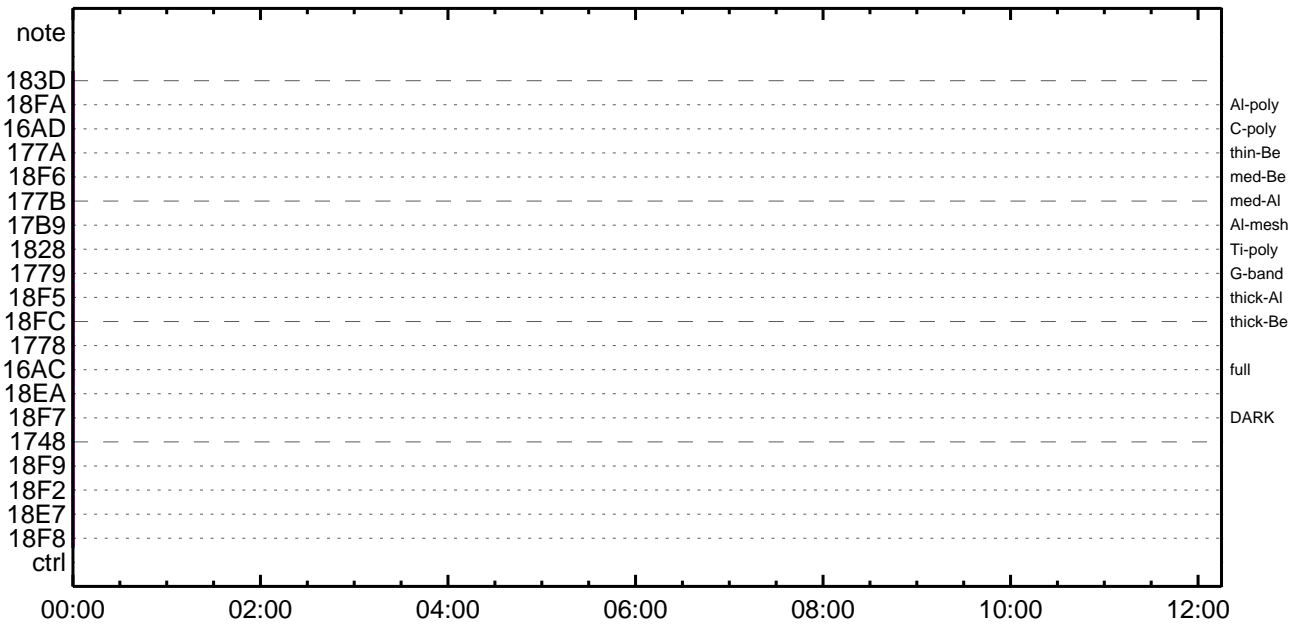




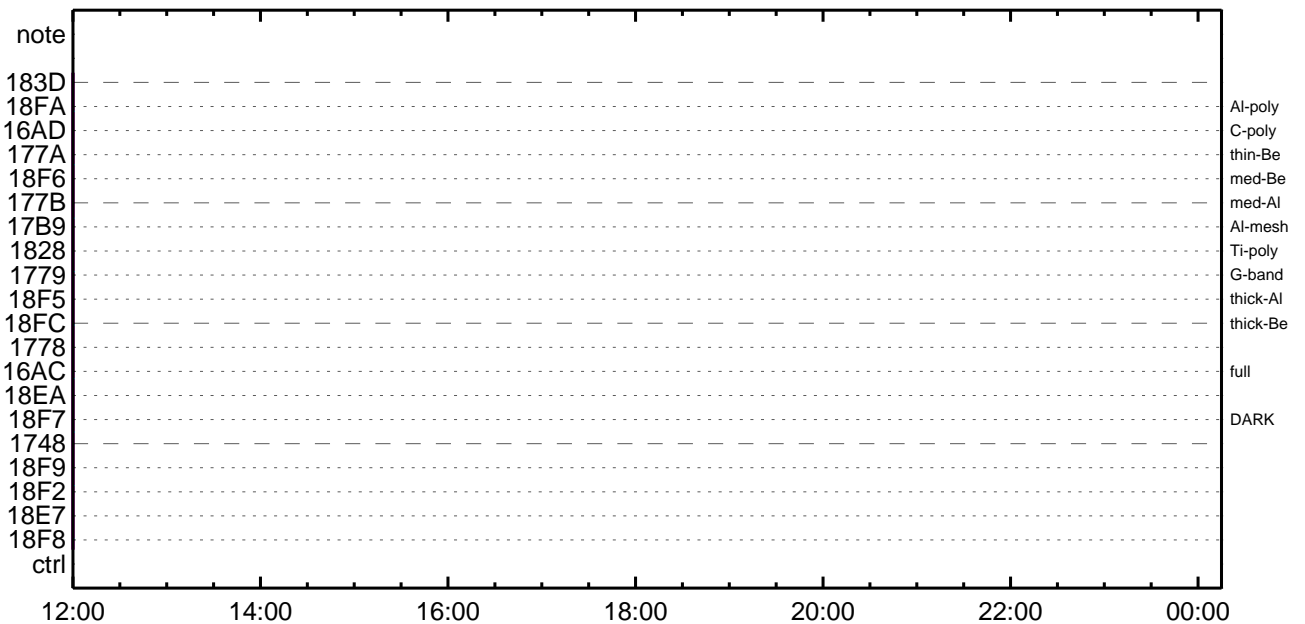
CMDI #0740 2012/07/07



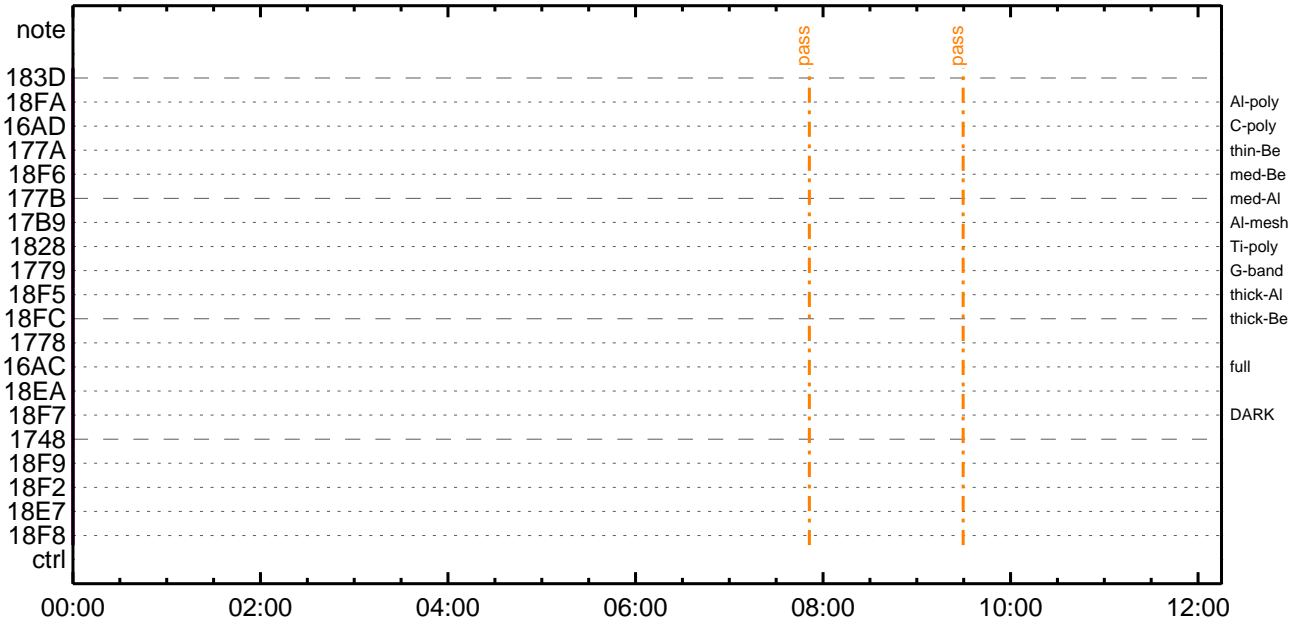
CMDI #0740 2012/07/08



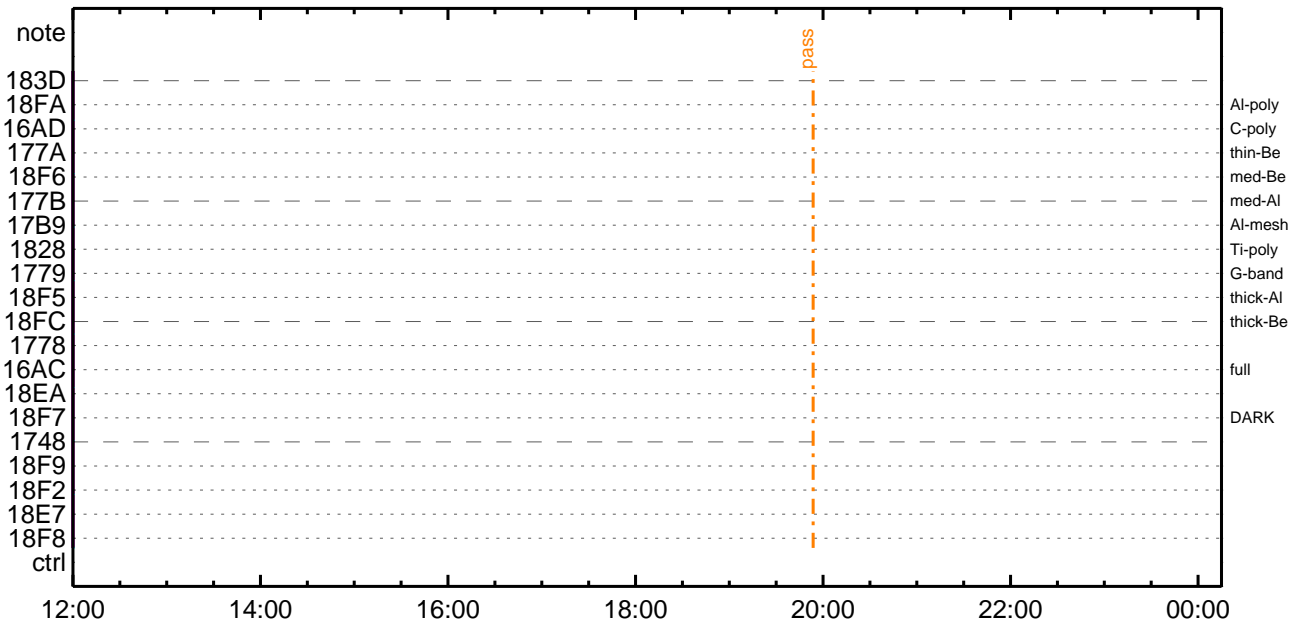
CMDI #0740 2012/07/08



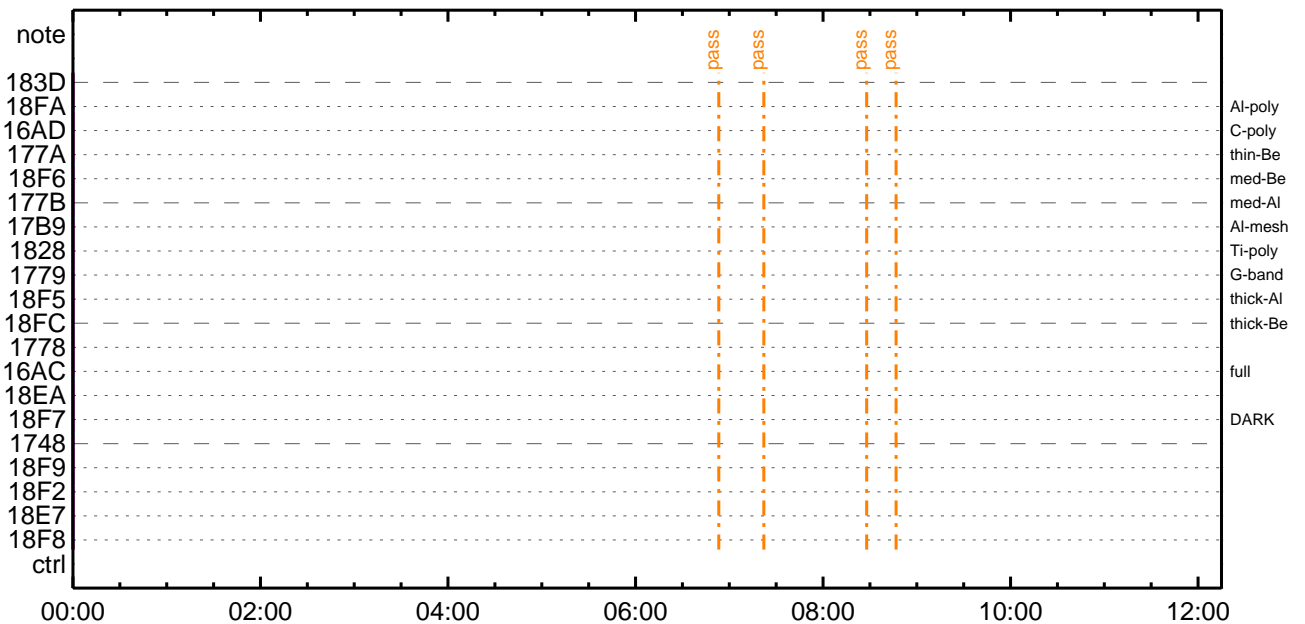
CMDI #0740 2012/07/09



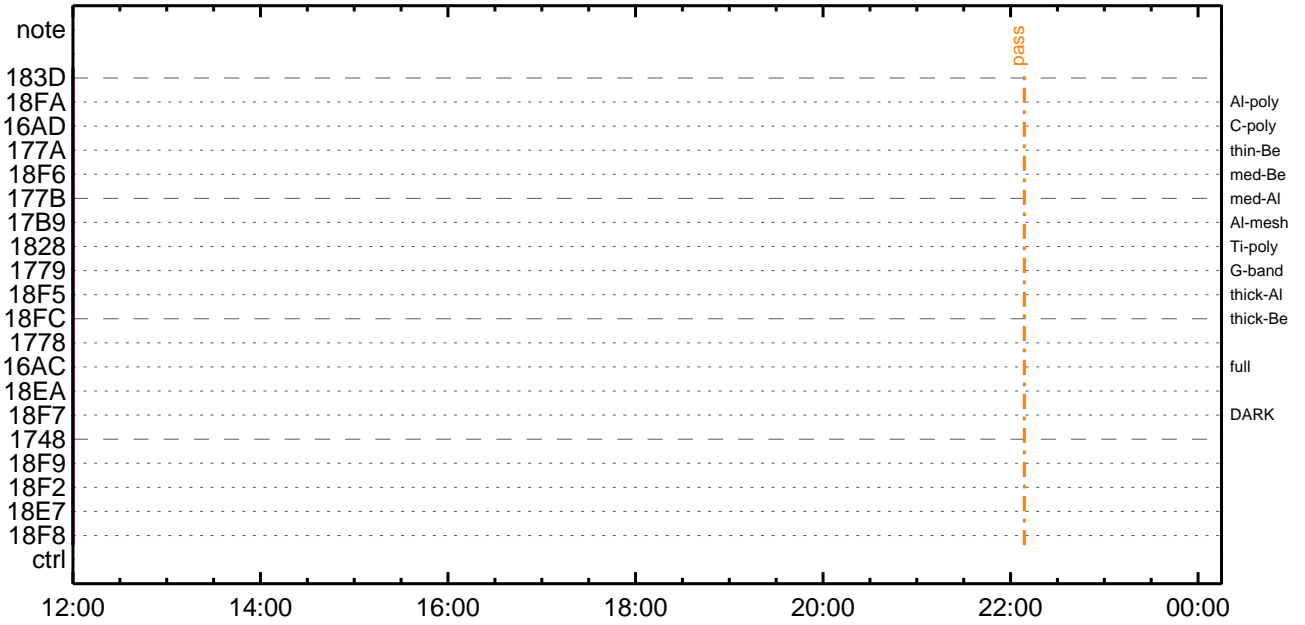
CMDI #0740 2012/07/09



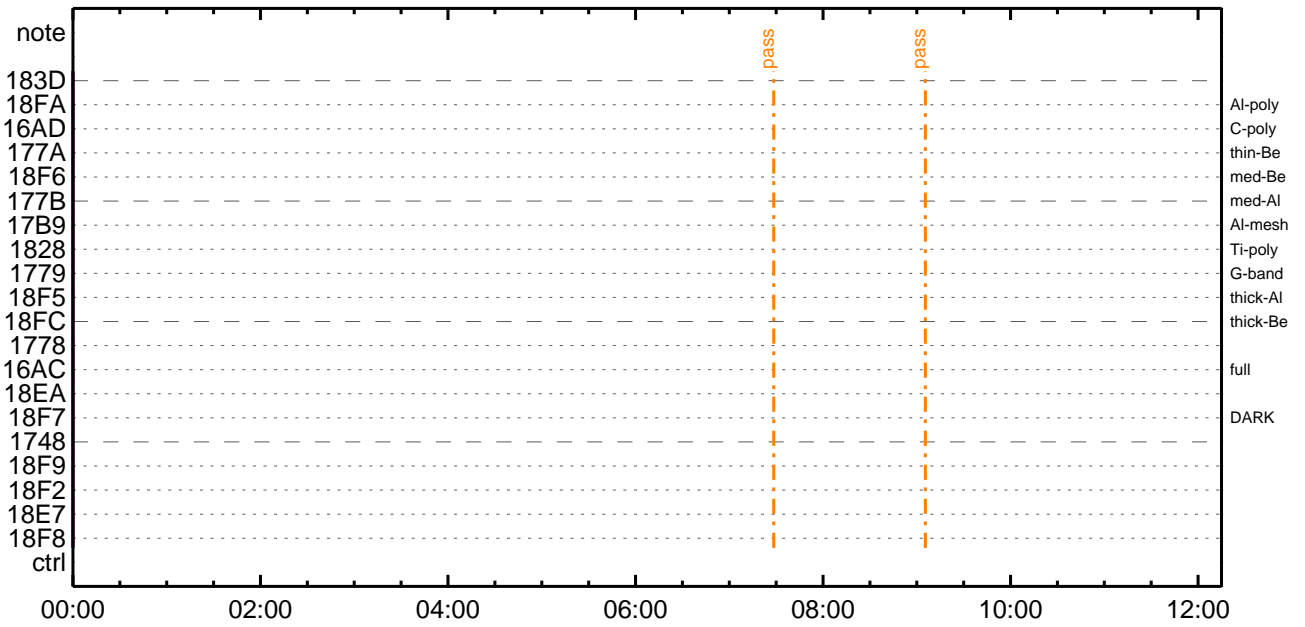
CMDI #0740 2012/07/10



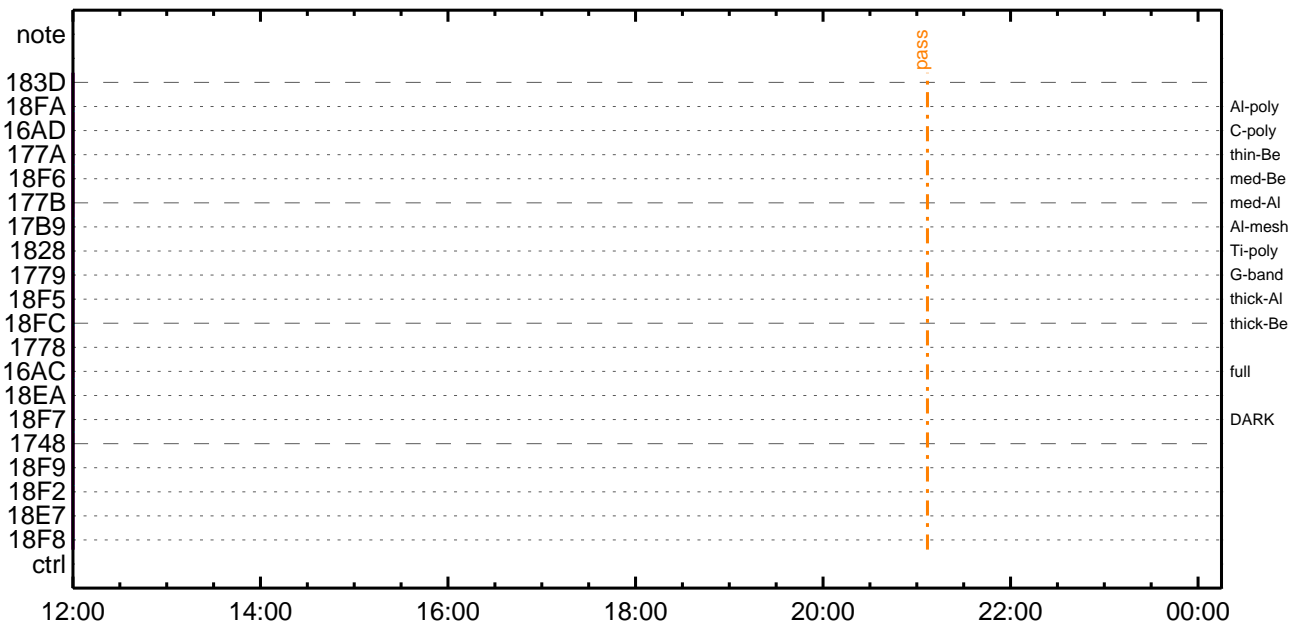
CMDI #0740 2012/07/10



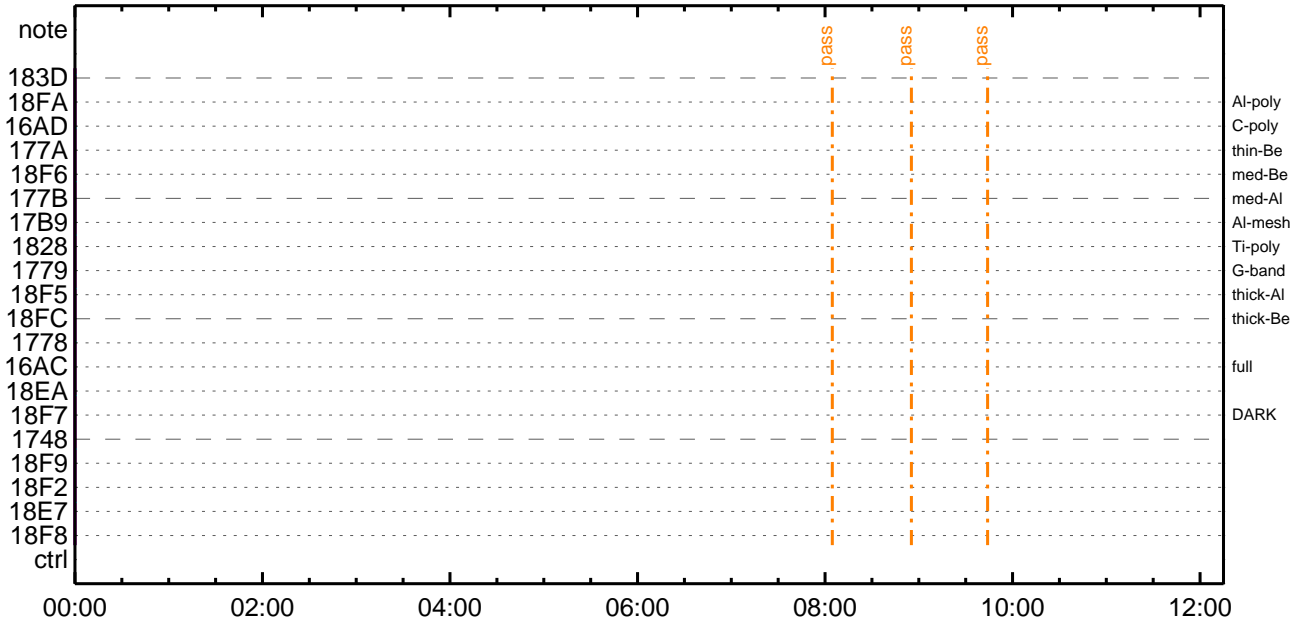
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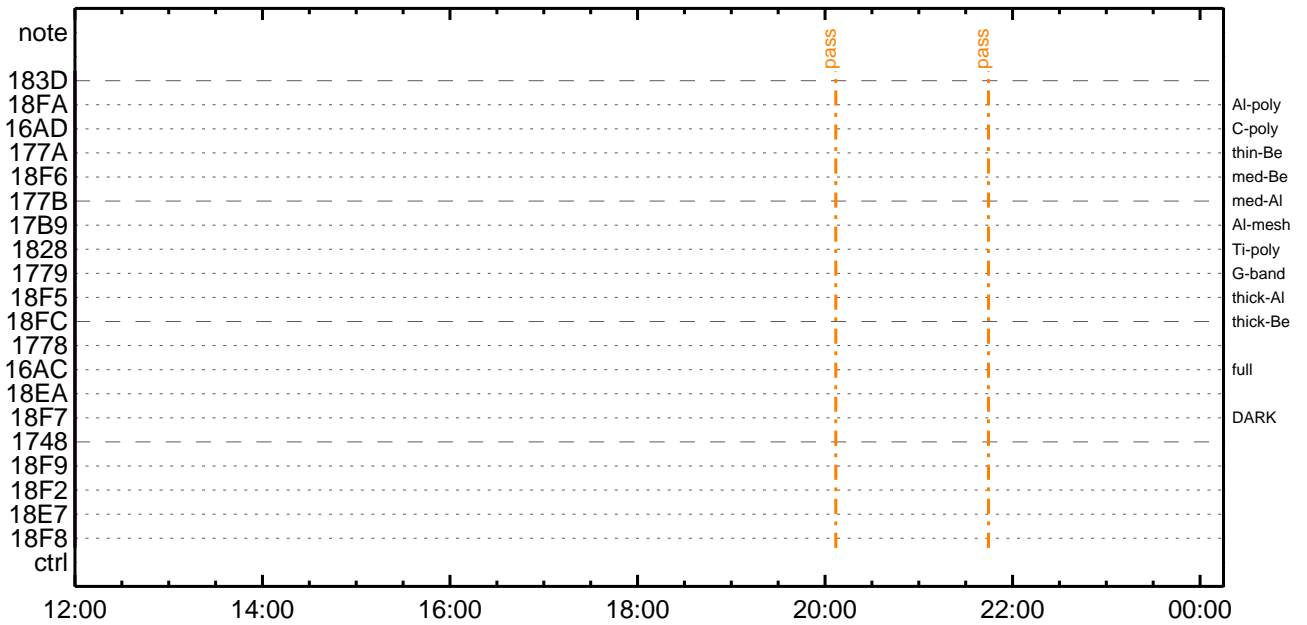
CMDI #0740 2012/07/11



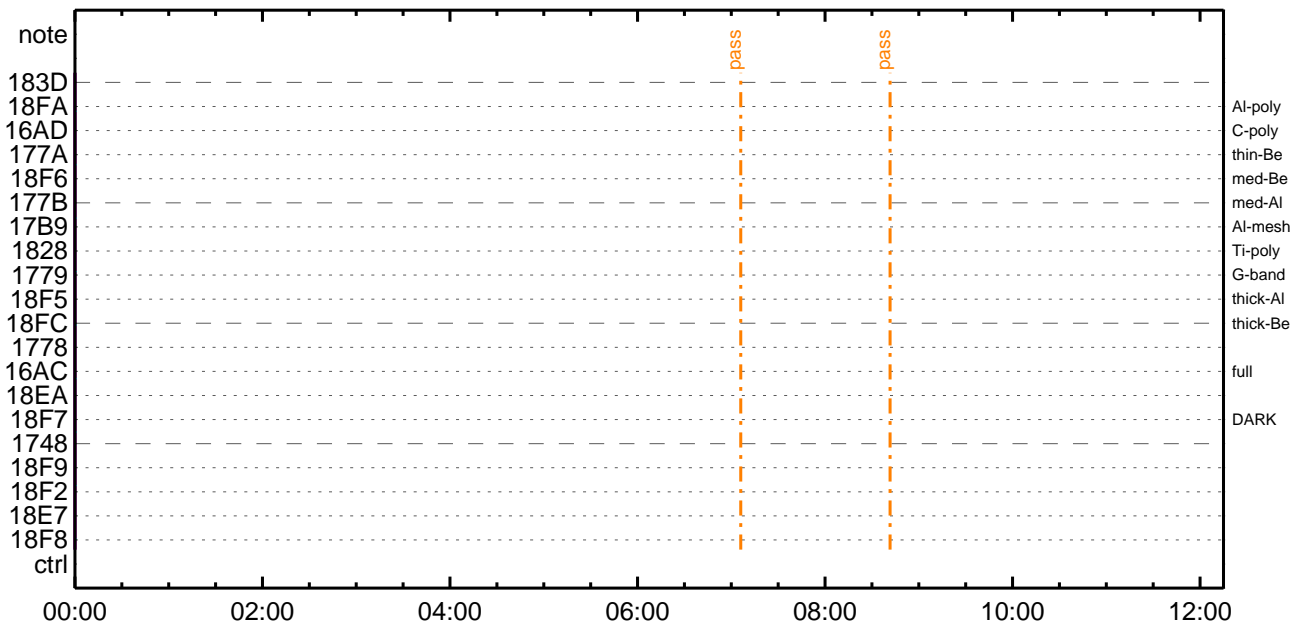
CMDI #0740 2012/07/12



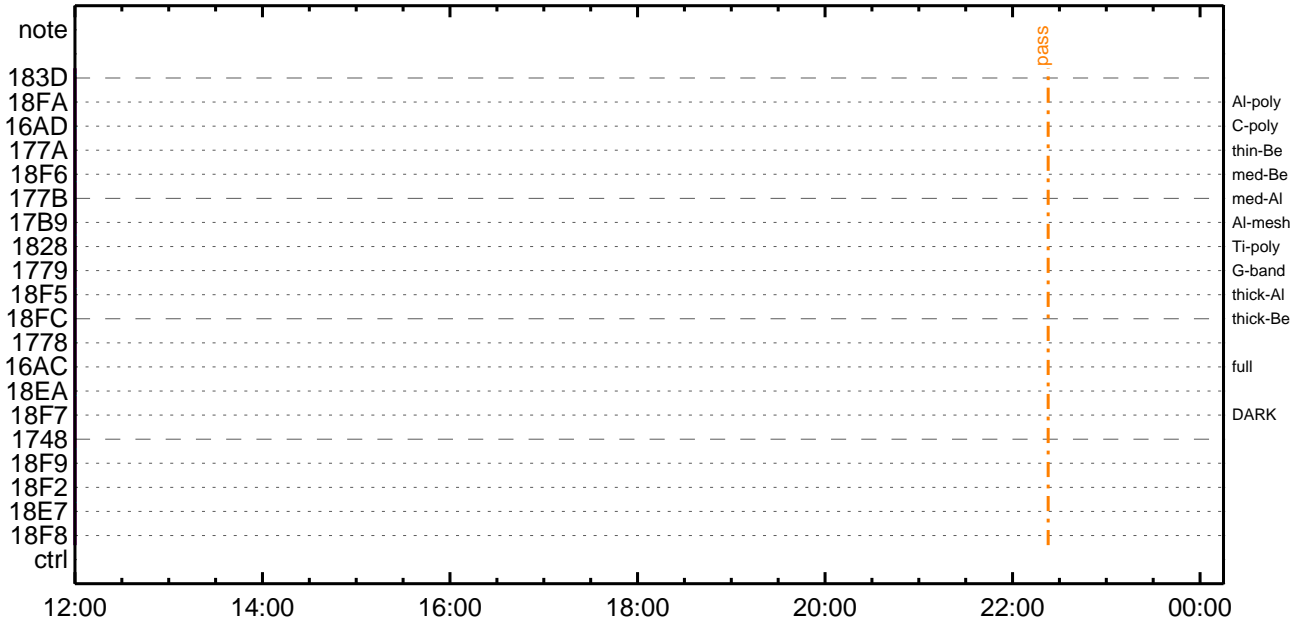
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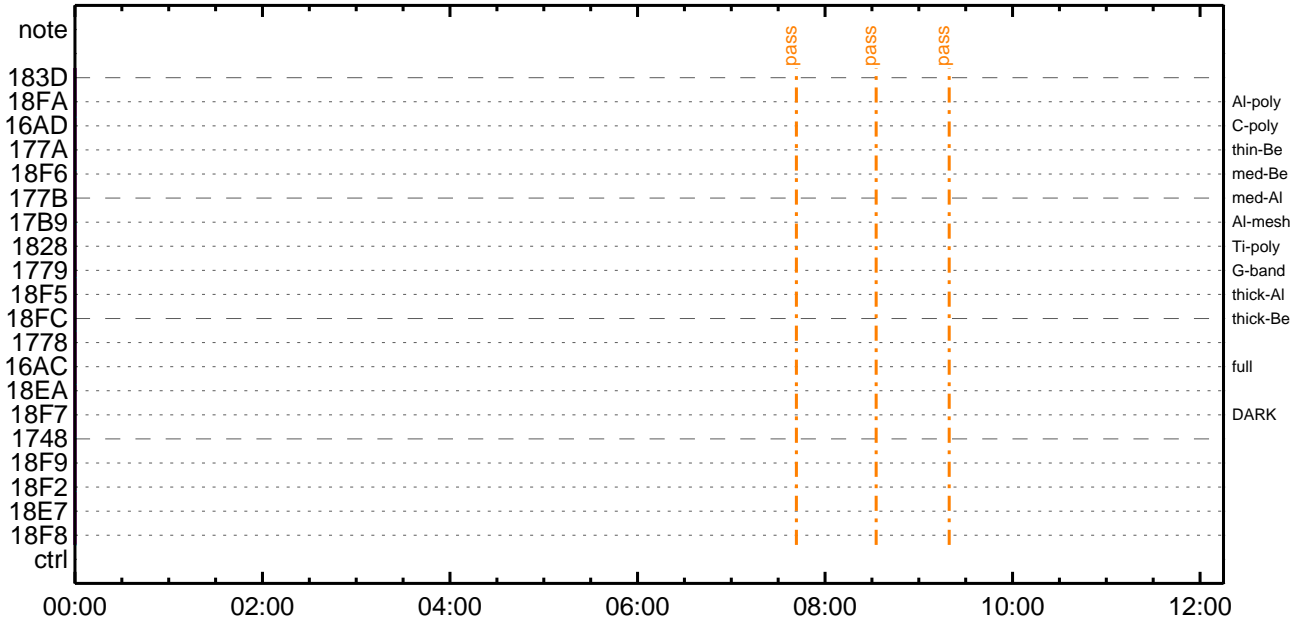
CMDI #0740 2012/07/13



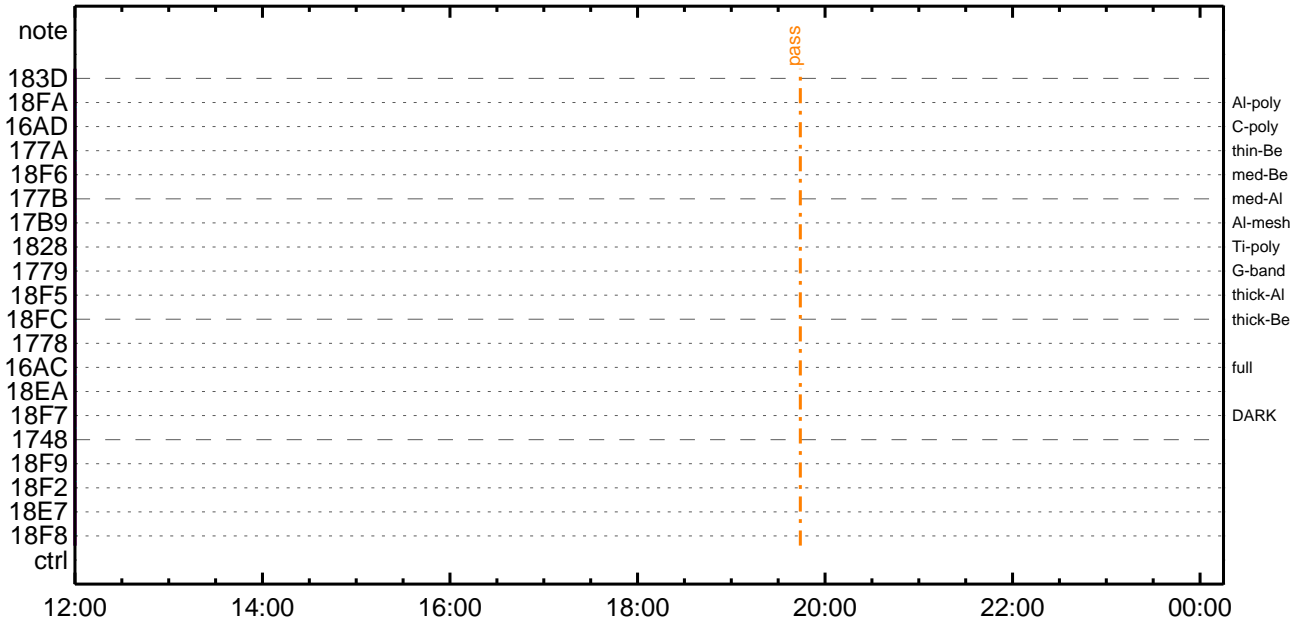
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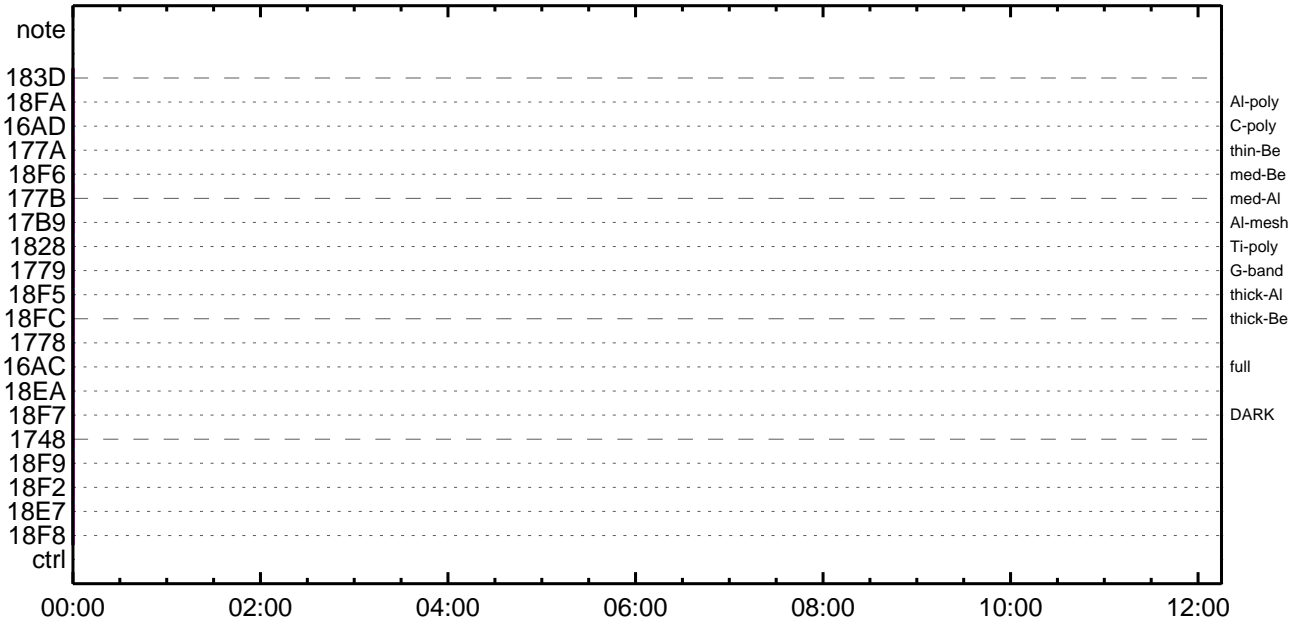
CMDI #0740 2012/07/14



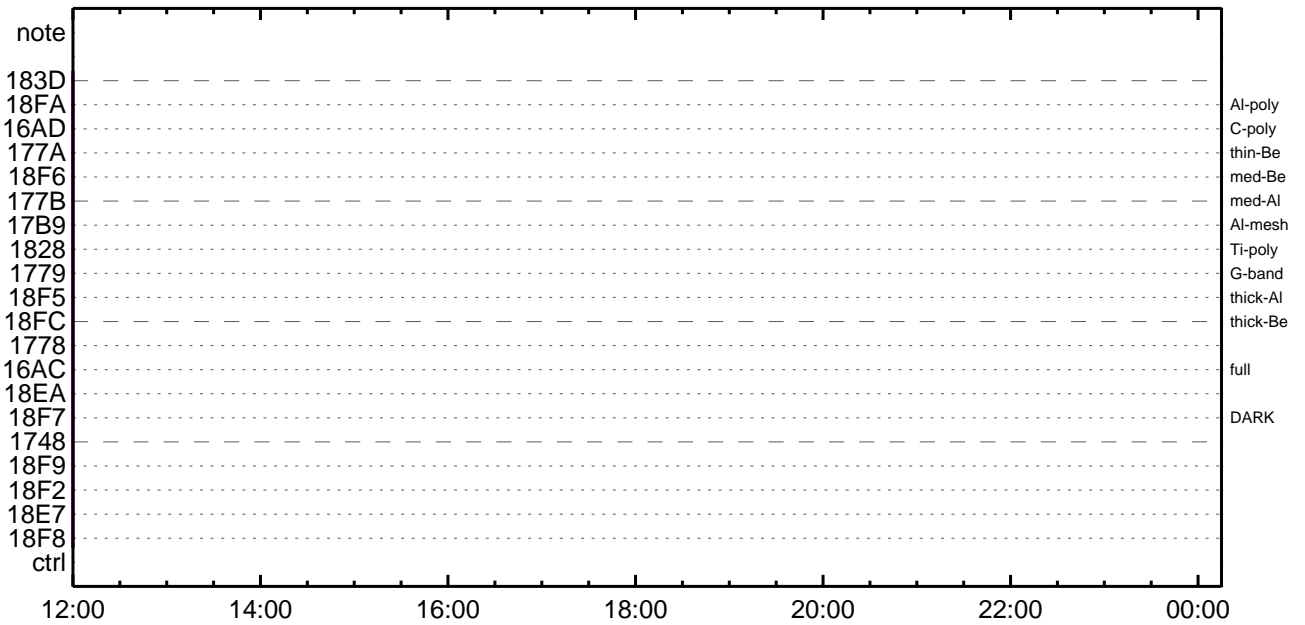
CMDI #0740 2012/07/14



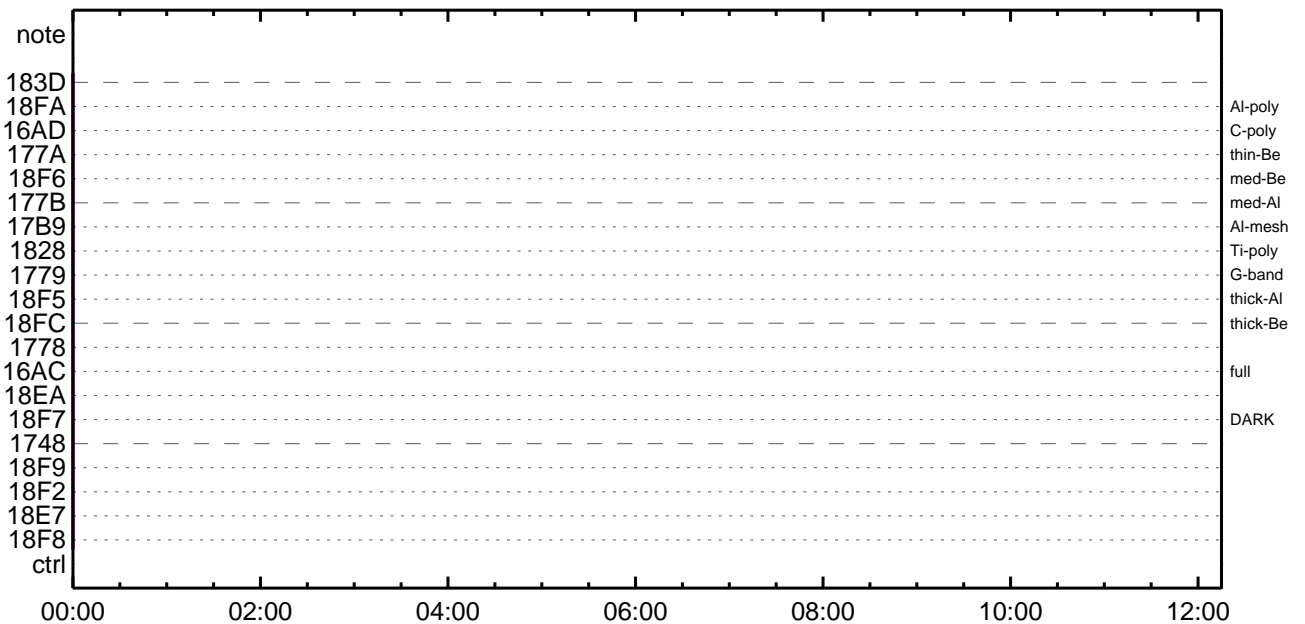
CMDI #0740 2012/07/15



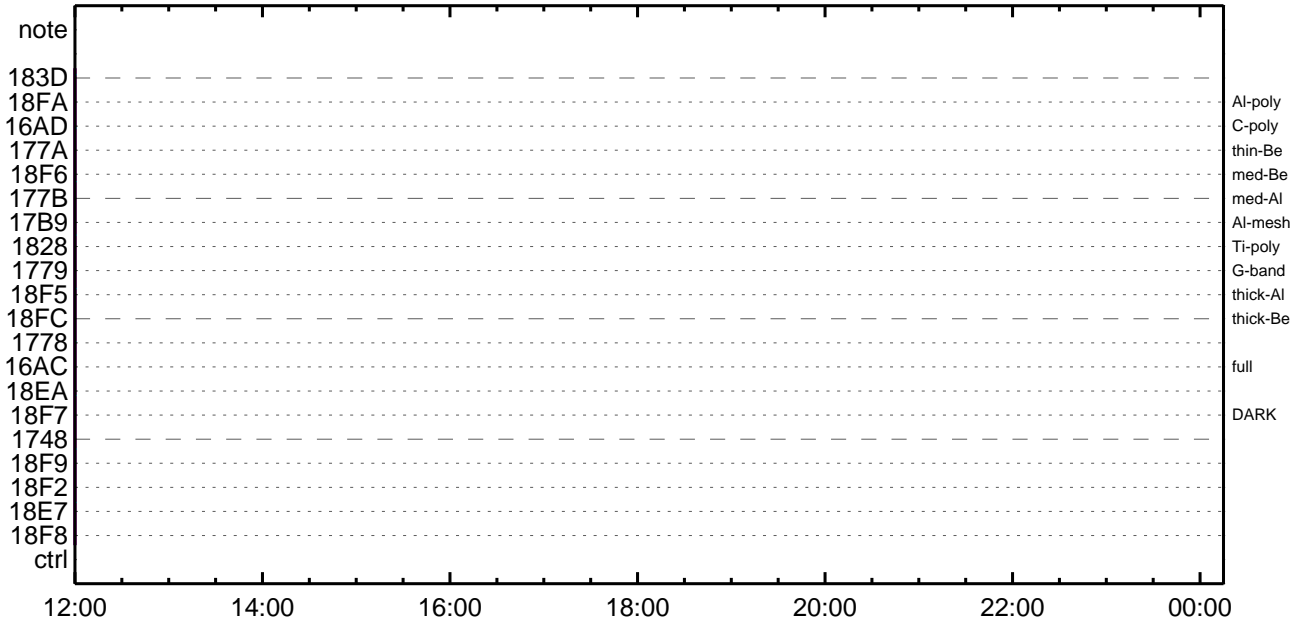
CMDI #0740 2012/07/15



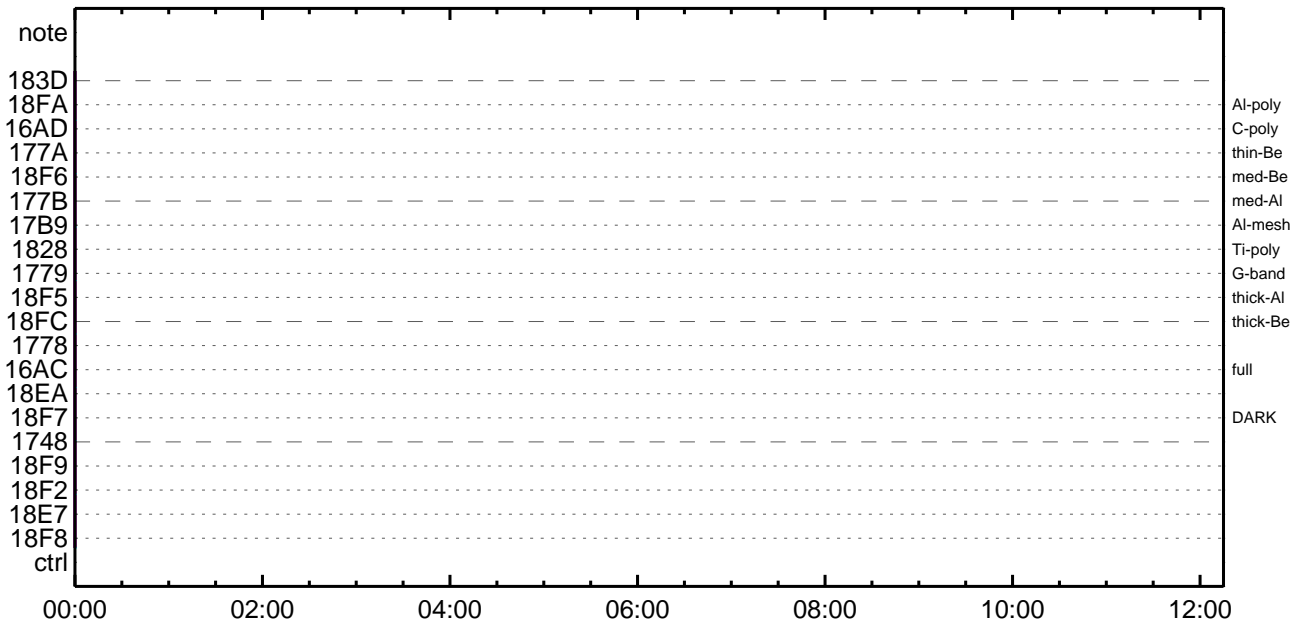
CMDI #0740 2012/07/16



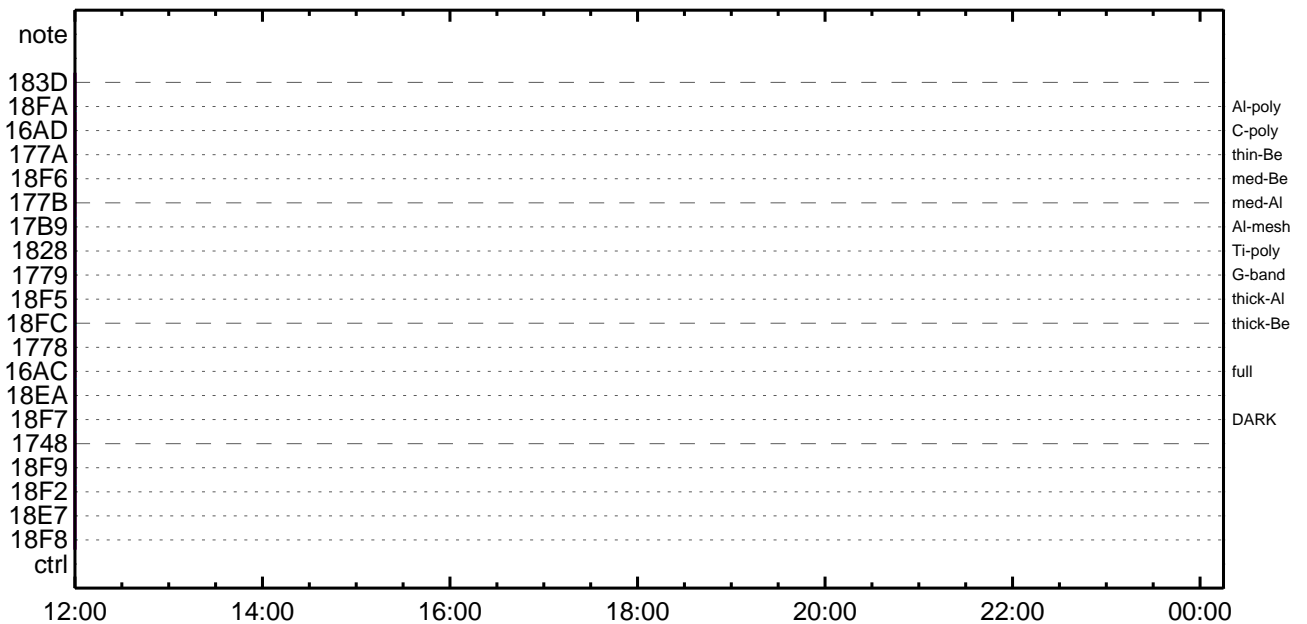
CMDI #0740 2012/07/16



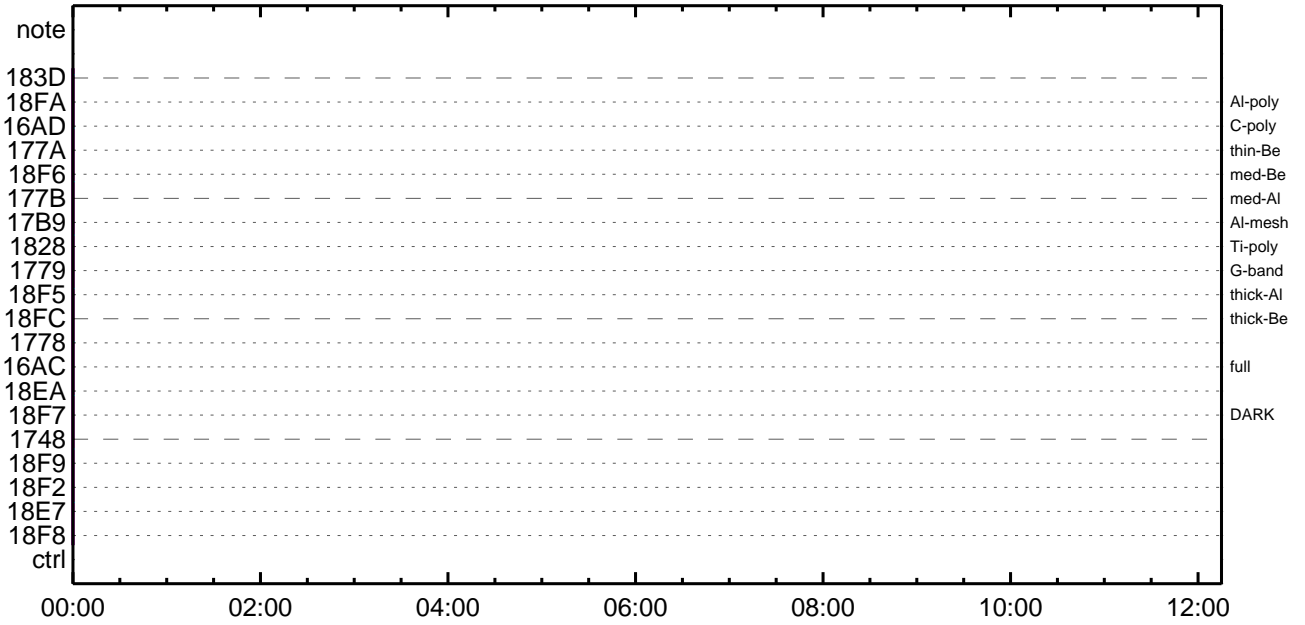
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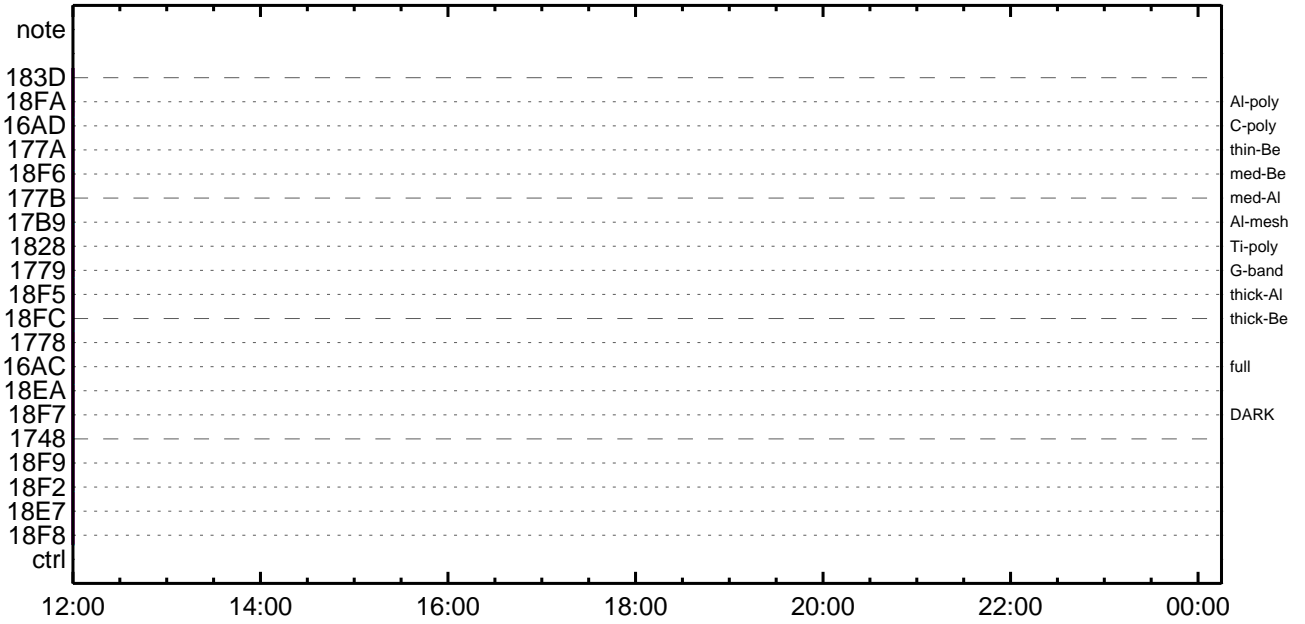
CMDI #0740 2012/07/17



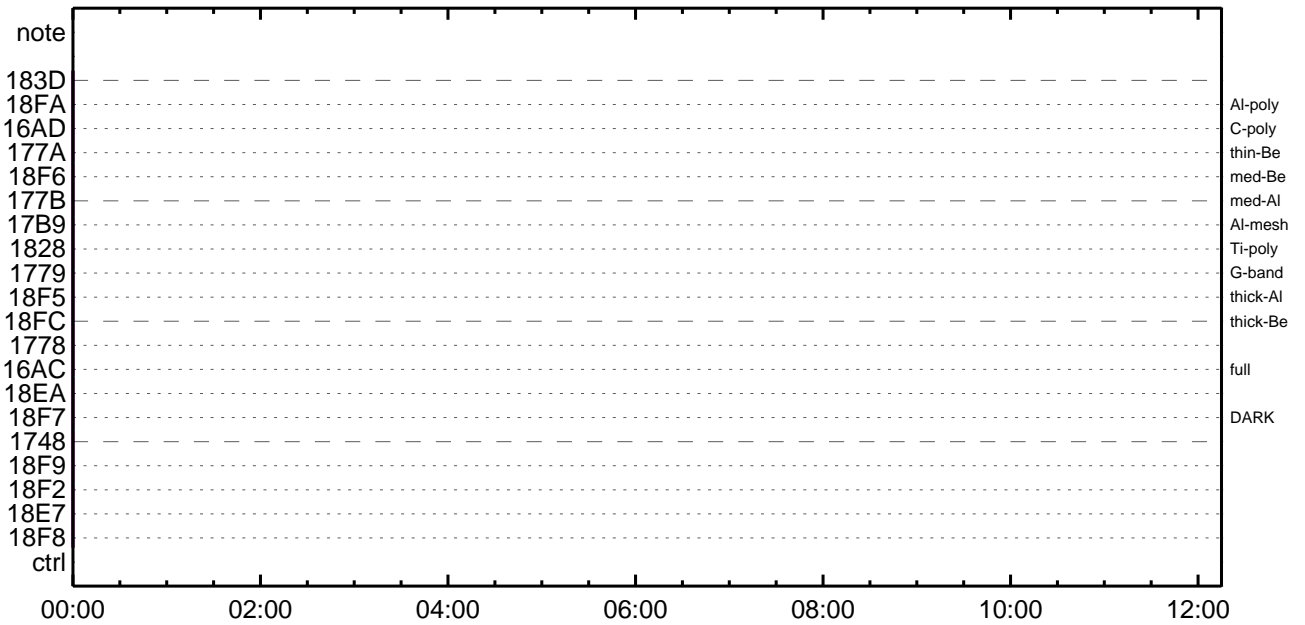
CMDI #0740 2012/07/18



CMDI #0740 2012/07/18

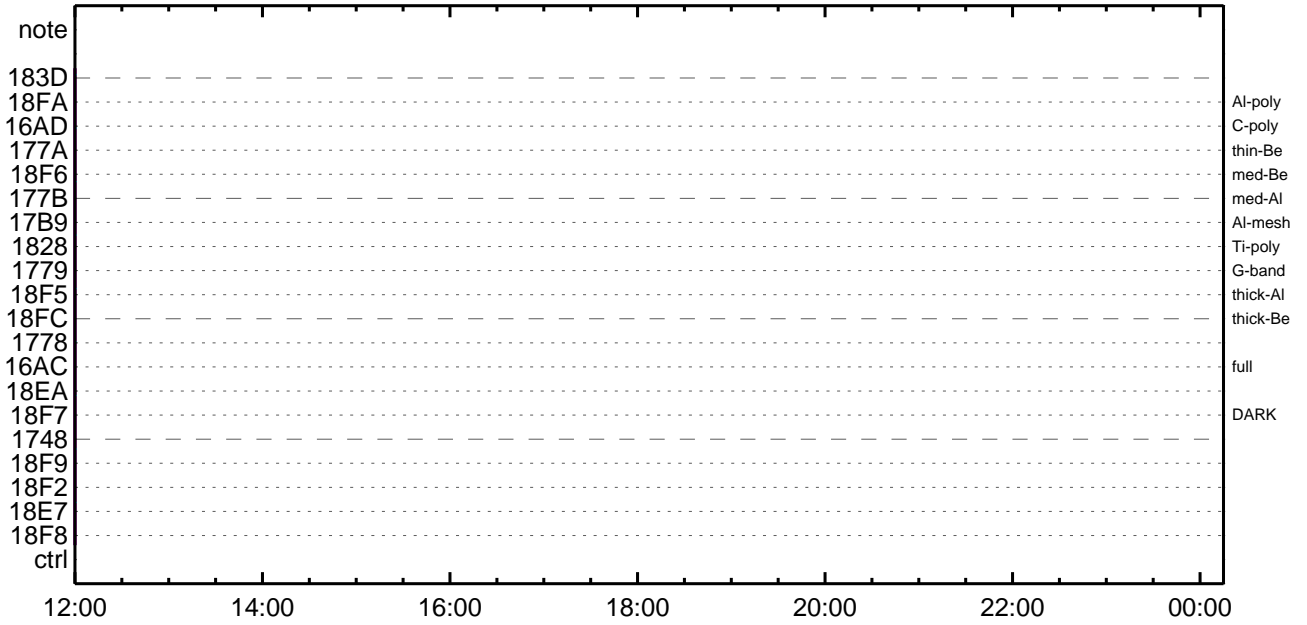


CMDI #0740 2012/07/19

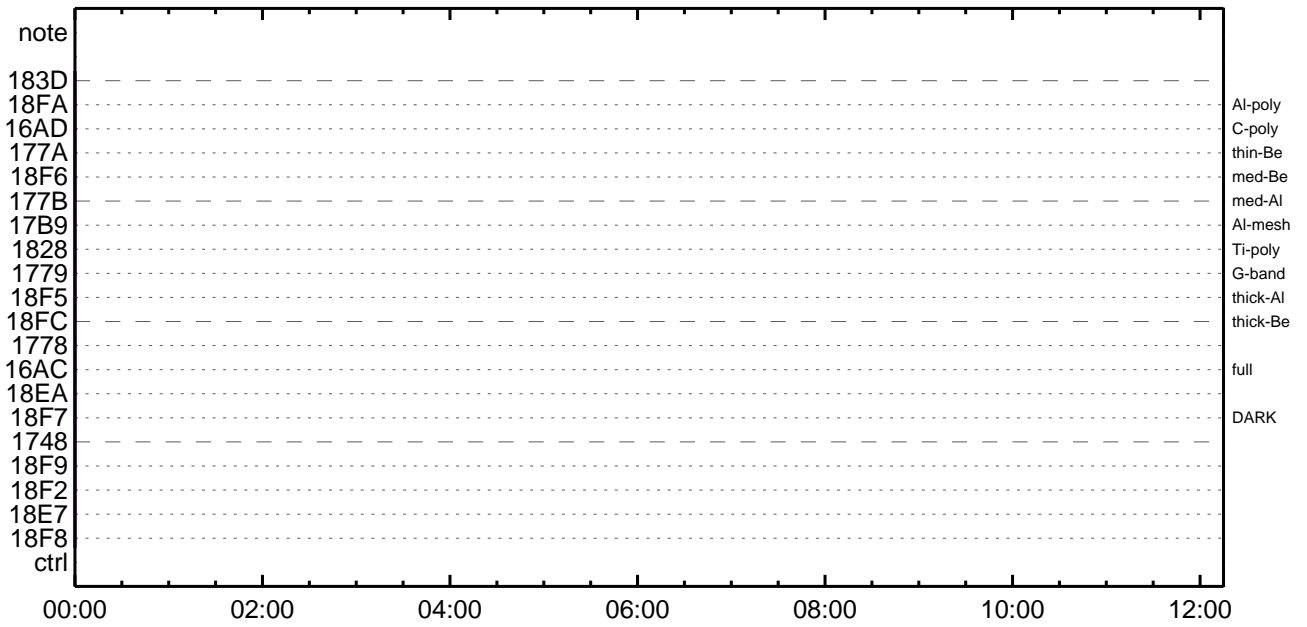




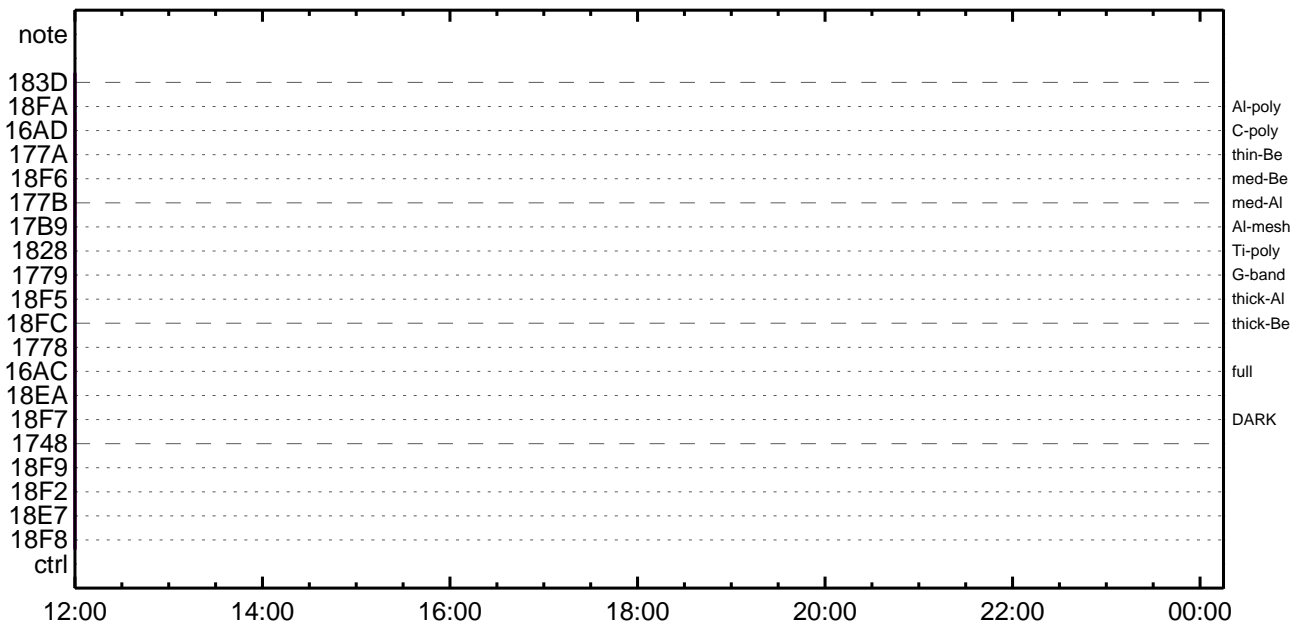
CMDI #0740 2012/07/19



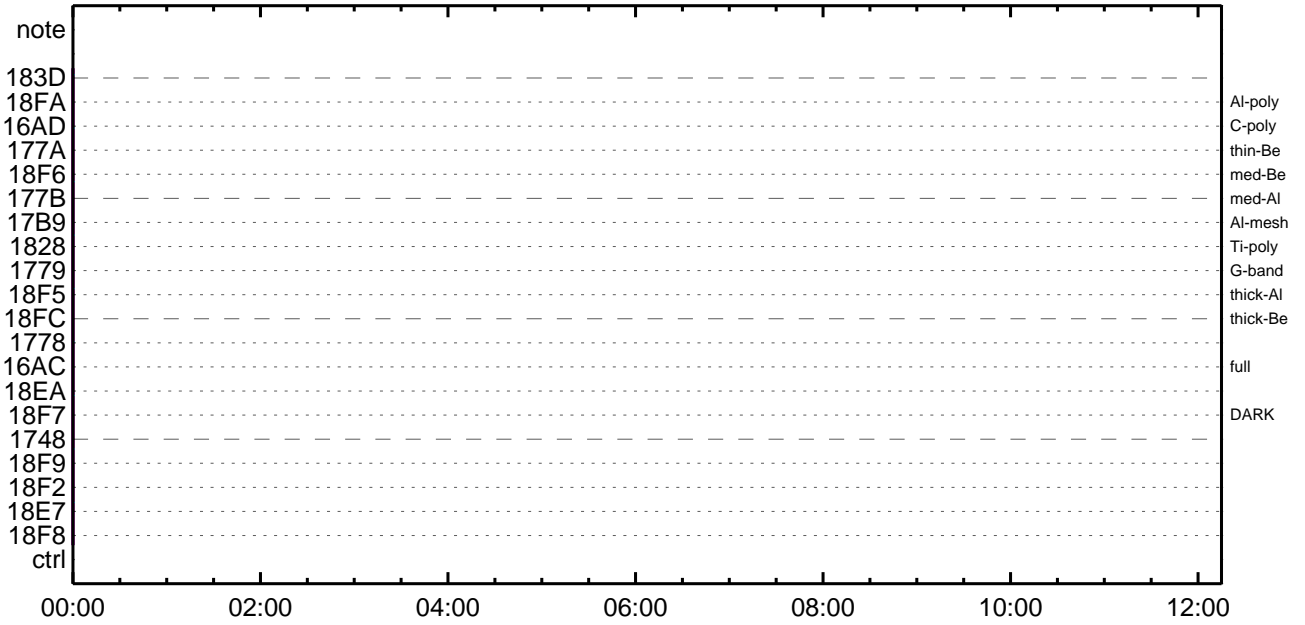
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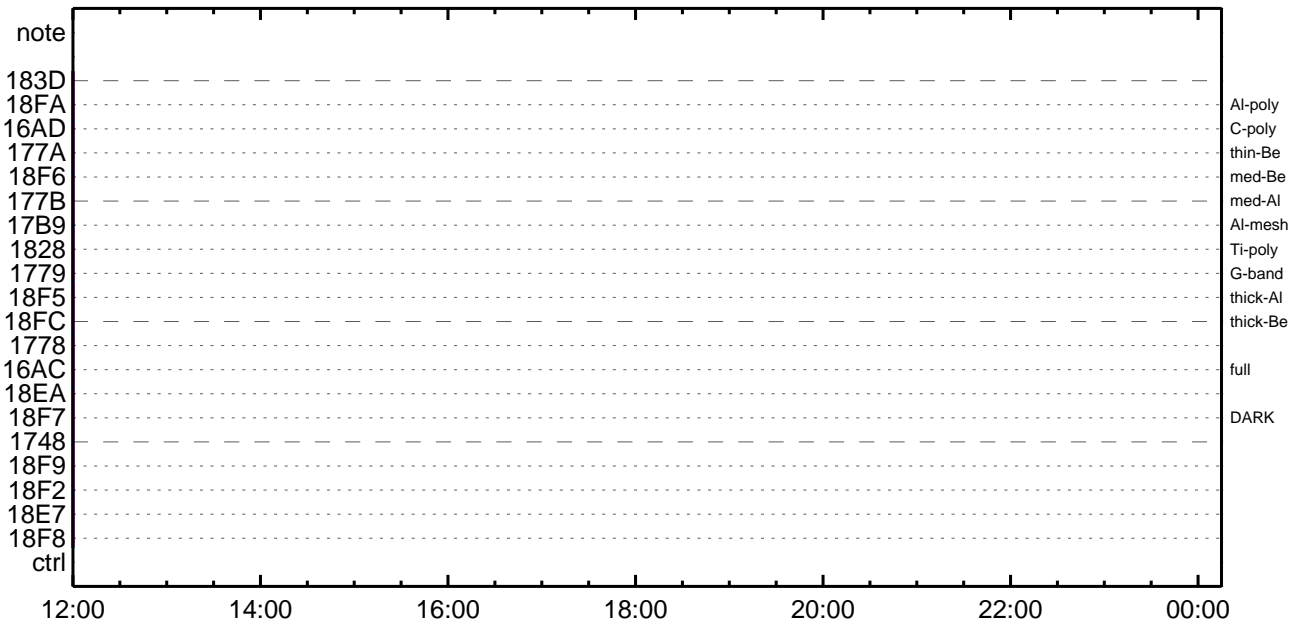
CMDI #0740 2012/07/20



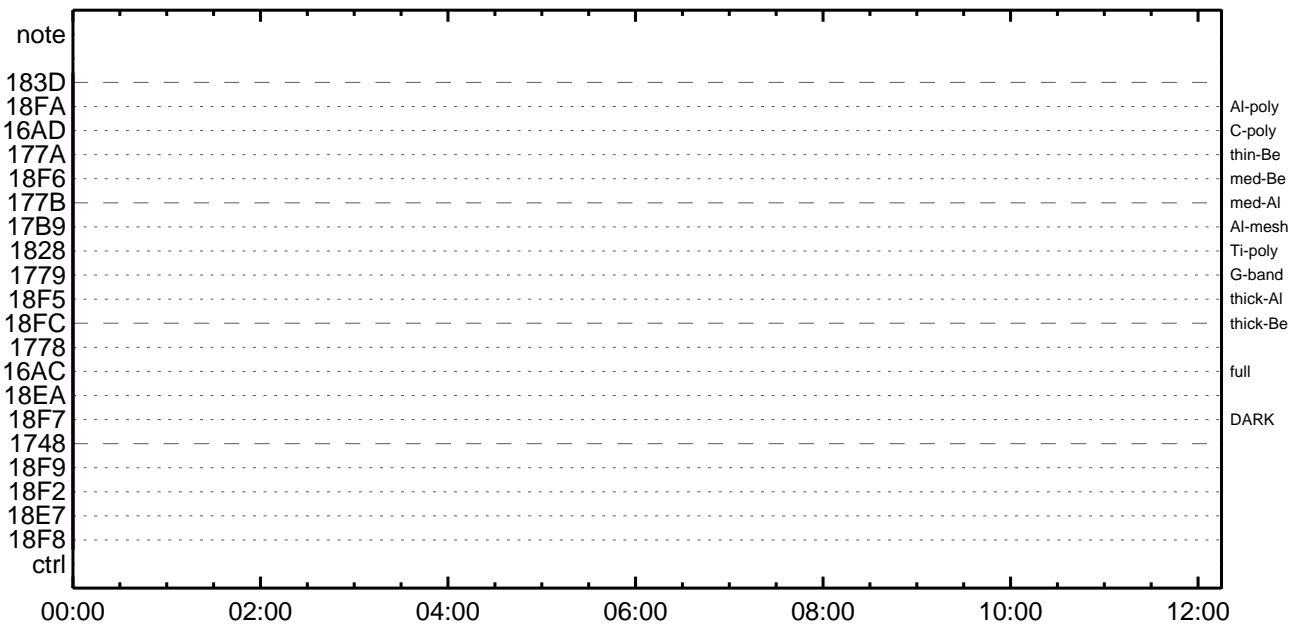
CMDI #0740 2012/07/21



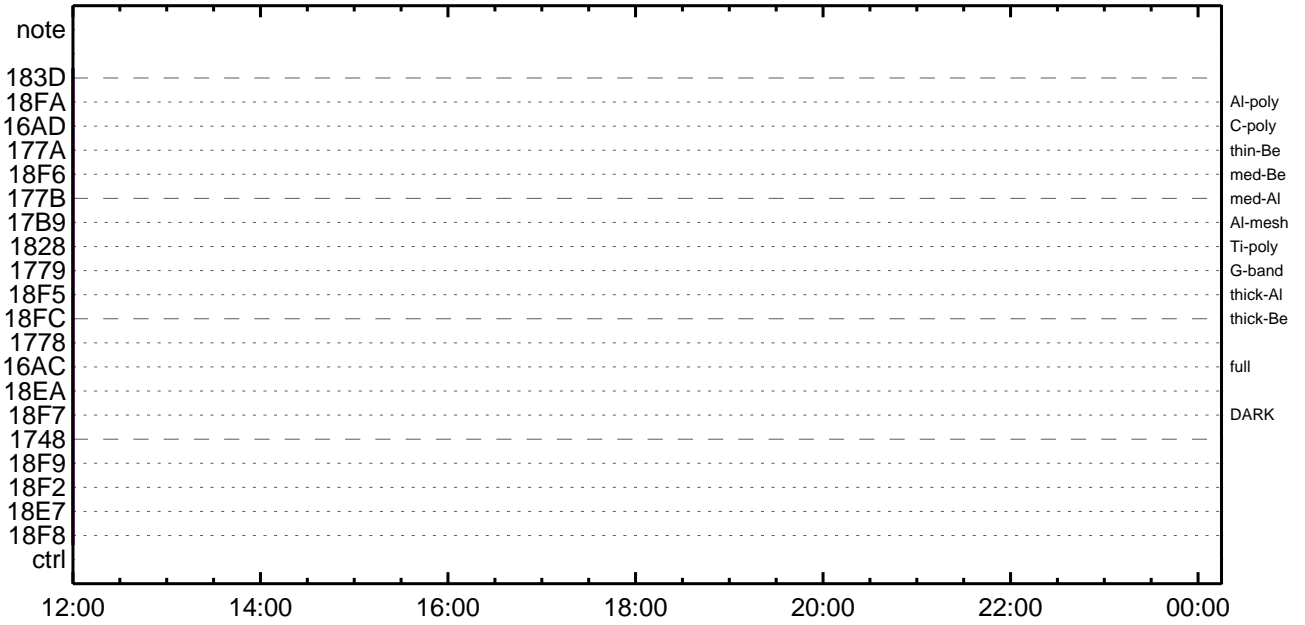
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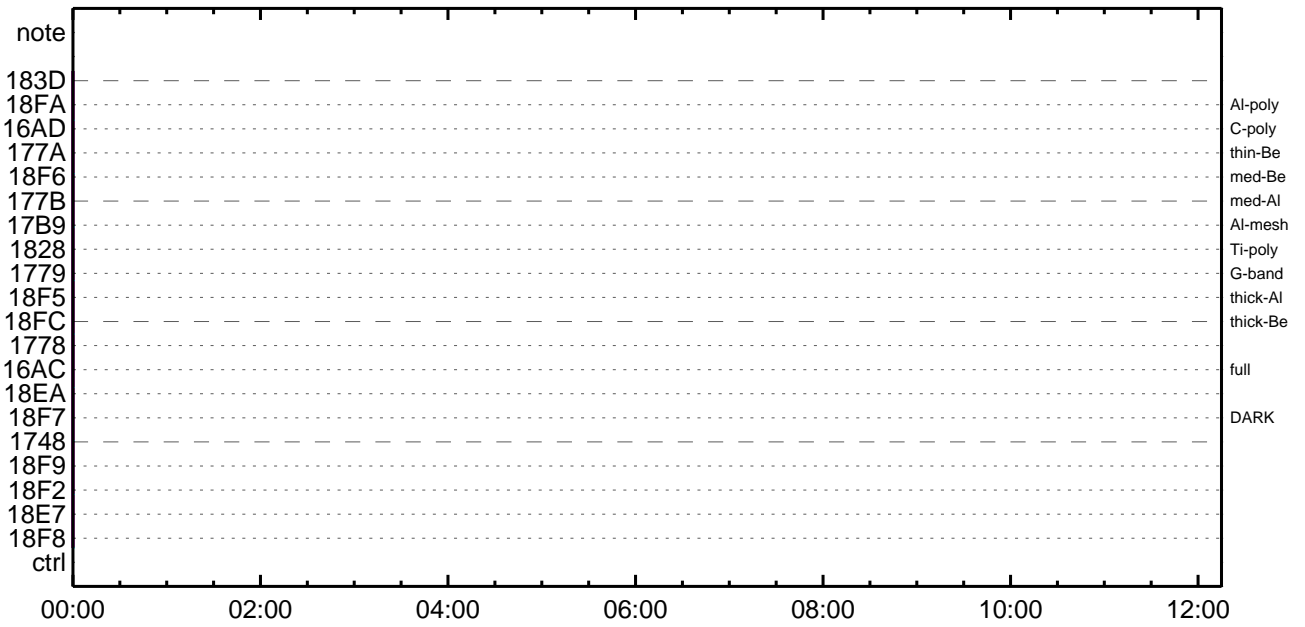
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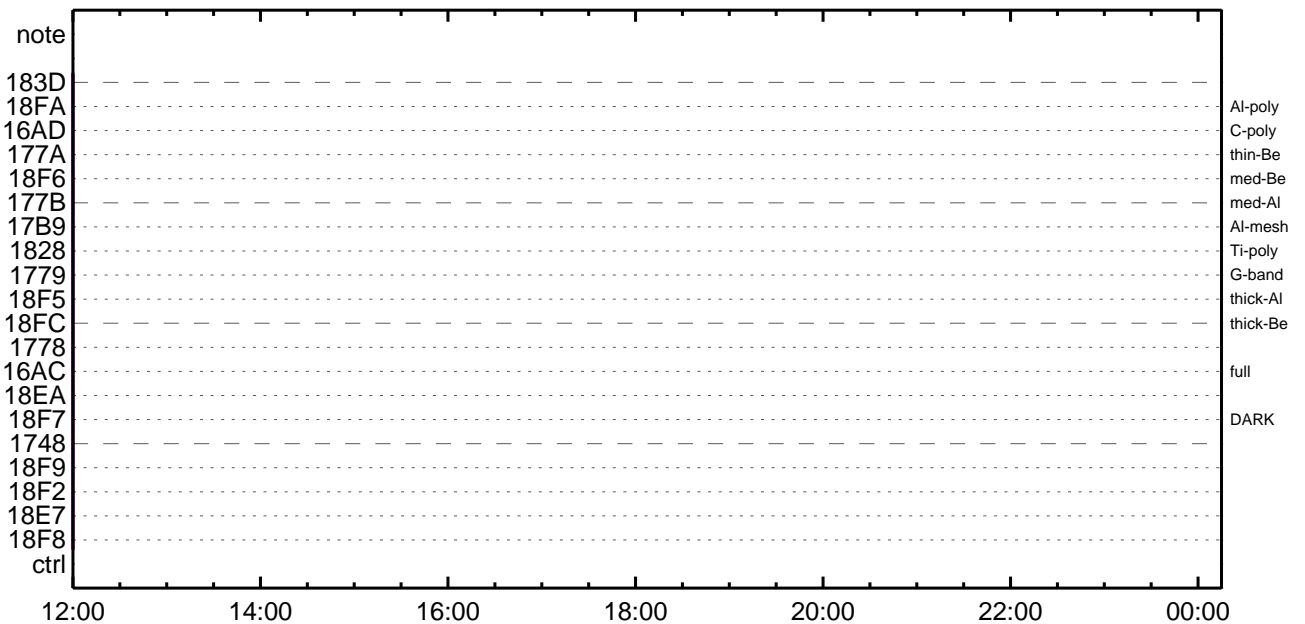
CMDI #0740 2012/07/22



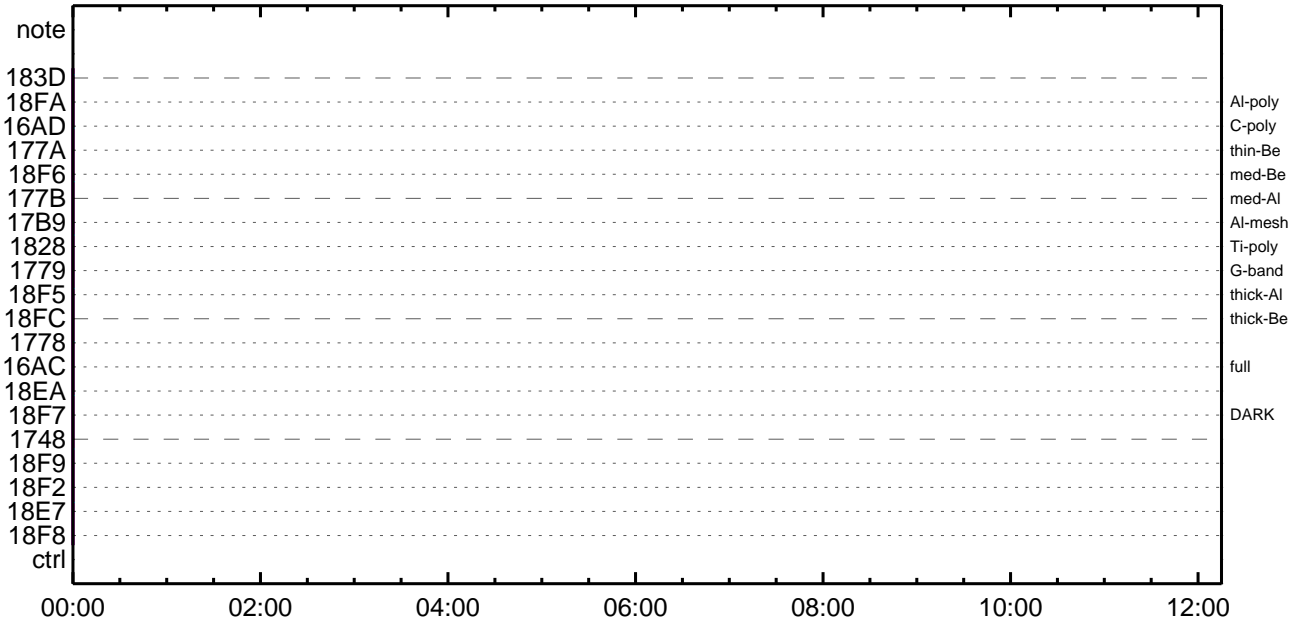
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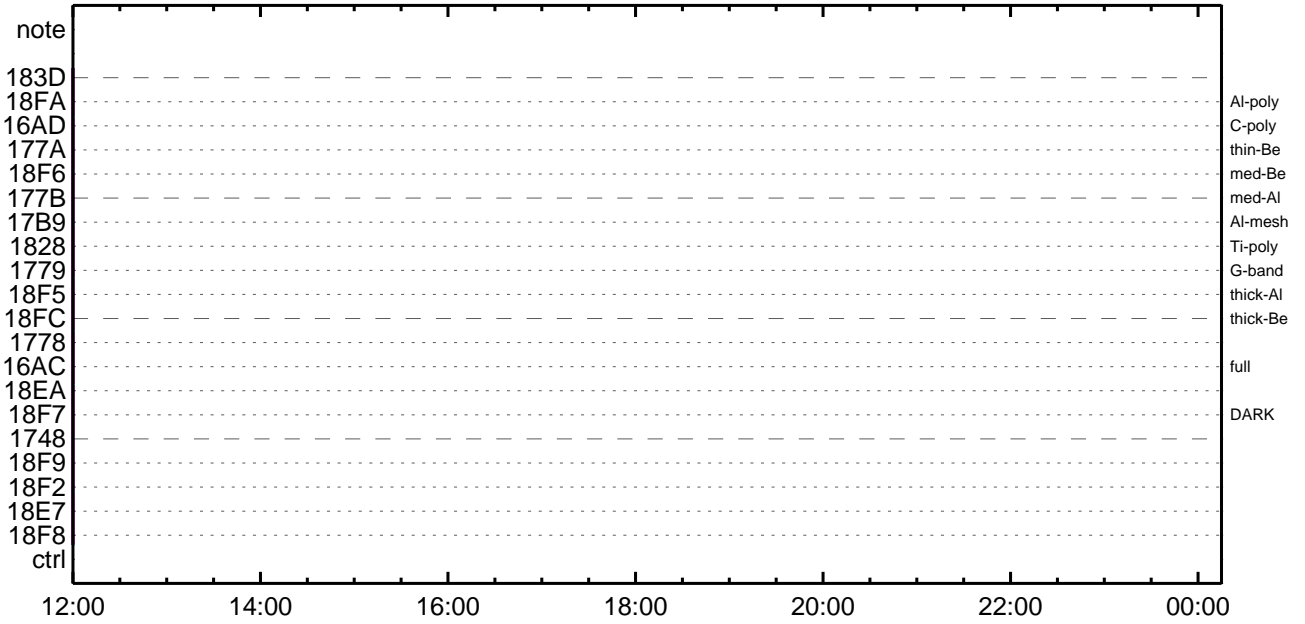
CMDI #0740 2012/07/23



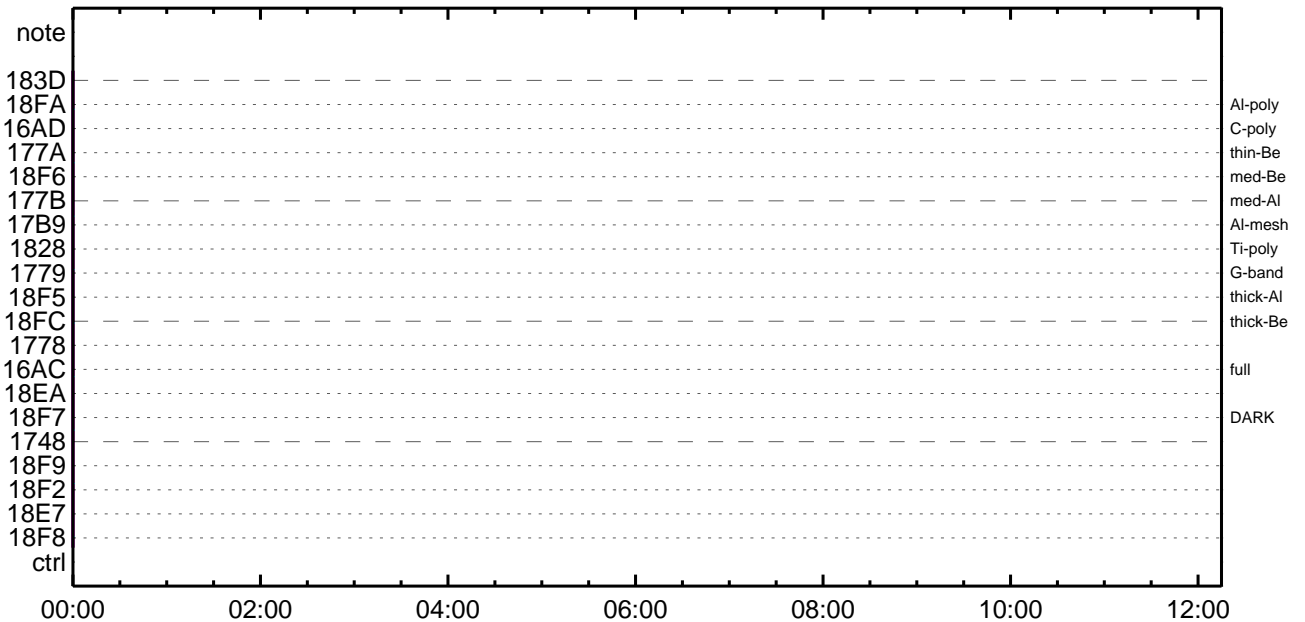
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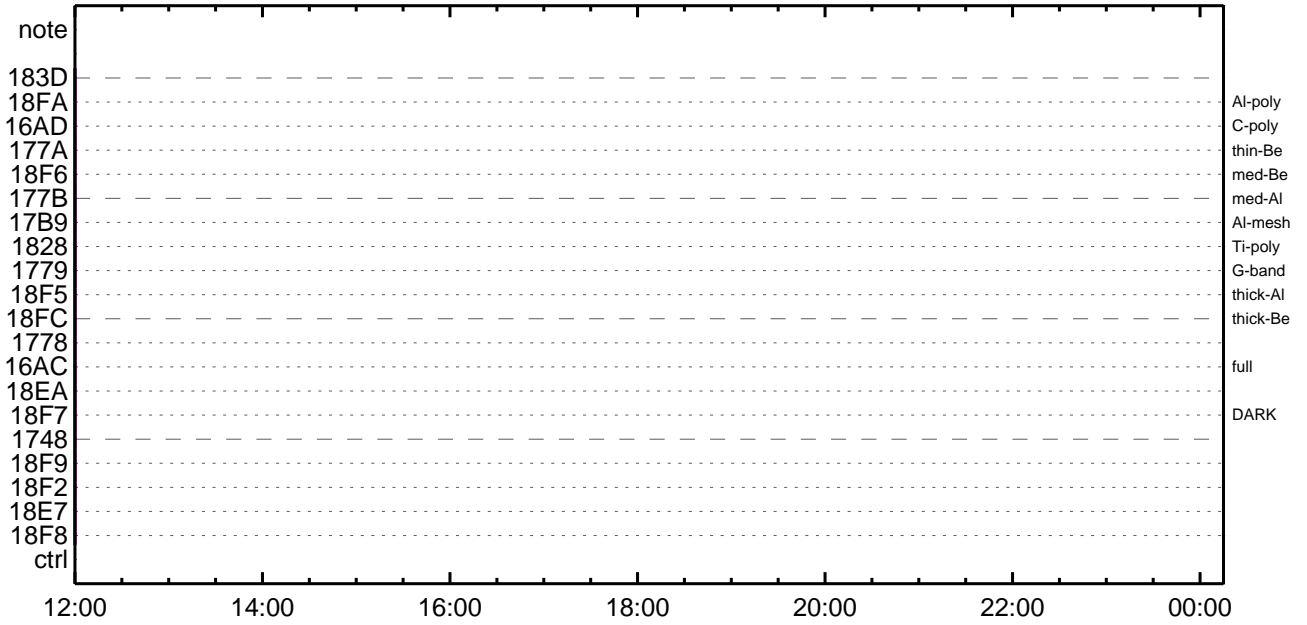
CMDI #0740 2012/07/24



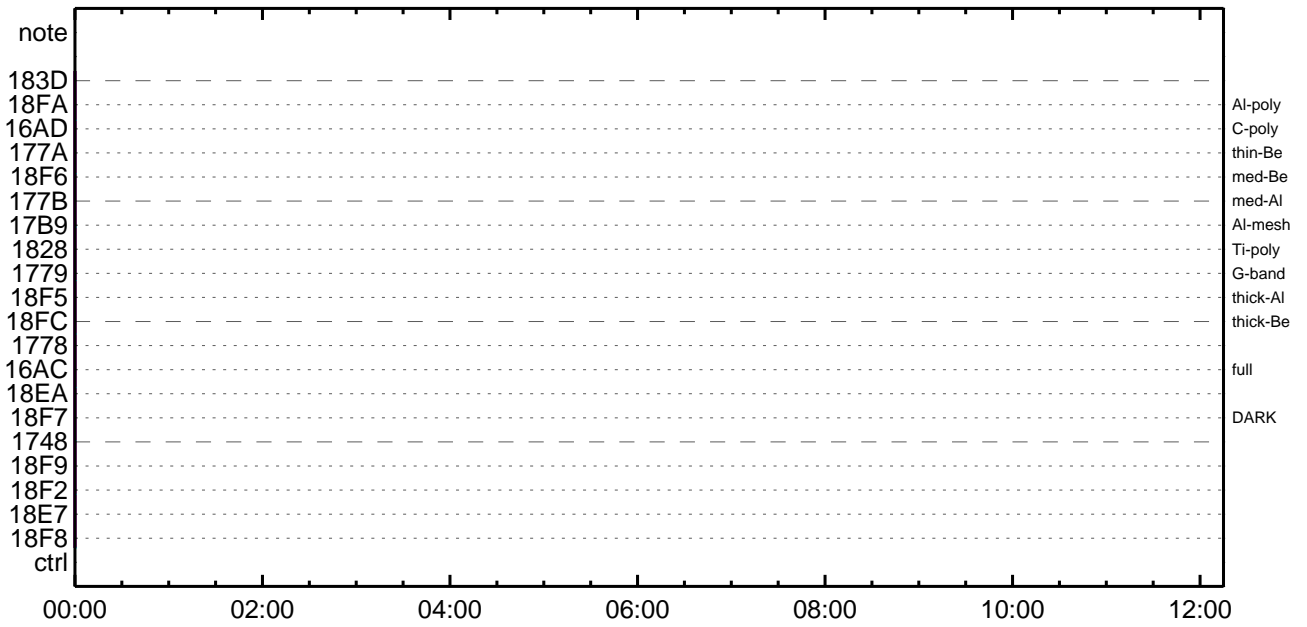
CMDI #0740 2012/07/25



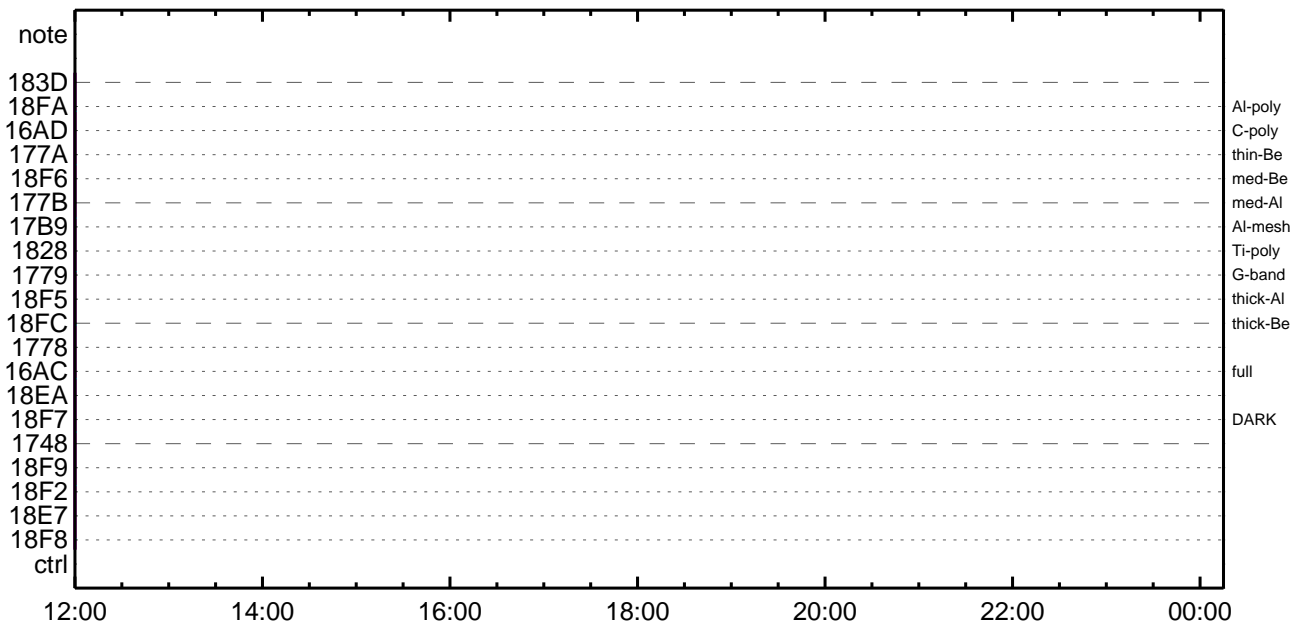
CMDI #0740 2012/07/25



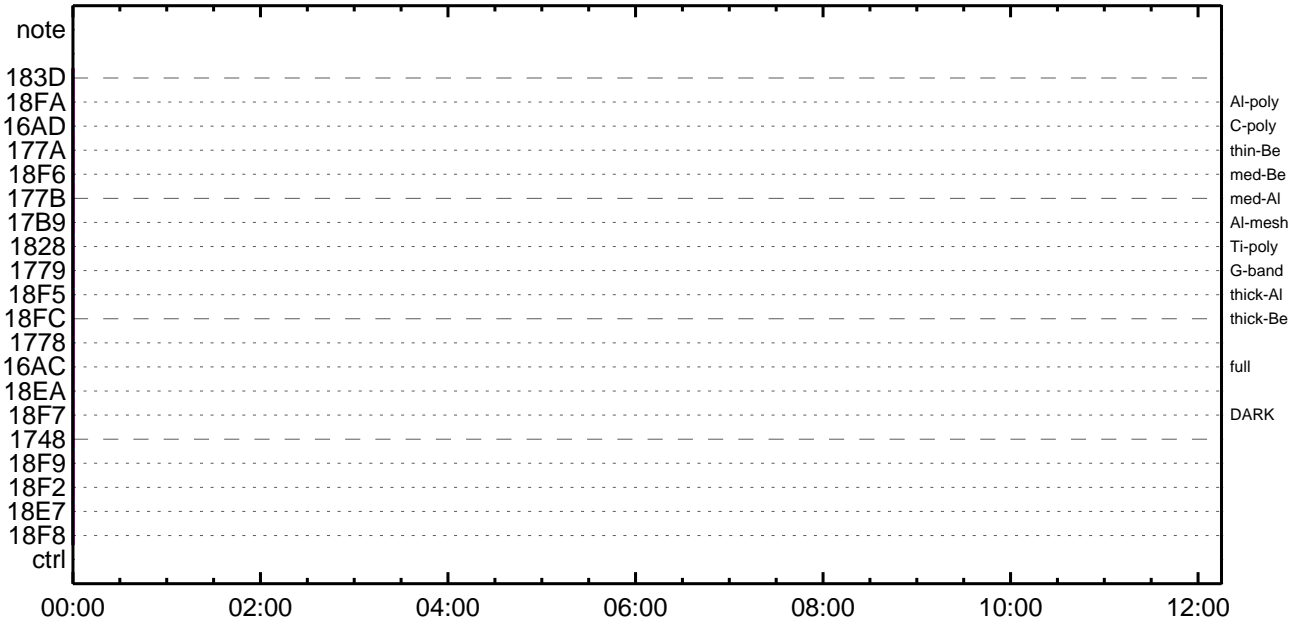
CMDI #0740 2012/07/26



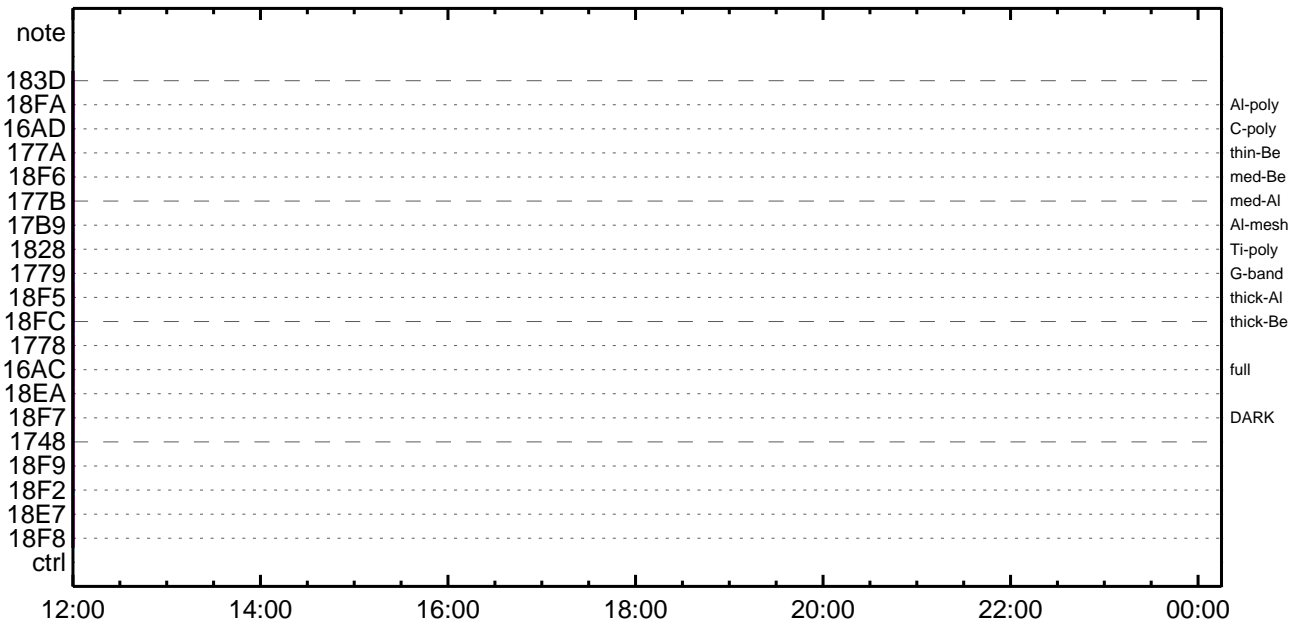
CMDI #0740 2012/07/26



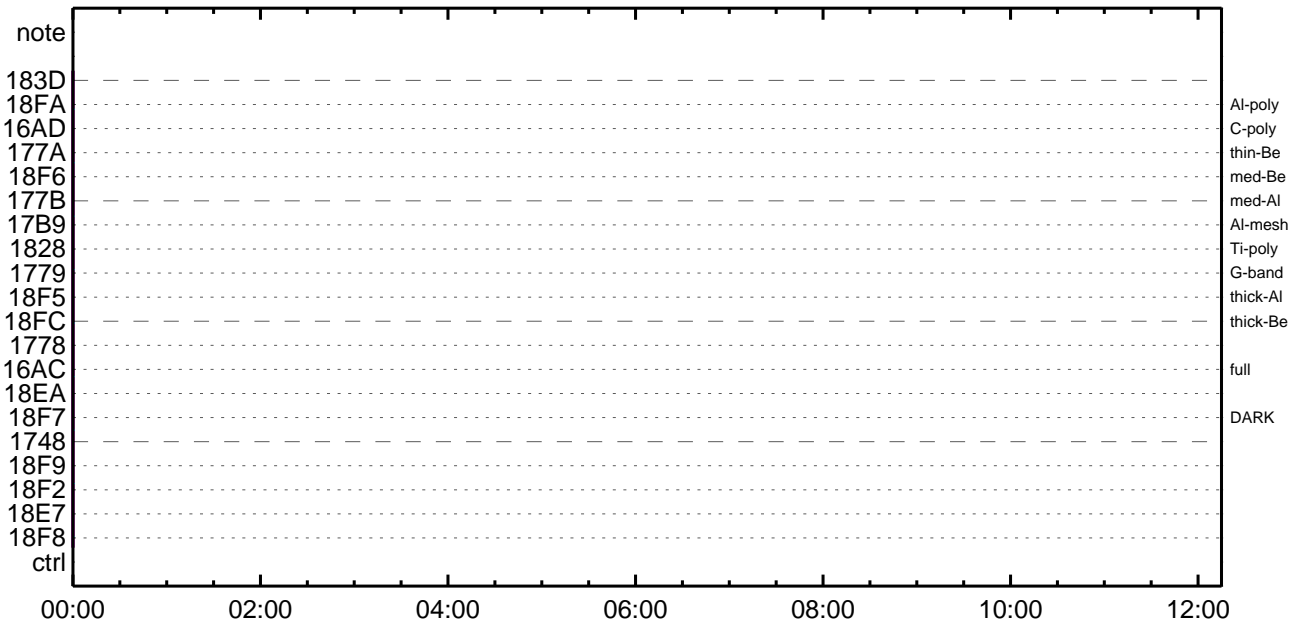
CMDI #0740 2012/07/27



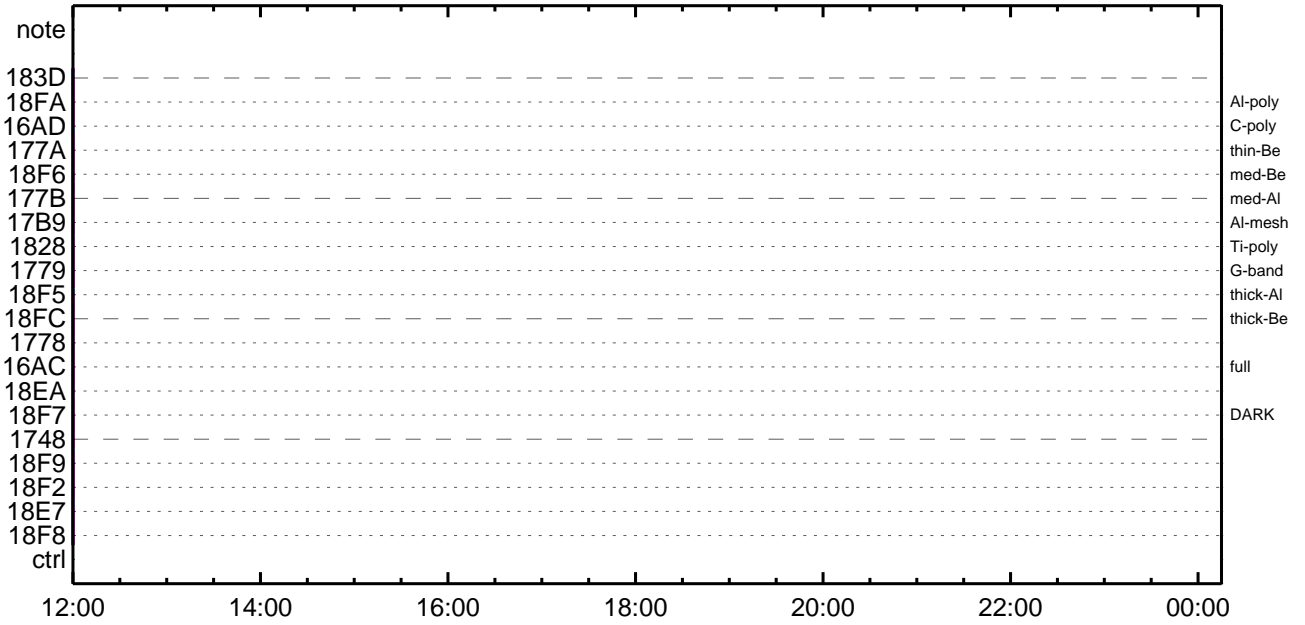
CMDI #0740 2012/07/27



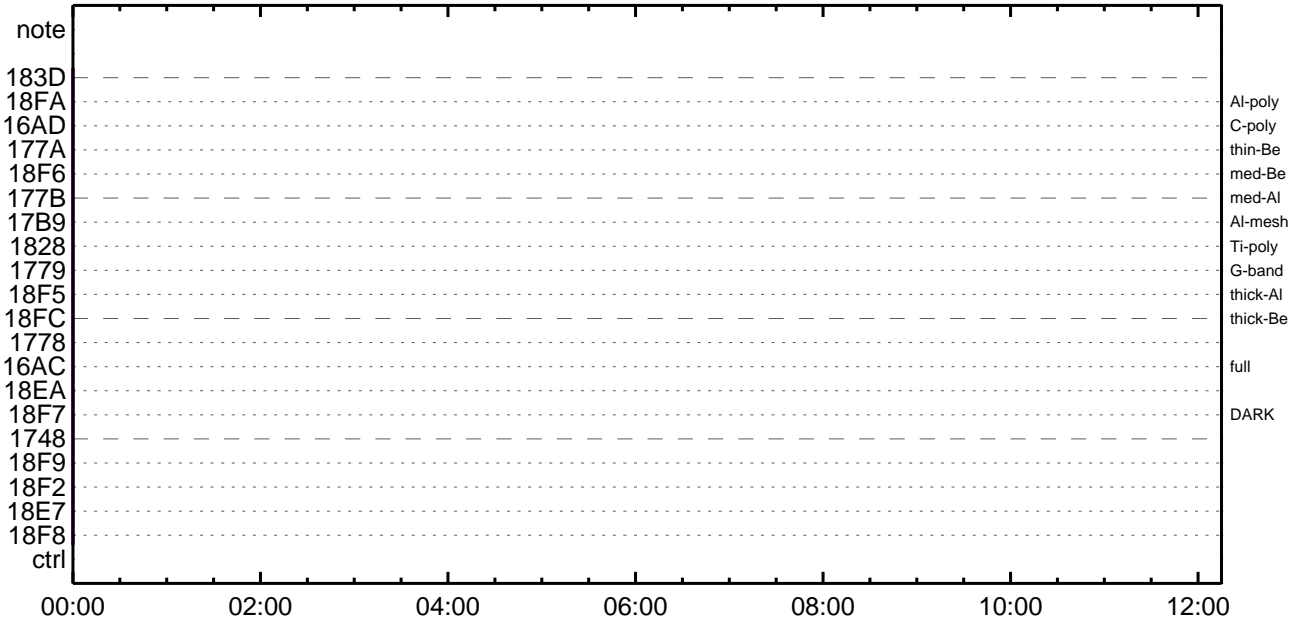
CMDI #0740 2012/07/28



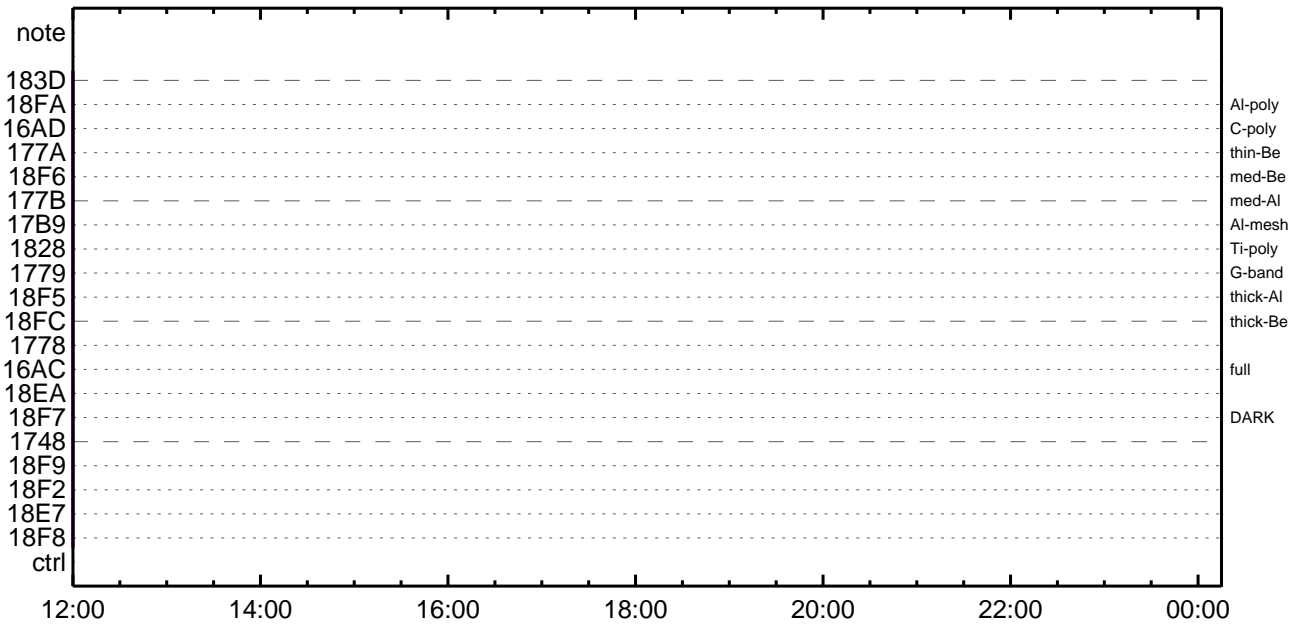
CMDI #0740 2012/07/28



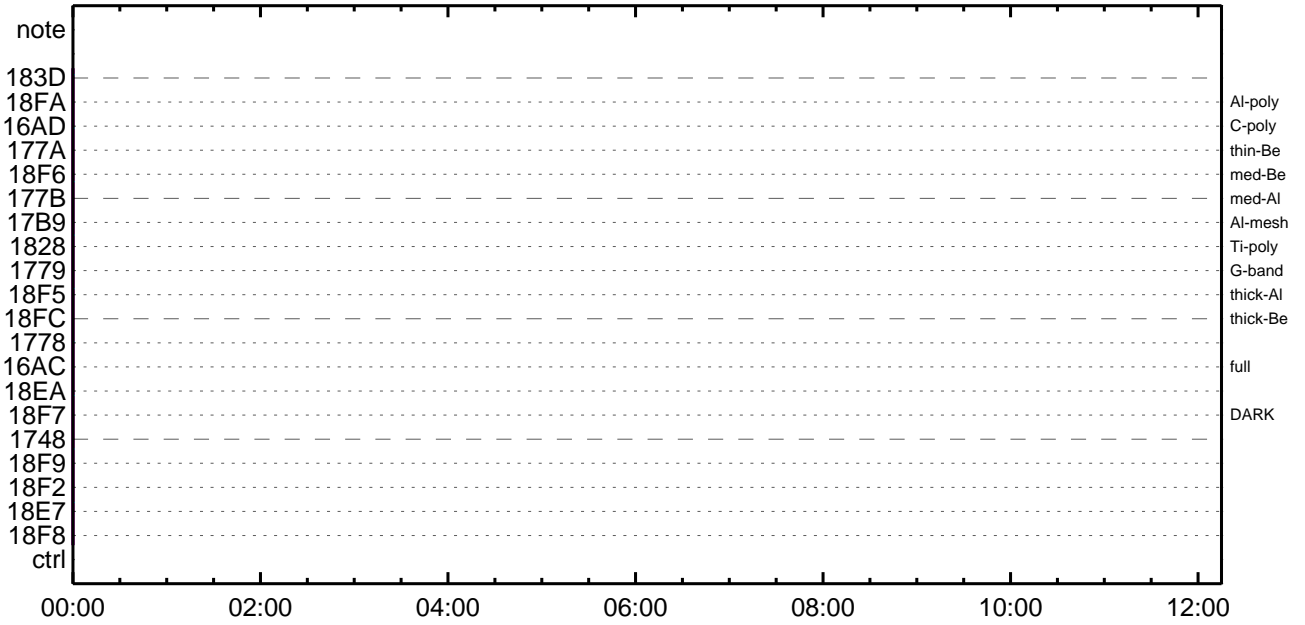
CMDI #0740 2012/07/29



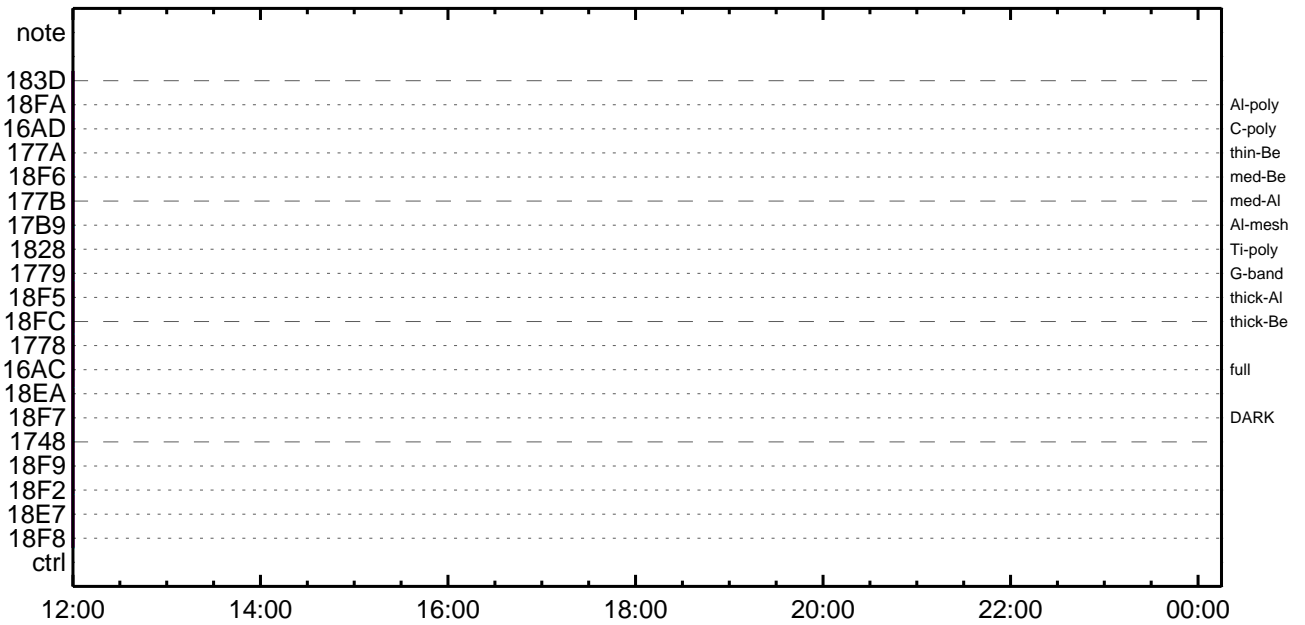
CMDI #0740 2012/07/29



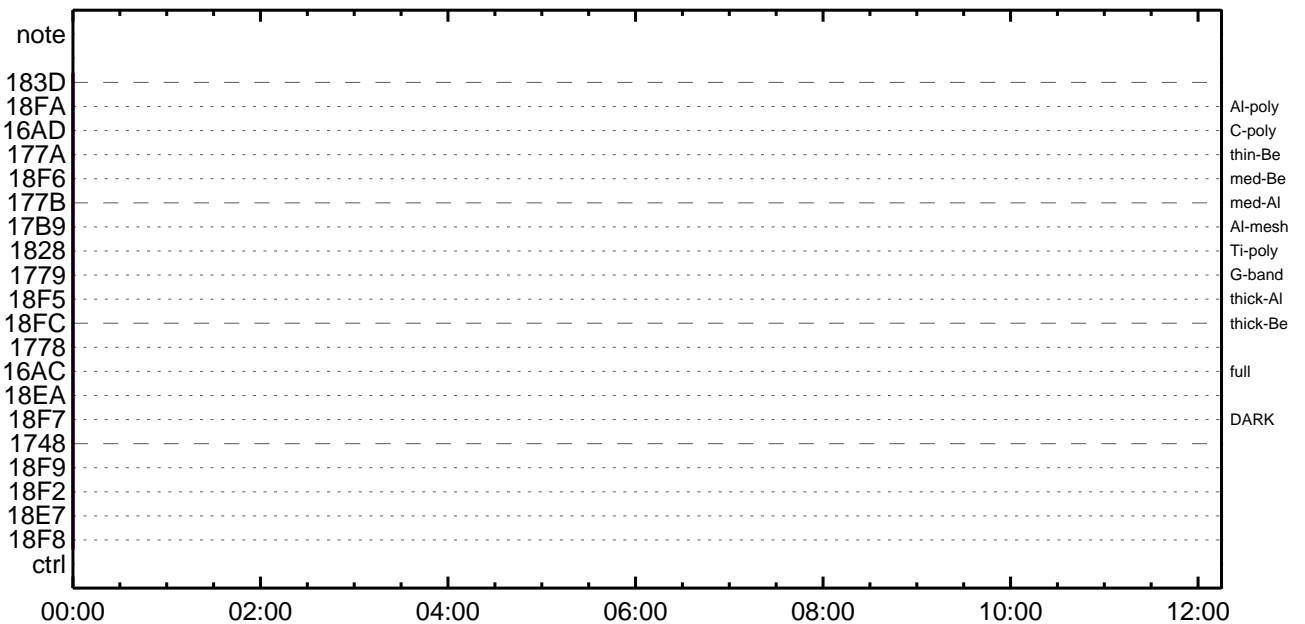
CMDI #0740 2012/07/30



CMDI #0740 2012/07/30

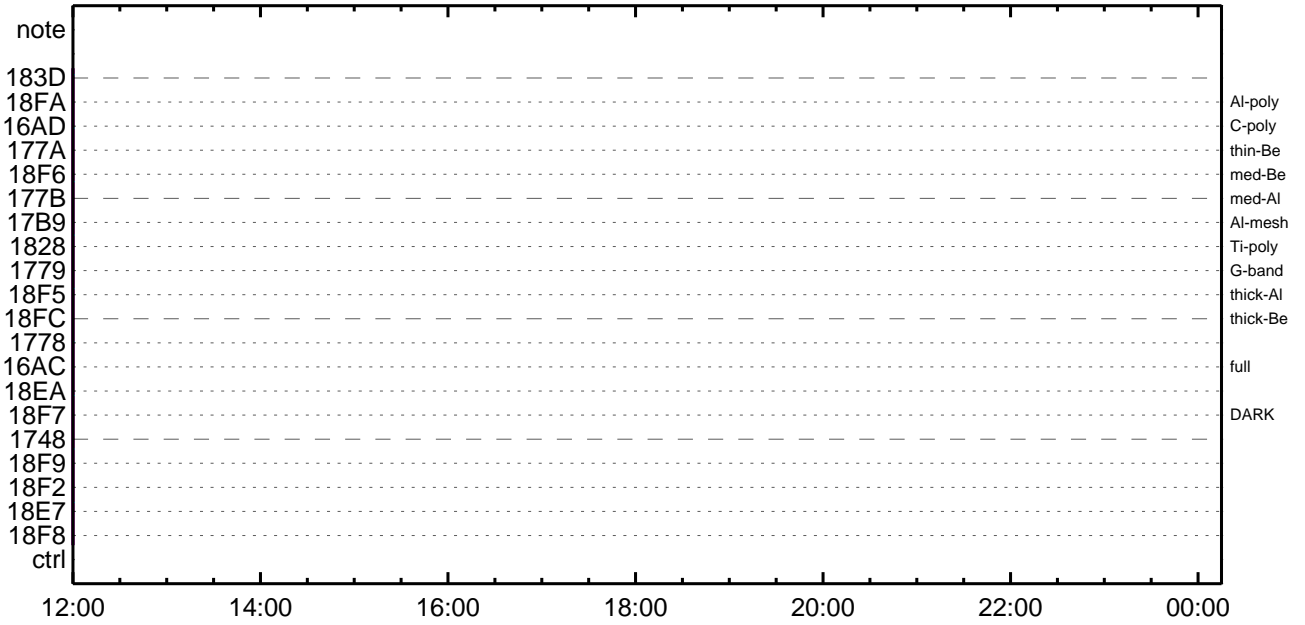


CMDI #0740 2012/07/31

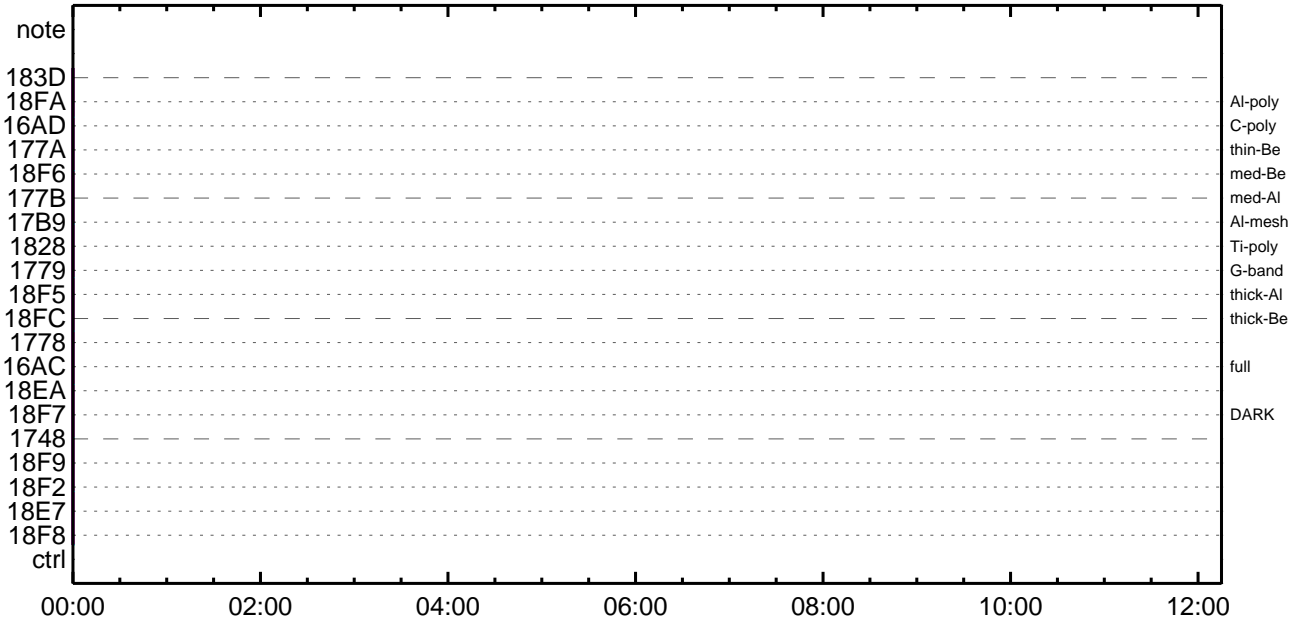




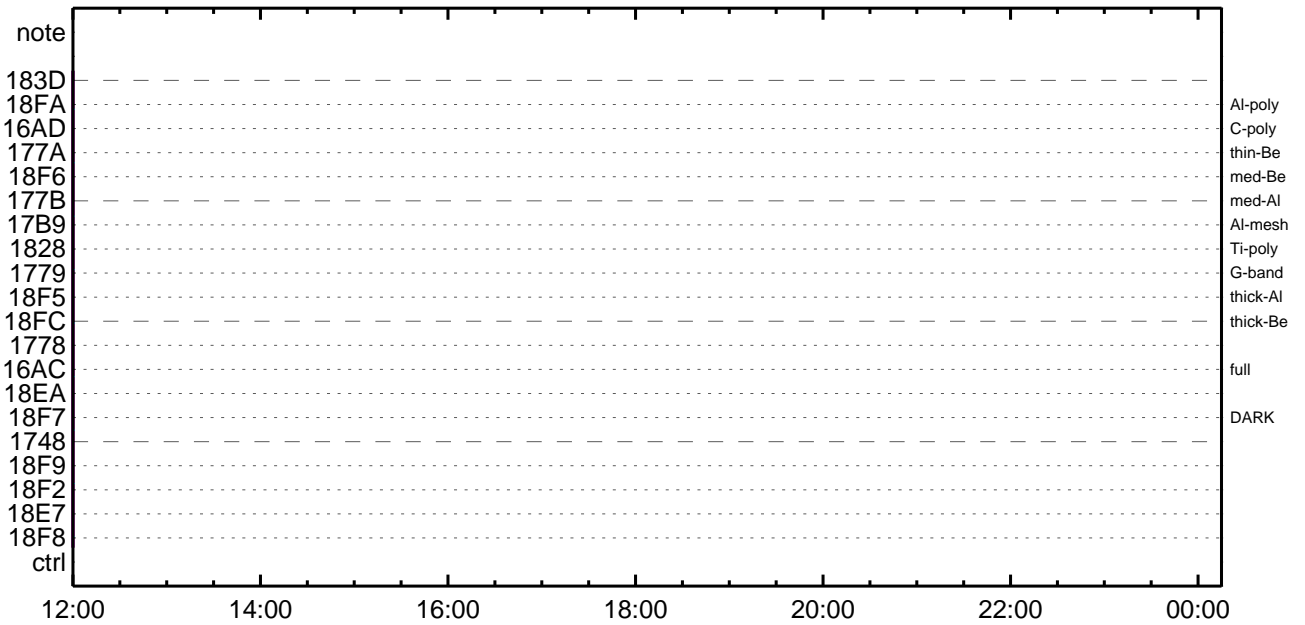
CMDI #0740 2012/07/31



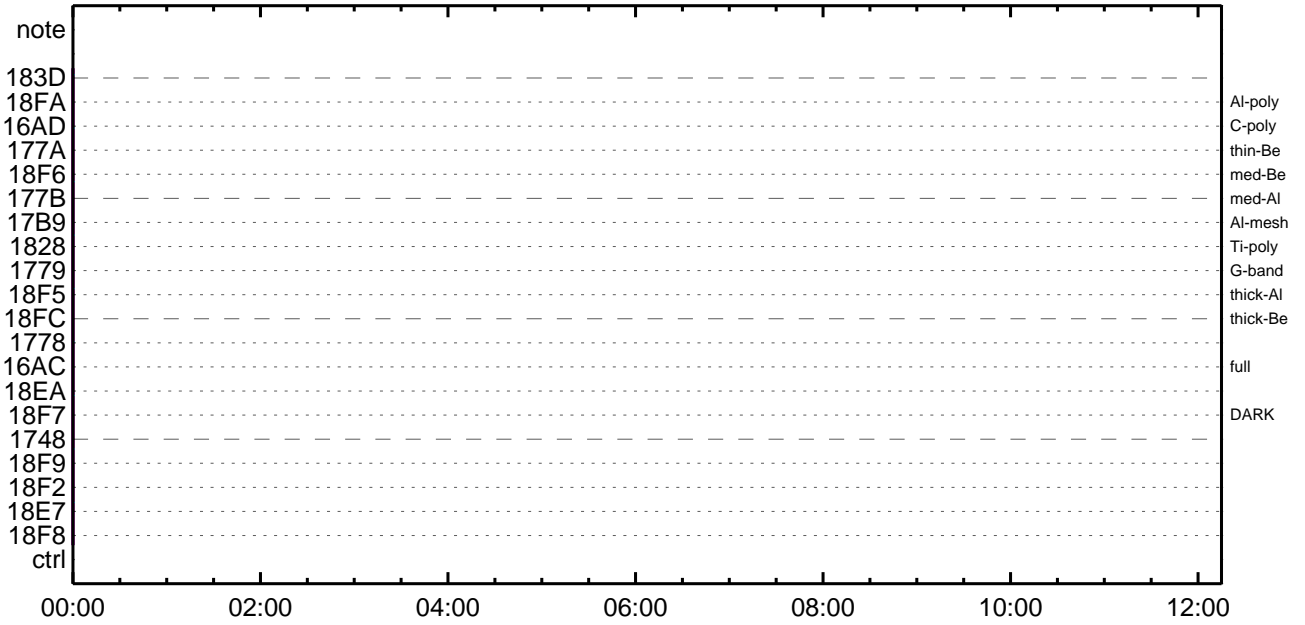
CMDI #0740 2012/08/01



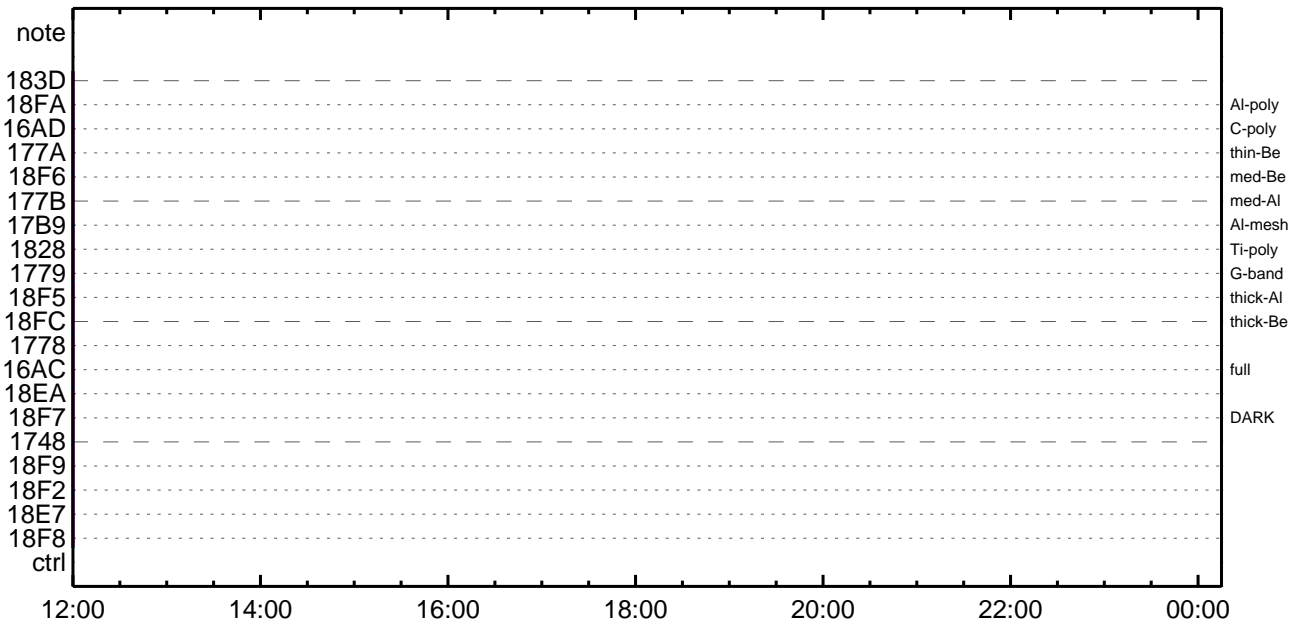
CMDI #0740 2012/08/01



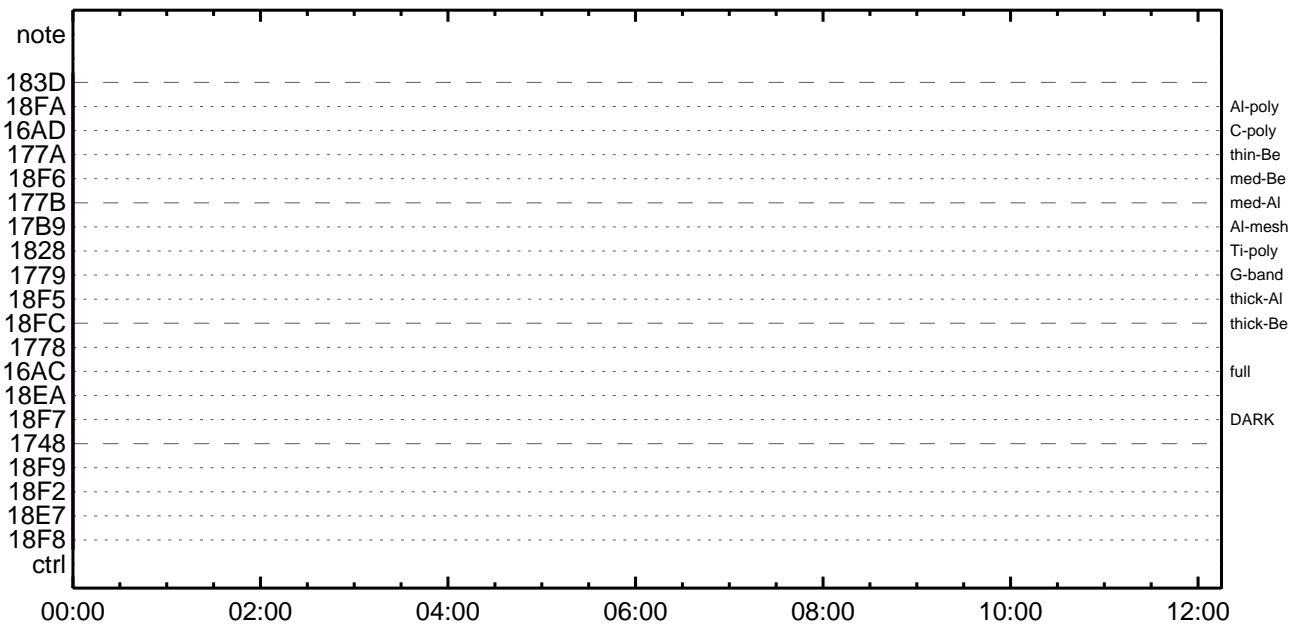
CMDI #0740 2012/08/02



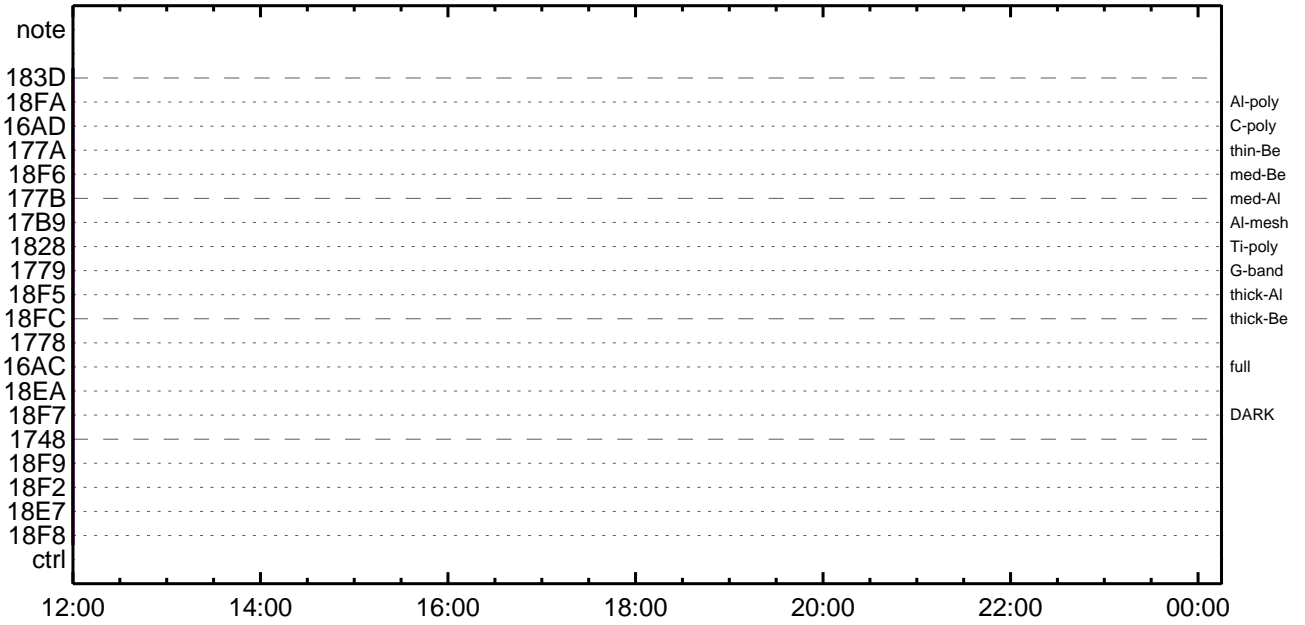
CMDI #0740 2012/08/02



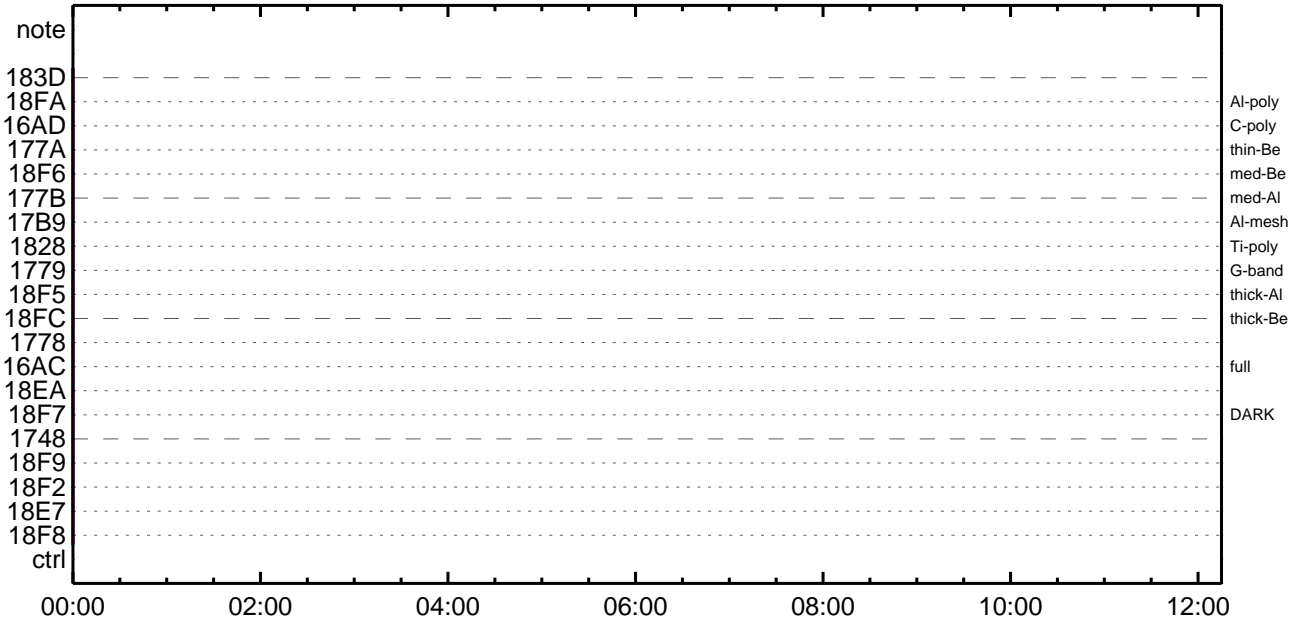
CMDI #0740 2012/08/03



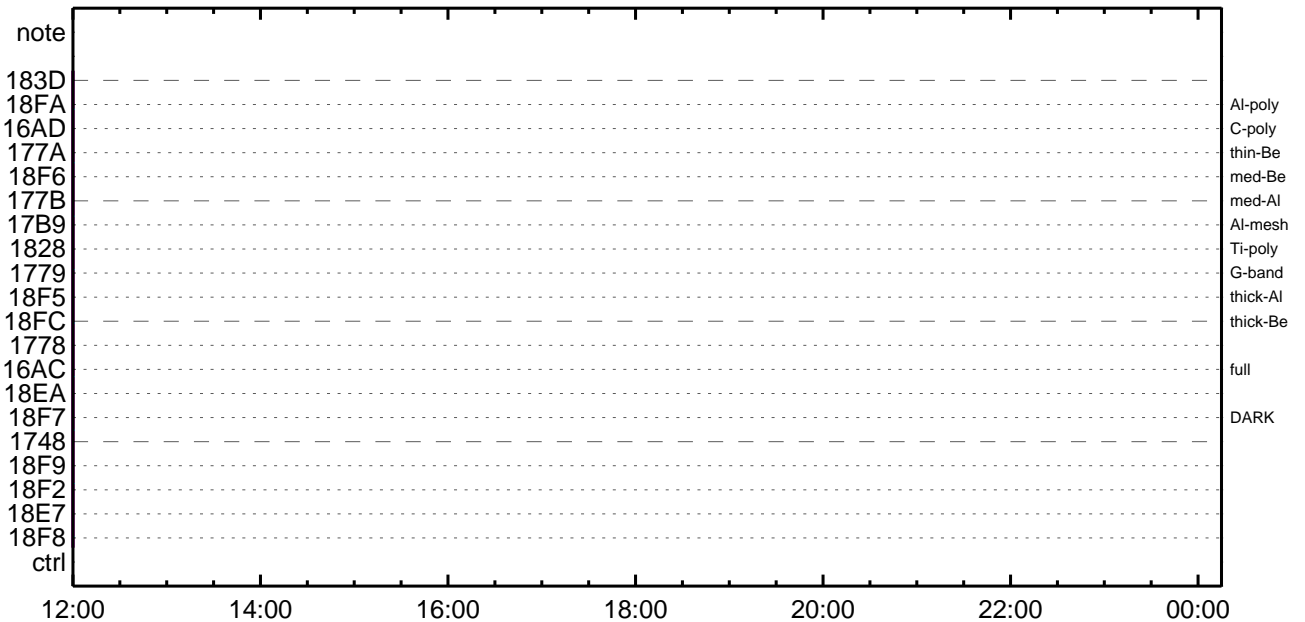
CMDI #0740 2012/08/03



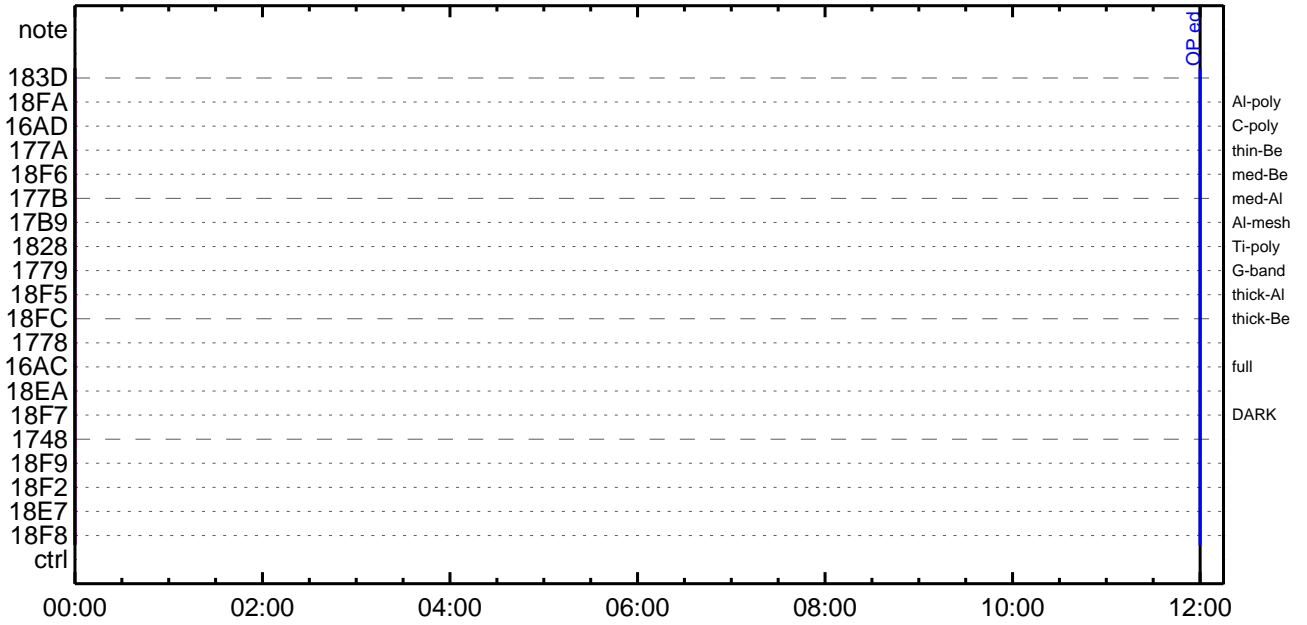
CMDI #0740 2012/08/04



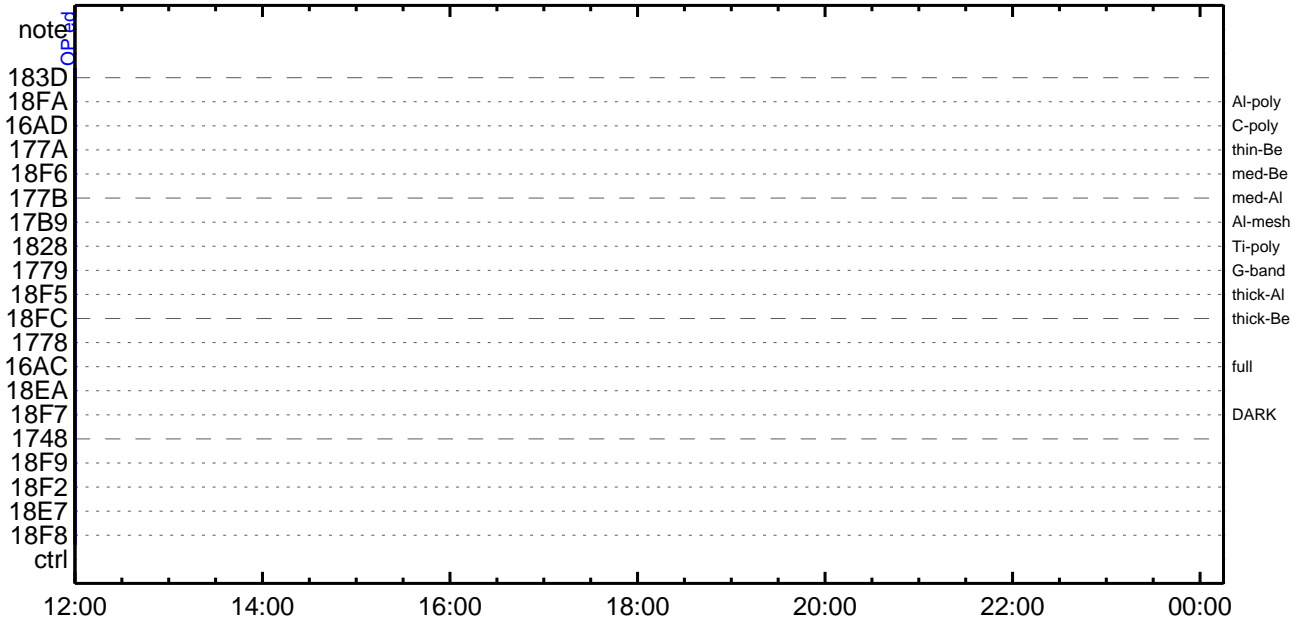
CMDI #0740 2012/08/04



CMDI #0740 2012/08/05



CMDI #0740 2012/08/05





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|AY6Yx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-977:OP
0104 ( )
0105 S. OG og-977:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAY6Yx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAY6Yx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAY6Yx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ¬ò°Á÷¿® (¼âµ-YAY6Yx½ê½çòðÁÔæòÇ¼ª°¬²è¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²¬Á÷¿®NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿®²î½Á¹Ôª°²E²ò²³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°îYNY¹ç¹Ôª²³²E;f
0181 C.
0182 C. TIY³Y²Y6YÉòðÁDî¿(UT)
0183 +. TI 2012-06-30 09:23:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-06-30 09:23:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-06-30 09:23:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```
0194 C.
0195 +. TI 2012-06-30 09:27:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.           ¢¢[HK1_TI_CMD_NUM]                    EQ      1COUNTUP
0198 C.
0199 C. °Ê¼ªíÄē%íññîîŷÄŷŷÄŷ¹àîŰ
0200 C.           ¢¢[HK1_TI_CMD_ENA/DIS]                  EQ      ENA
0201 C.           ¢¢[HK1_TI_CMD_NUM]                      EQ      4
0202 C.           ¢¢[HK1_NEXT_EXEC_PIM]                    EQ      DHU
0203 C.           ¢¢[HK1_NEXT_EXEC_DC]                      EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.           ¢¢[HK1_DMP_TOP_ADRS_1]                  EQ      07
0213 C.           ¢¢[HK1_DMP_TOP_ADRS_0]                  EQ      2B
0214 C.           ¢¢[HK1_DMP_BLOCK_NUM]                    EQ      3
0215 C.           ¢¢[HK1_DMP_REPEAT_NUM]                  EQ      0
0216 C.           ¢¢[HK1_DMA_DMP_PIM]                      EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.           ¢¢[HK1_PKT_FORM_NO]                      EQ      7
0220 C.           ¢¢[HK1_PKT_GEN_TIME]                      EQ      0.25 s
0221 C.           ¢¢[HK1_S_TLM_BIT_RATE]                  EQ      32k
0222 C.           ¢¢[HK1_X_TLM_BIT_RATE]                  EQ      4M
0223 C.           ¢¢[HK1_DMP_CHK_FLG]                      EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½¹î»ñð³îÇ§
0226 C.           ¢¢[HK1_DMP_CHK_FLG]                      EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLñîŷÈ¹ç·è²îOKñð³îÇ§
0229 C.
0230 C. DHUŷä;¼ŷÈ;È¼ŷ¼. ŷi;¼ŷÈ;Èñðîäñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.           ¢¢[HK1_PKT_FORM_NO]                      EQ      2
0234 C.           ¢¢[HK1_PKT_GEN_TIME]                      EQ      0.5S
0235 C.           ¢¢[HK1_S_TLM_BIT_RATE]                  EQ      32K
0236 C.           ¢¢[HK1_X_TLM_BIT_RATE]                  EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-06-30 09:27:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C.   HK1_TI_CMD_NUM        = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-06-30 09:27:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2012-06-30 09:27:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.           [ ] [HK1_TI_CMD_NUM]                    EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-06-30 09:27:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.           [ ] [HK1_TI_CMD_NUM]                    EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´úÄîñî»ö¼ŷñÈÄñ¹ñèDCBC·x²è *****
0276 C. (¼á°îŷÖŷÄŷÈŷŷŷÄŷŷŷÈŷ¼çŷèè¼ññ¼Ä»Űñ¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹·î Daily±çîññè'Øñ¹ñèDCBC·x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷŷŷÄŷŷ¹¼Ä»Ű;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-978 2012-06-30 14:46:20 82 33 SOLAR-B MAIN //
0001 . C.
0002 . C. ***** AOS *****
0003 . C.
0004 . C. ;ãAOSYÁYSYÁY~¼A»Û;ã
0005 . C.
0006 . C. YÀYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 . C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 . C. Áí;È□¿□Á□•μ°Æ»Í×ÁÇ□ÍYçYÁY×Yí;¼YÉ;ÈÈ%μ•íÉ;È□È¼°Ç□□•□¿¼l¹ç□Í;çÀ®, ù□¹□ē□□□çÁ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 . C.
0013 . C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 . C. Upload the Orbit Element and the Target Attitude
0016 . C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 . C.
0019 . C.
0020 . C.
0021 . C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 . BC (07 00 00 00 18 00)
0024 . C.
0025 . C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 . C.
0027 . C.
0028 . C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 . BC (04 0b f8)
0031 . C.
0032 . C. Wait for AOCSDUMP to end
0033 . C.
0034 . C. Check the dump memory
0035 . C.
0036 . C. Result = OK [ ]
0037 . C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 . BC (02 0a f8)
0040 . C.
0041 . C. <A_***>[TLM STS] FMT = 2 [ ]
0042 . C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 . BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 21s
0050 . C. *****
0051 . C. EIS START OBSTBL LOAD
0052 . C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 . C.
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 . BC (07 07 07 00 00 70 00)
0057 . C.
0058 . C. Execute, after the success of OBSTBL upload.
0059 . C. Set EIS TI-commands
0060 +. TI 2012-06-30 09:27:50.0
0061 . DC 07-FC EIS_MODE_CHG_ENA
0062 . BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 . C. *****
0065 . C. EIS END OBSTBL LOAD
0066 . C. *****
0067 . C.
0068 . C. ***** MDP ´ûÃî□î›ô%Y□ÈÁ□¹□ēDCBC•x²è *****
0069 . C. (¼á°îYÓYÁYÉYBÝÉYáYçYÈ□È¼°ç¼¼»Û□¹□ē)
0070 . S. DC-BC dcbc-402:DCBC
0071 . (MDP_known_event)
0072 . C.
0073 . C.
0074 . C. ***** YDY¹•İ Daily±¿İÑ□È´Ø□¹□ēDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 . (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 . C.
0078 . C.
0079 . C. ;ãLOSÝÁYSYÁY~¼A»Û;ã
0080 . C.
0081 . C. ***** LOS *****
0082 . C.
```





```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-268:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Resume FG table (auto mode) >
0120 +. DC 07-F0 MDP_FG_CTRL_AUTO
0121 BC (50)
0122 . C. -----
0123 C. MDP_FG_CTRL_MODE = AUTO [ ]
0124 C. -----
0125 C.
0126 C. *****
0127 C. SOT TI command set
0128 C. *****
0129 C. Execute, after the success of TBL upload.
0130 +. TI 2012-06-30 09:27:18.0
0131 DC 07-F0 MDP_SOT_MODE_OBSV
0132 BC (40)
0133 . C. -----
0134 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0135 C. -----
0136 C.
0137 C. Only when FG_CTRL_AUTO is used in RT.
0138 +. TI 2012-06-30 09:27:20.0
0139 DC 07-F0 MDP_FG_CTRL_AUTO
0140 BC (50)
0141 . C. -----
0142 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0143 C. -----
0144 C. ***** SOT END *****
0145 C.
0146 C. ***** XRT START *****
0147 C.
0148 +. DC 07-F0 MDP_XRT_CTRL_MANU
0149 BC (c1)
0150 + DC 07-F0 MDP_XRT_MODE_STBY
0151 BC (c3)
0152 . C. ----- Success Verify ? OK / NG____
0153 C.
0154 C. XRT Obs. Table Upload
0155 . S. RAM ram-291:MDP_OBS_X
0156 ( )
0157 C.
0158 +. DC 07-F0 MDP_DUMP_XRTTBL
0159 BC (84 07 00 00 00 3a d4)
0160 . C. ----- Comparison Check ? OK / ERR ____
0161 C.
0162 C.
0163 +. DC 07-F0 MDP_XRT_ROI_SET
0164 BC (cd 01 b1 b1 04 04)
0165 + DC 07-F0 MDP_XRT_ROI_SET
0166 BC (cd 02 b1 b1 08 08)
0167 + DC 07-F0 MDP_XRT_ROI_SET
0168 BC (cd 03 b1 b1 08 08)
0169 + DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 04 b1 b1 06 06)
0171 + DC 07-F0 MDP_XRT_ROI_SET
0172 BC (cd 05 85 83 06 06)
0173 + DC 07-F0 MDP_XRT_ROI_SET
0174 BC (cd 06 85 83 08 08)
0175 + DC 07-F0 MDP_XRT_ROI_SET
0176 BC (cd 07 80 80 20 20)
0177 + DC 07-F0 MDP_XRT_ROI_SET
0178 BC (cd 08 80 80 20 08)
0179 + DC 07-F0 MDP_XRT_ROI_SET
0180 BC (cd 09 80 80 08 20)
0181 + DC 07-F0 MDP_XRT_ROI_SET
0182 BC (cd 0a 85 83 0c 0c)
0183 + DC 07-F0 MDP_XRT_ROI_SET
0184 BC (cd 0b 85 83 06 06)
0185 + DC 07-F0 MDP_XRT_ROI_SET
0186 BC (cd 0f 80 80 06 06)
0187 + DC 07-F0 MDP_XRT_ROI_SET
0188 BC (cd 10 80 80 08 08)
0189 + DC 07-F0 MDP_XRT_FLD_DIS
0190 BC (d9)
0191 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0192 BC (c9)
0193 + DC 07-F0 MDP_XRT_AEC_RESET

```

```
0194 BC (d0)
0195 + DC 07-F0 MDP_XRT_ARS_DIS
0196 BC (d5)
0197 + DC 07-F0 MDP_XRT_FLD_RESET
0198 BC (da)
0199 + DC 07-F0 MDP_XRT_QT_PROG_SET
0200 BC (c4 01)
0201 + DC 07-F0 MDP_XRT_FL_PROG_SET
0202 BC (c5 0d)
0203 . C. ----- Success Verify ? OK / NG ____
0204 C.
0205 C.
0206 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0207 C.
0208 +. DC 07-F0 MDP_XRT_MODE_OBSV
0209 BC (c2)
0210 +. TI 2012-06-30 09:27:02.0
0211 DC 07-F0 MDP_XRT_MODE_OBSV
0212 BC (c2)
0213 . C. ----- Success Verify ? OK / NG ____
0214 C.
0215 C. ***** XRT END *****
0216 C.
0217 . C. ***** MDP 'úÃîâî»ö¼ÝðËÂð¹¹æDCBC•x²è *****
0218 C. (%ã°îÏ¥Ó¥Ã¥Ê¥Þ¥Ë¥á¥ç¥è¹¼¼¼¼¼»Û¹¹æ)
0219 . S. DC-BC dcbc-402:DCBC
0220 (MDP_known_event)
0221 C.
0222 C.
0223 . C. ***** ¥Ð¥¹•Ï Daily±¿ÎÑæ´Ø¹¹æDCBC•x²è *****
0224 . S. DC-BC dcbc-153:DCBC
0225 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0226 C.
0227 C.
0228 . C. ;ãLOS¥Á¥$¥Ã¥¹¼¼¼¼¼»Û;ã
0229 C.
0230 . C. ***** LOS *****
0231 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-980 2012-06-30 14:46:20 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÃY$YÃY-¼Ã»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÿóYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āí;Ē¿ð¿Åð•μ°Æ»Ī×ÁÇ¿ĪYçYÃY×YÍ;¼YÉ;ĒĒ%μ•ÍĪĒ;ĒðĒ¼°ÇŌð•¿¿¼Ī¹ç¿Ī;çÀ®, ù¿¹ðĒððÇÄ+¿®ð•ðĒðð³ðĒ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP `úĀĪðĪ»ð¼YðĒĀðð¹ðĒDCBC•x²è *****
0016 C. (%â°ĪYŌYĀYĒYðYĒYáYçYĒ%¼ð%Ā»Û¹ðĒ)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** YDŸ¹.Ī Daily+¿ĪÑðĒ`Ÿ¹ð¹ðĒDCBC•x²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. ;ãLOSŸÃY$YÃY-¼Ã»Û;ã
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

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\*\*\* OP Sequence for XRT \*\*\*

```

2012/06/30 09:37:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/06/30 09:37:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/06/30 09:37:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/06/30 09:40:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/06/30 11:00:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 00 00 00 00
2012/06/30 17:38:24.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/06/30 17:38:26.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2012/06/30 17:38:30.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 00 00 00 00
2012/06/30 17:38:46.0 XRT_FLD_DIS_402_OG [0x192]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2012/06/30 17:38:48.0 XRT_FLRCTRL_DIS_433_OG [0x1b1]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2012/06/30 17:38:50.0 XRT_ARS_DIS_438_OG [0x1b6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/06/30 17:41:28.0 XRT_QT_PROG_SET_435_OG [0x1b3]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0e
2012/06/30 17:41:30.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/06/30 17:48:24.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/06/30 17:48:30.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 48 00 2f 33
2012/06/30 17:50:56.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2012/06/30 17:51:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/06/30 17:51:18.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/06/30 17:51:20.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/06/30 17:51:22.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/06/30 17:51:24.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/06/30 17:51:26.0 XRT_QT_PROG_SET_434_OG [0x1b2]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 04
2012/06/30 17:51:28.0 XRT_FL_PROG_SET_449_OG [0x1c1]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2012/06/30 17:53:30.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/06/30 17:53:32.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/06/30 17:53:34.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/06/30 17:56:44.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/06/30 18:38:30.0 XRT_Custom_418_OG [0x1a2]
2012/06/30 18:39:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/06/30 19:32:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/06/30 19:32:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/06/30 19:32:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/06/30 19:35:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/06/30 20:15:30.0 XRT_Custom_418_OG [0x1a2]
2012/06/30 20:16:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/06/30 20:48:24.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/06/30 20:48:30.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 01 00 00 00 00
2012/06/30 20:50:56.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2012/06/30 20:51:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/06/30 20:51:18.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/06/30 20:51:20.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/06/30 20:51:22.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/06/30 20:51:24.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/06/30 20:51:26.0 XRT_QT_PROG_SET_415_OG [0x19f]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 13
2012/06/30 20:51:28.0 XRT_FL_PROG_SET_449_OG [0x1c1]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2012/06/30 20:51:30.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/06/30 21:10:30.0 XRT_CTRL_MANU_408_OG [0x198]

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2012/06/30	21:10:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/30	21:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/30	21:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/30	21:52:30.0	XRT_Custom_418_OG [0x1a2]								
2012/06/30	21:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/30	22:18:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/30	22:18:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00				
2012/06/30	22:20:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/06/30	22:21:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/06/30	22:21:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/06/30	22:21:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/06/30	22:21:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/06/30	22:21:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/30	22:21:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2012/06/30	22:21:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2012/06/30	22:21:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/06/30	22:49:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/06/30	22:49:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/06/30	22:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/06/30	22:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/06/30	23:24:00.5	XRT_Custom_418_OG [0x1a2]								
2012/06/30	23:25:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/01	00:27:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/01	00:27:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/01	00:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/01	00:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/01	00:57:30.0	XRT_Custom_418_OG [0x1a2]								
2012/07/01	00:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/01	02:06:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/01	02:06:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/01	02:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/01	02:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/01	02:36:00.0	XRT_Custom_418_OG [0x1a2]								
2012/07/01	02:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/01	03:36:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/01	03:36:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/01	03:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/01	03:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/01	04:14:30.0	XRT_Custom_418_OG [0x1a2]								
2012/07/01	04:15:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/01	05:14:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/01	05:14:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/01	05:14:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/01	05:17:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/01	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/01	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/07/01	06:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00				
2012/07/01	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/07/01	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]								

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2012/07/01	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
			MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/01	06:02:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2012/07/01	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/01	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/01	06:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00
2012/07/01	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/07/01	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/07/01	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/07/01	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/07/01	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/01	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/01	06:12:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2012/07/01	06:12:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2012/07/01	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/01	06:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/01	06:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/01	06:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/01	06:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/01	07:31:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/01	07:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/01	07:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/01	08:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	04 00 00 00 00
2012/07/01	11:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00
2012/07/01	16:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/01	17:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00
2012/07/01	17:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/07/01	17:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/07/01	17:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/07/01	17:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/07/01	17:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/01	17:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/01	17:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2012/07/01	17:02:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2012/07/01	17:38:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/01	17:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/01	17:55:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/01	17:55:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/07/01	17:56:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00
2012/07/01	17:56:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/07/01	17:56:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/07/01	17:56:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/01	17:58:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2012/07/01	17:59:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/01	18:05:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/01	18:06:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00
2012/07/01	18:08:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/07/01	18:08:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8

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2012/07/01	18:08:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/07/01	18:08:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/07/01	18:08:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/01	18:08:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/01	18:08:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2012/07/01	18:08:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2012/07/01	18:09:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/01	18:31:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/01	18:31:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/01	18:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/01	18:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/01	19:15:30.0	XRT_Custom_418_OG [0x1a2]			
2012/07/01	19:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/01	20:09:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/01	20:09:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/01	20:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/01	20:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/01	20:52:30.0	XRT_Custom_418_OG [0x1a2]			
2012/07/01	20:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/01	21:48:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/01	21:48:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/01	21:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/01	21:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/01	22:28:30.5	XRT_Custom_418_OG [0x1a2]			
2012/07/01	22:29:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/01	23:26:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/01	23:26:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/01	23:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/01	23:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/01	23:56:30.0	XRT_Custom_418_OG [0x1a2]			
2012/07/01	23:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/02	01:05:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/02	01:05:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/02	01:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/02	01:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/02	01:35:00.0	XRT_Custom_418_OG [0x1a2]			
2012/07/02	01:36:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/02	02:43:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/02	02:43:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/02	02:43:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/02	02:46:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/02	03:13:30.0	XRT_Custom_418_OG [0x1a2]			
2012/07/02	03:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/02	04:12:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/02	04:12:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/02	04:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/02	04:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/02	04:52:00.0	XRT_Custom_418_OG [0x1a2]			
2012/07/02	04:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/02	05:25:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1



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2012/07/02	05:25:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/07/02	05:25:30.0	AOCs_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/07/02	05:25:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/07/02	05:25:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/07/02	05:25:50.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/02	05:28:28.0	XRT_QT_PROG_SET_435_OG [0x1b3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2012/07/02	05:28:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/02	05:35:24.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/02	05:35:30.0	AOCs_OrE-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/07/02	05:37:56.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/07/02	05:38:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/07/02	05:38:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/07/02	05:38:20.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/07/02	05:38:22.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/02	05:38:24.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/02	05:38:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2012/07/02	05:38:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2012/07/02	05:38:30.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/02	05:52:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/02	05:52:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/02	05:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/02	05:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/02	06:30:30.0	XRT_Custom_418_OG [0x1a2]			
2012/07/02	06:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/02	07:33:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/02	07:33:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/02	07:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/02	07:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/02	08:00:00.0	AOCs_OrE-point_Start_6_OG [0x09c]			
		AOCU_NM	5	02-76	02 00 00 00 00
2012/07/02	11:00:00.0	AOCs_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/07/02	16:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/02	17:00:00.0	AOCs_OrE-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/07/02	17:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/07/02	17:02:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/07/02	17:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/07/02	17:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/07/02	17:02:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/02	17:02:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/02	17:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2012/07/02	17:02:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2012/07/02	17:03:00.5	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/02	17:30:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/02	17:30:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/02	17:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/02	17:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/02	18:15:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/02	18:15:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00

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2012/07/02	18:15:30.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00
2012/07/02	18:15:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/07/02	18:15:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/07/02	18:15:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/02	18:18:28.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2012/07/02	18:18:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/02	18:24:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/02	18:25:30.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00
2012/07/02	18:26:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/07/02	18:27:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/07/02	18:27:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/07/02	18:27:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/07/02	18:27:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/02	18:27:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/02	18:27:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2012/07/02	18:27:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2012/07/02	18:27:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/02	19:08:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/02	19:08:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/02	19:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/02	19:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/02	19:52:00.0	XRT_Custom_418_OG [0x1a2]				
2012/07/02	19:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/02	20:47:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/02	20:47:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/02	20:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/02	20:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/02	21:29:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/02	21:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/02	22:25:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/02	22:25:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/02	22:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/02	22:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/02	23:04:00.0	XRT_Custom_418_OG [0x1a2]				
2012/07/02	23:05:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/03	00:04:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/03	00:04:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/03	00:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/03	00:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/03	00:34:00.0	XRT_Custom_418_OG [0x1a2]				
2012/07/03	00:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/03	01:42:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/03	01:42:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/03	01:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/03	01:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/03	02:12:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/03	02:13:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/03	03:20:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/03	03:20:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da

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2012/07/03	03:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/03	03:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/03	03:51:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/03	03:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/03	04:50:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/03	04:50:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/03	04:50:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/03	04:53:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/03	05:29:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/03	05:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/03	06:00:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/03	06:00:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/07/03	06:01:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/07/03	06:01:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/07/03	06:01:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/07/03	06:01:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/03	06:03:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2012/07/03	06:04:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/03	06:10:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/03	06:10:56.0	XRT_FOCUS_RECALIBRATE_429_OG [0x1ad]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2012/07/03	06:11:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/07/03	06:14:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/07/03	06:15:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/07/03	06:15:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/07/03	06:15:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/07/03	06:15:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/03	06:15:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/03	06:15:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2012/07/03	06:15:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2012/07/03	06:15:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/03	06:31:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/03	06:31:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/03	06:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/03	06:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/03	07:08:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/03	07:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/03	07:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/03	08:00:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	02 00 00 12 a7				
2012/07/03	10:25:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				