

XRT Timeline to be uploaded on 2012/07/03

Period: 2012/07/03 10:15:00 - 2012/07/07 09:28:00

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Normal mode

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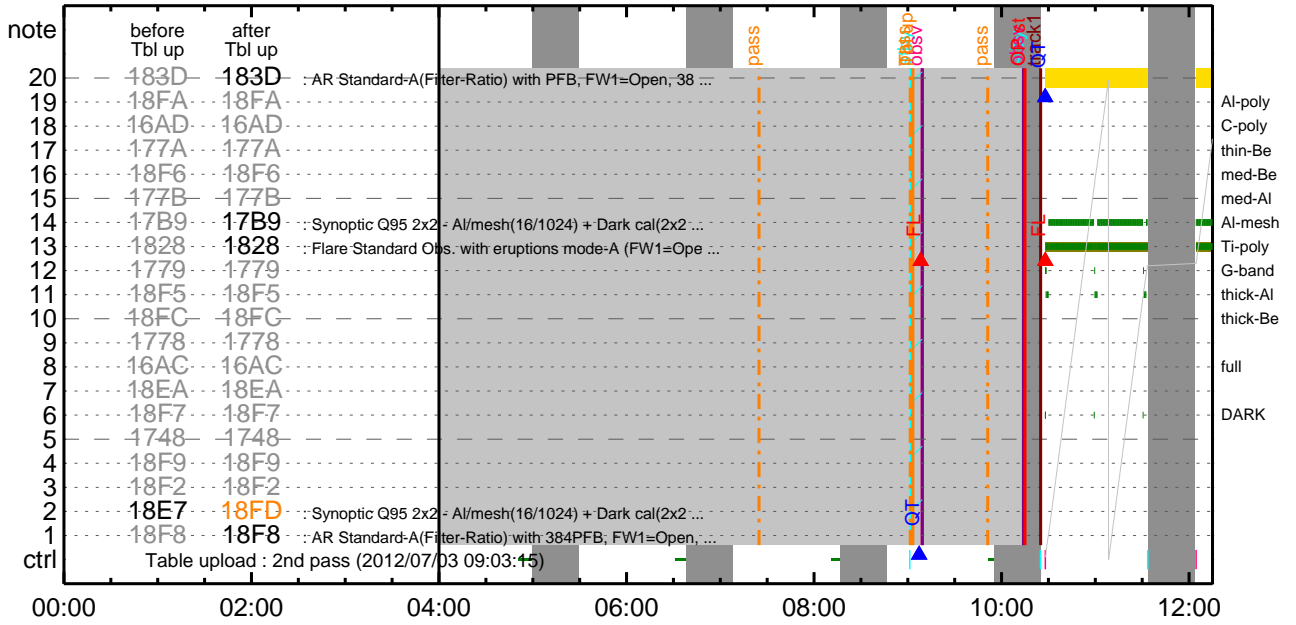
XOB #183D: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 100s cad												
Term	Pointing (x, y)					Comment						
07/03 10:28:00 - 07/03 17:52:24	Track (234.4, 163.3)	[Ⓜ] 07/03 10:25:00	# OP start + 10min, Obs. AR11513									
07/03 18:53:00 - 07/04 06:12:24	Track (299.2, 163.9)	[Ⓜ] 07/03 18:02:30	Obs. AR11513									
07/04 06:25:30 - 07/04 17:06:00	Track (400.4, 165.3)	[Ⓜ] 07/04 06:22:30	Obs. AR11513									
07/04 18:07:00 - 07/05 05:38:54	Track (286.3, -359.1)	[Ⓜ] 07/04 18:04:00	Obs. AR11515									
07/05 05:52:00 - 07/05 09:27:00	Track (379.0, -357.6)	[Ⓜ] 07/05 05:49:00	Obs. AR11515									
PROG= 20 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 1 4-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 49 18-time(s) 2.0sec												
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec												
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #18FD: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)												
Term	Pointing (x, y)					Comment						
07/03 17:55:30 - 07/03 18:02:24	Fixed (0.0, 0.0)		synoptic, shifted -7.5 min									
PROG= 02 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 7 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 8 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 4 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 59 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 75 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

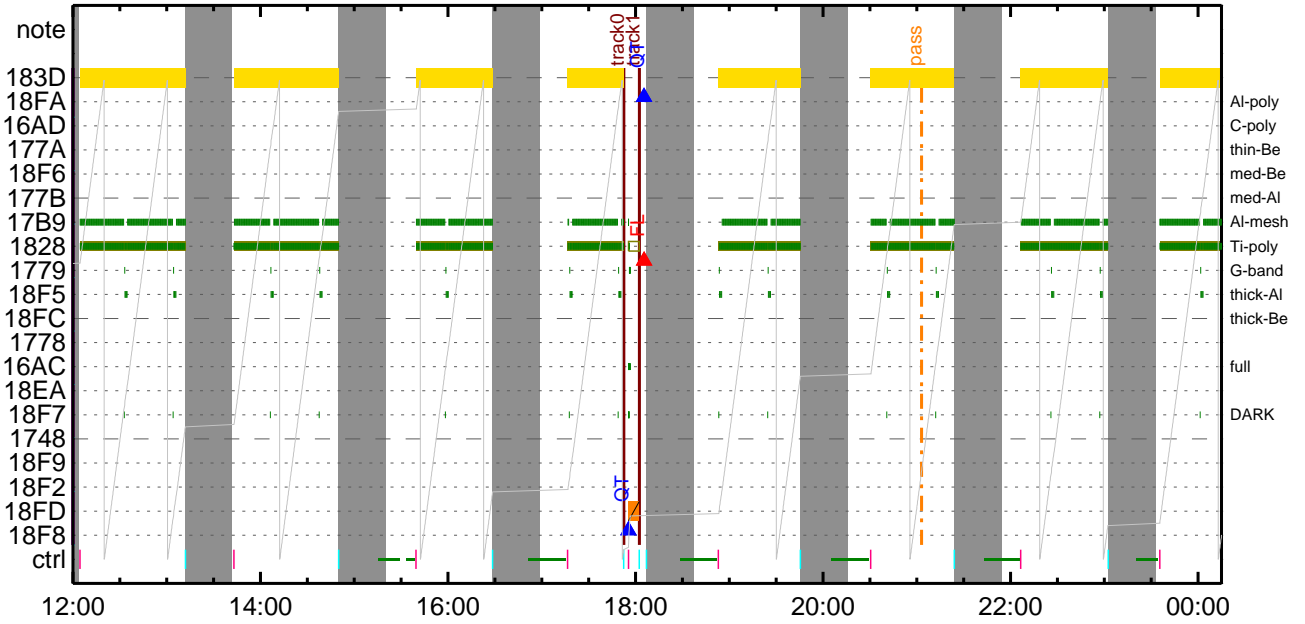
XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)												
Term	Pointing (x, y)					Comment						
07/04 06:15:30 - 07/04 06:22:24	Fixed (0.0, 0.0)		synoptic, shifted 12.5 min									
07/04 17:57:00 - 07/04 18:03:54	Fixed (0.0, 0.0)		synoptic, shifted -6.0 min									
07/05 05:42:00 - 07/05 05:48:54	Fixed (0.0, 0.0)		synoptic, shifted -21.0 min									
PROG= 14 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 7 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 8 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

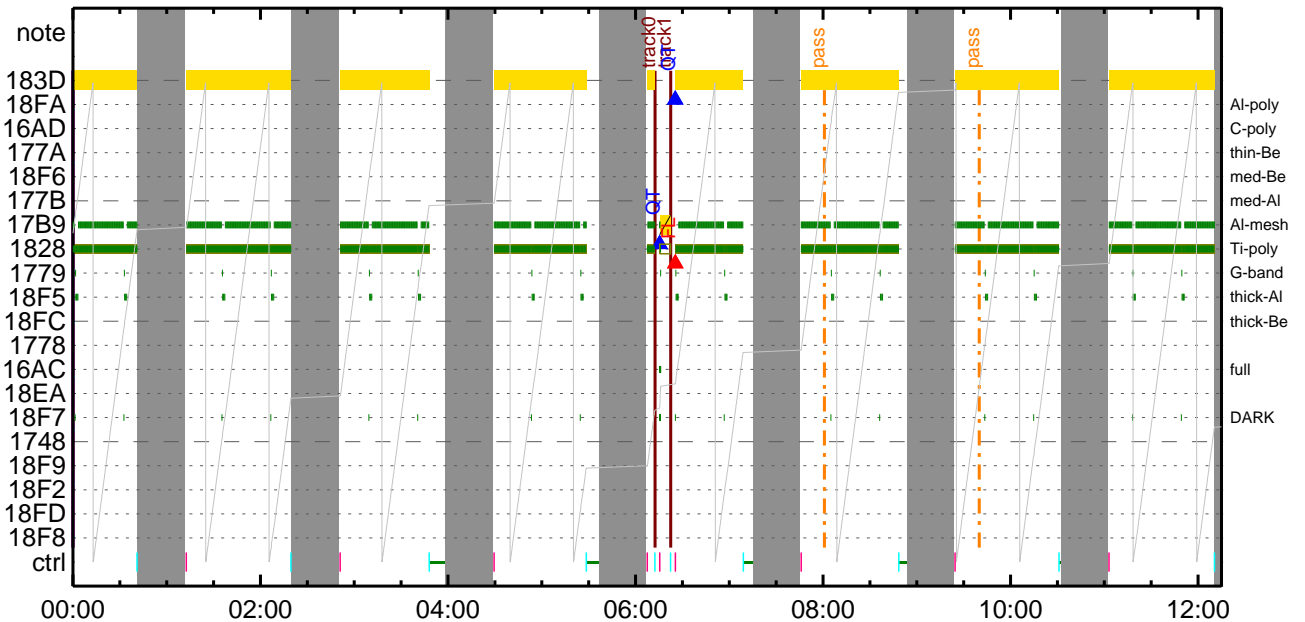
CMDI #0744 2012/07/03



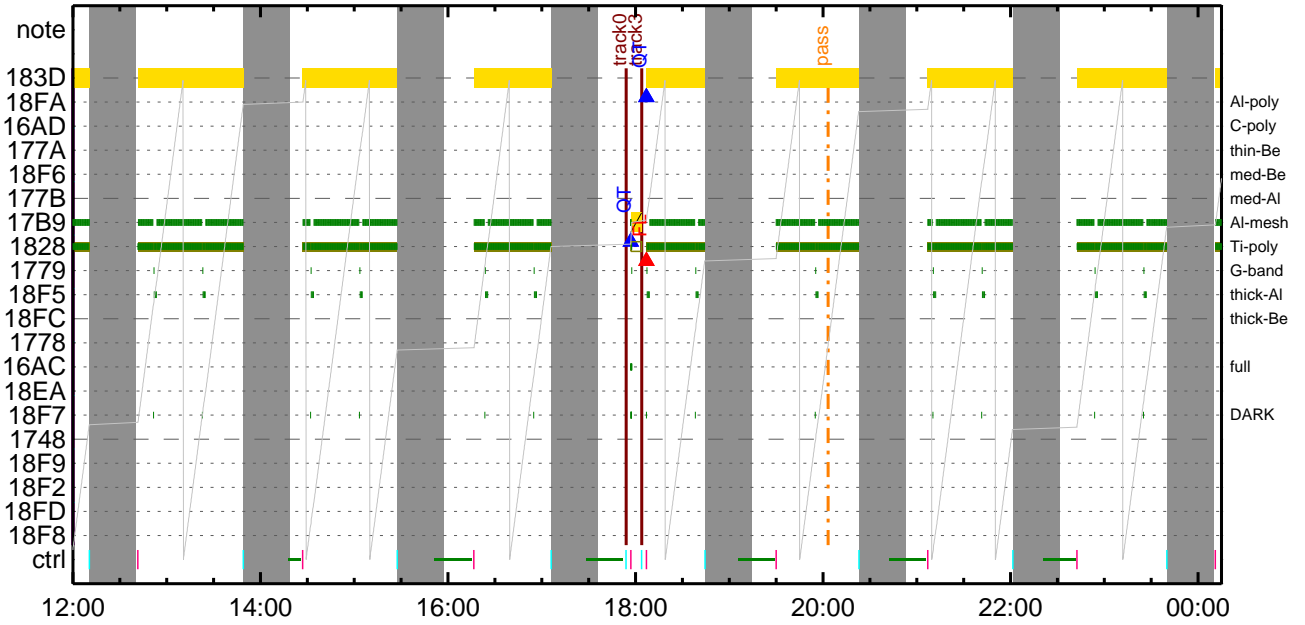
CMDI #0744 2012/07/03



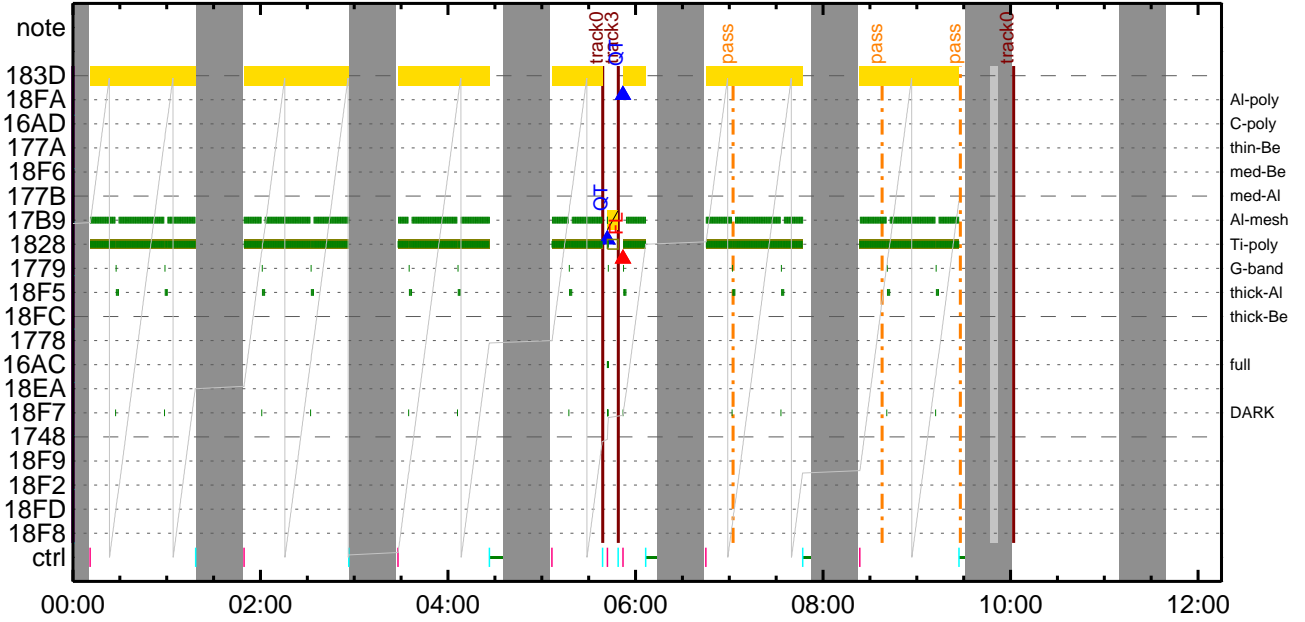
CMDI #0744 2012/07/04



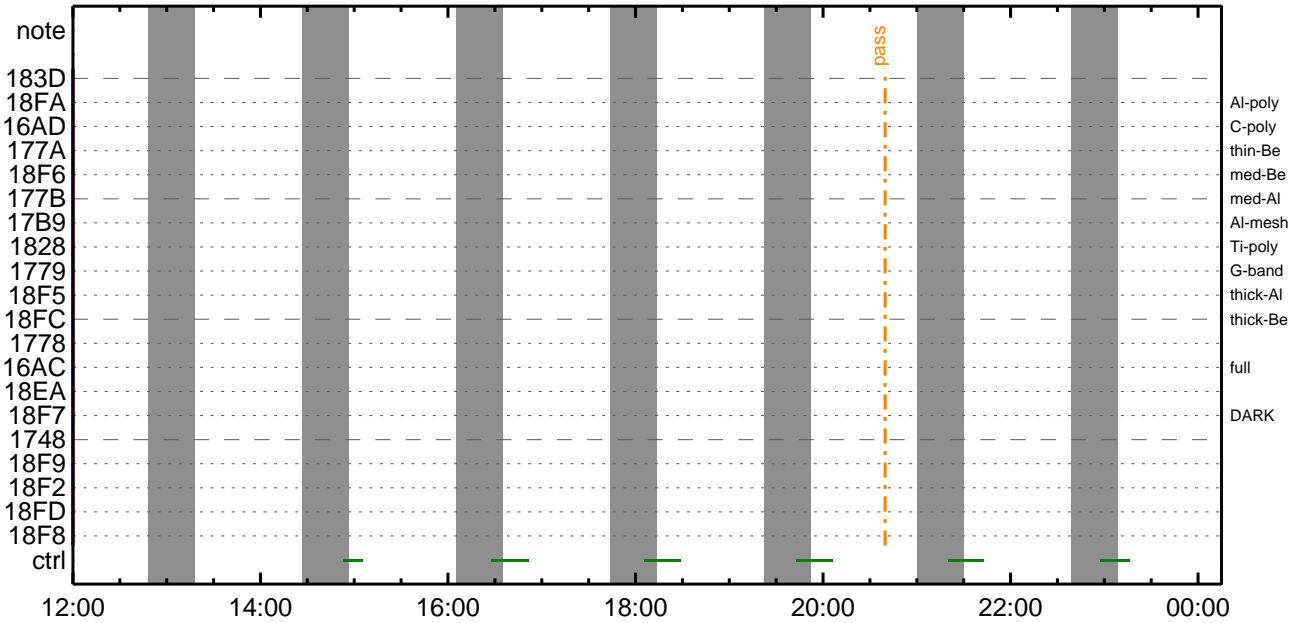
CMDI #0744 2012/07/04



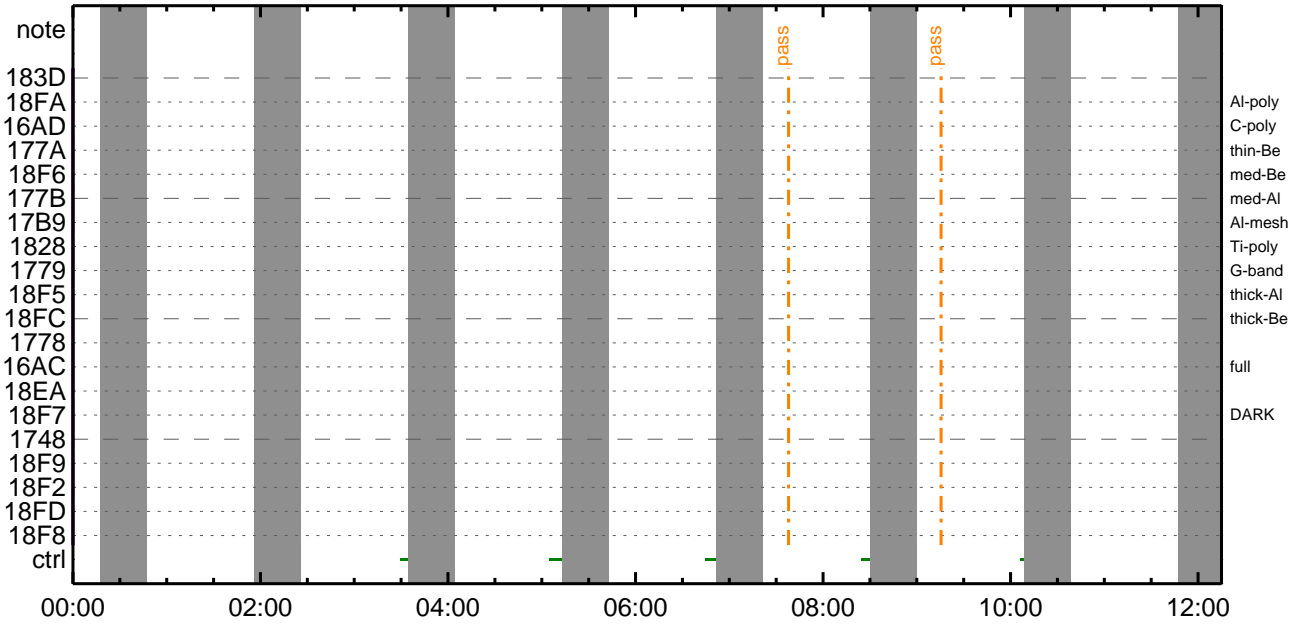
CMDI #0744 2012/07/05



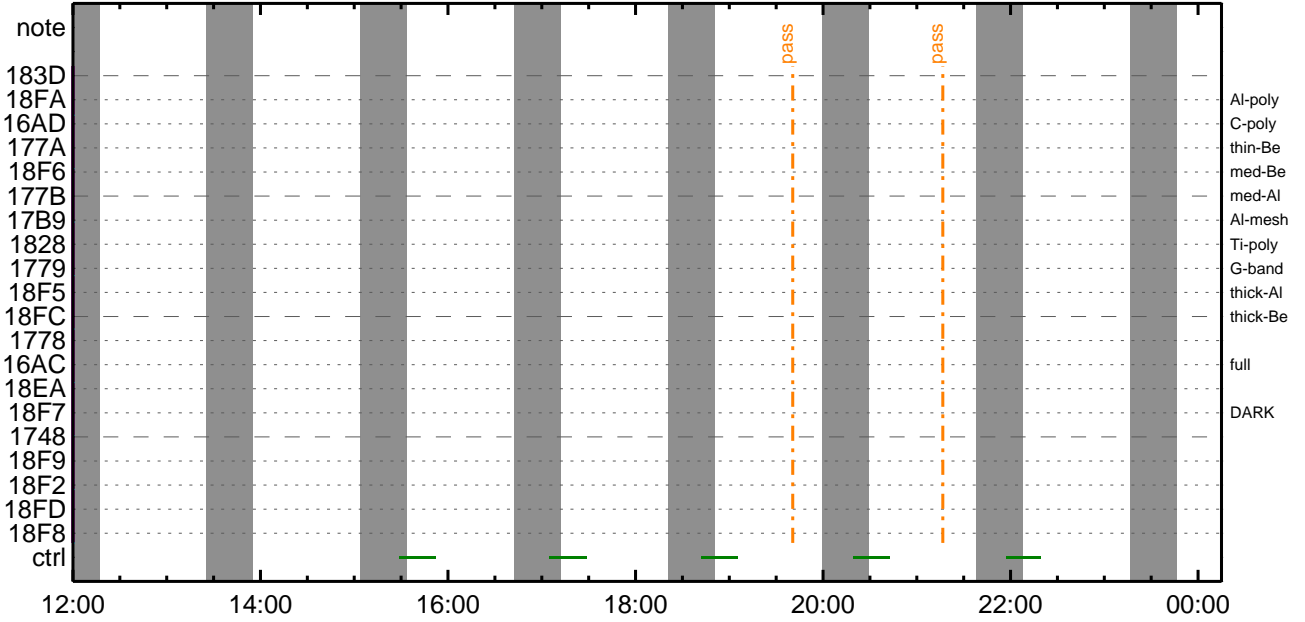
CMDI #0744 2012/07/05



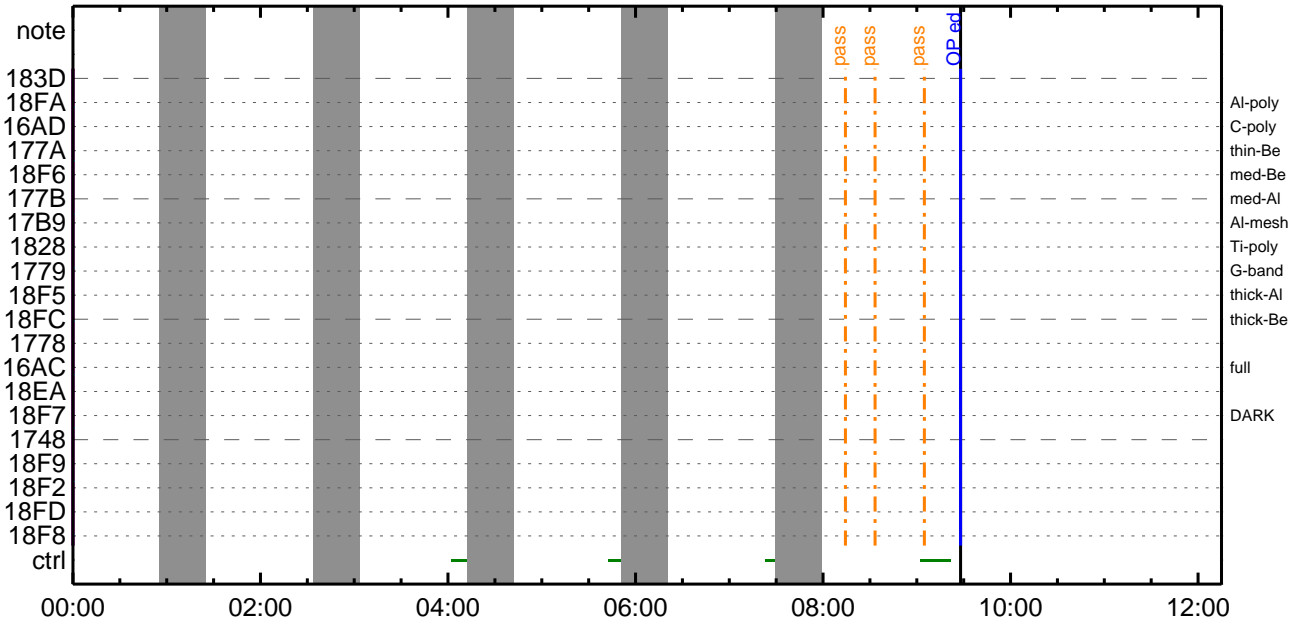
CMDI #0744 2012/07/06



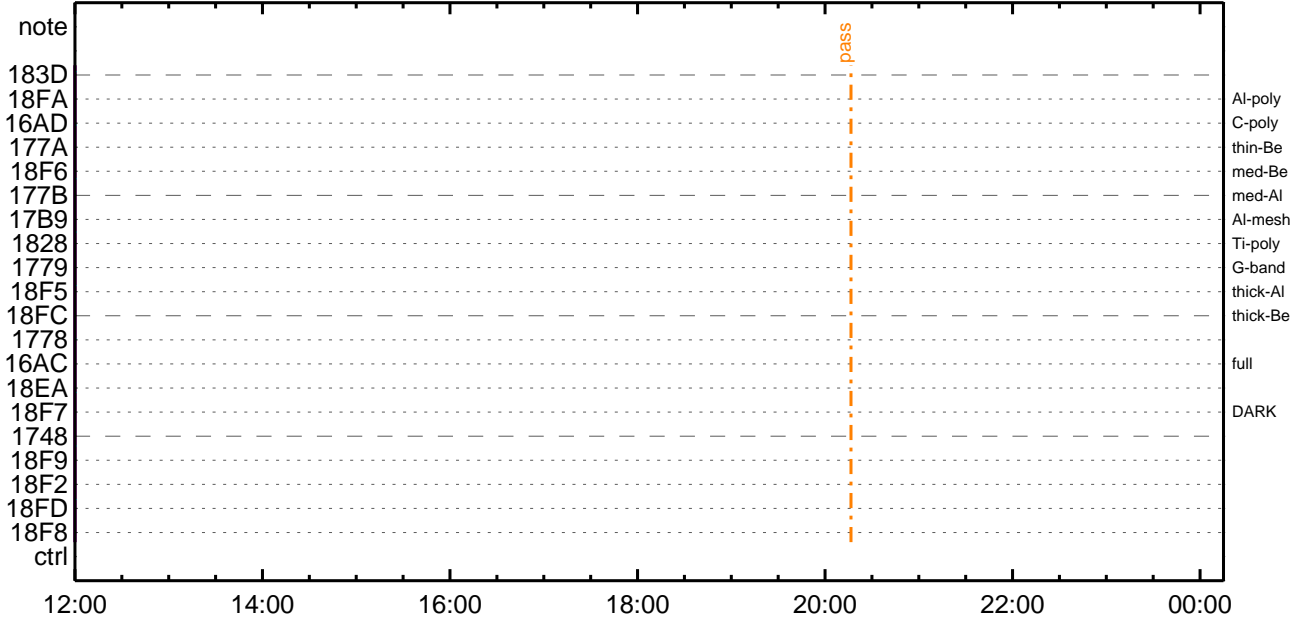
CMDI #0744 2012/07/06



CMDI #0744 2012/07/07



CMDI #0744 2012/07/07




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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 5810391.5 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 C.
0139 C. ***** XRT START *****
0140 C.
0141 +. DC 07-F0 MDP_XRT_CTRL_MANU
0142 BC (c1)
0143 + DC 07-F0 MDP_XRT_MODE_STBY
0144 BC (c3)
0145 . C. ----- Success Verify ? OK / NG_____
0146 C.
0147 C. XRT Obs. Table Upload
0148 . S. RAM ram-291:MDP_OBS_X
0149 ( )
0150 C.
0151 +. DC 07-F0 MDP_DUMP_XRTTBL
0152 BC (84 07 00 00 00 3a d4)
0153 . C. ----- Comparison Check ? OK / ERR _____
0154 C.
0155 C.
0156 +. DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 01 b1 b1 04 04)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 02 b1 b1 08 08)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 03 b1 b1 08 08)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 04 b1 b1 06 06)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 05 85 83 06 06)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 06 85 83 08 08)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 07 85 83 06 06)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 08 80 80 20 20)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 09 80 80 20 08)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 0a 80 80 08 20)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 0b 80 80 10 10)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 0f 80 80 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 10 80 80 08 08)
0182 + DC 07-F0 MDP_XRT_FLD_DIS
0183 BC (d9)
0184 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0185 BC (c9)
0186 + DC 07-F0 MDP_XRT_AEC_RESET
0187 BC (d0)
0188 + DC 07-F0 MDP_XRT_ARS_DIS
0189 BC (d5)
0190 + DC 07-F0 MDP_XRT_FLD_RESET
0191 BC (da)
0192 + DC 07-F0 MDP_XRT_QT_PROG_SET
0193 BC (c4 01)

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0096 (MDP_known_event)
0097 C.
0098 C.
0099 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0100 . S. DC-BC dcbc-153:DCBC
0101 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0102 C.
0103 C.
0104 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0105 C.
0106 . C. ***** LOS *****
0107 C.
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*** OP Sequence for XRT ***

2012/07/03	10:24:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/03	10:25:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/07/03	10:27:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/07/03	10:27:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/07/03	10:27:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/07/03	10:27:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/07/03	10:27:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/03	10:27:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/03	10:27:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2012/07/03	10:27:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2012/07/03	10:28:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/03	11:33:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/03	11:33:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/03	11:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/03	11:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/03	12:03:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/03	12:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/03	13:12:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/03	13:12:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/03	13:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/03	13:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/03	13:42:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/03	13:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/03	14:50:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/03	14:50:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/03	14:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/03	14:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/03	15:38:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/03	15:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/03	16:28:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/03	16:28:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/03	16:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/03	16:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/03	17:15:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/03	17:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/03	17:52:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/03	17:52:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/07/03	17:52:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/07/03	17:52:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/07/03	17:52:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/07/03	17:52:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/03	17:55:28.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2012/07/03	17:55:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/03	18:02:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/03	18:02:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/07/03	18:04:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/07/03	18:05:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/07/03	18:05:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							

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2012/07/03	18:05:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2012/07/03	18:05:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/03	18:05:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/03	18:05:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2012/07/03	18:05:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2012/07/03	18:07:00.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/03	18:07:02.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/03	18:07:04.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/03	18:10:14.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/03	18:52:00.0	XRT_Custom_418_OG [0x1a2]				
2012/07/03	18:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/03	19:45:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/03	19:45:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/03	19:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/03	19:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/03	20:29:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/03	20:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/03	21:24:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/03	21:24:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/03	21:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/03	21:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/03	22:05:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/03	22:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/03	23:02:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/03	23:02:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/03	23:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/03	23:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/03	23:34:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/03	23:35:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/04	00:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/04	00:41:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/04	00:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/04	00:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/04	01:11:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/04	01:12:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/04	02:19:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/04	02:19:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/04	02:19:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/04	02:22:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/04	02:50:00.0	XRT_Custom_418_OG [0x1a2]				
2012/07/04	02:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/04	03:48:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/04	03:48:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/04	03:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/04	03:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/04	04:28:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/04	04:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/04	05:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/04	05:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/04	05:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				

2012/07/04	05:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/04	06:06:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/04	06:07:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/04	06:12:24.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/04	06:12:26.0	XRT_FOCUS_POSITION_401_OG [0x191]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/07/04	06:12:30.0	AOCS_ORe-point_Start_2_OG [0x098]				
			AOCU_NM	5	02-76	00 00 00 00 00
2012/07/04	06:12:46.0	XRT_FLD_DIS_402_OG [0x192]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2012/07/04	06:12:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/07/04	06:12:50.0	XRT_ARS_DIS_438_OG [0x1b6]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/04	06:15:28.0	XRT_QT_PROG_SET_435_OG [0x1b3]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2012/07/04	06:15:30.0	XRT_CTRL_AUTO_406_OG [0x196]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/04	06:22:24.0	XRT_CTRL_MANU_439_OG [0x1b7]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/04	06:22:30.0	AOCS_ORe-point_Start_1_OG [0x097]				
			AOCU_NM	5	02-76	01 00 00 00 00
2012/07/04	06:24:56.0	XRT_FOCUS_POSITION_409_OG [0x199]				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/07/04	06:25:16.0	XRT_FLD_ENA_411_OG [0x19b]				
			MDP_XRT_FLD_ENA	1	07-F0	d8
2012/07/04	06:25:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/07/04	06:25:20.0	XRT_AEC_RESET_443_OG [0x1bb]				
			MDP_XRT_AEC_RESET	1	07-F0	d0
2012/07/04	06:25:22.0	XRT_ARS_DIS_431_OG [0x1af]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/04	06:25:24.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/04	06:25:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2012/07/04	06:25:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2012/07/04	06:25:30.0	XRT_CTRL_AUTO_406_OG [0x196]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/04	07:09:00.0	XRT_CTRL_MANU_408_OG [0x198]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/04	07:09:02.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/04	07:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/04	07:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/04	07:45:00.0	XRT_Custom_418_OG [0x1a2]				
2012/07/04	07:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/04	08:48:30.0	XRT_CTRL_MANU_408_OG [0x198]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/04	08:48:32.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/04	08:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/04	08:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/04	09:23:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/04	09:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/04	10:31:00.0	XRT_CTRL_MANU_408_OG [0x198]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/04	10:31:02.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/04	10:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/04	10:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/04	11:02:00.0	XRT_Custom_418_OG [0x1a2]				
2012/07/04	11:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/04	12:10:30.0	XRT_CTRL_MANU_408_OG [0x198]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/04	12:10:32.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/04	12:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/04	12:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/04	12:40:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/04	12:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/04	13:49:00.0	XRT_CTRL_MANU_408_OG [0x198]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/04	13:49:02.0	XRT_FLD_RESET_412_OG [0x19c]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/04	13:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]				

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2012/07/04	13:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/04	14:26:00.0	XRT_Custom_418_OG [0x1a2]								
2012/07/04	14:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/04	15:27:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/04	15:27:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/04	15:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/04	15:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/04	16:15:30.0	XRT_Custom_418_OG [0x1a2]								
2012/07/04	16:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/04	17:06:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/04	17:06:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/04	17:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/04	17:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/04	17:53:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/04	17:53:56.0	XRT_FOCUS_POSITION_401_OG [0x191]								
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/07/04	17:54:00.0	AOCS_Ore-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	00 00 00 00 00				
2012/07/04	17:54:16.0	XRT_FLD_DIS_402_OG [0x192]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/07/04	17:54:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/07/04	17:54:20.0	XRT_ARS_DIS_438_OG [0x1b6]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/04	17:56:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2012/07/04	17:57:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/04	18:03:54.0	XRT_CTRL_MANU_439_OG [0x1b7]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/04	18:04:00.0	AOCS_Ore-point_Start_3_OG [0x099]								
			AOCU_NM	5	02-76	03 00 00 00 00				
2012/07/04	18:06:26.0	XRT_FOCUS_POSITION_409_OG [0x199]								
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/07/04	18:06:46.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/07/04	18:06:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/07/04	18:06:50.0	XRT_AEC_RESET_443_OG [0x1bb]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/07/04	18:06:52.0	XRT_ARS_DIS_431_OG [0x1af]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/04	18:06:54.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/04	18:06:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2012/07/04	18:06:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2012/07/04	18:07:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/04	18:44:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/04	18:44:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/04	18:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/04	18:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/04	19:29:00.0	XRT_Custom_418_OG [0x1a2]								
2012/07/04	19:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/04	20:23:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/04	20:23:02.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/04	20:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/04	20:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/04	21:06:00.0	XRT_Custom_418_OG [0x1a2]								
2012/07/04	21:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/04	22:01:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/04	22:01:32.0	XRT_FLD_RESET_412_OG [0x19c]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/04	22:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/04	22:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2012/07/04	22:41:30.0	XRT_Custom_418_OG [0x1a2]						
2012/07/04	22:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/04	23:40:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/04	23:40:02.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/04	23:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/04	23:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/07/05	00:10:00.0	XRT_Custom_418_OG [0x1a2]						
2012/07/05	00:11:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/05	01:18:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/05	01:18:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/05	01:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/05	01:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/07/05	01:48:30.0	XRT_Custom_418_OG [0x1a2]						
2012/07/05	01:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/05	02:56:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/05	02:56:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/05	02:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/05	02:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/07/05	03:27:00.0	XRT_Custom_418_OG [0x1a2]						
2012/07/05	03:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/05	04:26:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/05	04:26:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/05	04:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/05	04:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/07/05	05:05:30.0	XRT_Custom_418_OG [0x1a2]						
2012/07/05	05:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/05	05:38:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/05	05:38:56.0	XRT_FOCUS_POSITION_401_OG [0x191]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/07/05	05:39:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2012/07/05	05:39:16.0	XRT_FLD_DIS_402_OG [0x192]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/07/05	05:39:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/07/05	05:39:20.0	XRT_ARS_DIS_438_OG [0x1b6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/07/05	05:41:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2012/07/05	05:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/05	05:48:54.0	XRT_CTRL_MANU_439_OG [0x1b7]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/05	05:49:00.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	03 00 00 00 00			
2012/07/05	05:51:26.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/07/05	05:51:46.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/07/05	05:51:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/07/05	05:51:50.5	XRT_AEC_RESET_443_OG [0x1bb]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/07/05	05:51:52.5	XRT_ARS_DIS_431_OG [0x1af]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/07/05	05:51:54.5	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/05	05:51:56.5	XRT_QT_PROG_SET_445_OG [0x1bd]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2012/07/05	05:51:58.5	XRT_FL_PROG_SET_449_OG [0x1c1]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2012/07/05	05:52:00.5	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/05	06:06:30.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/05	06:06:32.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/05	06:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/05	06:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			

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2012/07/05	06:44:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/05	06:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/05	07:47:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/05	07:47:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/05	07:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/05	07:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/05	08:22:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/05	08:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/05	09:27:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/05	09:27:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/05	09:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/05	09:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/05	10:02:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				