

XRT Timeline to be uploaded on 2012/07/07

Period: 2012/07/07 09:28:00 - 2012/07/12 10:56:00

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Normal mode

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XOB #183D: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 100s cad												
Term	Pointing (x, y)		Comment									
07/07 09:41:00 - 07/08 05:52:24	Track (719.0, -361.1) ^{Ⓢ 07/07 09:38:00}		# OP start + 10min, AR 1515 flare observations.									
07/08 06:05:30 - 07/09 06:01:54	Track (804.8, -352.1) ^{Ⓢ 07/08 06:02:30}		# AR cont.									
07/09 06:15:00 - 07/09 17:59:24	Track (867.0, -339.7) ^{Ⓢ 07/09 06:12:00}		# AR cont.									
07/09 18:12:30 - 07/10 05:28:54	Fixed (879.0, -333.0)		# AR cont. but switch to fixed pointing.									
07/10 05:42:00 - 07/10 09:00:00	Fixed (879.0, -333.0)		# AR cont.									
PROG= 20 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 1 4-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 49 18-time(s) 2.0sec												
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec												
└─ Open/Al-mesh Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec												
└─ Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18FF: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-band(12)												
Term	Pointing (x, y)		Comment									
07/08 05:55:30 - 07/08 06:02:24	Fixed (0.0, 0.0)		synoptic, shifted -7.5 min									
07/09 06:05:00 - 07/09 06:11:54	Fixed (0.0, 0.0)		synoptic, shifted 2.0 min									
07/09 18:02:30 - 07/09 18:09:24	Fixed (0.0, 0.0)		synoptic, shifted -0.5 min									
07/10 05:32:00 - 07/10 05:38:54	Fixed (0.0, 0.0)		synoptic, shifted -31.0 min									
PROG= 03 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 46 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 69 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 9 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term	Pointing (x, y)		Comment									
07/07 09:41:00 - 07/08 05:52:24	Track (719.0, -361.1) ^{Ⓢ 07/07 09:38:00}		# OP start + 10min, AR 1515 flare observations.									
07/08 06:05:30 - 07/09 06:01:54	Track (804.8, -352.1) ^{Ⓢ 07/08 06:02:30}		# AR cont.									
07/09 06:15:00 - 07/09 17:59:24	Track (867.0, -339.7) ^{Ⓢ 07/09 06:12:00}		# AR cont.									
07/09 18:12:30 - 07/10 05:28:54	Fixed (879.0, -333.0)		# AR cont. but switch to fixed pointing.									
07/10 05:42:00 - 07/10 09:00:00	Fixed (879.0, -333.0)		# AR cont.									
PROG= 13 1-time(s)												
└─ Subr= 1 30-time(s) 20.0sec												
└─ Seqn= 88 1-time(s) 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─ Seqn=100 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 90 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												

Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 88 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 88 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 88 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec												
Seqn= 89 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

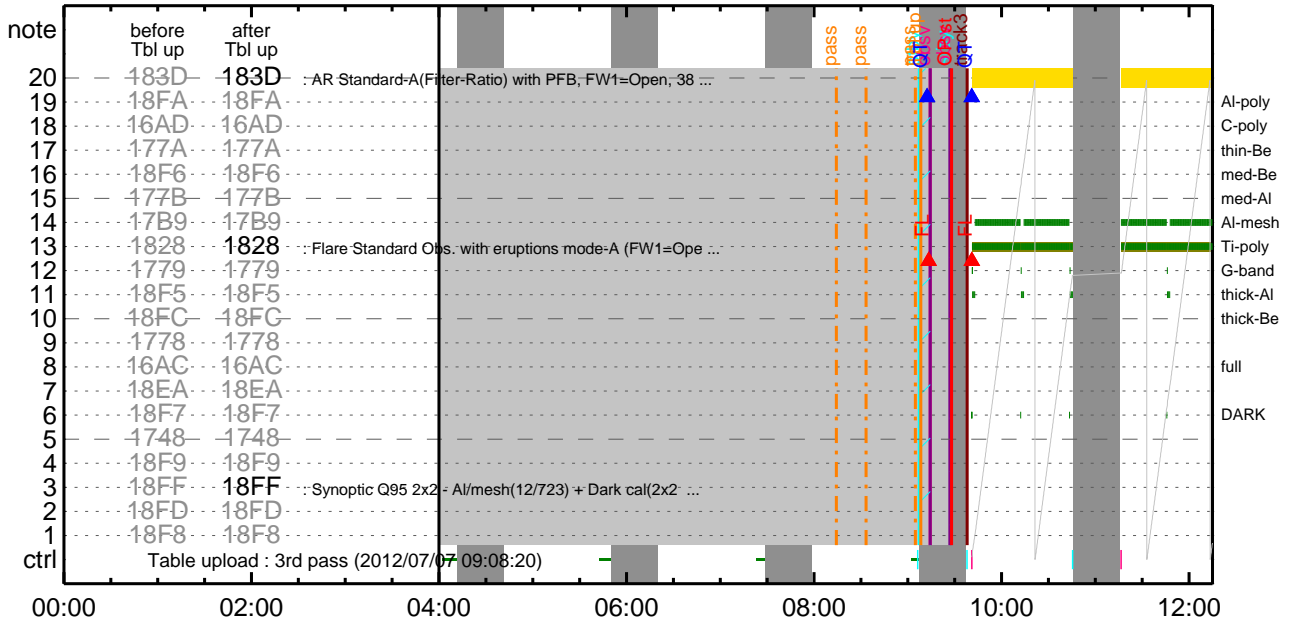
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Flare Detection

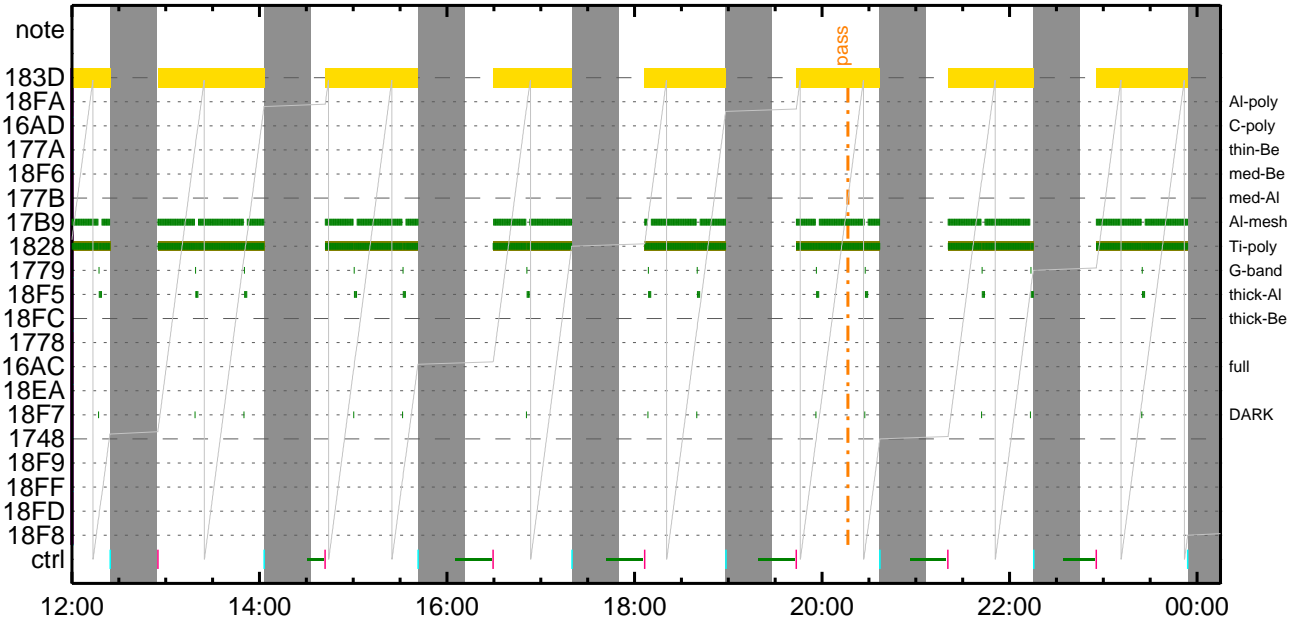
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FLD Patrol												
Term		Pointing (x, y)					Comment					
07/07 09:40:46 - 07/08 05:52:46	Track (719.0, -361.1)	© 07/07 09:38:00					# OP start + 10min, AR 1515 flare observations.					
07/08 06:05:16 - 07/09 06:02:16	Track (804.8, -352.1)	© 07/08 06:02:30					# AR cont.					
07/09 06:14:46 - 07/09 17:59:46	Track (867.0, -339.7)	© 07/09 06:12:00					# AR cont.					
07/09 18:12:16 - 07/10 05:29:16	Fixed (879.0, -333.0)						# AR cont. but switch to fixed pointing.					
07/10 05:41:46 - 07/12 10:56:00	Fixed (879.0, -333.0)						# AR cont.					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

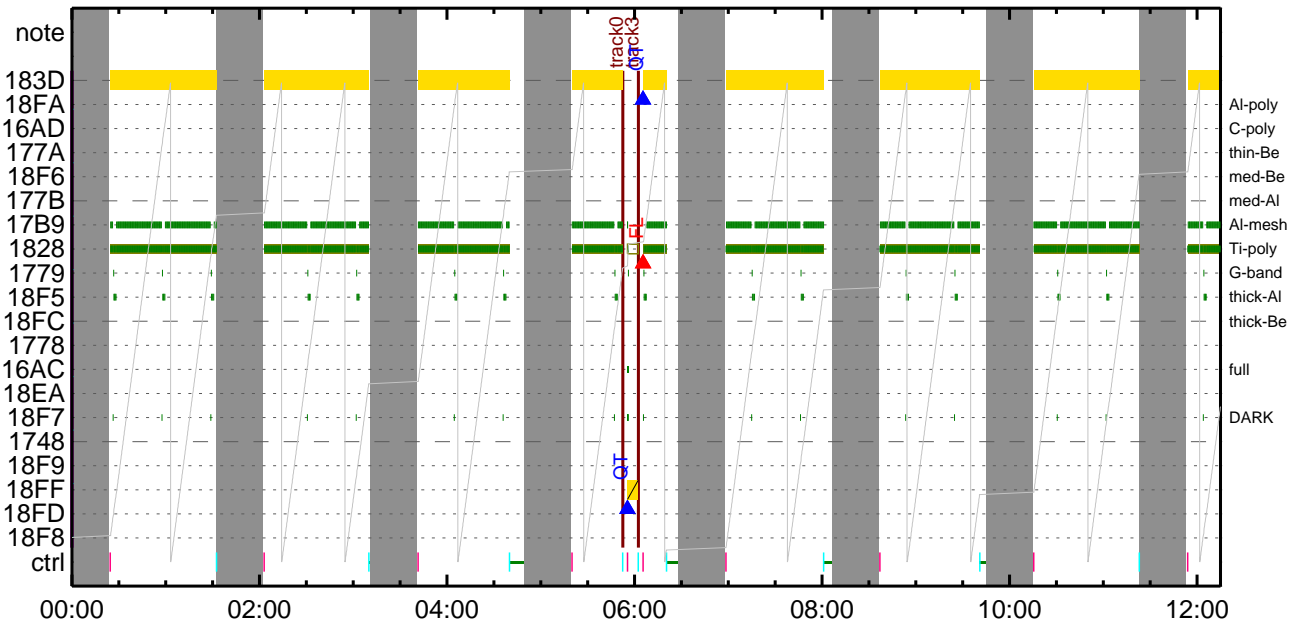
CMDI #0756 2012/07/07



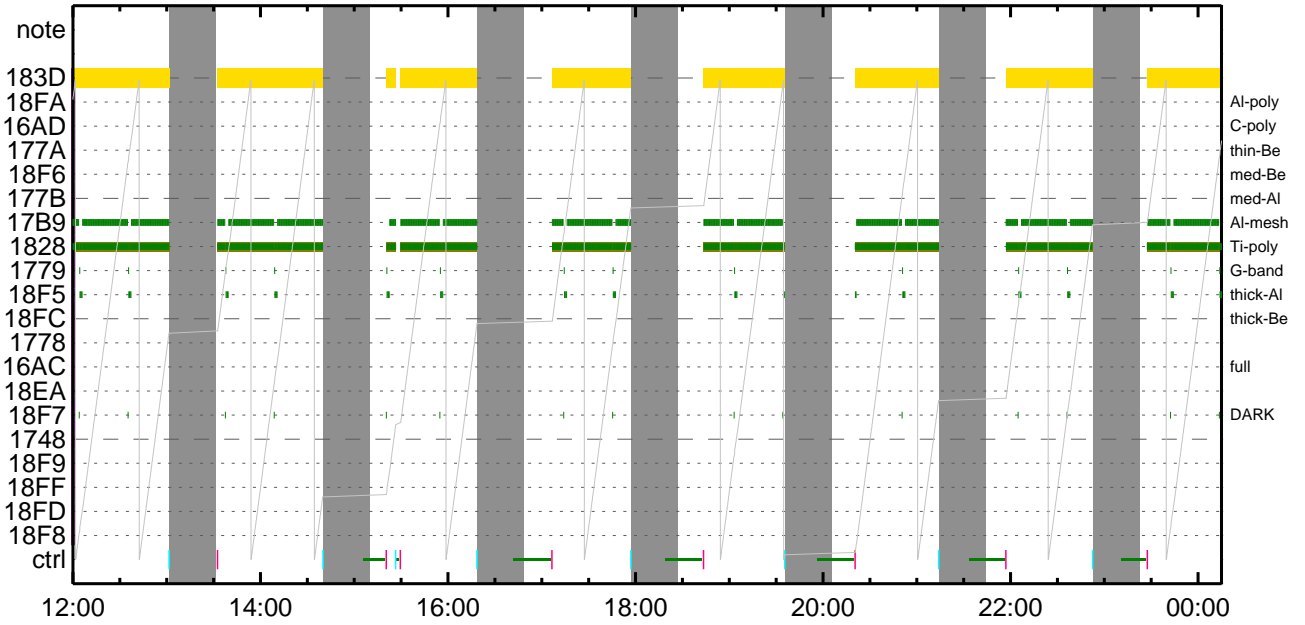
CMDI #0756 2012/07/07



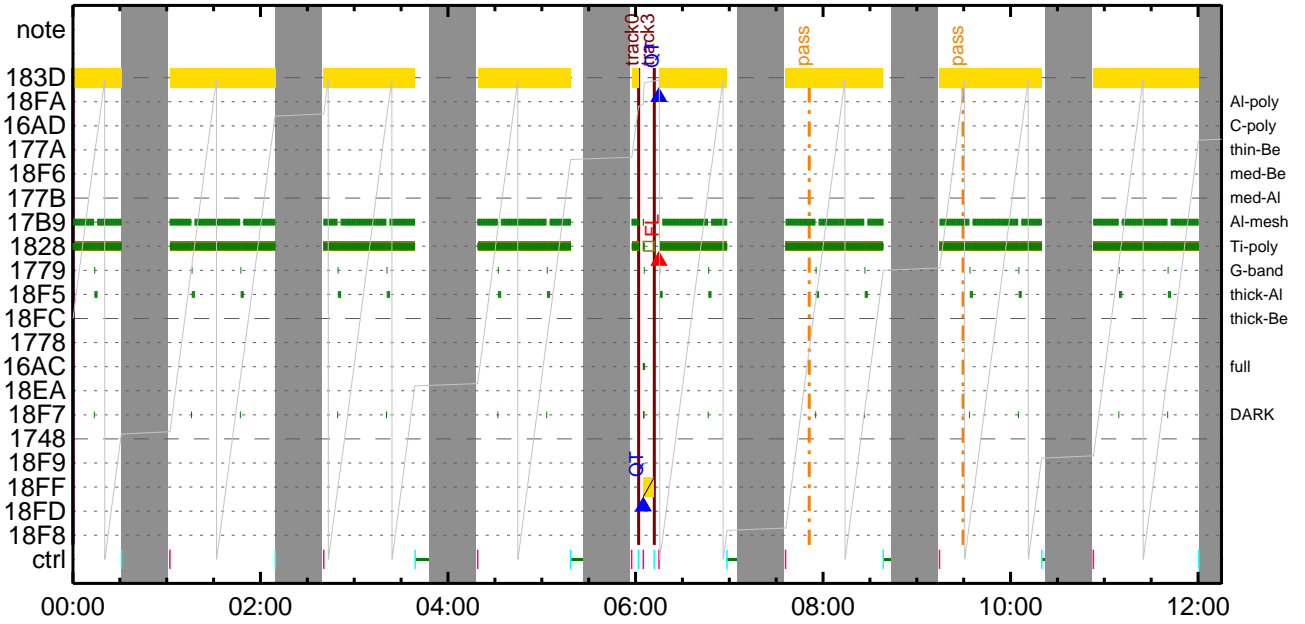
CMDI #0756 2012/07/08



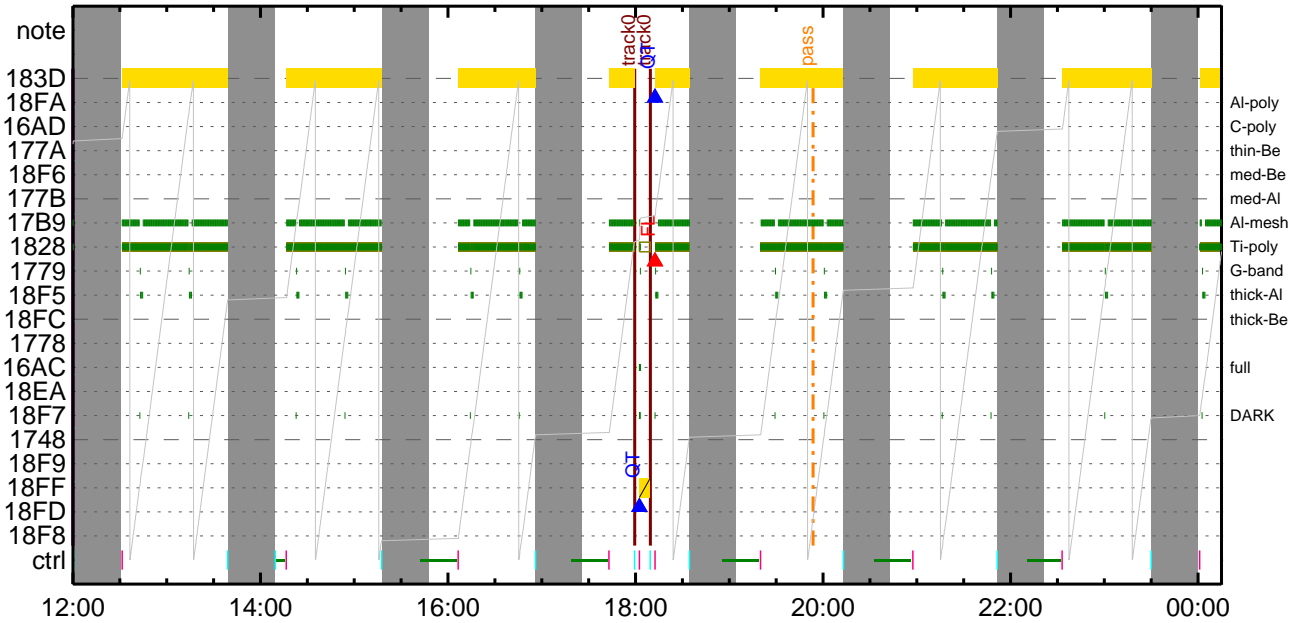
CMDI #0756 2012/07/08



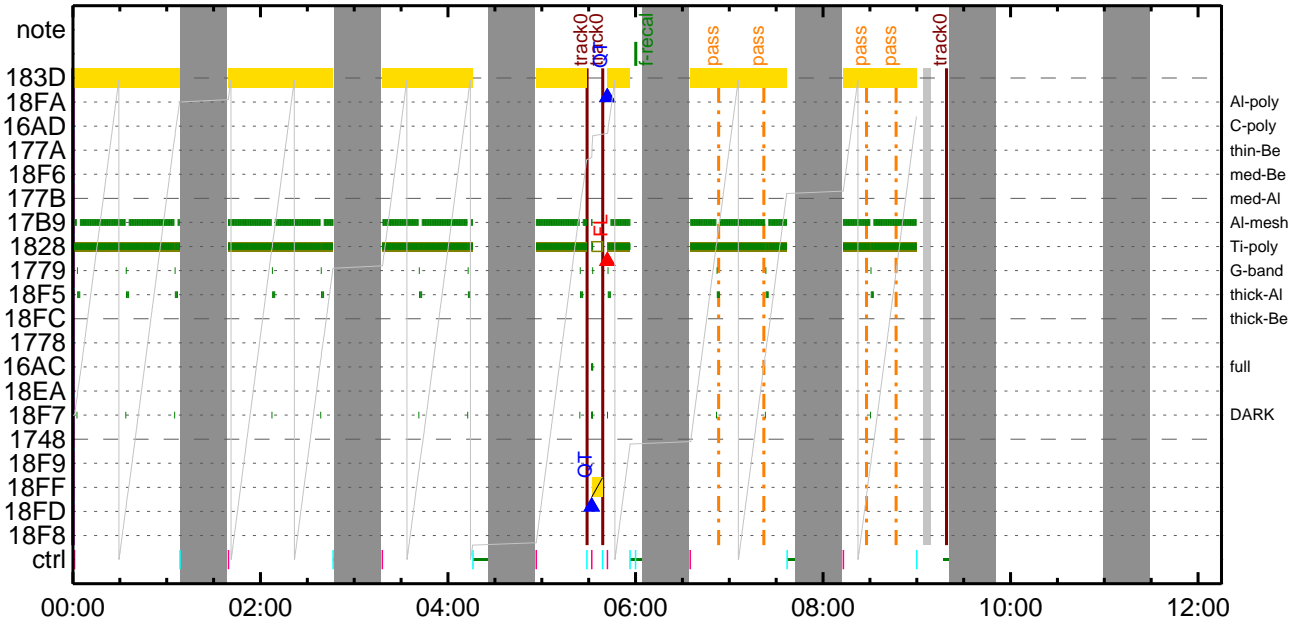
CMDI #0756 2012/07/09



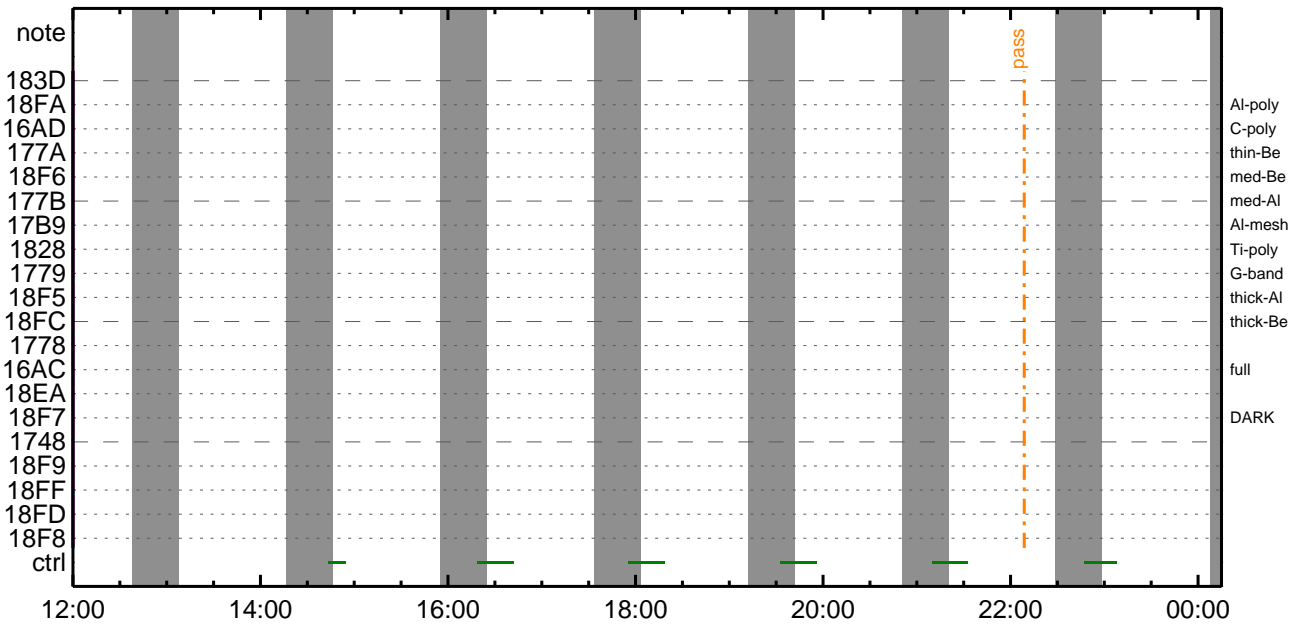
CMDI #0756 2012/07/09



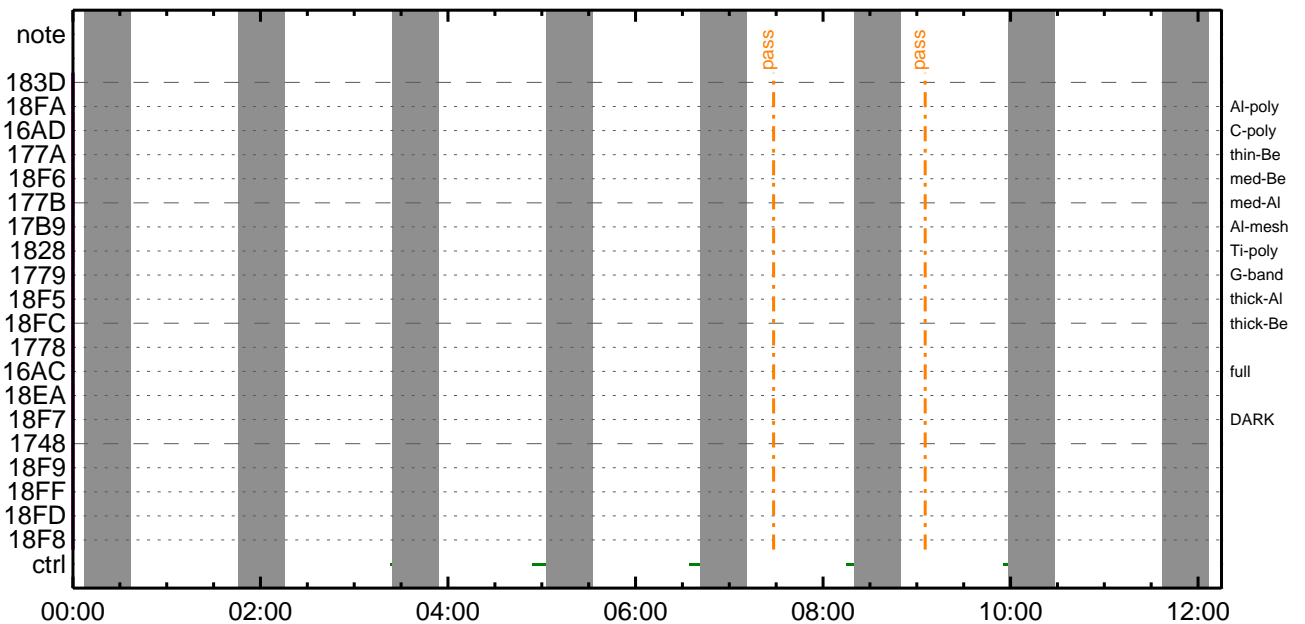
CMDI #0756 2012/07/10



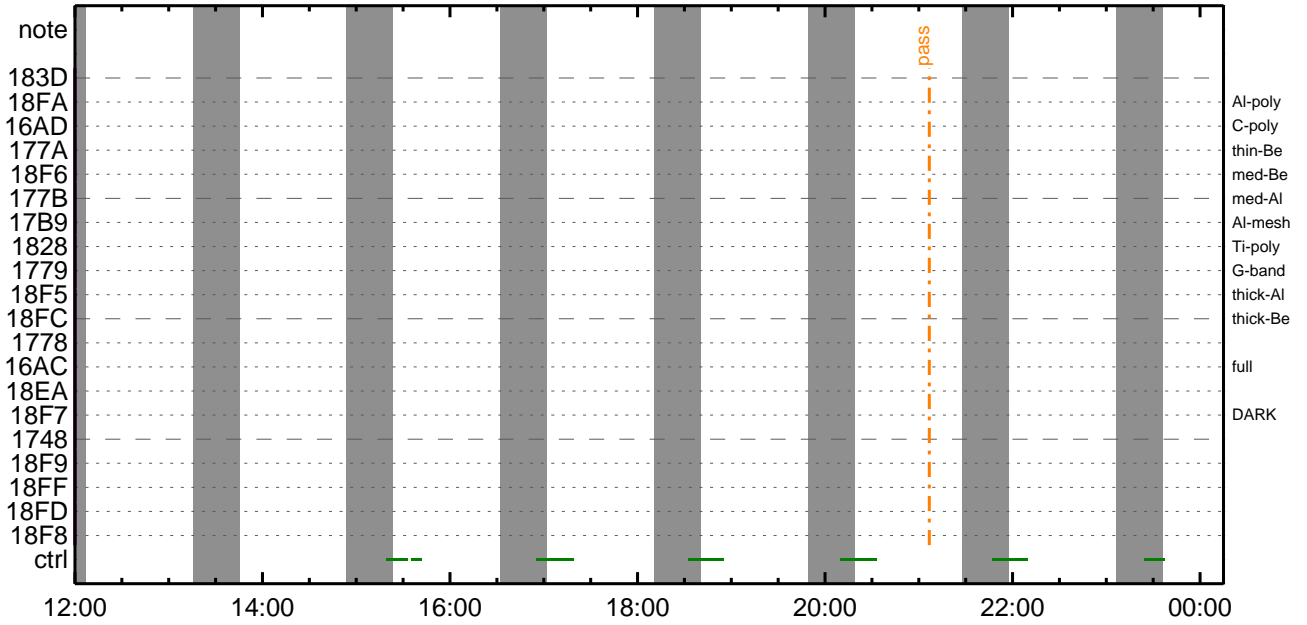
CMDI #0756 2012/07/10



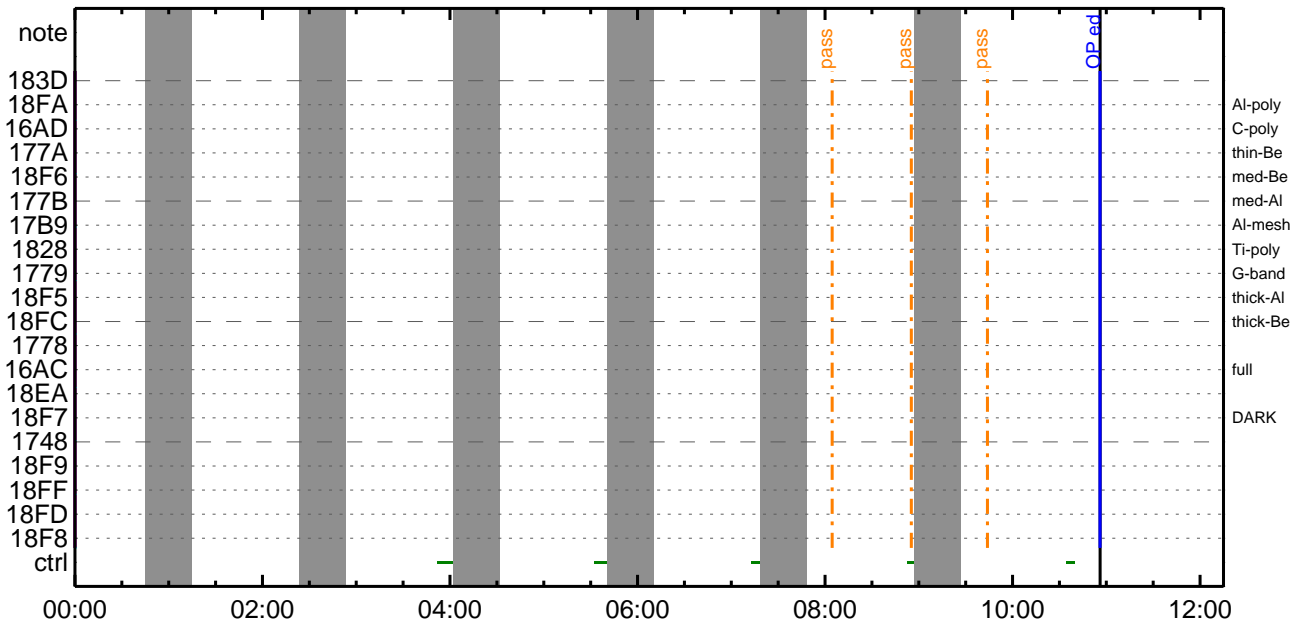
CMDI #0756 2012/07/11



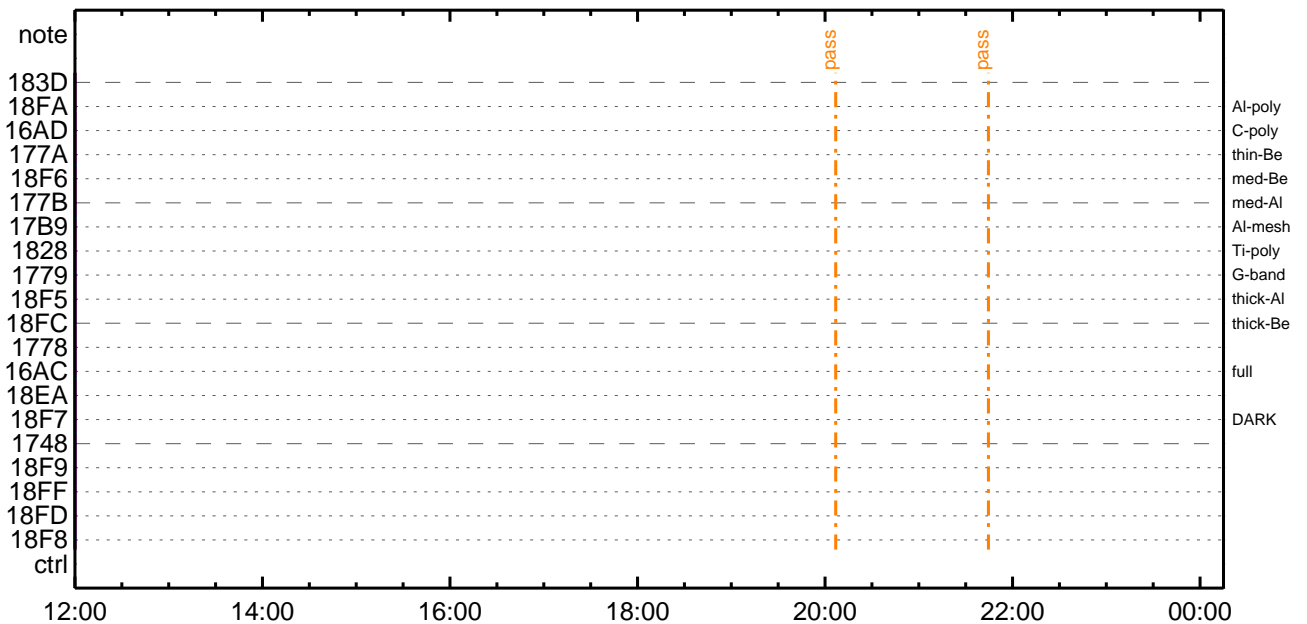
CMDI #0756 2012/07/11



CMDI #0756 2012/07/12



CMDI #0756 2012/07/12




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-992:OP
0104 ( )
0105 S. OG og-992:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ª î»ò³ îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è² îOKò³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ª î»ò³ îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è² îOKò³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ª î»ò³ îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è² îOKò³ îÇ§
0165 C.
0166 C. ***** °E²¼ò î½Ã´ ¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÔÃæç¼ª°¬òè¼î¹çòçðâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼ò îTI-CMDÁ÷¿@²î½Á¹Ô²°²E²²²³²E;f
0180 C. ²²²²;çSET²E²DUMP²î½±²î½Y¹²ç¹Ô²²²³²E;f
0181 C.
0182 C. TIY³Y½Y½E²òðÁDî¿(UT)
0183 +. TI 2012-07-07 09:23:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-07-07 09:23:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-07-07 09:23:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2012-07-07 09:27:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      ÷÷[HK1_TI_CMD_NUM]      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ñîÄè%îíññîîŷÄŷ§ŷÄŷ-¹àîŷ
0200 C.      ÷÷[HK1_TI_CMD_ENA/DIS]    EQ      ENA
0201 C.      ÷÷[HK1_TI_CMD_NUM]      EQ      4
0202 C.      ÷÷[HK1_NEXT_EXEC_PIM]    EQ      DHU
0203 C.      ÷÷[HK1_NEXT_EXEC_DC]    EQ      0xB3
0204 C.
0205 . C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      ÷÷[HK1_DMP_TOP_ADRS_1]  EQ      07
0213 C.      ÷÷[HK1_DMP_TOP_ADRS_0]  EQ      2B
0214 C.      ÷÷[HK1_DMP_BLOCK_NUM]   EQ      3
0215 C.      ÷÷[HK1_DMP_REPEAT_NUM]  EQ      0
0216 C.      ÷÷[HK1_DMA_DMP_PIM]     EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      ÷÷[HK1_PKT_FORM_NO]     EQ      7
0220 C.      ÷÷[HK1_PKT_GEN_TIME]    EQ      0.25 s
0221 C.      ÷÷[HK1_S_TLM_BIT_RATE]  EQ      32k
0222 C.      ÷÷[HK1_X_TLM_BIT_RATE]  EQ      4M
0223 C.      ÷÷[HK1_DMP_CHK_FLG]    EQ      EXEC
0224 C.
0225 . C. ŷÄŷÖŷ×½ªî»ò³îÇ§
0226 C.      ÷÷[HK1_DMP_CHK_FLG]    EQ      NON
0227 C.
0228 . C. RAM ID=TI_TBLñîî°è¹ç•è²îOKò³îÇ§
0229 C.
0230 . C. DHUŷâ;¼ŷÉ;È¼ŷ¼. ŷî;¼ŷÈ;Èòðîáñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      ÷÷[HK1_PKT_FORM_NO]     EQ      2
0234 C.      ÷÷[HK1_PKT_GEN_TIME]    EQ      0.5S
0235 C.      ÷÷[HK1_S_TLM_BIT_RATE]  EQ      32K
0236 C.      ÷÷[HK1_X_TLM_BIT_RATE]  EQ      4M
0237 C.
0238 . C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2012-07-07 09:27:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2012-07-07 09:27:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 . C.      [ ] [HK1_TI_CMD_NUM]  EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2012-07-07 09:27:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 . C. -----
0262 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-07-07 09:27:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 . C.      [ ] [HK1_TI_CMD_NUM]  EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 . C. ***** MDP ´úÃîñî»ö¼ŷñèÄðñ¹èDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÈŷŷŷÄŷŷŷèèè¼ññ¼ª»ŷñ¹èè)
0277 . S. DC-BC dcbc-402:DCBC
0278      (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ŷĐŷ¹•î Daily±çîññè'Øñ¹èèDCBC•x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C. ðãLOSŷÄŷ§ŷÄŷ-¼ª»ŷ;ã
0287 C.
0288 . C. ***** LOS *****
0289 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-993 2012-07-07 15:36:37 82 33 SOLAR-B MAIN //
0001   C.
0002   . C. ***** AOS *****
0003   C.
0004   . C. ;ãAOSYÁYŞYÁY-¼Å»Û;ä
0005   C.
0006   C. YÁYB;¼Y³YFYÓYÉÁ+ç®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008   C.
0009   . C. ***** AOCs : Reload orbital element (send every contact) *****
0010   C. Áí;ÈçðÄð•µ°È»Í×ÁÇçíYçYÁY×Yí;¼YÉ;ÈÈ%µ•ííÉ;ÈðE¼°ÇÖç•ç¼í¹ççí;çÁ®, ùç¹çèçðçÁ+ç®•çÈççç³çÈ;ç
0011   +. DC 02-8E AOCU_ORB_UPD
0012   C.
0013   C.
0014   . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015   C. Upload the Orbit Element and the Target Attitude
0016   C. RAM-ID:TARGET_ATT
0017   . S. RAM ram-150:TARGET_ATT
0018   ( )
0019   C.
0020   C.
0021   C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023   BC      (07 00 00 00 18 00)
0024   C.
0025   C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026   C.
0027   C.
0028   C. Change the TLMFormatNo for the AOCs Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030   BC      (04 0b f8)
0031   C.
0032   C. Wait for AOCSDUMP to end
0033   C.
0034   . C. Check the dump memory
0035   C.
0036   C.                                Result = OK [ ]
0037   C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039   BC      (02 0a f8)
0040   C.
0041   C.                                <A_***>[TLM STS] FMT = 2 [ ]
0042   C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046   +. DC 07-FC EIS_MODE_MANU
0047   BC      (21 02)
0048   . C. Verify EIS in MANUAL mode
0049   . C. Estimated OBSTBL upload time is 1m4s
0050   C. *****
0051   C. EIS START OBSTBL LOAD
0052   C. *****
0053   . S. RAM ram-821:EIS_OBSTBL
0054   ( )
0055   +. DC 07-FC EIS_DUMP_OBSTBL
0056   BC      (07 07 07 00 00 70 00)
0057   C.
0058   C. Execute, after the success of OBSTBL upload.
0059   C. Set EIS TI-commands
0060   +. TI 2012-07-07 09:27:50.0
0061   DC 07-FC EIS_MODE_CHG_ENA
0062   BC      (20)
0063   . C.                                [ ] [HK1_TI_CMD_NUM]    EQ    1 COUNTUP
0064   C. *****
0065   C. EIS END OBSTBL LOAD
0066   C. *****
0067   C.
0068   . C. ***** MDP `ûÁîçí»ó¼YçÈÁðç¹çèDCBC•×²è *****
0069   C. (¼á°íYÓYÁYÈYÈYÈYÈYçYçÈE¼ç¼ç¼Å»Ûç¹çè)
0070   . S. DC-BC dcbc-402:DCBC
0071   (MDP_known_event)
0072   C.
0073   C.
0074   . C. ***** YBY¹.İ Daily±;İñçÈ`Øç¹çèDCBC•×²è *****
0075   . S. DC-BC dcbc-153:DCBC
0076   (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077   C.
0078   C.
0079   . C. ;ãLOSŸÁYŞYÁY-¼Å»Û;ä
0080   C.
0081   . C. ***** LOS *****
0082   C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-994 2012-07-07 15:36:37 128 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αΑβ•μ°È»Í×ÁÇαÍYçYÁY×Yí;¼YÉ;ÈÈè%μ•ííÉ;ÈαÈ¼°ÇÇα•α¿¼í¹çαÍ;çÀ®, ùα¹αèαβαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0f 80 80 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 10 80 80 08 08)
0054 + DC 07-F0 MDP_XRT_FLD_DIS
0055 BC (d9)
0056 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0057 BC (c9)
0058 + DC 07-F0 MDP_XRT_AEC_RESET
0059 BC (d0)
0060 + DC 07-F0 MDP_XRT_ARS_DIS
0061 BC (d5)
0062 + DC 07-F0 MDP_XRT_FLD_RESET
0063 BC (da)
0064 + DC 07-F0 MDP_XRT_QT_PROG_SET
0065 BC (c4 14)
0066 + DC 07-F0 MDP_XRT_FL_PROG_SET
0067 BC (c5 0d)
0068 . C. ----- Success Verify ? OK / NG ____
0069 C.
0070 C.
0071 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0072 C.
0073 +. DC 07-F0 MDP_XRT_MODE_OBSV
0074 BC (c2)
0075 +. TI 2012-07-07 09:27:02.0
0076 DC 07-F0 MDP_XRT_MODE_OBSV
0077 BC (c2)
0078 . C. ----- Success Verify ? OK / NG ____
0079 C.
0080 C. ***** XRT END *****
0081 . C. *****
0082 C. SOT table upload
0083 C. *****
0084 . C. < Stop FG table >
0085 +. DC 07-F0 MDP_FG_CTRL_MANU
0086 BC (51)
0087 . C. -----
0088 C. MDP_FG_CTRL_MODE = MANU [ ]
0089 C. -----
0090 C.
0091 . C. <Upload FG Observation Table>
0092 . S. RAM ram-262:MDP_OBS_F
0093 ( )
0094 C.
0095 . C. < Dump RAMID=MDP_OBS_F >
```

```
0096 +. DC 07-F0 MDP_DUMP_FGTBL
0097 BC (82 07 00 00 00 38 b8)
0098 C. -----
0099 C. MDP_OBS_F verify = OK/NG [ ]
0100 C. -----
0101 C.
0102 C. *****
0103 C. SOT TI command set
0104 C. *****
0105 C. Execute, after the success of TBL upload.
0106 +. TI 2012-07-07 09:27:18.0
0107 DC 07-F0 MDP_SOT_MODE_OBSV
0108 BC (40)
0109 . C. -----
0110 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0111 C. -----
0112 C.
0113 C.
0114 . C. ***** MDP `úÃîâî»ô¼ŸαĒĀĐα¹αēDCBC•x²è *****
0115 C. (¼á°îŸÓŸĀŸĒŸĤŸĒŸĀŸçŸēαĒ¼α¼Ā»Ūα¹αē)
0116 . S. DC-BC dcbc-402:DCBC
0117 (MDP_known_event)
0118 C.
0119 C.
0120 . C. ***** ŸĐŸ¹•Ī Daily±¿ĪŃαĒĒ´Øα¹αēDCBC•x²è *****
0121 . S. DC-BC dcbc-153:DCBC
0122 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0123 C.
0124 C.
0125 . C. ¡ãLOSŸĀŸŸŸĀŸ-¼Ā»Ū¡ã
0126 C.
0127 . C. ***** LOS *****
0128 C.
```

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*** OP Sequence for XRT ***

2012/07/07	09:37:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/07	09:38:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/07/07	09:40:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/07/07	09:40:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/07/07	09:40:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/07/07	09:40:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/07/07	09:40:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/07	09:40:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/07	09:40:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2012/07/07	09:40:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2012/07/07	09:41:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/07	10:45:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/07	10:45:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/07	10:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/07	10:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/07	11:15:30.0	XRT_Custom_418_OG [0x1a2]			
2012/07/07	11:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/07	12:24:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/07	12:24:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/07	12:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/07	12:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/07	12:54:00.0	XRT_Custom_418_OG [0x1a2]			
2012/07/07	12:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/07	14:03:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/07	14:03:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/07	14:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/07	14:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/07	14:41:00.0	XRT_Custom_418_OG [0x1a2]			
2012/07/07	14:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/07	15:41:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/07	15:41:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/07	15:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/07	15:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/07	16:28:30.0	XRT_Custom_418_OG [0x1a2]			
2012/07/07	16:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/07	17:20:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/07	17:20:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/07	17:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/07	17:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/07	18:05:30.5	XRT_Custom_418_OG [0x1a2]			
2012/07/07	18:06:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/07	18:58:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/07	18:58:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/07	18:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/07	19:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/07	19:42:30.0	XRT_Custom_418_OG [0x1a2]			
2012/07/07	19:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/07	20:37:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/07	20:37:02.0	XRT_FLD_RESET_412_OG [0x19c]			

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2012/07/07	20:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/07/07	20:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/07/07	21:19:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/07	21:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/07	22:15:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/07	22:15:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/07/07	22:15:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/07/07	22:18:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/07	22:54:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/07	22:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/07	23:54:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/07	23:54:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/07	23:54:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/07/07	23:57:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/07/08	00:23:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/08	00:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/08	01:32:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/08	01:32:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/08	01:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/07/08	01:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/07/08	02:02:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/08	02:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/08	03:10:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/08	03:10:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/08	03:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/07/08	03:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/07/08	03:40:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/08	03:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/08	04:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/08	04:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/08	04:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/07/08	04:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/07/08	05:19:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/08	05:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/08	05:52:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/08	05:52:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/08	05:52:30.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/07/08	05:52:46.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00	
2012/07/08	05:52:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/07/08	05:52:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/07/08	05:55:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/07/08	05:55:30.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2012/07/08	06:02:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/08	06:02:30.0	AOCs_OrE-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/08	06:04:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	AOCU_NM	5	02-76	03 00 00 00 00	
2012/07/08	06:05:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/07/08	06:05:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/07/08	06:05:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/07/08	06:05:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
			MDP_XRT_ARS_DIS	1	07-F0	d5	

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2012/07/08	06:05:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/08	06:05:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2012/07/08	06:05:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2012/07/08	06:05:30.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/08	06:20:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/08	06:20:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/08	06:20:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/08	06:23:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/08	06:57:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/08	06:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/08	08:01:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/08	08:01:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/08	08:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/08	08:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/08	08:36:00.0	XRT_Custom_418_OG [0x1a2]				
2012/07/08	08:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/08	09:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/08	09:41:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/08	09:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/08	09:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/08	10:14:30.5	XRT_Custom_418_OG [0x1a2]				
2012/07/08	10:15:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/08	11:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/08	11:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/08	11:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/08	11:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/08	11:53:00.0	XRT_Custom_418_OG [0x1a2]				
2012/07/08	11:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/08	13:01:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/08	13:01:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/08	13:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/08	13:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/08	13:31:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/08	13:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/08	14:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/08	14:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/08	14:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/08	14:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/08	15:19:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/08	15:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/08	15:26:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/08	15:26:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/08	15:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/08	15:28:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/08	15:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/08	15:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/08	16:18:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/08	16:18:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/08	16:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/08	16:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2012/07/08	17:05:30.0	XRT_Custom_418_OG [0x1a2]		
2012/07/08	17:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/07/08	17:57:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/07/08	17:57:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/07/08	17:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/07/08	18:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/07/08	18:42:30.0	XRT_Custom_418_OG [0x1a2]		
2012/07/08	18:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/07/08	19:35:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/07/08	19:35:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/07/08	19:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/07/08	19:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/07/08	20:19:30.0	XRT_Custom_418_OG [0x1a2]		
2012/07/08	20:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/07/08	21:14:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/07/08	21:14:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/07/08	21:14:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/07/08	21:17:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/07/08	21:56:00.0	XRT_Custom_418_OG [0x1a2]		
2012/07/08	21:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/07/08	22:52:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/07/08	22:52:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/07/08	22:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/07/08	22:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/07/08	23:26:30.0	XRT_Custom_418_OG [0x1a2]		
2012/07/08	23:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/07/09	00:31:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/07/09	00:31:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/07/09	00:31:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/07/09	00:34:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/07/09	01:01:00.0	XRT_Custom_418_OG [0x1a2]		
2012/07/09	01:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/07/09	02:09:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/07/09	02:09:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/07/09	02:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/07/09	02:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/07/09	02:39:30.0	XRT_Custom_418_OG [0x1a2]		
2012/07/09	02:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/07/09	03:39:00.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/07/09	03:39:02.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/07/09	03:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/07/09	03:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/07/09	04:18:00.0	XRT_Custom_418_OG [0x1a2]		
2012/07/09	04:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/07/09	05:18:30.0	XRT_CTRL_MANU_408_OG [0x198]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2012/07/09	05:18:32.0	XRT_FLD_RESET_412_OG [0x19c]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2012/07/09	05:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2012/07/09	05:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2012/07/09	05:56:30.0	XRT_Custom_418_OG [0x1a2]		
2012/07/09	05:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2012/07/09	06:01:54.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1

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2012/07/09	06:01:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2012/07/09	06:02:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00
2012/07/09	06:02:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/07/09	06:02:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/07/09	06:02:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/07/09	06:04:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2012/07/09	06:05:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/09	06:11:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/09	06:12:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03	00	00	00
2012/07/09	06:14:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2012/07/09	06:14:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/07/09	06:14:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/07/09	06:14:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/07/09	06:14:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/07/09	06:14:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/09	06:14:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14		
2012/07/09	06:14:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2012/07/09	06:15:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/09	06:58:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/09	06:58:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/09	06:58:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/09	07:01:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/07/09	07:35:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/09	07:36:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/09	08:38:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/09	08:38:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/09	08:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/09	08:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/07/09	09:13:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/09	09:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/09	10:20:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/09	10:20:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/09	10:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/09	10:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/07/09	10:52:00.5	XRT_Custom_418_OG [0x1a2]							
2012/07/09	10:53:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/09	12:00:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/09	12:00:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/09	12:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/09	12:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/07/09	12:30:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/09	12:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/09	13:39:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/09	13:39:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/09	13:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/09	13:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/07/09	14:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/09	14:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/09	14:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							

2012/07/09	14:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/07/09	14:15:30.0	XRT_Custom_418_OG [0x1a2]						
2012/07/09	14:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/07/09	15:17:30.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/09	15:17:32.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/07/09	15:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/07/09	15:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/07/09	16:05:30.0	XRT_Custom_418_OG [0x1a2]						
2012/07/09	16:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/07/09	16:56:00.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/09	16:56:02.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/07/09	16:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/07/09	16:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/07/09	17:42:00.0	XRT_Custom_418_OG [0x1a2]						
2012/07/09	17:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/07/09	17:59:24.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/09	17:59:26.0	XRT_FOCUS_POSITION_401_OG [0x191]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/07/09	17:59:30.0	AOCS_Ore-point_Start_2_OG [0x098]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2012/07/09	17:59:46.0	XRT_FLD_DIS_402_OG [0x192]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/07/09	17:59:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/07/09	17:59:50.0	XRT_ARS_DIS_438_OG [0x1b6]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/07/09	18:02:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2012/07/09	18:02:30.0	XRT_CTRL_AUTO_406_OG [0x196]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/07/09	18:09:24.0	XRT_CTRL_MANU_439_OG [0x1b7]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/09	18:09:30.0	AOCS_ORe-point_Start_3_OG [0x099]						
			AOCU_NM	5	02-76	00 1d 99 b1 dc		
2012/07/09	18:11:56.0	XRT_FOCUS_POSITION_409_OG [0x199]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/07/09	18:12:16.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/07/09	18:12:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/07/09	18:12:20.0	XRT_AEC_RESET_443_OG [0x1bb]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/07/09	18:12:22.0	XRT_ARS_DIS_431_OG [0x1af]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/07/09	18:12:24.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/07/09	18:12:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2012/07/09	18:12:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2012/07/09	18:12:30.0	XRT_CTRL_AUTO_406_OG [0x196]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/07/09	18:34:30.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/09	18:34:32.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/07/09	18:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/07/09	18:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/07/09	19:19:00.0	XRT_Custom_418_OG [0x1a2]						
2012/07/09	19:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/07/09	20:13:00.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/09	20:13:02.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/07/09	20:13:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/07/09	20:16:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/07/09	20:56:30.0	XRT_Custom_418_OG [0x1a2]						
2012/07/09	20:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/07/09	21:51:30.0	XRT_CTRL_MANU_408_OG [0x198]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/09	21:51:32.0	XRT_FLD_RESET_412_OG [0x19c]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/07/09	21:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]						

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2012/07/09	21:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/09	22:32:00.0	XRT_Custom_418_OG [0x1a2]					
2012/07/09	22:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/09	23:30:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/09	23:30:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/07/09	23:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/07/09	23:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/10	00:00:00.0	XRT_Custom_418_OG [0x1a2]					
2012/07/10	00:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/10	01:08:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/10	01:08:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/07/10	01:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/07/10	01:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/10	01:38:30.0	XRT_Custom_418_OG [0x1a2]					
2012/07/10	01:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/10	02:46:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/10	02:46:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/07/10	02:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/07/10	02:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/10	03:17:00.0	XRT_Custom_418_OG [0x1a2]					
2012/07/10	03:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/10	04:16:00.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/10	04:16:02.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/07/10	04:16:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/07/10	04:19:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/10	04:55:30.0	XRT_Custom_418_OG [0x1a2]					
2012/07/10	04:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/10	05:28:54.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/10	05:28:56.0	XRT_FOCUS_POSITION_401_OG [0x191]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/07/10	05:29:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2012/07/10	05:29:16.0	XRT_FLD_DIS_402_OG [0x192]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/07/10	05:29:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/07/10	05:29:20.0	XRT_ARS_DIS_438_OG [0x1b6]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/07/10	05:31:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2012/07/10	05:32:00.5	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/10	05:38:54.0	XRT_CTRL_MANU_439_OG [0x1b7]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/10	05:39:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	00 1d 99 b1 dc	
2012/07/10	05:41:26.0	XRT_FOCUS_POSITION_409_OG [0x199]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/07/10	05:41:46.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/07/10	05:41:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/07/10	05:41:50.0	XRT_AEC_RESET_443_OG [0x1bb]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/07/10	05:41:52.0	XRT_ARS_DIS_431_OG [0x1af]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/07/10	05:41:54.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/07/10	05:41:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2012/07/10	05:41:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2012/07/10	05:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/10	05:56:30.0	XRT_CTRL_MANU_408_OG [0x198]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/10	05:56:32.0	XRT_FLD_RESET_412_OG [0x19c]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2012/07/10	05:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]					

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2012/07/10	05:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/10	06:00:00.0	XRT_CTRL_MANU_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/10	06:00:10.0	XRT_FOCUS_RECALIBRATE_429_OG [0x1ad]	XRT_FOCUS_RECAL	2	07-F8	78	00
2012/07/10	06:04:10.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2012/07/10	06:34:00.0	XRT_Custom_418_OG [0x1a2]					
2012/07/10	06:35:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/10	07:37:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/10	07:37:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/07/10	07:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/07/10	07:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/07/10	08:12:00.0	XRT_Custom_418_OG [0x1a2]					
2012/07/10	08:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/07/10	09:00:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/07/10	09:19:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00