XRT Timeline to be uploaded on 2012/07/17

Period: 2012/07/17 10:46:00 - 2012/07/21 10:31:00

* * * * * **Normal mode** * * * * *

XOB #1905: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long-2 - w leak image

Term	Pointing (x, y)	Comment
07/18 00:08:00 - 07/18 00:14:54	Fixed (-528.4, -528.4)	XRT Quadrant Obs. (1/4)

PROG= 07 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 38 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 59 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 9 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1906: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh,Ti/Poly -long-w leak image

Term	Pointing (x, y)	Comment
07/18 00:18:00 - 07/18 00:24:54	Fixed (528.4, -528.4)	XRT Quadrant Obs. (2/4)

PROG= 06 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 36 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 59 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 9 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1907: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long-w leak image

Term	Pointing (x, y)	Comment
07/18 00:28:00 - 07/18 00:34:54	Fixed (528.4, 528.4)	XRT Quadrant Obs. (3/4)

PROG= 19 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 39 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 59 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 9 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1908: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long-w leak image

Term	Pointing (x, y)	Comment
07/18 00:38:00 - 07/18 00:44:54	Fixed (-528.4, 528.4)	XRT Quadrant Obs. (4/4)

PROG= 04 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 40 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 93 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 1-time(s) 2.0sec												
Seqn= 59 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 9 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #190A: Synoptic Q95 2x2 Movie - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-ban

Term	Pointing (x, y)	Comment
07/18 00:48:06 - 07/18 06:50:00	Track (855.3, -340.6) @ 07/18 00:45:00	# AR11520
07/18 09:03:08 - 07/19 00:59:54	Fixed (0.0, 0.0)	SOT Flat Field
07/19 05:36:30 - 07/19 07:40:00	Track (-8.3, 0.0) @ 07/19 05:00:00	EIS Sensitive monitor

PROG= 05 Inf.-time(s)													
Subr= 1 1-time(s) 1800.0sec													
Seqn= 46 1-time(s) 4.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 69 1-time(s) 4.0sec													
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 9 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #190B: G-Band Alignment with North Pole Q90 2x2(G-band only) - 12msec - 5min cadence - Partial Sun-wNGT-2

Term	Pointing (x, y)	Comment
07/19 01:15:00 - 07/19 02:59:54	Fixed (0.0, 945.0)	Co-Alignment (N-Limb)

PROG= 16 1-time(s)													
Subr= 1 1-time(s) 360.0sec													
Seqn= 26 24-time(s) 300.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #190C: G-Band Alignment with East limb Q90 2x2 (G-band only) - 12msec- 8 min cadence-wNGT-2

Term	Pointing (x, y)	Comment
07/19 03:15:00 - 07/19 04:59:54	Fixed (-954.0, 0.0)	Co-Aligment (E-Limb)

PROG= 02 1-time(s)													
Subr= 1 1-time(s) 360.0sec													
Seqn= 44 15-time(s) 480.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * **Flare mode** * * * * *

XOB #1909: Flare Standard Obs. with eruptions mode-A (FW1=Open)-45ms G-band

Term	Pointing (x, y)	Comment
07/18 00:48:06 - 07/18 06:50:00	Track (855.3, -340.6) @ 07/18 00:45:00	# AR11520
07/18 09:03:08 - 07/19 00:59:54	Fixed (0.0, 0.0)	SOT Flat Field
07/19 05:36:30 - 07/19 07:40:00	Track (-8.3, 0.0) @ 07/19 05:00:00	EIS Sensitive monitor

PROG= 14 1-time(s)													
Subr= 1 30-time(s) 20.0sec													
Seqn= 88 1-time(s) 2.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn=100 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 43 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 88 1-time(s) 2.0sec													

Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 43 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 88 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 43 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 88 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec												
Seqn= 89 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

Active Region Search

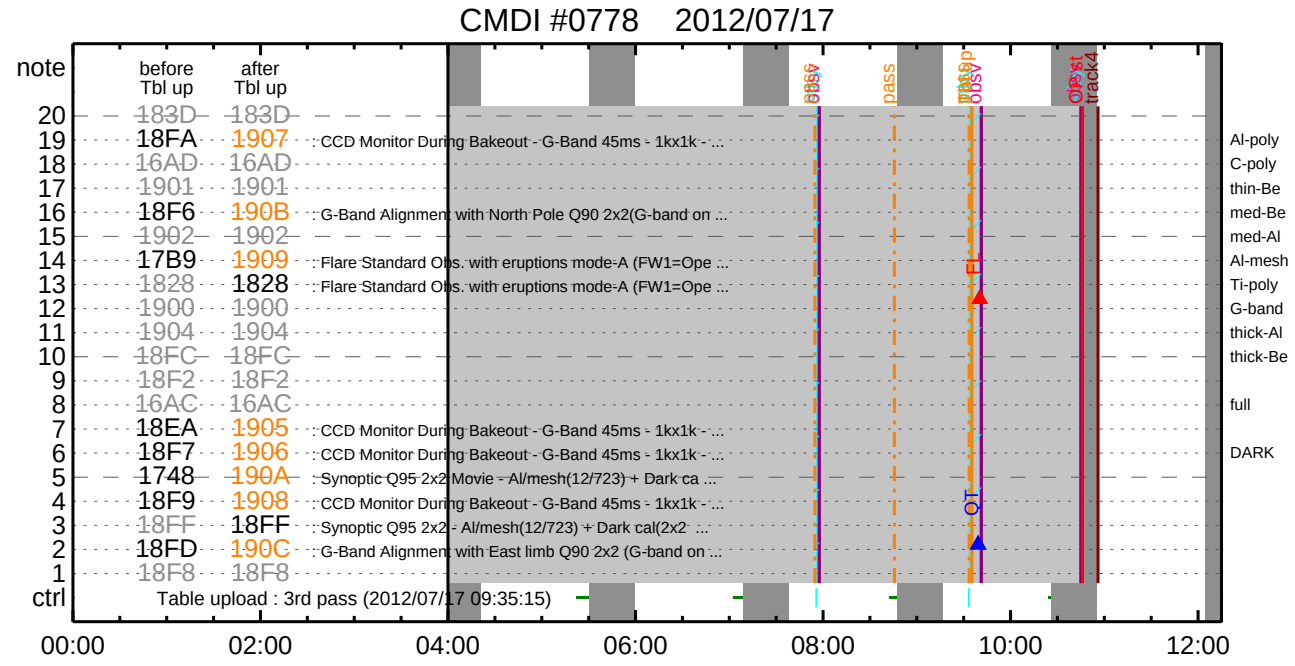
NOT USED

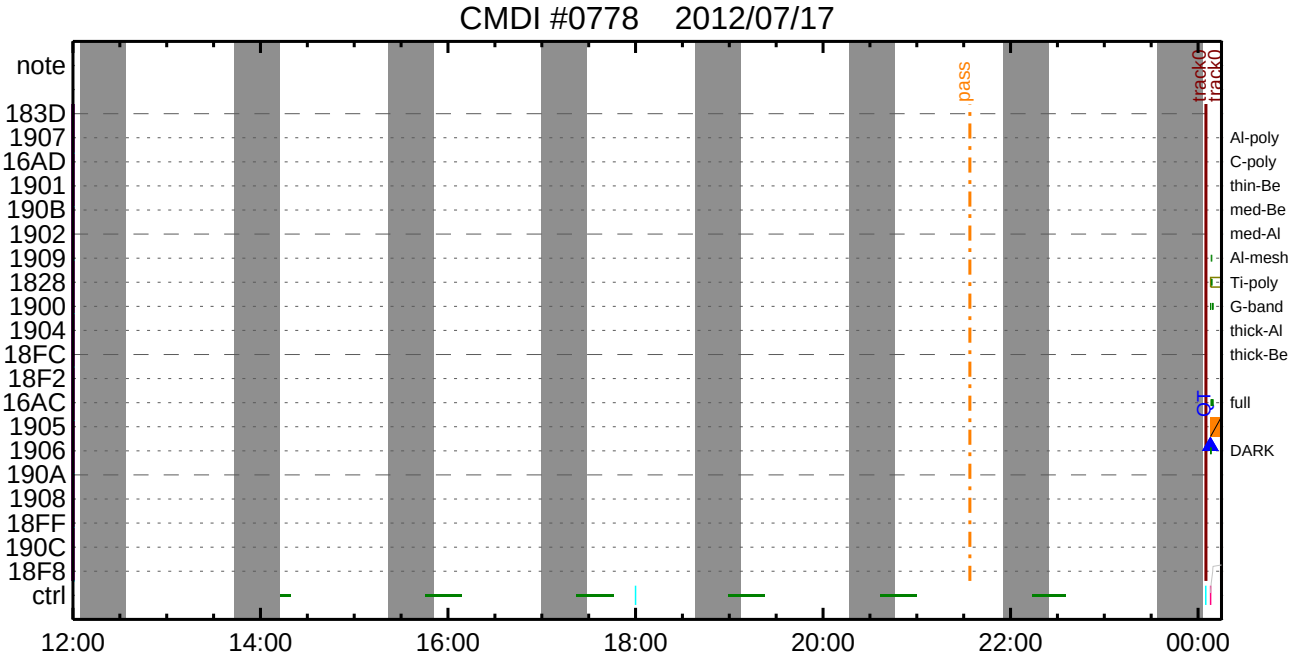
Flare Detection

FLD Patrol

Term	Pointing (x, y)	Comment
07/18 00:45:16 - 07/19 01:14:48	Track (855.3, -340.6) @ 07/18 00:45:00	# AR11520
07/19 05:00:16 - 07/21 10:31:00	Track (-8.3, 0.0) @ 07/19 05:00:00	EIS Sensitive monitor

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval


CMDI #0778 2012/07/17
note
before Tbl up
after Tbl up
20 183D 183D
19 18FA 1907 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
18 16AD 16AD
17 1901 1901
16 18F6 190B : G-Band Alignment with North Pole Q90 2x2(G-band on ...
15 1902 1902
14 17B9 1909 : Flare Standard Obs. with eruptions mode-A (FW1=Ope ...
13 1828 1828 : Flare Standard Obs. with eruptions mode-A (FW1=Ope ...
12 1900 1900
11 1904 1904
10 18FC 18FC
9 18F2 18F2
8 16AC 16AC
7 18EA 1905 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
6 18F7 1906 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
5 1748 190A : Synoptic Q95 2x2 Movie - Al/mesh(12/723) + Dark ca ...
4 18F9 1908 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
3 18FF 18FF : Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 ...
2 18FD 190C : G-Band Alignment with East limb Q90 2x2 (G-band on ...
1 18F8 18F8
ctrl Table upload : 3rd pass (2012/07/17 09:35:15)
pass
FL
OT
track4
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0778 2012/07/17
note
183D
1907
16AD
1901
190B
1902
1909
1828
1900
1904
18FC
18F2
16AC
1905
1906
190A
1908
18FF
190C
18F8
ctrl
pass
track0
OT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
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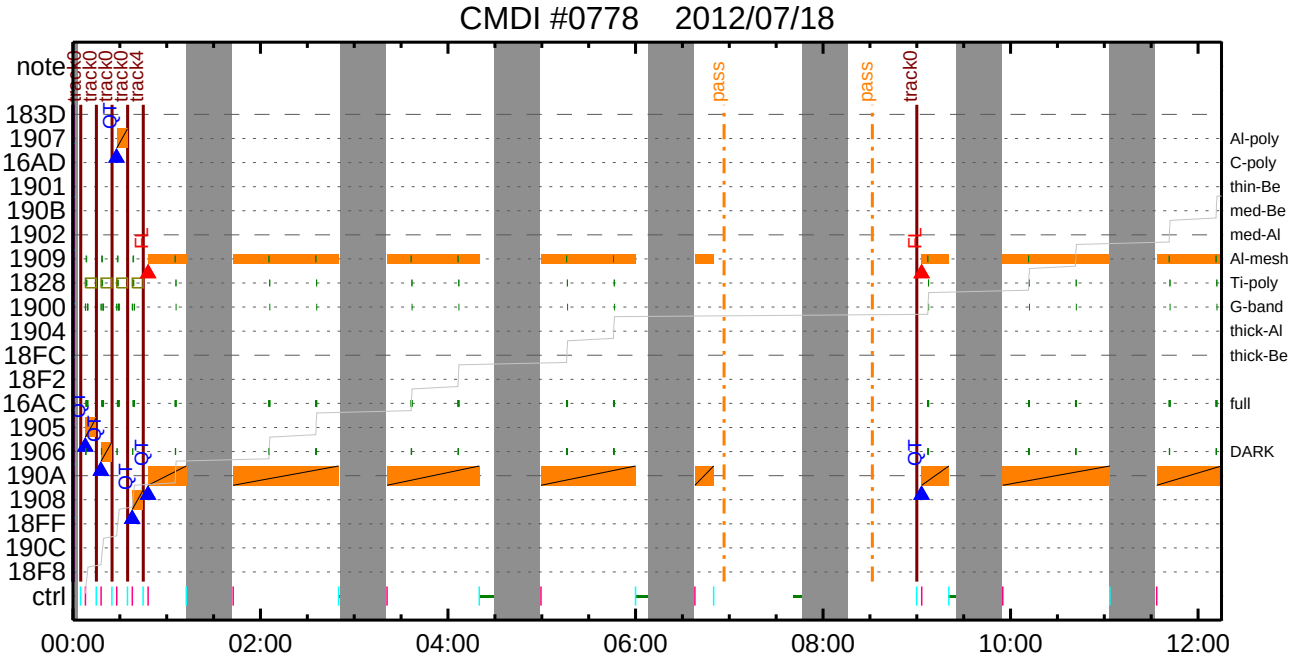
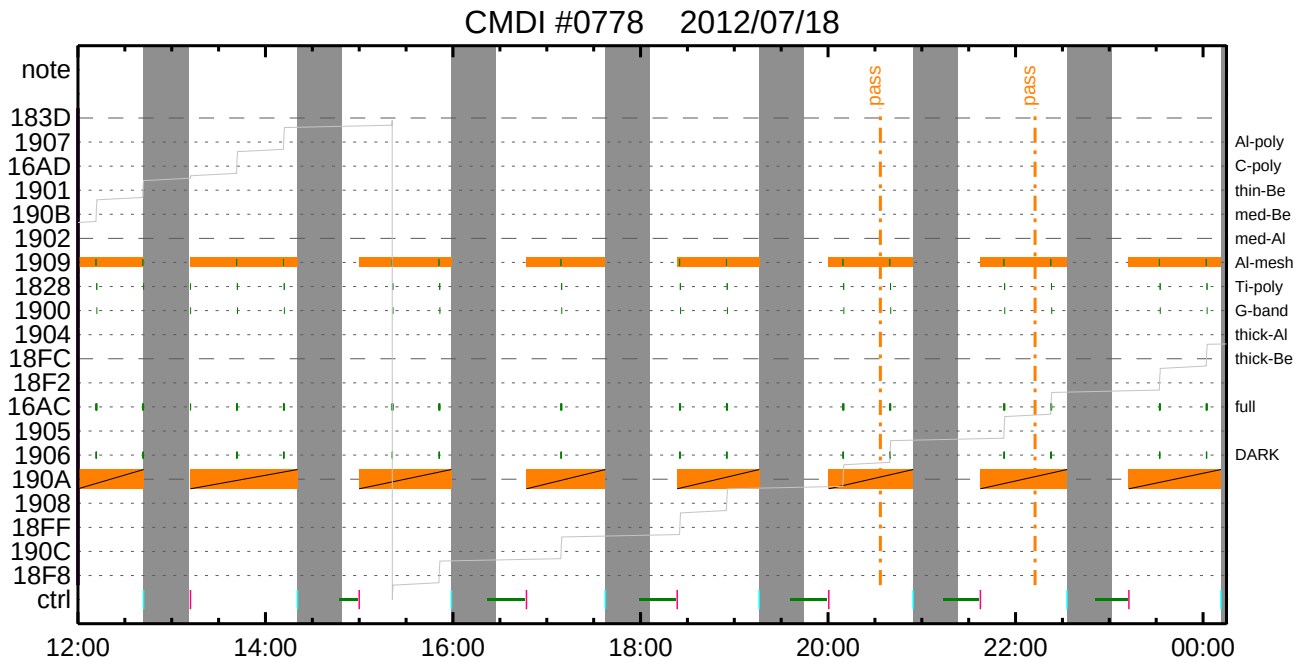
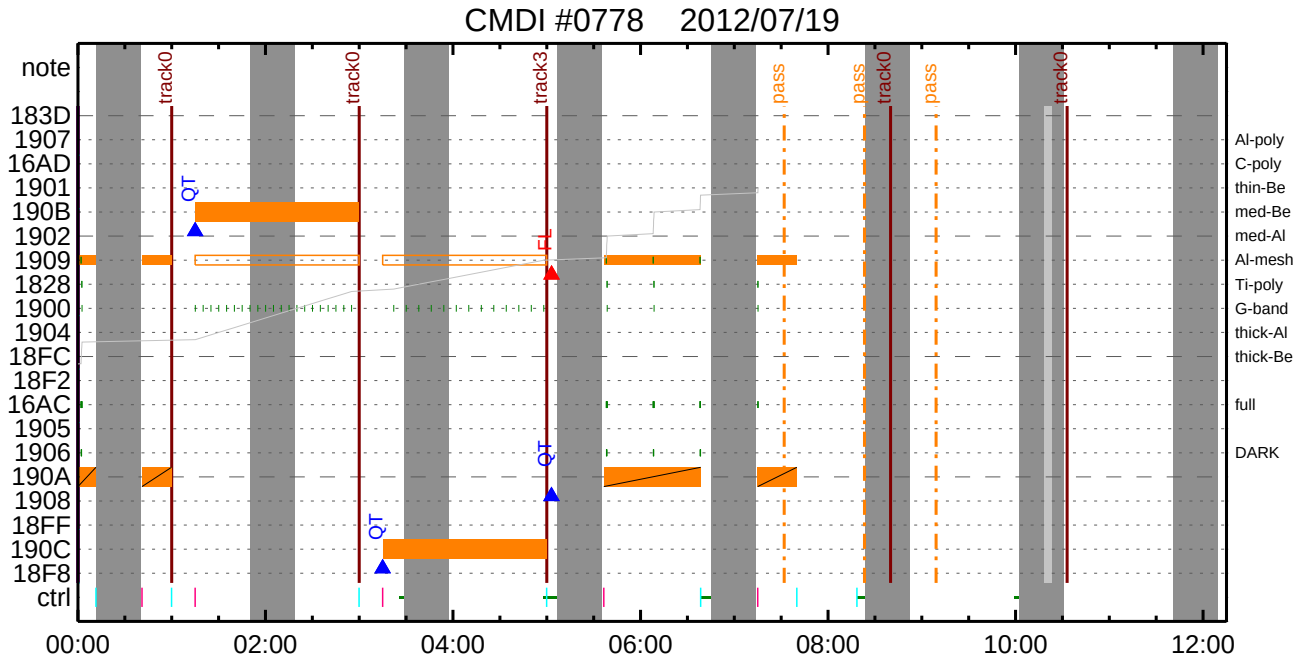
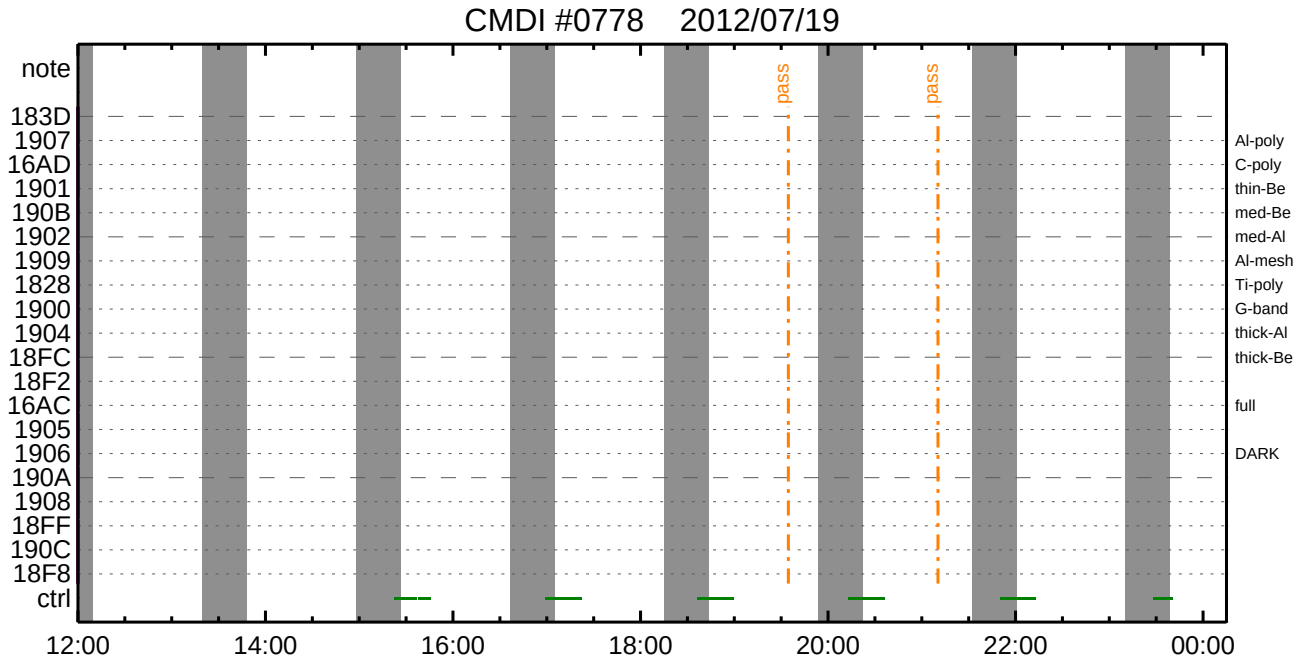
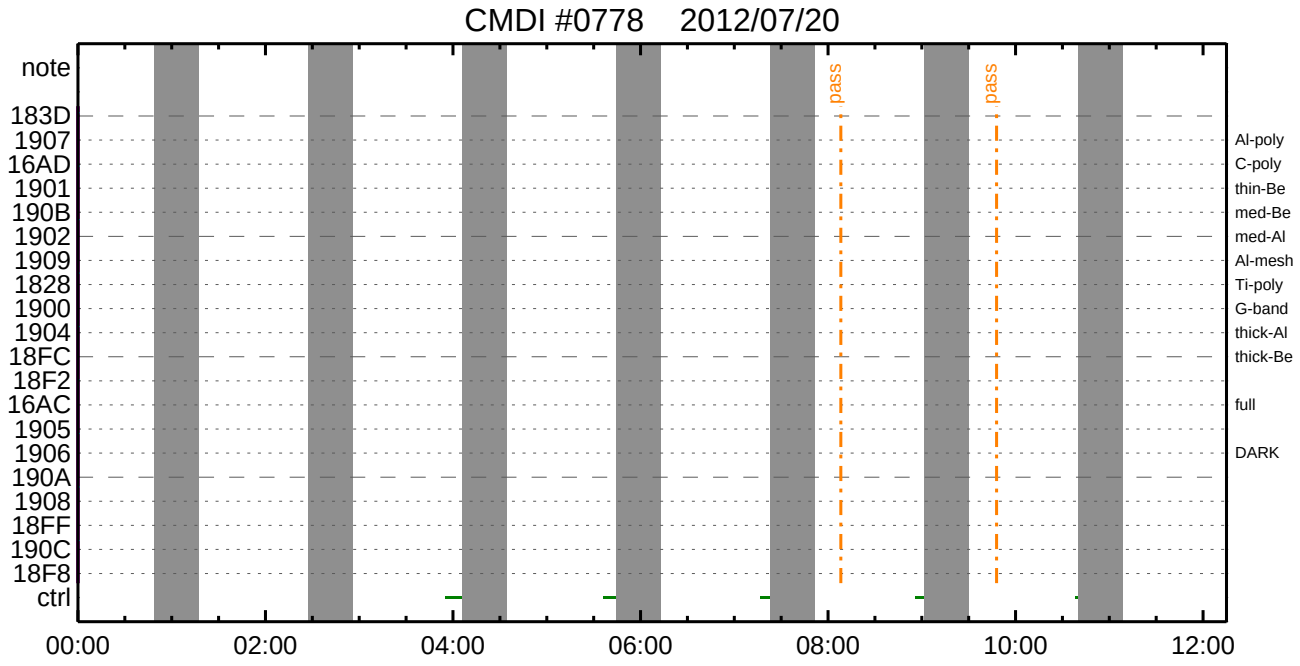
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

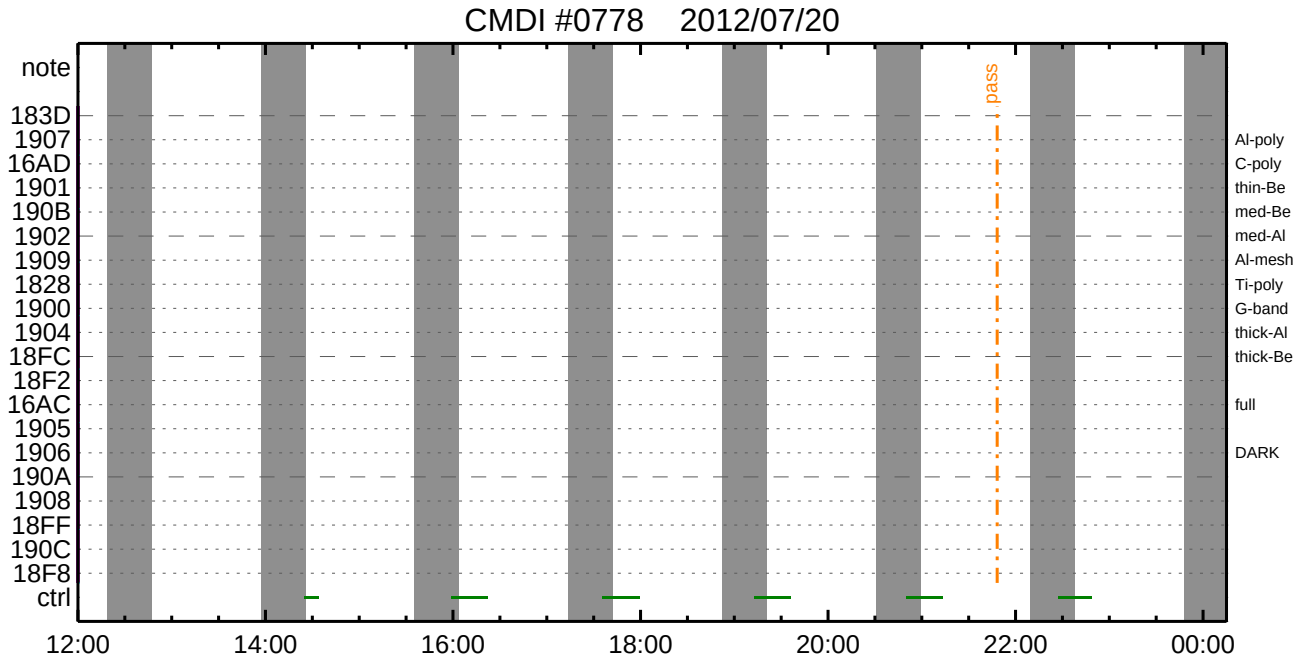

CMDI #0778 2012/07/18
note
183D
1907
16AD
1901
190B
1902
1909
1828
1900
1904
18FC
18F2
16AC
1905
1906
190A
1908
18FF
190C
18F8
ctrl
track0
track4
OT
FL
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

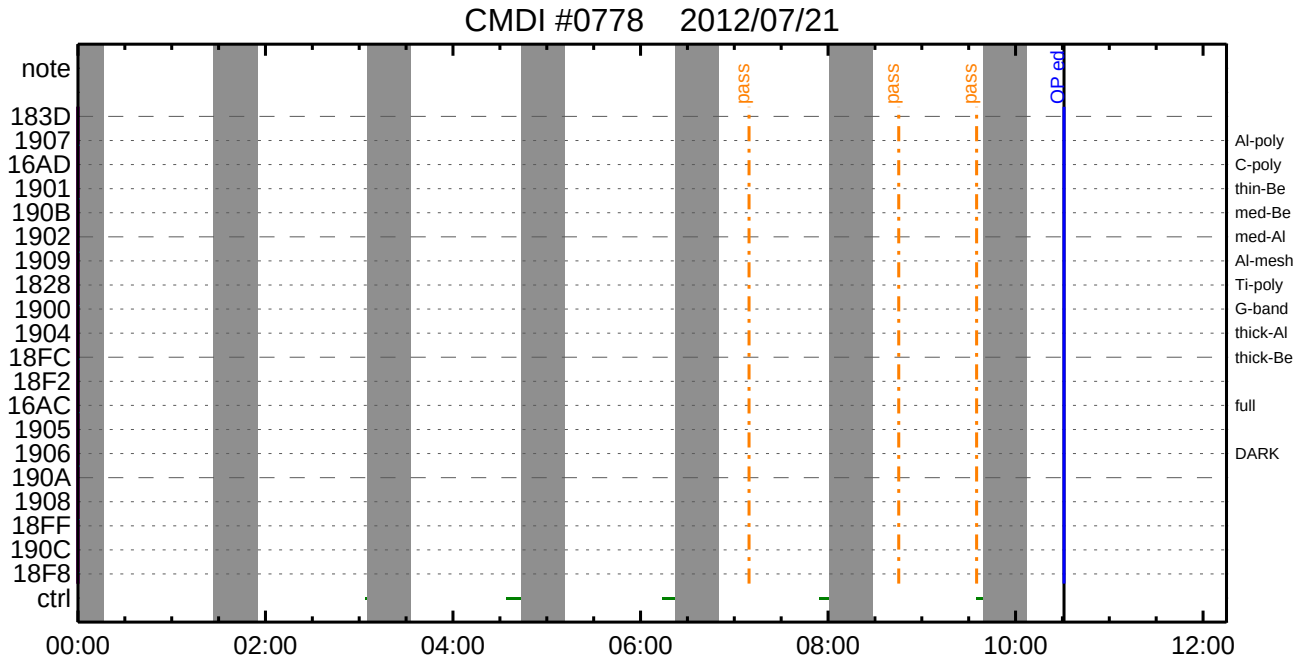

CMDI #0778 2012/07/18
note
183D
1907
16AD
1901
190B
1902
1909
1828
1900
1904
18FC
18F2
16AC
1905
1906
190A
1908
18FF
190C
18F8
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

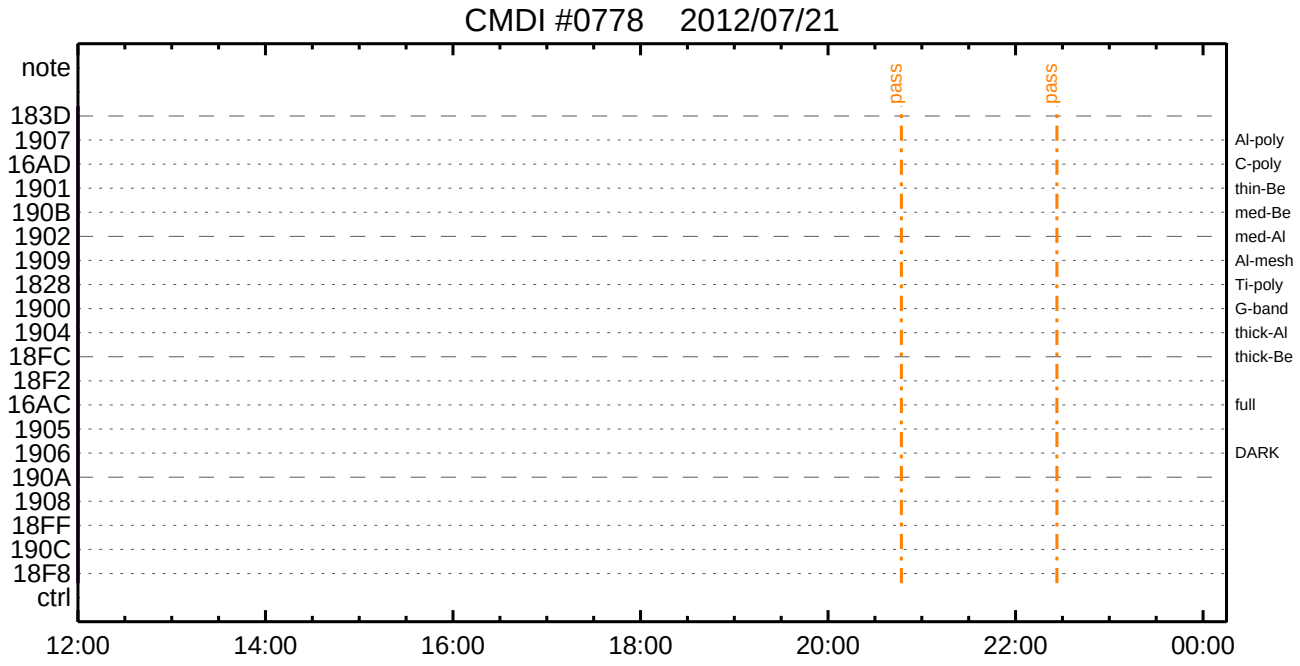

CMDI #0778 2012/07/19
note
183D
1907
16AD
1901
190B
1902
1909
1828
1900
1904
18FC
18F2
16AC
1905
1906
190A
1908
18FF
190C
18F8
ctrl
track0
track0
track3
track0
track0
QT
FL
QT
QT
pass
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0778 2012/07/19
note
183D
1907
16AD
1901
190B
1902
1909
1828
1900
1904
18FC
18F2
16AC
1905
1906
190A
1908
18FF
190C
18F8
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0778 2012/07/20
note
183D
1907
16AD
1901
190B
1902
1909
1828
1900
1904
18FC
18F2
16AC
1905
1906
190A
1908
18FF
190C
18F8
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0778 2012/07/20
note
183D
1907
16AD
1901
190B
1902
1909
1828
1900
1904
18FC
18F2
16AC
1905
1906
190A
1908
18FF
190C
18F8
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0778 2012/07/21
note
183D
1907
16AD
1901
190B
1902
1909
1828
1900
1904
18FC
18F2
16AC
1905
1906
190A
1908
18FF
190C
18F8
ctrl
pass
pass
pass
QP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0778 2012/07/21
note
183D
1907
16AD
1901
190B
1902
1909
1828
1900
1904
18FC
18F2
16AC
1905
1906
190A
1908
18FF
190C
18F8
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-022 2012-07-17 16:25:19 332 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-022:OP
0104     ()
0105   . S. OG    og-022:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2012-07-17 10:41:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2012-07-17 10:41:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2012-07-17 10:41:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195  +. TI 2012-07-17 10:45:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                        ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                        ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                        ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205   . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217  +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230   . C. DHU¥â¡¼¥É¡Ê¼ý¸¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231  +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ******************************
0239     C.  SOT TI command set
0240     C. ******************************
0241     C. Execute, after the success of OP upload.
0242  +. TI 2012-07-17 10:45:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245   . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249   . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255  +. TI 2012-07-17 10:45:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258  +. TI 2012-07-17 10:45:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261   . C.                        [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ************
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ************
0267     C. Execute, after the success of OP upload.
0268  +. TI 2012-07-17 10:45:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271   . C.                        [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ************
0274     C. ************************************
0275     C.  XRT THERMAL TABLE UPLOAD OPERATION
0276     C. ************************************
0277     C.
0278  +. DC 07-F0 MDP_XRT_CTRL_MANU
0279     BC       (c1)
0280  +  DC 07-F0 MDP_XRT_MODE_STBY
0281     BC       (c3)
0282     C.          <MDP ,XRT>      [MDP_XRT_CTRL_MODE]     = MANU ?        OK / NG _____
0283     C.          <MDP ,XRT>      [MDP_XRT_MODE]          = STBY ?        OK / NG _____
0284     C.
0285  +. DC 07-F8 XRT_STBY
0286     BC       (03 01)
0287     C.          <XRT1,XRTD_STS> [XRTD_S_S/W_MODE]       = STBY  P / F
0288     C.
0289  +. DC 07-F8 XRT_SAFEHOLD
0290     BC       (03 03)
0291     C.          <XRT1,XRTD_STS> [XRTD_S_S/W_MODE]       = SFHD  P / F
```

```
0292     C.
0293  +. DC 07-F8 XRT_MAINTENANCE
0294     BC       (03 05)
0295     C.          <XRT1,XRTD_STS> [XRTD_S_S/W_MODE]       = MTNC  P / F
0296     C.
0297   . C. <--- Memory Load for XRTD-DINT --->
0298     C.
0299   . C. CMD:XRT_MEMORY_LOAD(ram-745)
0300     C.
0301   . C. <--- Memory Dump XRTD-DINT       : 0x070000-0x07F7FF --->
0302  +. DC 07-F8 XRT_MEMORY_DUMP
0303     BC       (42 00 07 20 00 32 80 00)
0304     C.          Comparison check with a STORED image    = OK    P / F
0305  +. DC 07-F8 XRT_STBY
0306     BC       (03 01)
0307     C.          <XRT1,XRTD_STS> [XRTD_S_S/W_MODE]       = STBY  P / F
0308     C.
0309  +. DC 07-F8 XRT_OPERATE
0310     BC       (03 02)
0311     C.          <XRT1,XRTD_STS> [XRTD_S_S/W_MODE]       = OPER  P / F
0312     C.
0313   . C. ¢¨¾åµ-Á´¤Æ¤¬Àµ¾ï¤À¤Ã¤¿¾ì¹ç¤Ë¸Â¤ê¡¢°Ê²¼¤ÎOBSV¥³¥Þ¥ó¥É¤òÁ÷¿®¤¹¤ë¡£¡Ê°Ê¾å¤ÇËÜ¼ê½ç¤Ï½ªÎ»¡Ë
0314  +. DC 07-F0 MDP_XRT_MODE_OBSV
0315     BC       (c2)
0316     C.             <MDP ,XRT>      [MDP_XRT__MODE]         = OBSV  P / F
0317     C.
0318   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0319     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0320   . S. DC-BC dcbc-402:DCBC
0321     (MDP_known_event)
0322     C.
0323     C.
0324   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0325   . S. DC-BC dcbc-153:DCBC
0326     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0327     C.
0328     C.
0329   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0330     C.
0331   . C. ********** LOS **********
0332     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-023 2012-07-17 16:25:19 91 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022  +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029  +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038  +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043  +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. ***** AOCS Commands (Orbital Element Update) *****
0046     C. Update the orbital element
0047  +. DC 02-50 AOCU_ORB_PRPGT_START
0048     BC       (16)
0049  +  DC 02-8E AOCU_ORB_UPD
0050     C.
0051     C.       <A_ORB>[ORBIT] EPC = 7023601.2 +- 1.0 (s) [ ]
0052     C.
0053   . C.
0054   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055  +. DC 07-FC EIS_MODE_MANU
0056     BC       (21 02)
0057   . C. Verify EIS in MANUAL mode
0058   . C. Estimated OBSTBL upload time is 23s
0059     C. ********************************
0060     C. EIS START OBSTBL LOAD
0061     C. ********************************
0062   . S. RAM   ram-820:EIS_OBSTBL
0063     ()
0064  +. DC 07-FC EIS_DUMP_OBSTBL
0065     BC       (07 07 07 00 00 70 00)
0066     C.
0067     C. Execute, after the success of OBSTBL upload.
0068     C. Set EIS TI-commands
0069  +. TI 2012-07-17 10:45:50.0
0070     DC 07-FC EIS_MODE_CHG_ENA
0071     BC       (20)
0072   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0073     C. ********************************
0074     C. EIS END OBSTBL LOAD
0075     C. ********************************
0076     C.
0077   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0078     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0079   . S. DC-BC dcbc-402:DCBC
0080     (MDP_known_event)
0081     C.
0082     C.
0083   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0084   . S. DC-BC dcbc-153:DCBC
0085     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086     C.
0087     C.
0088   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0089     C.
0090   . C. *********** LOS ***********
0091     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-024 2012-07-17 16:25:19 231 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸ å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ********************************
0099     C.  SOT table upload
0100     C. ********************************
0101   . C. < Upload DPL table >
0102   +. DC 07-F0 MDP_FG_CTRL_MANU
0103     BC       (51)
0104   . C. ------------------------------------
0105     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0106     C. ------------------------------------
0107     C.
0108     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0109     C.
0110   . S. RAM   ram-271:MDP_DPL
0111     ()
0112     C.
0113   . C. < Dump RAMID=MDP_DPL >
0114   +. DC 07-F0 MDP_DUMP_FGTBL
0115     BC       (82 07 00 38 b8 00 40)
0116     C. ------------------------------------
0117     C.  MDP_DPL verify          = OK    [ ]
0118     C. ------------------------------------
0119     C.
0120     C. STS_CHK¤òON¤Ë¤¹¤ë
0121     C.
0122   . C. < Update MDP DSC PAR1 >
0123   +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0124     BC       (4c)
0125     C.  MDP_CMD_CODE          = F04C0700[ ]
0126     C.  MDP_CMD_CNT        (count-up 1) [ ]
0127     C. ------------------------------------
0128     C.
0129   . C.
0130     C. ********************************
0131     C.  SOT TI command set
0132     C. ********************************
0133     C. Execute, after the success of TBL upload.
0134   +. TI 2012-07-17 10:45:18.0
0135     DC 07-F0 MDP_SOT_MODE_OBSV
0136     BC       (40)
0137   . C. ------------------------------------
0138     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0139     C. ------------------------------------
0140     C.
0141     C.
0142     C. ************ XRT START ************
0143     C.
0144   +. DC 07-F0 MDP_XRT_CTRL_MANU
0145     BC       (c1)
0146   +  DC 07-F0 MDP_XRT_MODE_STBY
0147     BC       (c3)
0148   . C. ------------ Success Verify ?              OK / NG____
0149     C.
0150     C. XRT Obs. Table Upload
0151   . S. RAM   ram-291:MDP_OBS_X
0152     ()
0153     C.
0154   +. DC 07-F0 MDP_DUMP_XRTTBL
0155     BC       (84 07 00 00 00 3a d4)
0156   . C. ------------ Comparison Check ?                       OK / ERR ____
0157     C.
0158     C.
0159   +. DC 07-F0 MDP_XRT_ROI_SET
0160     BC       (cd 01 b1 b1 04 04)
0161   +  DC 07-F0 MDP_XRT_ROI_SET
0162     BC       (cd 02 b1 b1 08 08)
0163   +  DC 07-F0 MDP_XRT_ROI_SET
0164     BC       (cd 03 b1 b1 08 08)
0165   +  DC 07-F0 MDP_XRT_ROI_SET
0166     BC       (cd 04 b1 b1 06 06)
0167   +  DC 07-F0 MDP_XRT_ROI_SET
0168     BC       (cd 06 80 80 20 20)
0169   +  DC 07-F0 MDP_XRT_ROI_SET
0170     BC       (cd 07 80 80 20 08)
0171   +  DC 07-F0 MDP_XRT_ROI_SET
0172     BC       (cd 08 80 80 08 20)
0173   +  DC 07-F0 MDP_XRT_ROI_SET
0174     BC       (cd 09 c0 c0 10 10)
0175   +  DC 07-F0 MDP_XRT_ROI_SET
0176     BC       (cd 0a 40 c0 10 10)
0177   +  DC 07-F0 MDP_XRT_ROI_SET
0178     BC       (cd 0b 40 40 10 10)
0179   +  DC 07-F0 MDP_XRT_ROI_SET
0180     BC       (cd 0c c0 40 10 10)
0181   +  DC 07-F0 MDP_XRT_ROI_SET
0182     BC       (cd 0d 80 60 20 18)
0183   +  DC 07-F0 MDP_XRT_ROI_SET
0184     BC       (cd 0e a0 80 18 20)
0185   +  DC 07-F0 MDP_XRT_ROI_SET
0186     BC       (cd 0f 80 80 06 06)
0187   +  DC 07-F0 MDP_XRT_ROI_SET
0188     BC       (cd 10 80 80 08 08)
0189   +  DC 07-F0 MDP_XRT_FLD_DIS
0190     BC       (d9)
0191   +  DC 07-F0 MDP_XRT_FLRCTRL_DIS
0192     BC       (c9)
0193   +  DC 07-F0 MDP_XRT_AEC_RESET
```

```
0194     BC       (d0)
0195  +  DC 07-F0 MDP_XRT_ARS_DIS
0196     BC       (d5)
0197  +  DC 07-F0 MDP_XRT_FLD_RESET
0198     BC       (da)
0199  +  DC 07-F0 MDP_XRT_QT_PROG_SET
0200     BC       (c4 03)
0201  +  DC 07-F0 MDP_XRT_FL_PROG_SET
0202     BC       (c5 0d)
0203   . C. ---------- Success Verify ?            OK / NG ____
0204     C.
0205     C.
0206   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0207     C.
0208  +. DC 07-F0 MDP_XRT_MODE_OBSV
0209     BC       (c2)
0210  +. TI 2012-07-17 10:45:02.0
0211     DC 07-F0 MDP_XRT_MODE_OBSV
0212     BC       (c2)
0213   . C. ---------- Success Verify ?            OK / NG ____
0214     C.
0215     C. ************ XRT END ***********
0216     C.
0217   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0218     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0219   . S. DC-BC dcbc-402:DCBC
0220     (MDP_known_event)
0221     C.
0222     C.
0223   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0224   . S. DC-BC dcbc-153:DCBC
0225     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0226     C.
0227     C.
0228   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0229     C.
0230   . C. *********** LOS ***********
0231     C.
```

```
*** OP Sequence for XRT ***

2012/07/17  10:56:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 00 00 00 00
2012/07/17  18:00:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/17  18:00:02.0  XRT_TCIB_XRT_S_HTR_A_DIS_437_OG [0x1b5]
                                TCIB_XRT_S_HTR_A_DIS 0 04-C0
2012/07/18  00:04:54.0  XRT_CTRL_MANU_407_OG [0x197]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  00:05:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 2e f9 2e f9
2012/07/18  00:07:32.0  XRT_FOCUS_POSITION_447_OG [0x1bf]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/07/18  00:07:52.0  XRT_QT_PROG_SET_423_OG [0x1a7]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2012/07/18  00:07:54.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/07/18  00:07:56.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/07/18  00:07:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/07/18  00:08:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  00:14:54.0  XRT_CTRL_MANU_407_OG [0x197]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  00:15:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 2e f9 d1 07
2012/07/18  00:17:32.0  XRT_FOCUS_POSITION_447_OG [0x1bf]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/07/18  00:17:52.0  XRT_QT_PROG_SET_430_OG [0x1ae]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2012/07/18  00:17:54.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/07/18  00:17:56.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/07/18  00:17:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/07/18  00:18:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  00:24:54.0  XRT_CTRL_MANU_407_OG [0x197]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  00:25:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 d1 07 d1 07
2012/07/18  00:27:32.0  XRT_FOCUS_POSITION_447_OG [0x1bf]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/07/18  00:27:52.0  XRT_QT_PROG_SET_415_OG [0x19f]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2012/07/18  00:27:54.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/07/18  00:27:56.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/07/18  00:27:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/07/18  00:28:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  00:34:54.0  XRT_CTRL_MANU_407_OG [0x197]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  00:35:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 d1 07 2e f9
2012/07/18  00:37:32.0  XRT_FOCUS_POSITION_447_OG [0x1bf]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/07/18  00:37:52.0  XRT_QT_PROG_SET_434_OG [0x1b2]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2012/07/18  00:37:54.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/07/18  00:37:56.0  XRT_FLRCTRL_DIS_433_OG [0x1b1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/07/18  00:37:58.0  XRT_ARS_DIS_431_OG [0x1af]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/07/18  00:38:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  00:44:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  00:44:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/07/18  00:45:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 00 00 00 00
2012/07/18  00:45:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2012/07/18  00:45:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/07/18  00:45:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2012/07/18  00:45:22.0  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/07/18  00:48:00.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  00:48:02.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2012/07/18  00:48:04.0  XRT_FL_PROG_SET_440_OG [0x1b8]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2012/07/18  00:48:06.0  XRT_CTRL_AUTO_406_OG [0x196]
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                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  01:12:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  01:12:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  01:12:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/18  01:15:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/18  01:41:30.0  XRT_Custom_418_OG [0x1a2]
2012/07/18  01:42:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  02:50:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  02:50:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  02:50:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/18  02:53:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/18  03:20:00.0  XRT_Custom_418_OG [0x1a2]
2012/07/18  03:21:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  04:20:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  04:20:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  04:20:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/18  04:23:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/18  04:58:30.0  XRT_Custom_418_OG [0x1a2]
2012/07/18  04:59:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  06:00:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  06:00:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  06:00:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/18  06:03:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/18  06:37:00.0  XRT_Custom_418_OG [0x1a2]
2012/07/18  06:38:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  06:50:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  08:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  08:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/07/18  09:00:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2012/07/18  09:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/07/18  09:00:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/07/18  09:00:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/07/18  09:00:22.0  XRT_ARS_DIS_438_OG [0x1b6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/07/18  09:03:00.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  09:03:02.5  XRT_QT_PROG_SET_420_OG [0x1a4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2012/07/18  09:03:05.0  XRT_FL_PROG_SET_440_OG [0x1b8]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2012/07/18  09:03:08.0  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  09:20:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  09:20:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  09:20:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/18  09:23:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/18  09:54:00.0  XRT_Custom_418_OG [0x1a2]
2012/07/18  09:55:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  11:03:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  11:03:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  11:03:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/18  11:06:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/18  11:32:30.0  XRT_Custom_418_OG [0x1a2]
2012/07/18  11:33:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  12:42:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
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2012/07/18  12:42:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  12:42:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/18  12:45:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/18  13:11:00.0  XRT_Custom_418_OG [0x1a2]
2012/07/18  13:12:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  14:20:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  14:20:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  14:20:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/18  14:23:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/18  14:59:00.5  XRT_Custom_418_OG [0x1a2]
2012/07/18  15:00:00.5  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  15:59:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  15:59:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  15:59:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/18  16:02:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/18  16:46:00.0  XRT_Custom_418_OG [0x1a2]
2012/07/18  16:47:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  17:37:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  17:37:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  17:37:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/18  17:40:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/18  18:22:30.0  XRT_Custom_418_OG [0x1a2]
2012/07/18  18:23:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  19:16:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  19:16:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  19:16:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/18  19:19:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/18  19:59:30.0  XRT_Custom_418_OG [0x1a2]
2012/07/18  20:00:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  20:54:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  20:54:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  20:54:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/18  20:57:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/18  21:36:30.0  XRT_Custom_418_OG [0x1a2]
2012/07/18  21:37:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/18  22:33:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/18  22:33:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/18  22:33:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/18  22:36:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/18  23:11:30.0  XRT_Custom_418_OG [0x1a2]
2012/07/18  23:12:30.5  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/19  00:11:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/19  00:11:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/19  00:11:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/19  00:14:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/19  00:40:00.0  XRT_Custom_418_OG [0x1a2]
2012/07/19  00:41:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/19  00:59:54.0  XRT_CTRL_MANU_417_OG [0x1a1]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/19  01:00:00.5  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 ac 00 00 00
2012/07/19  01:14:28.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/07/19  01:14:48.0  XRT_FLD_DIS_425_OG [0x1a9]
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                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2012/07/19  01:14:50.0  XRT_FLRCTRL_DIS_446_OG [0x1be]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/07/19  01:14:52.0  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2012/07/19  01:14:54.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/07/19  01:14:56.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/19  01:14:58.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2012/07/19  01:15:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/19  02:59:54.0  XRT_CTRL_MANU_417_OG [0x1a1]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/19  03:00:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                    AOCU_NM              5 02-76 00 00 00 54 cc
2012/07/19  03:14:28.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/07/19  03:14:48.0  XRT_FLD_DIS_425_OG [0x1a9]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2012/07/19  03:14:50.0  XRT_FLRCTRL_DIS_446_OG [0x1be]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/07/19  03:14:52.0  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2012/07/19  03:14:54.0  XRT_ARS_DIS_431_OG [0x1af]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/07/19  03:14:56.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/19  03:14:58.0  XRT_QT_PROG_SET_405_OG [0x195]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2012/07/19  03:15:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/19  04:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/19  04:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/07/19  05:00:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                    AOCU_NM              5 02-76 03 00 00 00 00
2012/07/19  05:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2012/07/19  05:00:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/07/19  05:00:20.0  XRT_AEC_RESET_443_OG [0x1bb]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2012/07/19  05:00:22.0  XRT_ARS_DIS_438_OG [0x1b6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2012/07/19  05:03:00.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/19  05:03:02.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2012/07/19  05:03:04.0  XRT_FL_PROG_SET_440_OG [0x1b8]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2012/07/19  05:35:30.0  XRT_Custom_418_OG [0x1a2]
2012/07/19  05:36:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/19  06:38:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/19  06:38:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/19  06:38:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/19  06:41:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/19  07:14:00.0  XRT_Custom_418_OG [0x1a2]
2012/07/19  07:15:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/07/19  07:40:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/19  08:18:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/07/19  08:18:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2012/07/19  08:18:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/07/19  08:21:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/07/19  08:40:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                    AOCU_NM              5 02-76 00 1e a7 b3 1b
2012/07/19  10:33:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                    AOCU_NM              5 02-76 00 00 00 00 00
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