

XRT Timeline to be uploaded on 2012/07/21

Period: 2012/07/21 10:31:00 - 2012/07/26 09:16:00

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Normal mode

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XOB #1910: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 70s cad With G-band Test												
Term	Pointing (x, y)		Comment									
07/21 10:44:00 - 07/21 17:37:24	Track (-89.0, -428.9)	@ 07/21 10:41:00	# OP start + 10min, track AR 11524/525.								
07/21 20:50:30 - 07/22 05:59:54	Track (-5.2, -429.9)	@ 07/21 20:47:30	* AR 11524/525								
07/22 06:13:00 - 07/22 09:59:54	Track (72.7, -430.2)	@ 07/22 06:10:00	# Cont.								
07/22 18:06:00 - 07/23 02:40:30	Track (170.4, -429.6)	@ 07/22 18:03:00	# AR 11524/525								
07/23 03:10:30 - 07/23 03:57:54	Track (234.7, -428.6)	@ 07/23 02:00:00	* HOP 173, AR 11524/525, joint with HS students.								
07/23 04:49:00 - 07/23 05:50:00	Track (252.0, -428.3)	@ 07/23 04:10:00	# Cont.								
07/23 06:27:30 - 07/23 08:59:54	Track (267.9, -427.9)	@ 07/23 06:10:00	* Cont. AR obs								
07/23 15:03:00 - 07/23 17:27:30	Track (336.8, -426.1)	@ 07/23 15:00:00	* AR 11524/525								
07/23 18:26:00 - 07/24 01:40:00	Track (362.5, -425.2)	@ 07/23 18:23:00	# Cont.								
07/24 02:09:00 - 07/24 03:59:54	Track (419.2, -423.0)	@ 07/24 02:00:00	* HOP 173, AR11524/525, joint with HS students.								
07/24 04:13:00 - 07/24 06:28:00	Track (434.9, -422.3)	@ 07/24 04:10:00	# Cont.								
07/24 07:04:30 - 07/24 09:48:30	Track (448.0, -421.6)	@ 07/24 06:00:00	* Cont., AR obs.								
PROG= 08 Inf.-time(s)												
Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 83	2-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
└─ Seqn= 35	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 1	4-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 3	1-time(s)	2.0sec										
└─ Seqn= 53	50-time(s)	70.0sec										
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18FF: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-band(12)												
Term	Pointing (x, y)		Comment									
07/21 17:40:30 - 07/21 17:47:24	Fixed (0.0, 0.0)	synoptic, shifted -22.5 min									
07/21 18:37:00 - 07/21 20:47:24	Fixed (-932.0, 0.0)	# E limb obs, EIS joint Fe XIII obs with COMP.									
07/22 06:03:00 - 07/22 06:09:54	Fixed (0.0, 0.0)	synoptic									
07/22 17:56:00 - 07/22 18:02:54	Fixed (0.0, 0.0)	synoptic, shifted -7.0 min									
07/23 04:01:00 - 07/23 04:09:30	Fixed (0.0, 0.0)	synoptic, shifted manually									
07/23 18:16:00 - 07/23 18:22:54	Fixed (0.0, 0.0)	synoptic, shifted 13.0 min									
07/24 04:03:00 - 07/24 04:09:54	Fixed (0.0, 0.0)	synoptic, shifted manually.									
PROG= 03 1-time(s)												
└─ Subr= 1	1-time(s)	12.0sec										
└─ Seqn= 46	1-time(s)	4.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 69	1-time(s)	4.0sec										
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 9	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #190F: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384 45ms												
Term	Pointing (x, y)		Comment									
07/22 10:03:00 - 07/22 17:52:54	Fixed (-4.0, 854.0)	* HOP 81, polar monitoring, N pole.									
07/23 09:03:00 - 07/23 14:59:54	Fixed (-4.0, -966.0)	* HOP 81, polar monitoring, S pole.									
PROG= 20 Inf.-time(s)												
└─ Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 83	2-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec

Subr= 2	1-time(s)	2.0sec																		
└ Seqn= 81	1-time(s)	30.0sec																		
└└ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec								
Subr= 3	30-time(s)	2.0sec																		
└ Seqn= 58	2-time(s)	30.0sec																		
└└ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec								
└└ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec								
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

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Flare mode

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XOB #1909: Flare Standard Obs. with eruptions mode-A (FW1=Open)-45ms G-band																				
Term		Pointing (x, y)										Comment								
07/21 10:44:00 - 07/21 17:37:24		Track (-89.0, -428.9) <small>Ⓔ 07/21 10:41:00</small>										# OP start + 10min, track AR 11524/525.								
07/21 20:50:30 - 07/22 05:59:54		Track (-5.2, -429.9) <small>Ⓔ 07/21 20:47:30</small>										* AR 11524/525								
07/22 06:13:00 - 07/22 09:59:54		Track (72.7, -430.2) <small>Ⓔ 07/22 06:10:00</small>										# Cont.								
07/22 10:03:00 - 07/22 17:52:54		Fixed (-4.0, 854.0)										* HOP 81, polar monitoring, N pole.								
07/22 18:06:00 - 07/23 02:40:30		Track (170.4, -429.6) <small>Ⓔ 07/22 18:03:00</small>										# AR 11524/525								
07/23 03:10:30 - 07/23 03:57:54		Track (234.7, -428.6) <small>Ⓔ 07/23 02:00:00</small>										* HOP 173, AR 11524/525, joint with HS students.								
07/23 04:49:00 - 07/23 05:50:00		Track (252.0, -428.3) <small>Ⓔ 07/23 04:10:00</small>										# Cont.								
07/23 06:27:30 - 07/23 08:59:54		Track (267.9, -427.9) <small>Ⓔ 07/23 06:10:00</small>										* Cont. AR obs								
07/23 09:03:00 - 07/23 14:59:54		Fixed (-4.0, -966.0)										* HOP 81, polar monitoring, S pole.								
07/23 15:03:00 - 07/23 17:27:30		Track (336.8, -426.1) <small>Ⓔ 07/23 15:00:00</small>										* AR 11524/525								
07/23 18:26:00 - 07/24 01:40:00		Track (362.5, -425.2) <small>Ⓔ 07/23 18:23:00</small>										# Cont.								
07/24 02:09:00 - 07/24 03:59:54		Track (419.2, -423.0) <small>Ⓔ 07/24 02:00:00</small>										* HOP 173, AR11524/525, joint with HS students.								
07/24 04:13:00 - 07/24 06:28:00		Track (434.9, -422.3) <small>Ⓔ 07/24 04:10:00</small>										# Cont.								
07/24 07:04:30 - 07/24 09:48:30		Track (448.0, -421.6) <small>Ⓔ 07/24 06:00:00</small>										* Cont., AR obs.								
PROG= 14 1-time(s)																				
Subr= 1 30-time(s) 20.0sec																				
└ Seqn= 88 1-time(s) 2.0sec																				
└└ Open/thick-Al		Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec							
└└ Open/thick-Be		Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec							
└ Seqn=100 1-time(s) 2.0sec																				
└└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec							
└└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec							
Subr= 2 1-time(s) 2.0sec																				
└ Seqn= 43 1-time(s) 2.0sec																				
└└ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec							
└└ Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec							
└└ Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec							
Subr= 3 30-time(s) 60.0sec																				
└ Seqn= 88 1-time(s) 2.0sec																				
└└ Open/thick-Al		Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec							
└└ Open/thick-Be		Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec							
└ Seqn= 60 1-time(s) 2.0sec																				
└└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec							
└└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec							
Subr= 2 1-time(s) 2.0sec																				
└ Seqn= 43 1-time(s) 2.0sec																				
└└ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec							
└└ Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec							
└└ Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec							
Subr= 3 30-time(s) 60.0sec																				
└ Seqn= 88 1-time(s) 2.0sec																				
└└ Open/thick-Al		Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec							
└└ Open/thick-Be		Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec							
└ Seqn= 60 1-time(s) 2.0sec																				
└└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec							
└└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec							
Subr= 2 1-time(s) 2.0sec																				
└ Seqn= 43 1-time(s) 2.0sec																				
└└ Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec							
└└ Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec							
└└ Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec							
Subr= 3 30-time(s) 60.0sec																				
└ Seqn= 88 1-time(s) 2.0sec																				
└└ Open/thick-Al		Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec							
└└ Open/thick-Be		Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec							
└ Seqn= 60 1-time(s) 2.0sec																				
└└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec							
└└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec							
Subr= 4 24-time(s) 600.0sec																				
└ Seqn= 89 1-time(s) 2.0sec																				
└└ Open/Al-mesh		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec							
└└ Open/Ti-poly		Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec							
└└ Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec							
└└ Open/Al-mesh		Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

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Active Region Search

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NOT USED

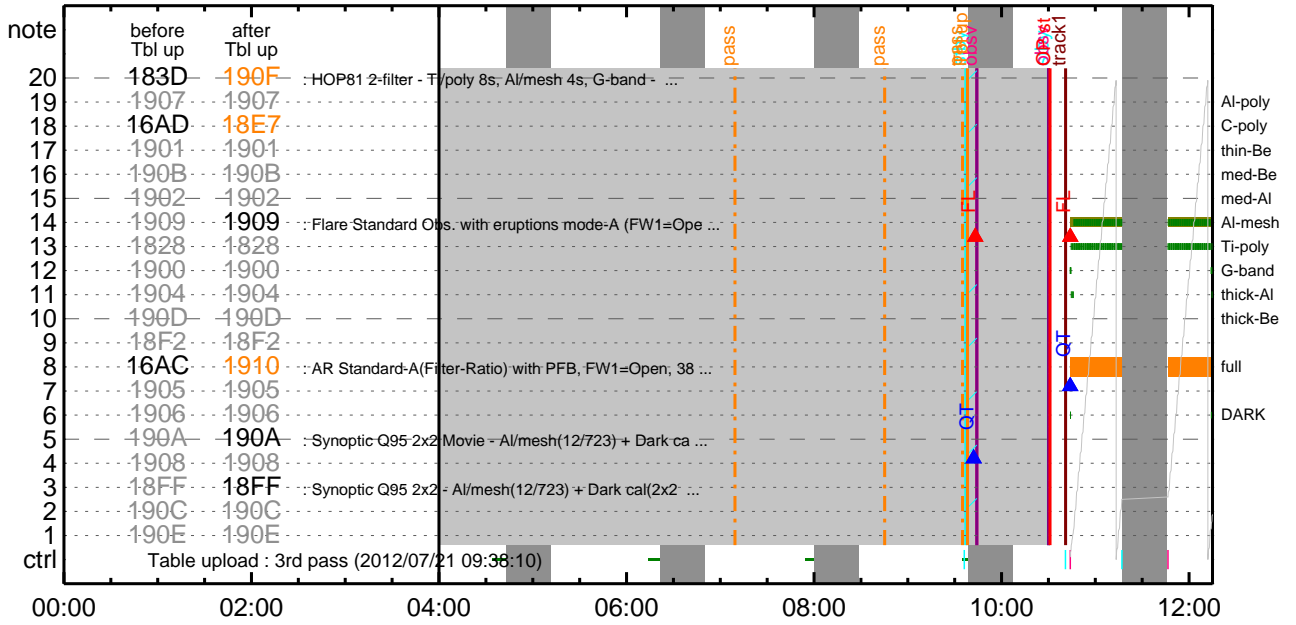
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Flare Detection

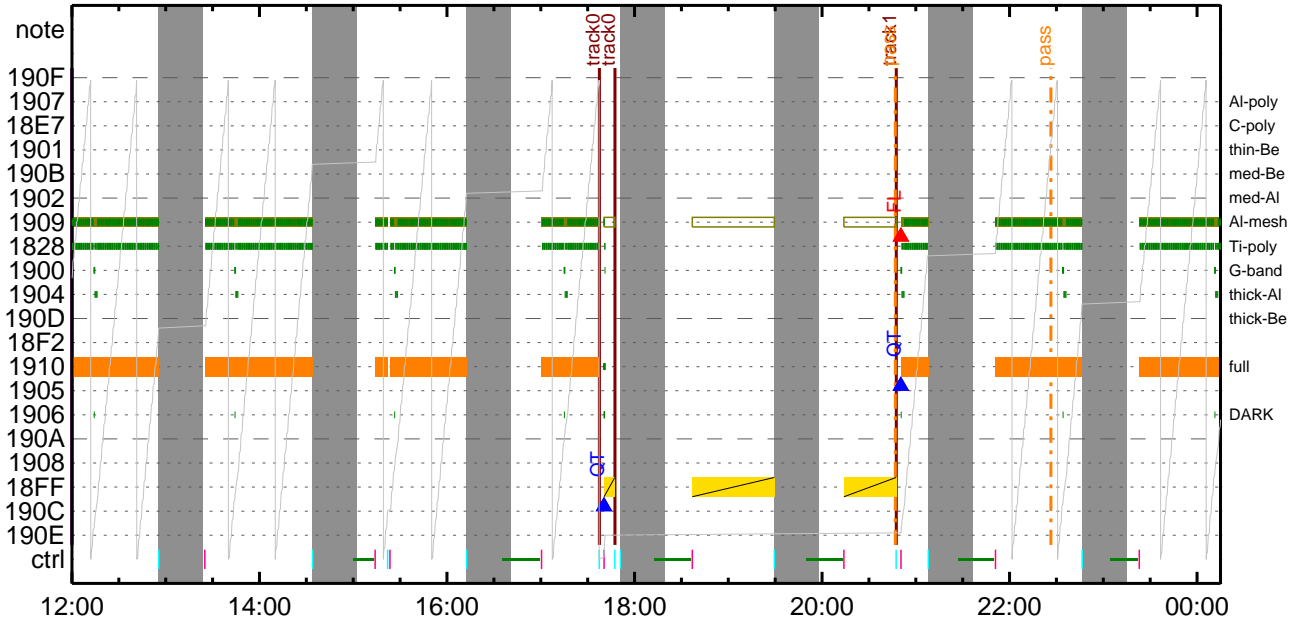
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FLD Patrol												
Term	Pointing (x, y)							Comment				
07/21 10:43:46 - 07/21 17:37:46	Track (-89.0,	-428.9)	<small>@ 07/21 10:41:00</small>	# OP start + 10min, track AR 11524/525.							
07/21 20:50:16 - 07/22 06:00:16	Track (-5.2,	-429.9)	<small>@ 07/21 20:47:30</small>	* AR 11524/525							
07/22 06:12:46 - 07/22 17:53:16	Track (72.7,	-430.2)	<small>@ 07/22 06:10:00</small>	# Cont.							
07/22 18:05:46 - 07/23 03:58:16	Track (170.4,	-429.6)	<small>@ 07/22 18:03:00</small>	# AR 11524/525							
07/23 04:16:46 - 07/23 18:13:16	Track (252.0,	-428.3)	<small>@ 07/23 04:10:00</small>	# Cont.							
07/23 18:25:46 - 07/24 04:00:16	Track (362.5,	-425.2)	<small>@ 07/23 18:23:00</small>	# Cont.							
07/24 04:12:46 - 07/26 09:16:00	Track (434.9,	-422.3)	<small>@ 07/24 04:10:00</small>	# Cont.							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

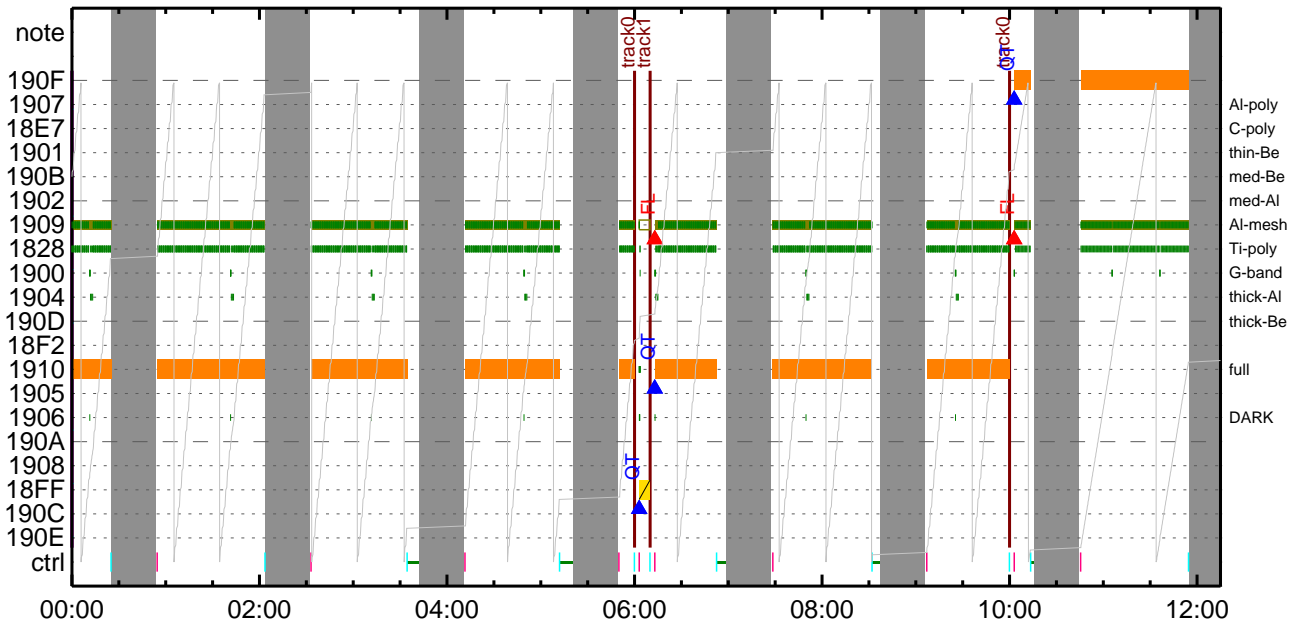
CMDI #0786 2012/07/21



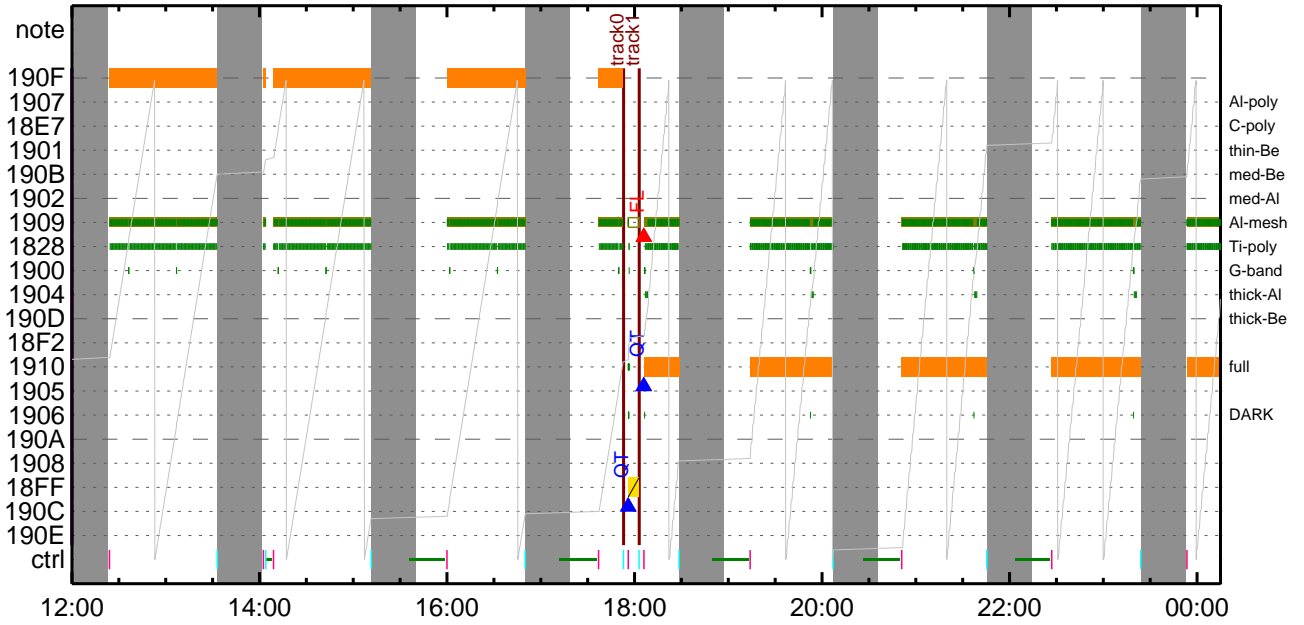
CMDI #0786 2012/07/21



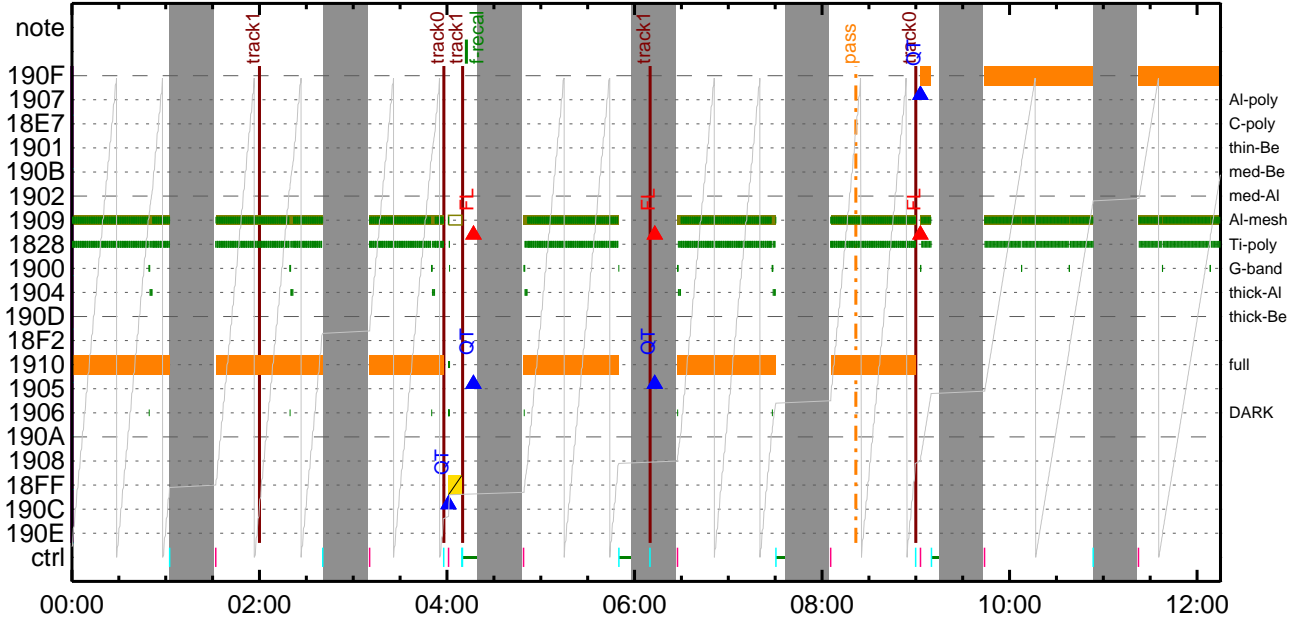
CMDI #0786 2012/07/22



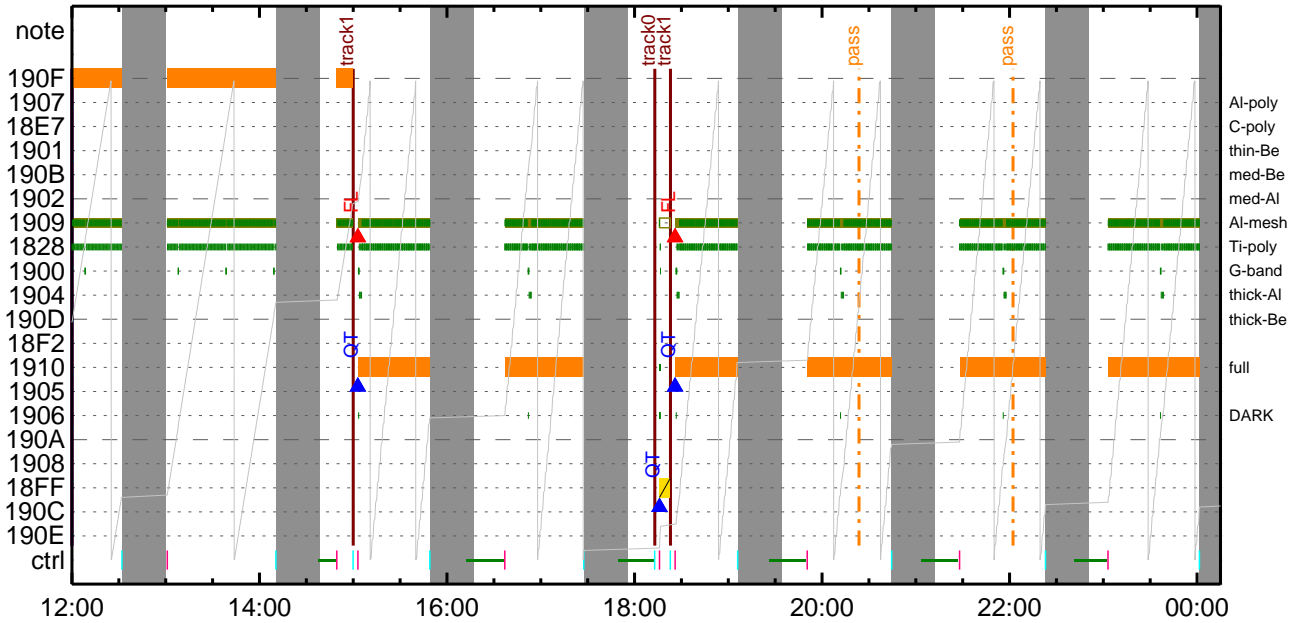
CMDI #0786 2012/07/22



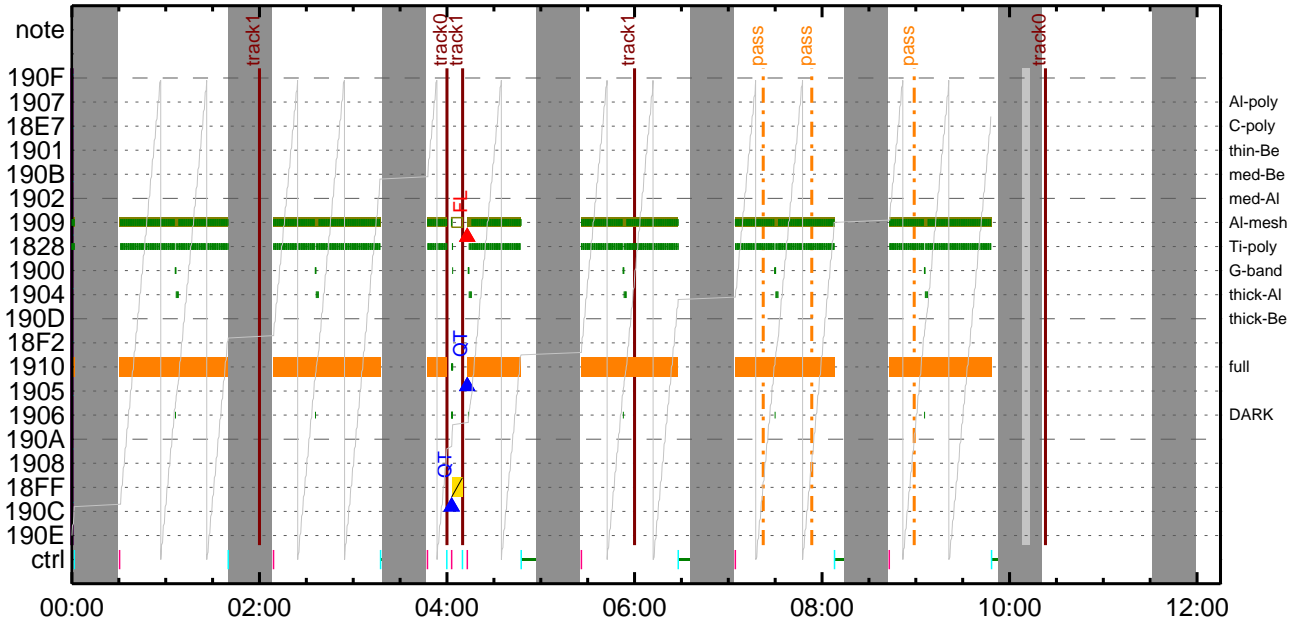
CMDI #0786 2012/07/23



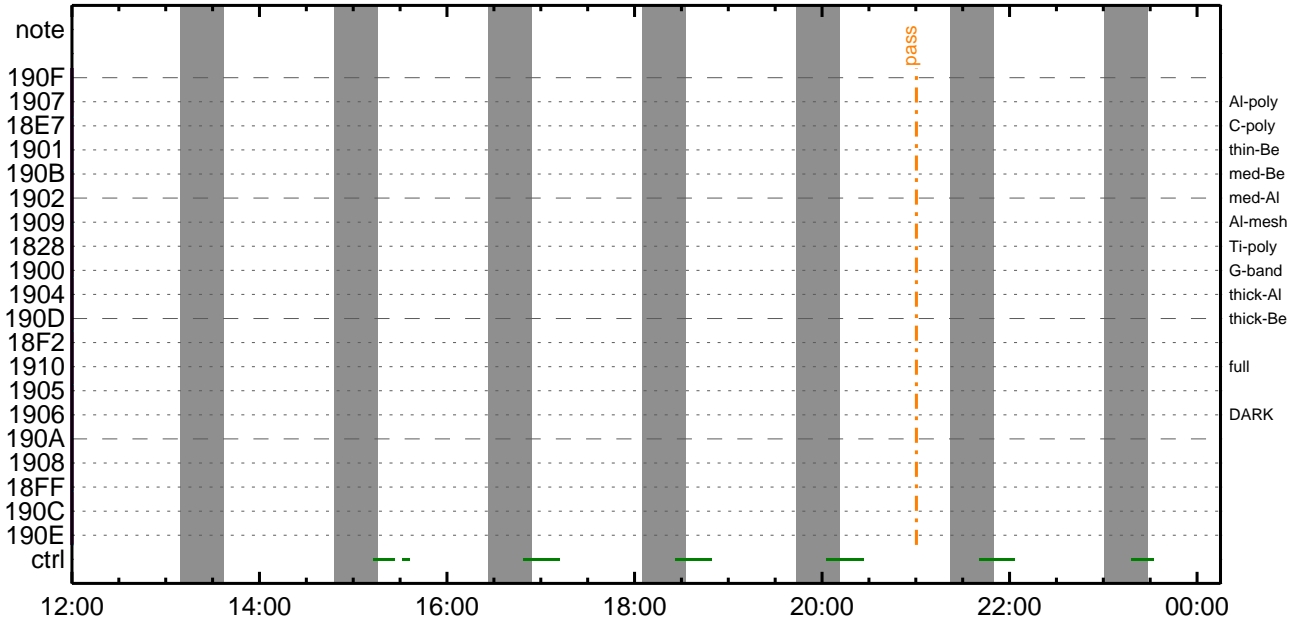
CMDI #0786 2012/07/23



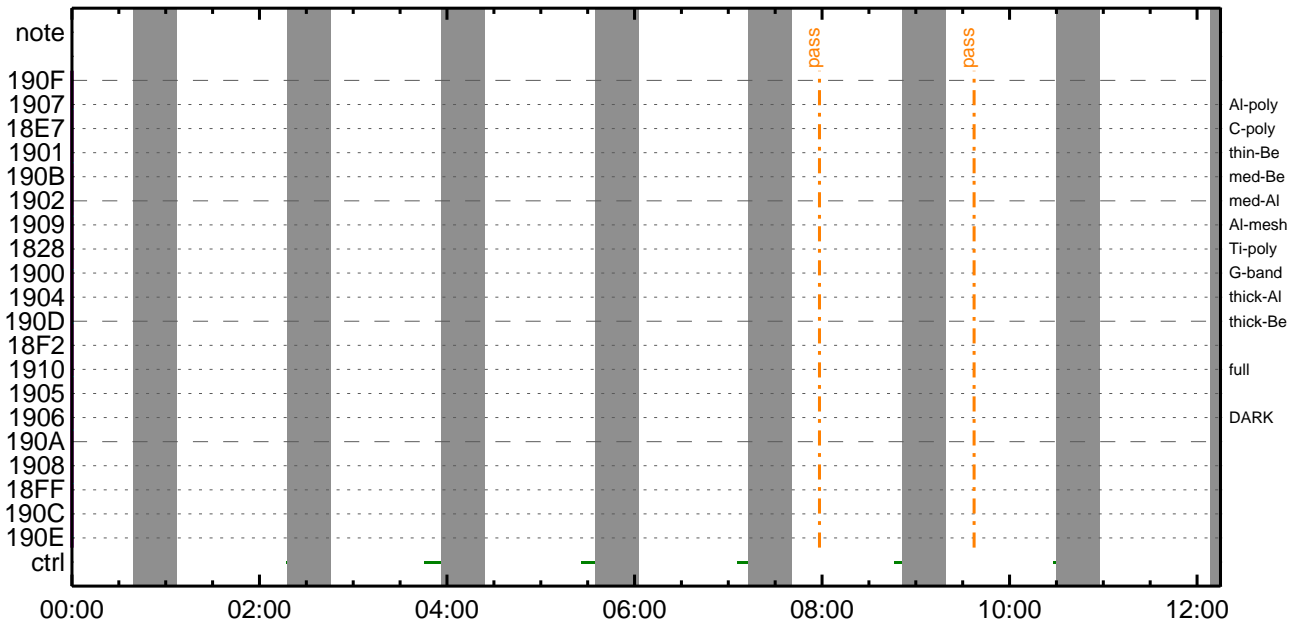
CMDI #0786 2012/07/24



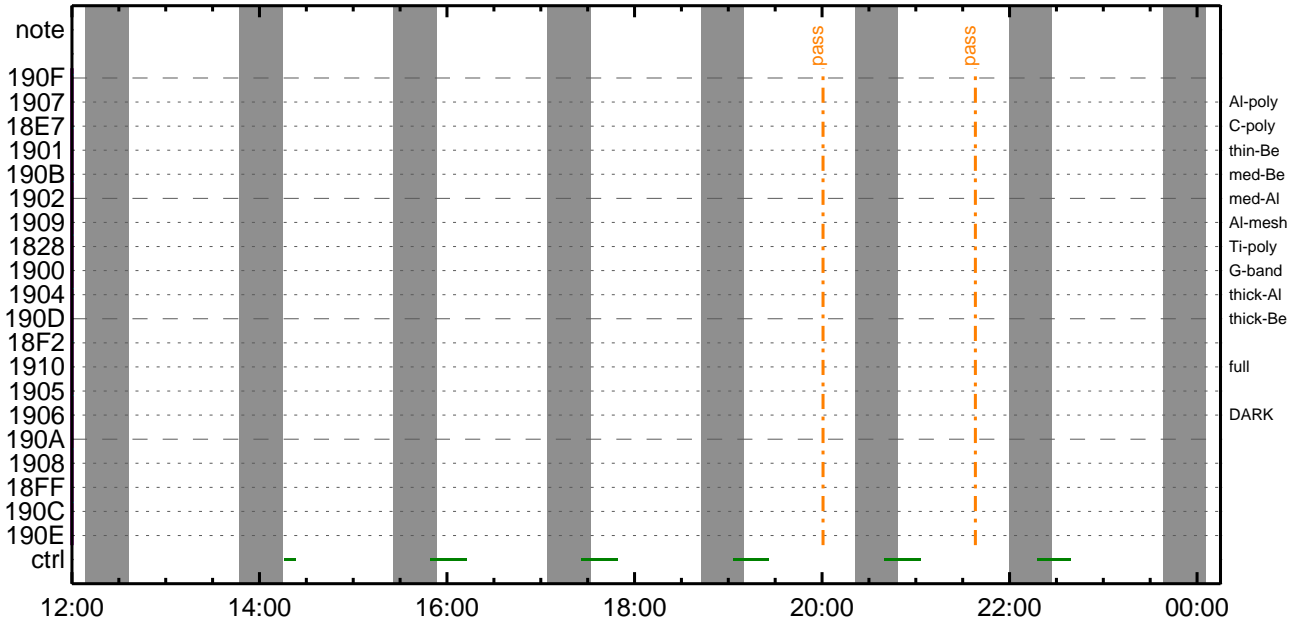
CMDI #0786 2012/07/24



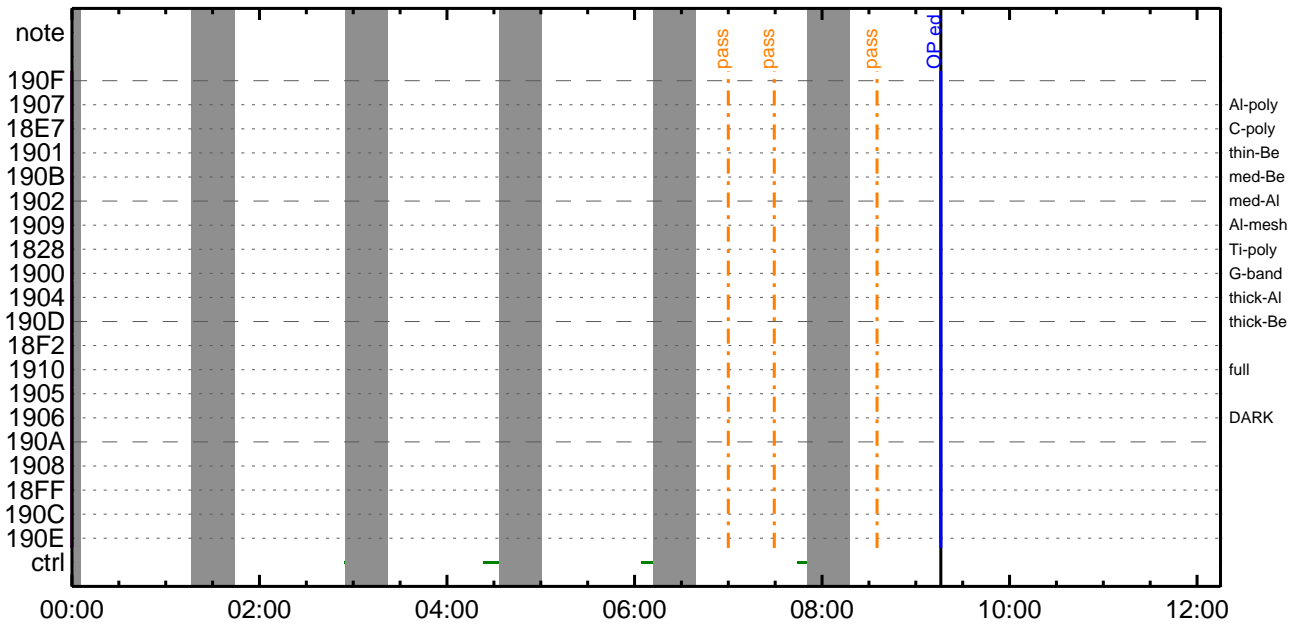
CMDI #0786 2012/07/25



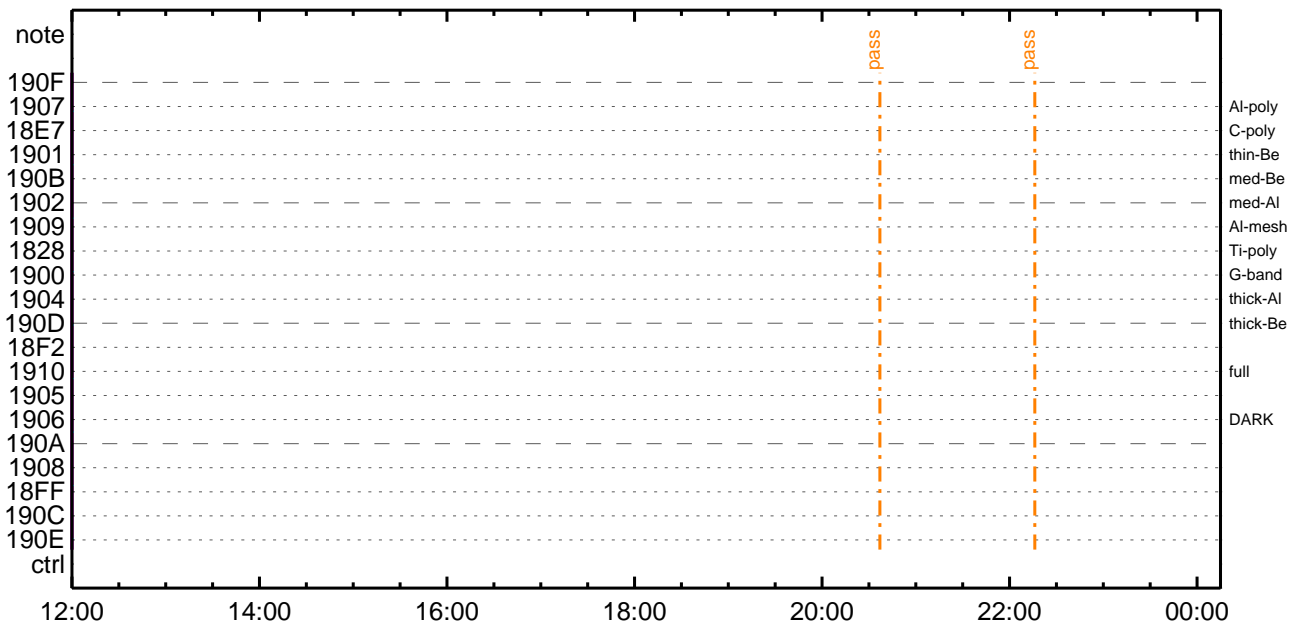
CMDI #0786 2012/07/25



CMDI #0786 2012/07/26



CMDI #0786 2012/07/26




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-031:OP
0104 ( )
0105 S. OG og-031:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ª î»ð³ ÎÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGαî¼E¹ç•è² İOKαð³ ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ª î»ð³ ÎÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGαî¼E¹ç•è² İOKαð³ ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ª î»ð³ ÎÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPαî¼E¹ç•è² İOKαð³ ÎÇ§
0165 C.
0166 C. ***** °Ê²¼α İ¼Ä´ ¶Á° ÒÊÊ¬α ÒÄ÷¿® (¼âµ-YAYOXx½ª ê¼çα ðÄÖÆα Ç¼ª α° ðê¼i¹ çççâ) *****
0167 C. DHUYâ;4YE;Ê¼Y½, Yî;4YE;Êα ð Îãα¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ; § OPOG UPLOADα-Á÷¿@NGUî¼i¹ç; ç°Ê²¼α îTI-CMDÁ÷¿@α İ¼Ä¹ Öα• ðÊααα³ ðÊ; f
0180 C. αßα¿; çSETα ÊDUMPα İÆ±° iYNY¹ ç¹ Öα | α³ ðÊ; f
0181 C.
0182 C. TIY³ YP YOXE ð ð Ä Î ¿ (UT)
0183 +. TI 2012-07-21 10:26:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-07-21 10:26:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-07-21 10:26:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2012-07-21 10:30:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °È²¼□îÄë%îíñ□îŷÄŷ§ŷÄŷ-¹âiü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 . C. *****
0206 C. TIíî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]    EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 . C. ŷÄŷÖŷ×½ª î»□ò³ îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 . C. RAM ID=TI_TBL□îŷÄŷÖŷ×ªª² îOK□ò³ îÇ§
0229 C.
0230 . C. DHUŷª;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;È□òîª□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-07-21 10:30:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 . C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 . C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-07-21 10:30:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2012-07-21 10:30:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 . C.          [ ] [HK1_TI_CMD_NUM]     EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-07-21 10:30:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 . C.          [ ] [HK1_TI_CMD_NUM]     EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 . C. ***** MDP ´ûÄî□î»ö¼ŷ□ÈÄ□¹□èDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÈŷŷŷÄŷÇŷè□È¼□□¼Ä»Ûª□è)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ŷÐŷ¹•î Daily±çîñ□èŷ´Øª¹□èDCBC•x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 . C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STSI>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. *****
0130 C. FPP DC MODE OFF
0131 C. *****
0132 C.
0133 . C. < FPP Decontam Mode Disable >
0134 +. DC 07-F2 FPP_PWR_ON_ENA
0135 BC (12 02)
0136 +. DC 07-F2 FPP_DE_CONTAM_MODE
0137 BC (24 03 00)
0138 . C. -----
0139 C. FPP_DE_CONTAM_MODE = OFF [ ]
0140 C. -----
0141 C.
0142 . C. < Set duties for CCD Op heaters >
0143 +. DC 07-F2 FPP_FG_OPHTR_DUTY
0144 BC (54 03 00)
0145 +. DC 07-F2 FPP_SP_OPHTR_DUTY
0146 BC (4c 03 19)
0147 . C. -----
0148 C. FPP_FG_CCDHTR_DC = 0 [ ]
0149 C. FPP_SP_CCDHTR_DC = 25 [ ]
0150 C. -----
0151 C.
0152 . C. < Enable SP Heater >
0153 +. DC 07-F2 FPP_PWR_ON_ENA
0154 BC (12 02)
0155 +. DC 07-F2 FPP_SP_OPHTR_ENA
0156 BC (48 02)
0157 . C. -----
0158 C. FPP_SPHTR_STAT > 01 [ ]
0159 C. -----
0160 C.
0161 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0162 +. DC 07-FC EIS_MODE_MANU
0163 BC (21 02)
0164 . C. Verify EIS in MANUAL mode
0165 . C. Estimated OBSTBL upload time is 50s
0166 C. *****
0167 C. EIS START OBSTBL LOAD
0168 C. *****
0169 . S. RAM ram-820:EIS_OBSTBL
0170 ( )
0171 +. DC 07-FC EIS_DUMP_OBSTBL
0172 BC (07 07 07 00 00 70 00)
0173 C.
0174 C. Execute, after the success of OBSTBL upload.
0175 C. Set EIS TI-commands
0176 +. TI 2012-07-21 10:30:50.0
0177 DC 07-FC EIS_MODE_CHG_ENA
0178 BC (20)
0179 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0180 C. *****
0181 C. EIS END OBSTBL LOAD
0182 C. *****
0183 C.
0184 . C. ***** MDP `úÃîï»ò%ÿñÊðï¹èDCBC•x²è *****
0185 C. (%ã°îÿÓÿÃÿÊÿÛÿÿÿáÿçÿèñ½¾¼¼À»Ûñ¹è)
0186 . S. DC-BC dcbc-402:DCBC
0187 (MDP_known_event)
0188 C.
0189 C.
0190 . C. ***** ÿÐÿ¹•Ï Daily±¿îÑñÊ´Øñ¹èDCBC•x²è *****
0191 . S. DC-BC dcbc-153:DCBC
0192 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0193 C.

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0194 C.
0195 . C. ;ãLOS¥Á¥§¥Ã¥-¼Á»Û;ä
0196 C.
0197 . C. ***** LOS *****
0198 C.


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0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 80 80 08 20)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 85 83 08 08)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0a 85 83 06 06)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0f 80 80 06 06)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 10 80 80 08 08)
0106 + DC 07-F0 MDP_XRT_FLD_DIS
0107 BC (d9)
0108 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0109 BC (c9)
0110 + DC 07-F0 MDP_XRT_AEC_RESET
0111 BC (d0)
0112 + DC 07-F0 MDP_XRT_ARS_DIS
0113 BC (d5)
0114 + DC 07-F0 MDP_XRT_FLD_RESET
0115 BC (da)
0116 + DC 07-F0 MDP_XRT_QT_PROG_SET
0117 BC (c4 05)
0118 + DC 07-F0 MDP_XRT_FL_PROG_SET
0119 BC (c5 0e)
0120 . C. ----- Success Verify ? OK / NG ____
0121 C.
0122 C.
0123 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0124 C.
0125 +. DC 07-F0 MDP_XRT_MODE_OBSV
0126 BC (c2)
0127 +. TI 2012-07-21 10:30:02.0
0128 DC 07-F0 MDP_XRT_MODE_OBSV
0129 BC (c2)
0130 . C. ----- Success Verify ? OK / NG ____
0131 C.
0132 C. ***** XRT END *****
0133 C.
0134 . C. ***** MDP 'úÃîñî»ö¼ýñëâðñ¹ñëDCBC•x²è *****
0135 C. (%á°îÿÓÿÄÿÉÿÏÿËÿáÿçÿèñ¼ññ¼Ä»Ûñ¹ñè)
0136 . S. DC-BC dcbc-402:DCBC
0137 (MDP_known_event)
0138 C.
0139 C.
0140 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎññÉ´Øñ¹ñëDCBC•x²è *****
0141 . S. DC-BC dcbc-153:DCBC
0142 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0143 C.
0144 C.
0145 . C. ;ãLOSÿÃÿ§ÿÄÿÿ¼Ä»Û;ã
0146 C.
0147 . C. ***** LOS *****
0148 C.
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*** OP Sequence for XRT ***

2012/07/21	10:40:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/21	10:41:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/07/21	10:43:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/07/21	10:43:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/07/21	10:43:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/07/21	10:43:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/07/21	10:43:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/21	10:43:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/21	10:43:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2012/07/21	10:43:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2012/07/21	10:44:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/21	11:17:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/21	11:17:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/21	11:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/21	11:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/21	11:45:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/21	11:46:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/21	12:55:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/21	12:55:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/21	12:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/21	12:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/21	13:24:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/21	13:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/21	14:34:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/21	14:34:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/21	14:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/21	14:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/21	15:13:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/21	15:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/21	15:22:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/21	15:22:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/21	15:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/21	15:22:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/21	15:23:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/21	15:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/21	16:12:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/21	16:12:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/21	16:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/21	16:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/21	16:59:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/21	17:00:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/21	17:37:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/21	17:37:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/07/21	17:37:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/07/21	17:37:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/07/21	17:37:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/07/21	17:37:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/21	17:40:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				

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2012/07/21	17:40:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/21	17:47:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/21	17:47:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 52	d9			
2012/07/21	17:51:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/21	17:51:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/21	17:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/21	17:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/21	18:36:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/21	18:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/21	19:29:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/21	19:29:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/21	19:29:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/21	19:32:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/21	20:13:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/21	20:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/21	20:47:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/21	20:47:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/07/21	20:49:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/07/21	20:50:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/07/21	20:50:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/07/21	20:50:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/07/21	20:50:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/21	20:50:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/21	20:50:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2012/07/21	20:50:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2012/07/21	20:50:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/21	21:08:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/21	21:08:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/21	21:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/21	21:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/21	21:50:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/21	21:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/21	22:46:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/21	22:46:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/21	22:46:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/21	22:49:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/21	23:22:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/21	23:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/22	00:25:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/22	00:25:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/22	00:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/22	00:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/22	00:53:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/22	00:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/22	02:03:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/22	02:03:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/22	02:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/22	02:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/22	02:32:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/22	02:33:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							

2012/07/22	03:34:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/22	03:34:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/07/22	03:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/07/22	03:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/07/22	04:10:30.0	XRT_Custom_418_OG [0x1a2]						
2012/07/22	04:11:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/07/22	05:12:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/22	05:12:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/07/22	05:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/07/22	05:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/07/22	05:49:00.0	XRT_Custom_418_OG [0x1a2]						
2012/07/22	05:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/07/22	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/22	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/07/22	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2012/07/22	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/07/22	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/07/22	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/07/22	06:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2012/07/22	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/07/22	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/22	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00		
2012/07/22	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/07/22	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/07/22	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/07/22	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/07/22	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/07/22	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/07/22	06:12:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2012/07/22	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e		
2012/07/22	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/07/22	06:52:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/22	06:52:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/07/22	06:52:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/07/22	06:55:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/07/22	07:27:30.0	XRT_Custom_418_OG [0x1a2]						
2012/07/22	07:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/07/22	08:32:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/22	08:32:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2012/07/22	08:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/07/22	08:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/07/22	09:06:00.0	XRT_Custom_418_OG [0x1a2]						
2012/07/22	09:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/07/22	09:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/07/22	10:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 b4 19 00 5a		
2012/07/22	10:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/07/22	10:02:46.0	XRT_FLD_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/07/22	10:02:48.0	XRT_FLRCTRL_ENA_428_OG [0x1ac]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		

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2012/07/22	10:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/07/22	10:02:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/22	10:02:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/22	10:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2012/07/22	10:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2012/07/22	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/22	10:13:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/22	10:13:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/22	10:13:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/22	10:16:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/22	10:44:30.0	XRT_Custom_418_OG [0x1a2]			
2012/07/22	10:45:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/22	11:54:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/22	11:54:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/22	11:54:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/22	11:57:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/22	12:23:00.0	XRT_Custom_418_OG [0x1a2]			
2012/07/22	12:24:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/22	13:33:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/22	13:33:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/22	13:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/22	13:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/22	14:01:30.0	XRT_Custom_418_OG [0x1a2]			
2012/07/22	14:02:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/22	14:04:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/22	14:04:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/22	14:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/22	14:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/22	14:08:00.0	XRT_Custom_418_OG [0x1a2]			
2012/07/22	14:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/22	15:11:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/22	15:11:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/22	15:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/22	15:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/22	15:59:00.0	XRT_Custom_418_OG [0x1a2]			
2012/07/22	16:00:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/22	16:50:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/22	16:50:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/22	16:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/22	16:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/22	17:36:00.0	XRT_Custom_418_OG [0x1a2]			
2012/07/22	17:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/22	17:52:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/22	17:52:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/07/22	17:53:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/07/22	17:53:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/07/22	17:53:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/07/22	17:53:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/22	17:55:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2012/07/22	17:56:00.0	XRT_CTRL_AUTO_406_OG [0x196]			

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2012/07/22	18:02:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/22	18:03:00.0	AOCS_Ore-point_Start_1_OG [0x097]		5	02-76	01 00 00 00 00
			AOCU_NM			
2012/07/22	18:05:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/07/22	18:05:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/07/22	18:05:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/07/22	18:05:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/07/22	18:05:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/07/22	18:05:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/22	18:05:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2012/07/22	18:05:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2012/07/22	18:06:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/22	18:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/22	18:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/22	18:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/22	18:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/22	19:13:00.0	XRT_Custom_418_OG [0x1a2]				
2012/07/22	19:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/22	20:07:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/22	20:07:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/22	20:07:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/22	20:10:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/22	20:50:00.0	XRT_Custom_418_OG [0x1a2]				
2012/07/22	20:51:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/22	21:45:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/22	21:45:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/22	21:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/22	21:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/22	22:26:00.5	XRT_Custom_418_OG [0x1a2]				
2012/07/22	22:27:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/22	23:24:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/22	23:24:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/22	23:24:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/22	23:27:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/22	23:52:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/22	23:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/23	01:02:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/23	01:02:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/23	01:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/23	01:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/23	01:31:00.0	XRT_Custom_418_OG [0x1a2]				
2012/07/23	01:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/23	02:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]		5	02-76	01 00 00 00 00
			AOCU_NM			
2012/07/23	02:40:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/07/23	02:40:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/07/23	02:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/07/23	02:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/07/23	03:09:30.0	XRT_Custom_418_OG [0x1a2]				
2012/07/23	03:10:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/07/23	03:57:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2012/07/23	03:57:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/07/23	03:58:00.0	AOCs_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2012/07/23	03:58:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/07/23	03:58:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/07/23	03:58:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/23	04:00:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2012/07/23	04:01:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	04:09:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	04:09:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	04:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/23	04:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	04:10:00.0	AOCs_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2012/07/23	04:12:26.0	XRT_FOCUS_RECALIBRATE_429_OG [0x1ad]							
		XRT_FOCUS_RECAL	2	07-F8	78	00			
2012/07/23	04:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/23	04:16:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/07/23	04:16:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/07/23	04:16:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/07/23	04:16:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/07/23	04:16:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/23	04:16:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	04:16:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2012/07/23	04:16:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2012/07/23	04:48:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/23	04:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	05:50:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	05:50:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	05:50:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/23	05:53:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/23	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	06:10:00.0	AOCs_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2012/07/23	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/07/23	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/07/23	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/07/23	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/07/23	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/23	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	06:12:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2012/07/23	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2012/07/23	06:26:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/23	06:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	07:30:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	07:30:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	07:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/23	07:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/23	08:04:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/23	08:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	08:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	09:00:00.0	AOCs_OrE-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	55	db	00	5a

2012/07/23	09:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/07/23	09:02:46.0	XRT_FLD_ENA_403_OG [0x193]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/07/23	09:02:48.0	XRT_FLRCTRL_ENA_428_OG [0x1a6]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/07/23	09:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/07/23	09:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/23	09:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	09:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2012/07/23	09:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2012/07/23	09:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	09:10:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	09:10:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	09:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/23	09:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/23	09:43:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/23	09:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	10:53:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	10:53:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	10:53:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/23	10:56:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/23	11:21:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/23	11:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	12:32:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	12:32:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	12:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/23	12:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/23	13:00:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/23	13:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	14:10:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	14:10:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	14:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/23	14:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/23	14:48:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/23	14:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	14:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	15:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2012/07/23	15:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/07/23	15:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/07/23	15:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/07/23	15:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/07/23	15:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/23	15:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	15:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2012/07/23	15:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2012/07/23	15:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	15:49:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	15:49:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	15:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/23	15:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/23	16:36:00.0	XRT_Custom_418_OG [0x1a2]							

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2012/07/23	16:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	17:27:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	17:27:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	17:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/23	17:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/23	18:12:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	18:12:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/07/23	18:13:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/07/23	18:13:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/07/23	18:13:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/07/23	18:13:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/23	18:15:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2012/07/23	18:16:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	18:22:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	18:23:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/07/23	18:25:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/07/23	18:25:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/07/23	18:25:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/07/23	18:25:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/07/23	18:25:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/07/23	18:25:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	18:25:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2012/07/23	18:25:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2012/07/23	18:26:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	19:06:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	19:06:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	19:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/23	19:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/23	19:49:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/23	19:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	20:44:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	20:44:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	20:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/23	20:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/23	21:27:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/23	21:28:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/23	22:23:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/23	22:23:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/23	22:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/23	22:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/23	23:02:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/23	23:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/24	00:01:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/07/24	00:01:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/07/24	00:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/07/24	00:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/07/24	00:29:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/24	00:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/07/24	01:40:00.0	XRT_CTRL_MANU_408_OG [0x198]							

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2012/07/24	01:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/24	01:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/24	01:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/07/24	02:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00			
2012/07/24	02:08:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/24	02:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/24	03:17:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/24	03:17:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/24	03:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/24	03:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/07/24	03:46:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/24	03:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/24	03:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/24	03:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/07/24	04:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2012/07/24	04:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/07/24	04:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/07/24	04:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/07/24	04:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2012/07/24	04:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/24	04:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/24	04:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00			
2012/07/24	04:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/07/24	04:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/07/24	04:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/07/24	04:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/07/24	04:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/07/24	04:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/24	04:12:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2012/07/24	04:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e			
2012/07/24	04:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/24	04:47:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/24	04:47:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/24	04:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/24	04:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/07/24	05:25:00.0	XRT_Custom_418_OG [0x1a2]							
2012/07/24	05:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/24	06:00:00.5	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00			
2012/07/24	06:28:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/24	06:28:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/24	06:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/24	06:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/07/24	07:03:30.0	XRT_Custom_418_OG [0x1a2]							
2012/07/24	07:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/07/24	08:08:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/07/24	08:08:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/07/24	08:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/07/24	08:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			

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2012/07/24	08:42:00.0	XRT_Custom_418_OG [0x1a2]																		
2012/07/24	08:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]																		
		MDP_XRT_CTRL_AUTO	1	07-F0	c0															
2012/07/24	09:48:30.5	XRT_CTRL_MANU_408_OG [0x198]																		
		MDP_XRT_CTRL_MANU	1	07-F0	c1															
2012/07/24	09:48:32.5	XRT_FLD_RESET_412_OG [0x19c]																		
		MDP_XRT_FLD_RESET	1	07-F0	da															
2012/07/24	09:48:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]																		
		MDP_XRT_PREFLR_STRT	1	07-F0	e8															
2012/07/24	09:51:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]																		
		MDP_XRT_PREFLR_STOP	1	07-F0	e9															
2012/07/24	10:23:00.0	AOCS_ORe-point_Start_2_OG [0x098]																		
		AOCU_NM	5	02-76	00 00 00 00 00 00															